

MUWIT-402 TX Board Reference

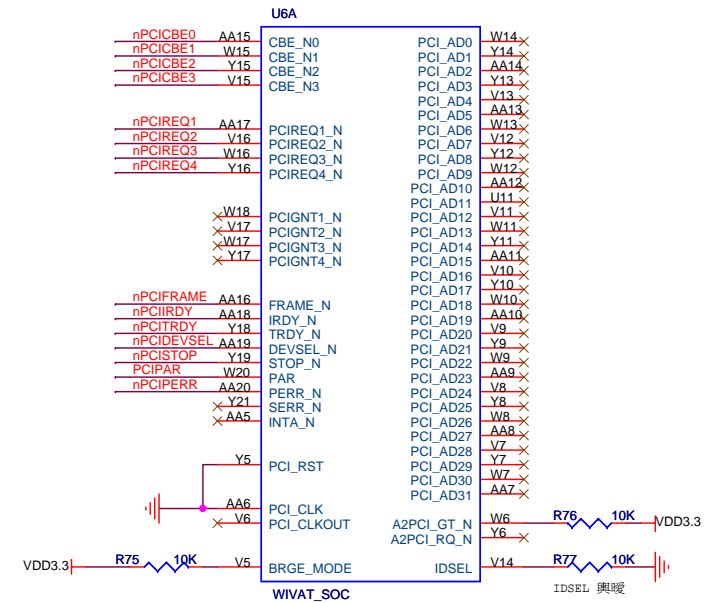
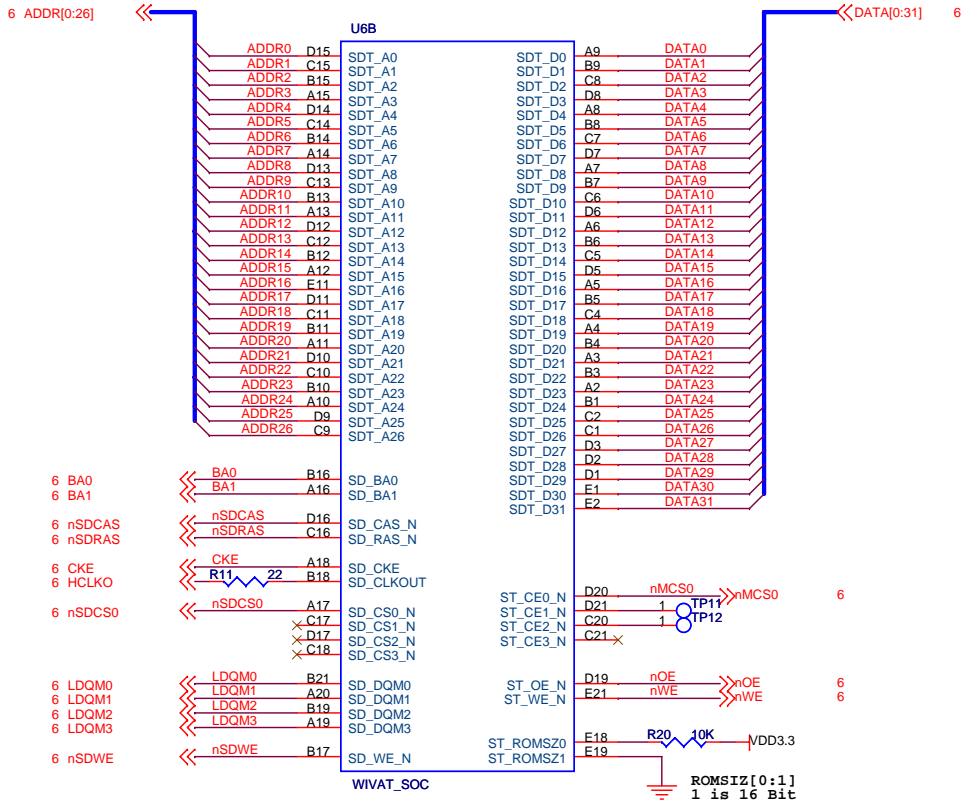
\*\* Version History

Date	Ver.	Author	Description
2007-07-03	0.9	by hslee	- Initial designed
2008-12-05	3.0	Bruce	SDRAM change to mobile SDRAM
2009-04-09	4.0	Bruce	

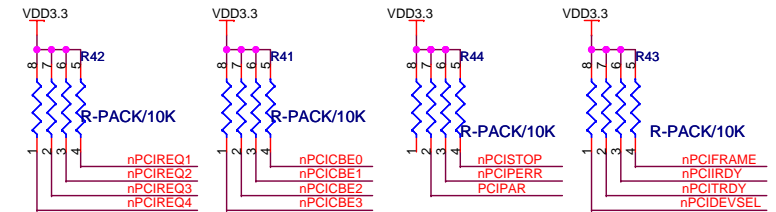
\*\* Remark Recommend

\*\* Board Placement

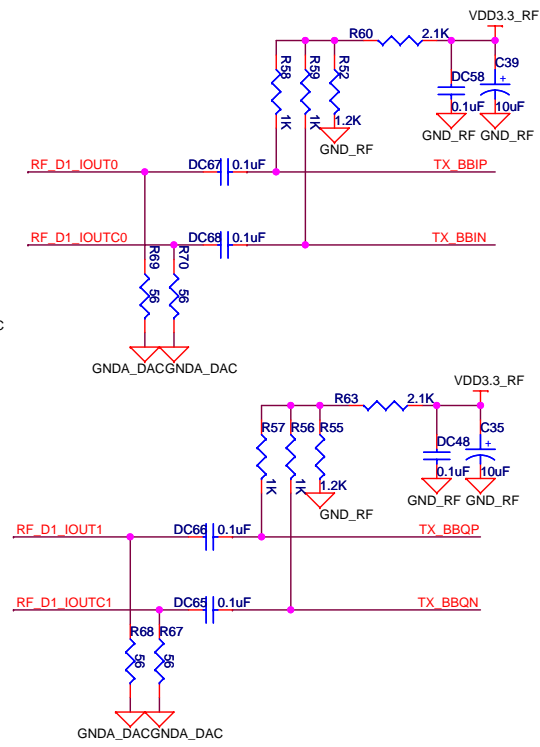
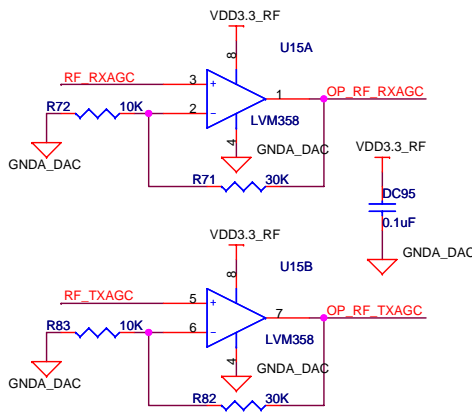
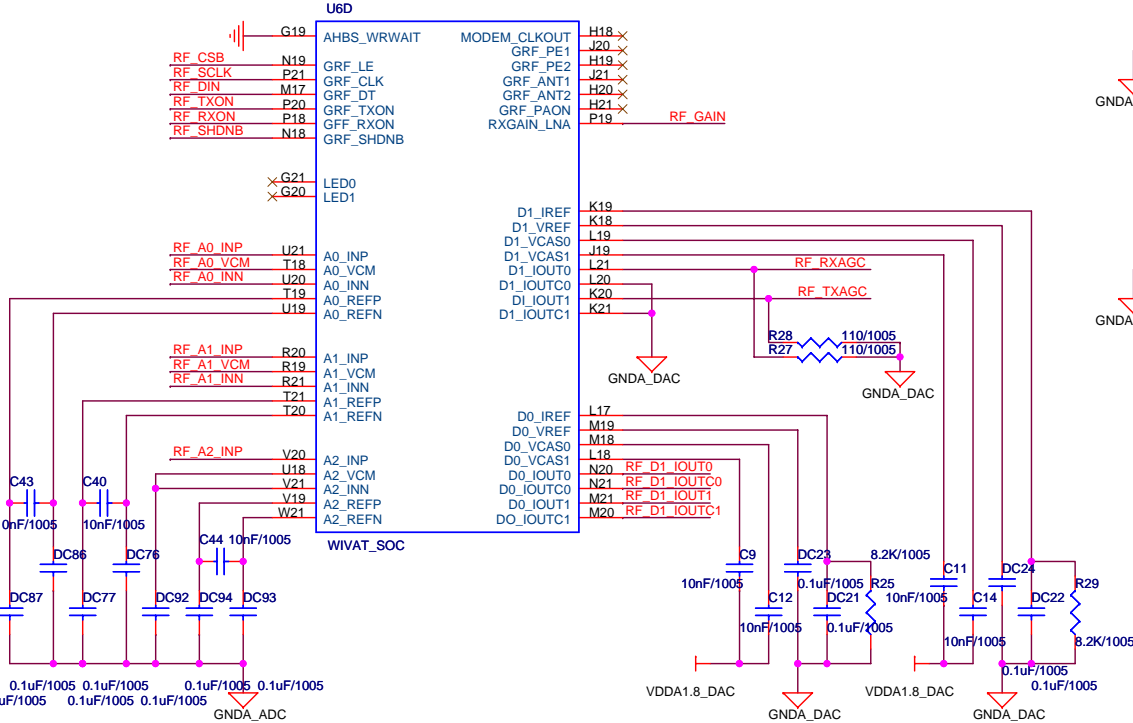
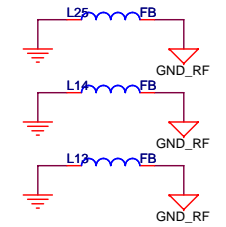
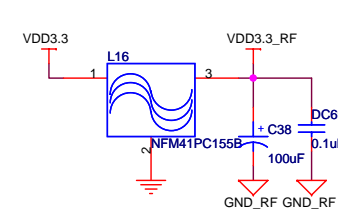
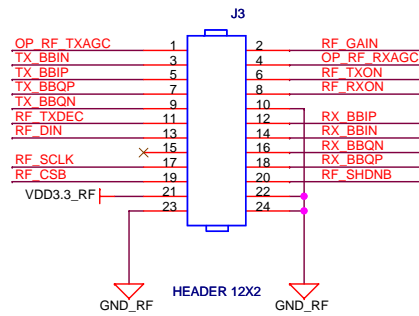
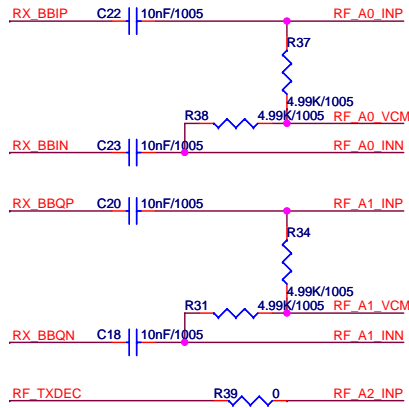
\*\* PCB Layer



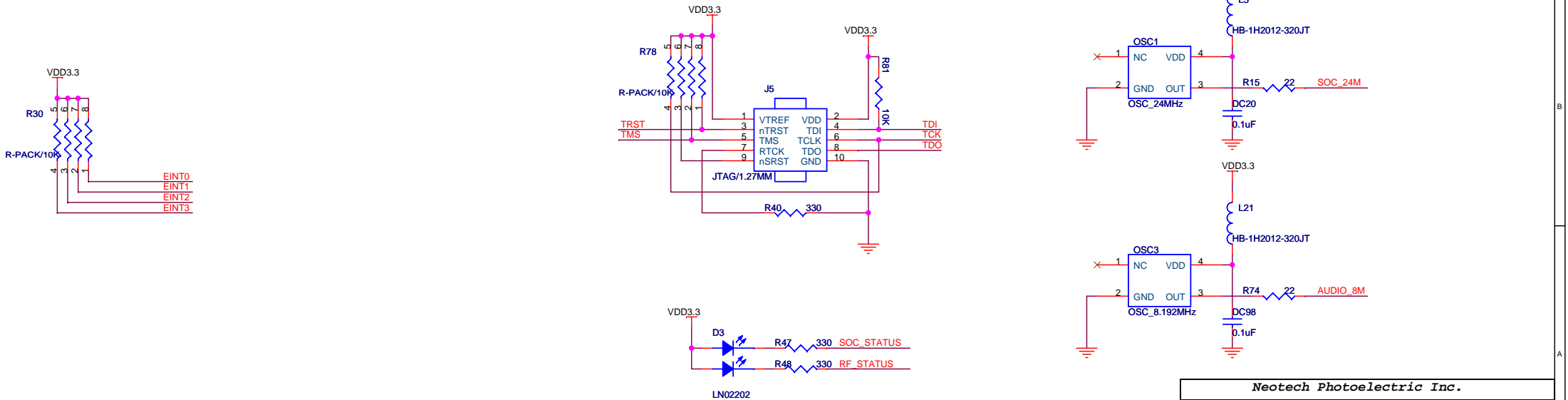
- PCI NOT USED

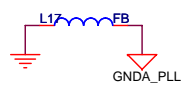
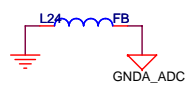
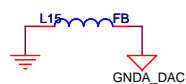
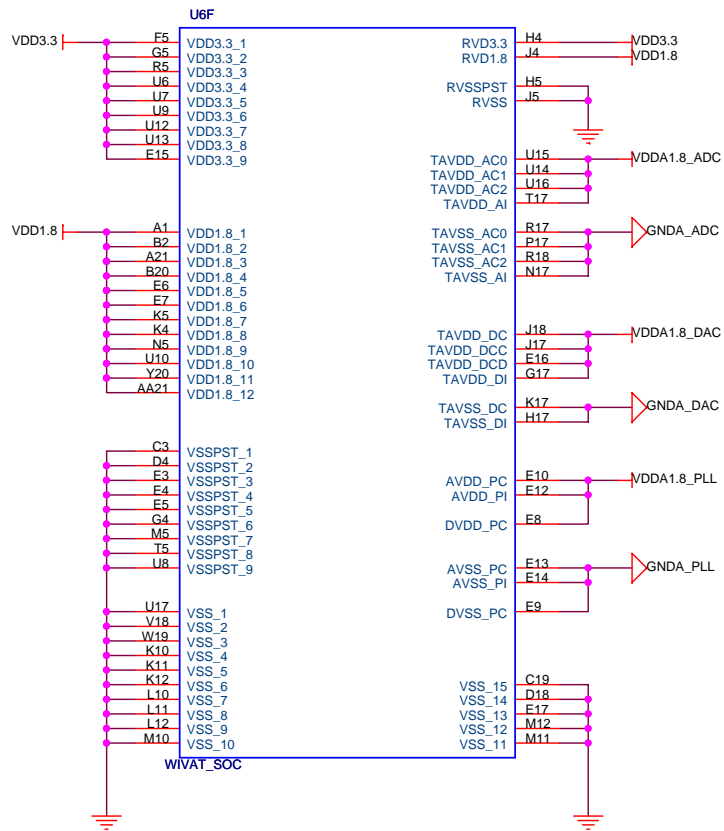


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Size	Document Number	Rev	
B	<Doc> SOC Memory PCI Interface	4.0	
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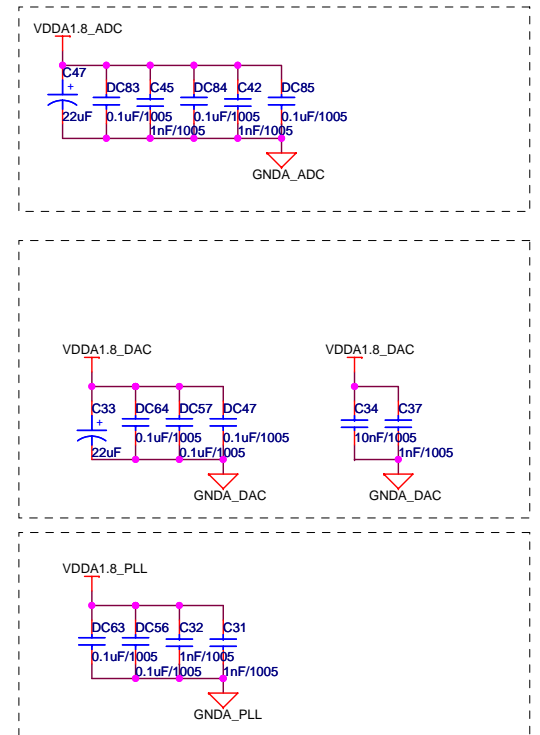
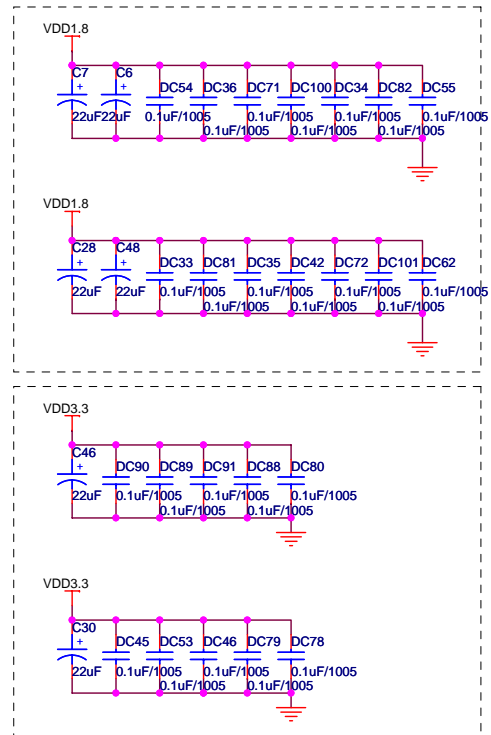


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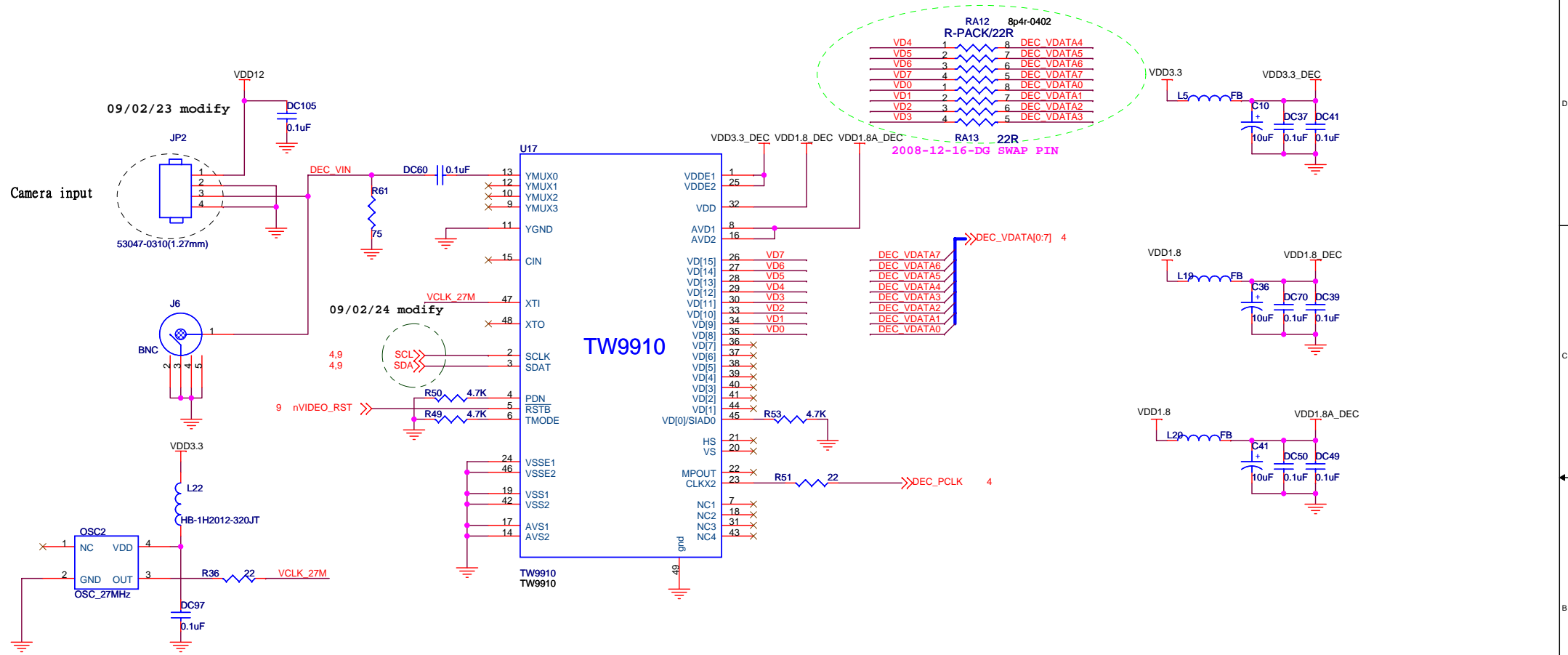


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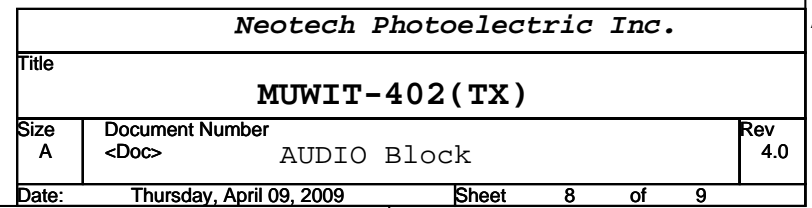


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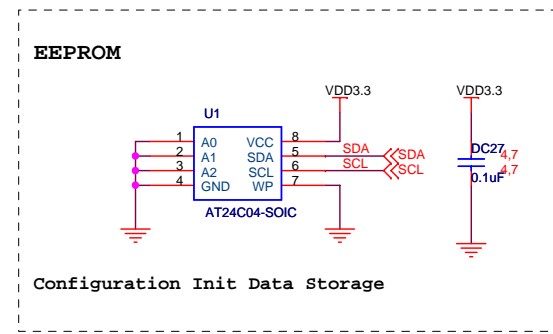
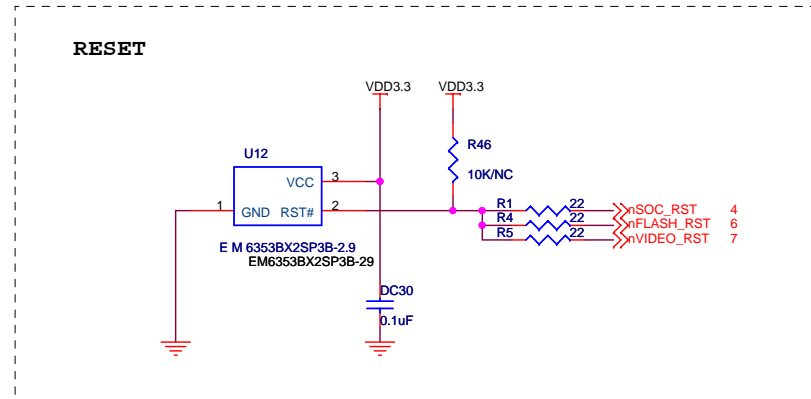
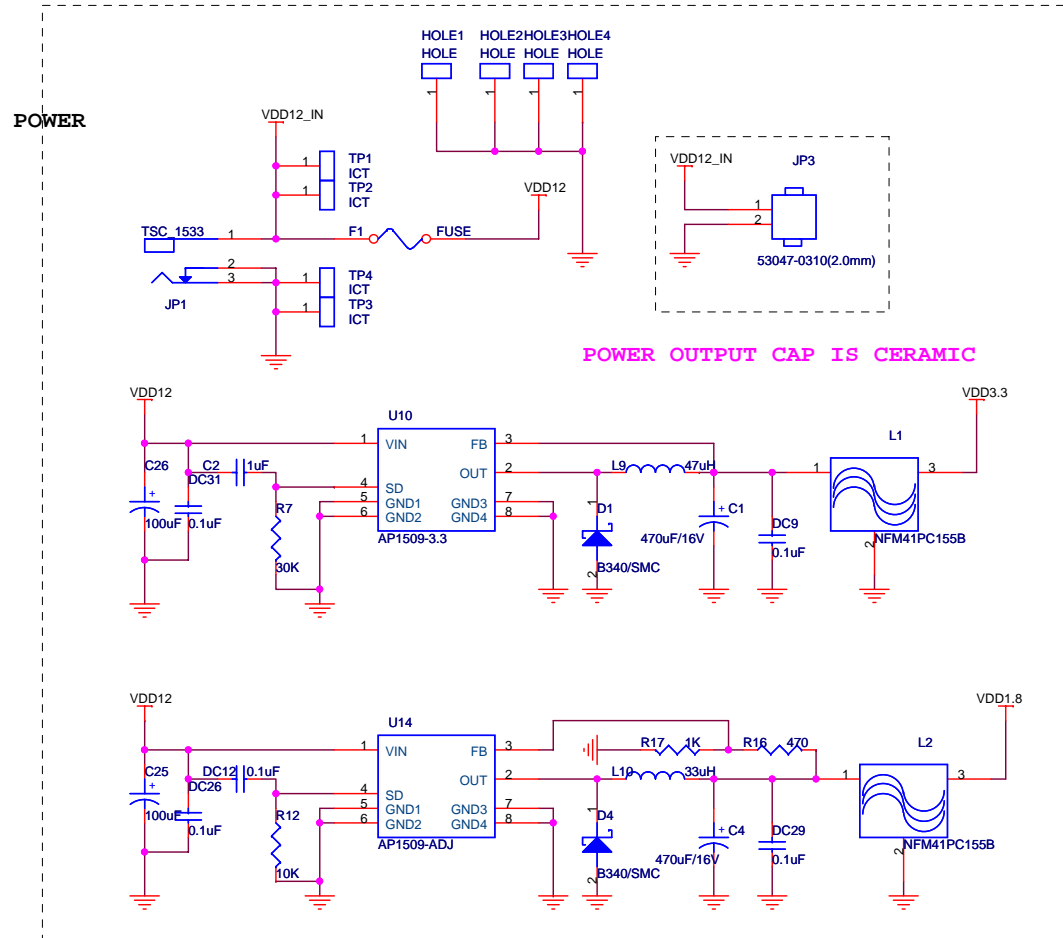
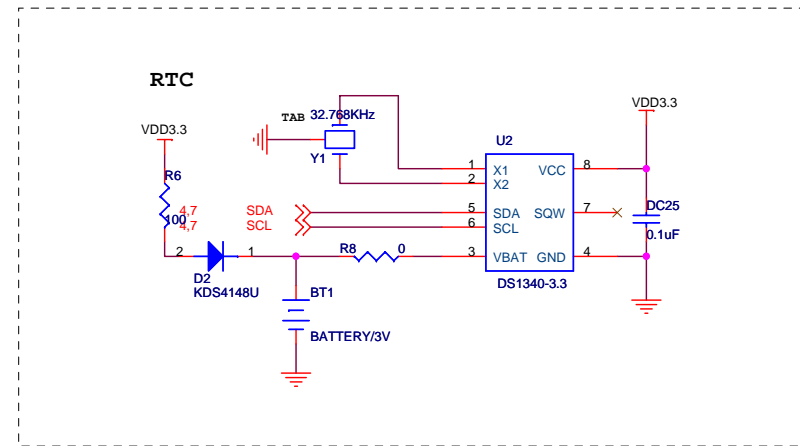
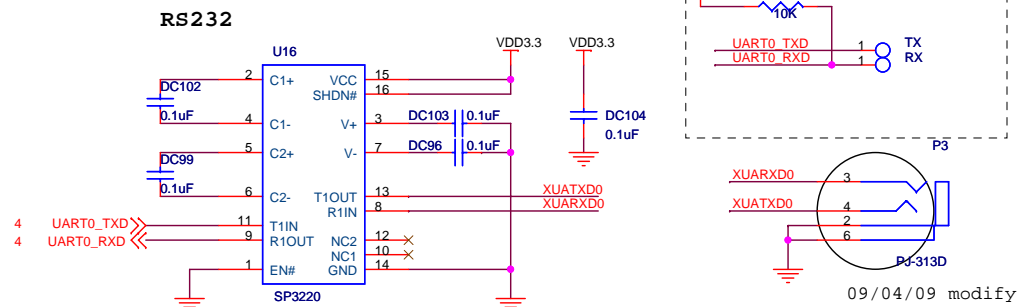




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