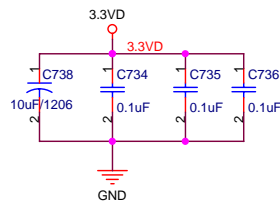
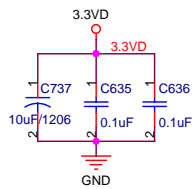
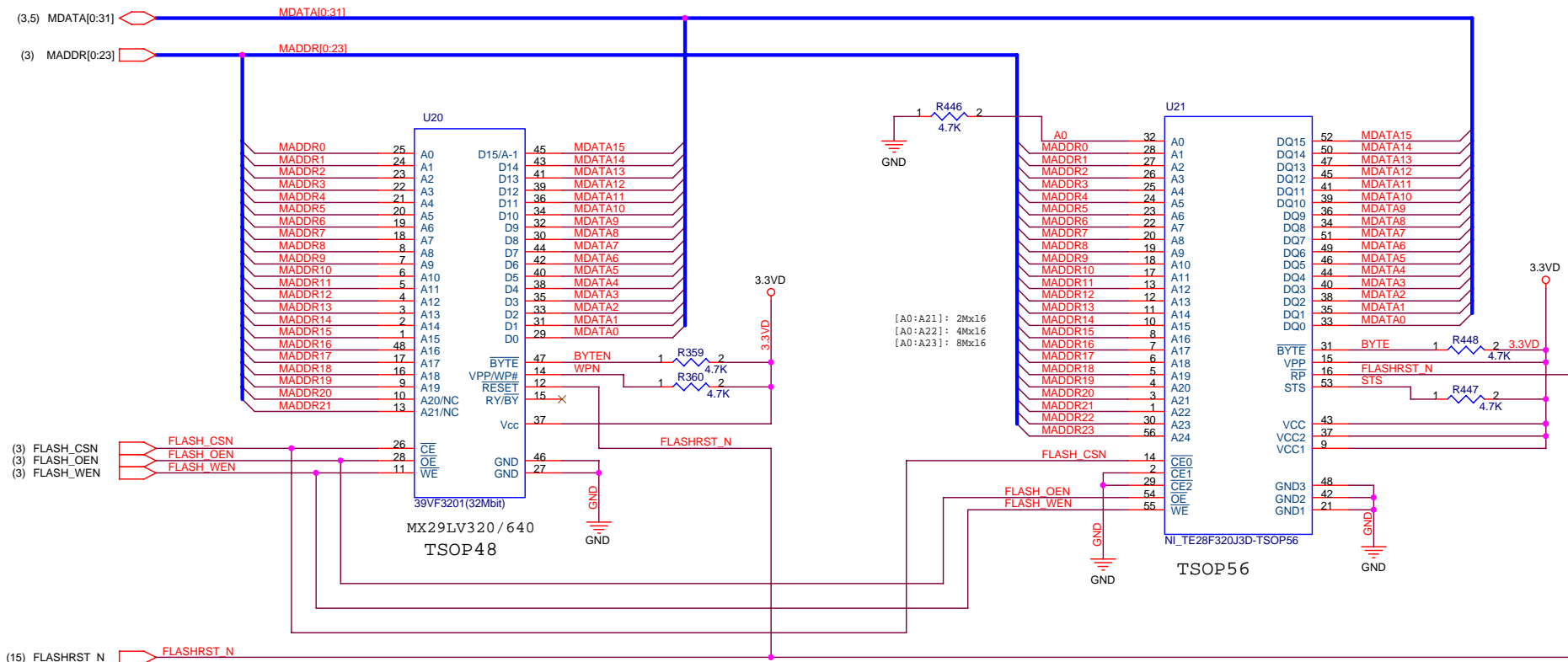
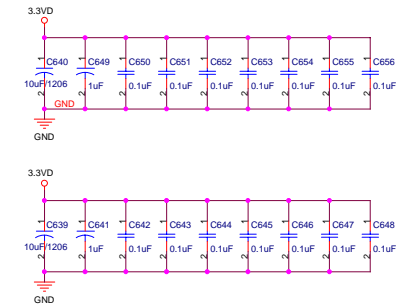
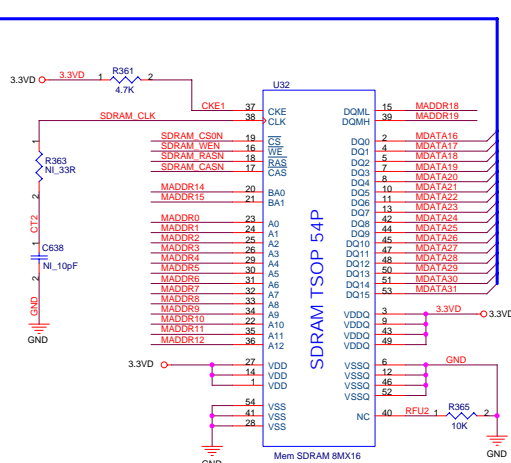


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AP-RT2880+RT2850-2X3-GB-V6_091013.DSN

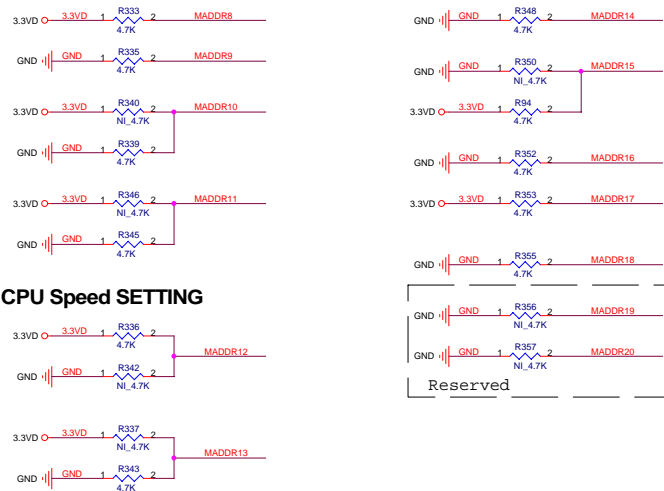
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AP RT2880+RT2850 with Gigabit Switch		
Size	Document Number	
	AP-RT2880+RT2850-2X3-GB-V6_091013	
B	Rev	
		V6
Date:	Thursday, October 29, 2009	Sheet 1 of 17

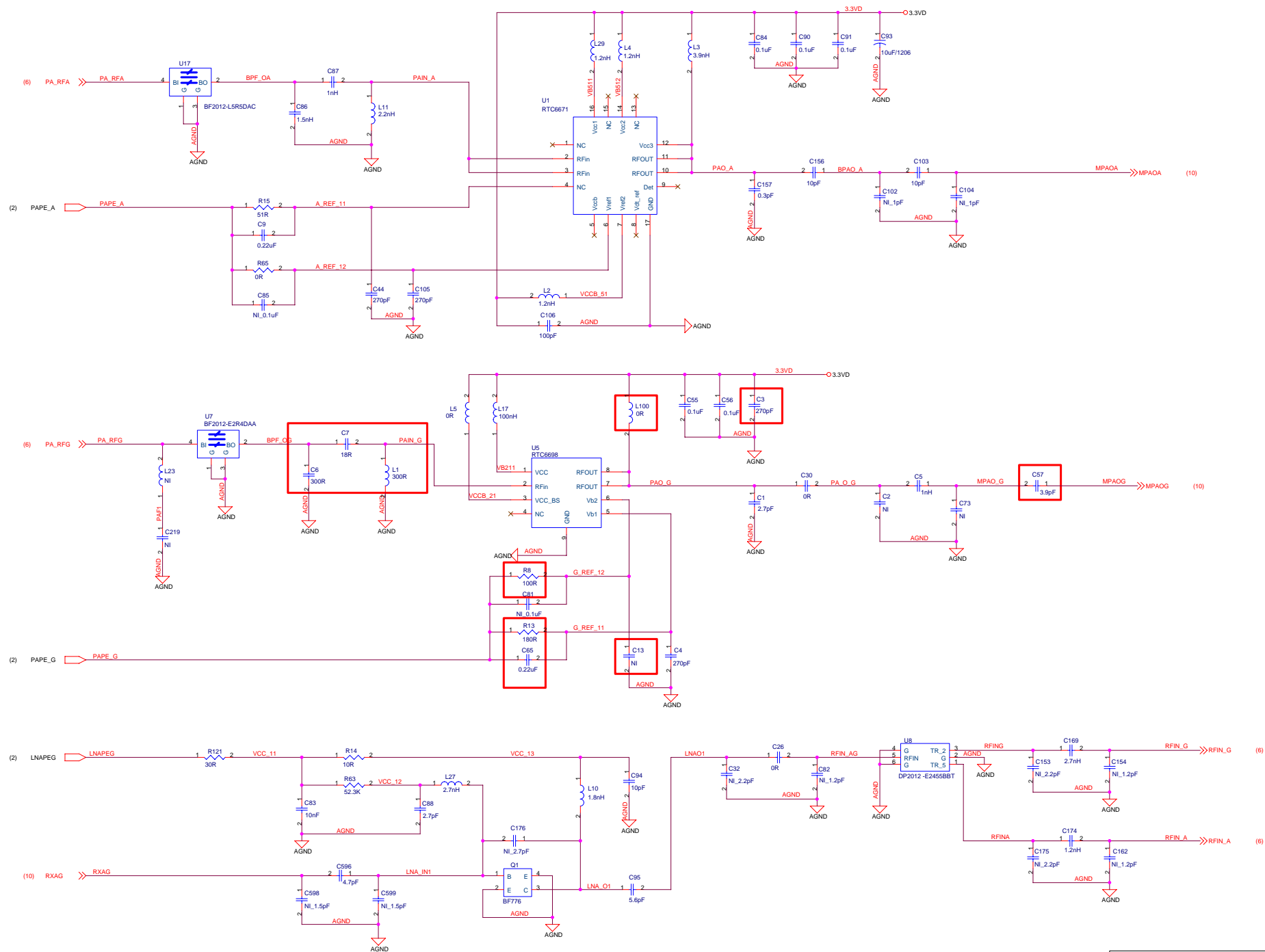


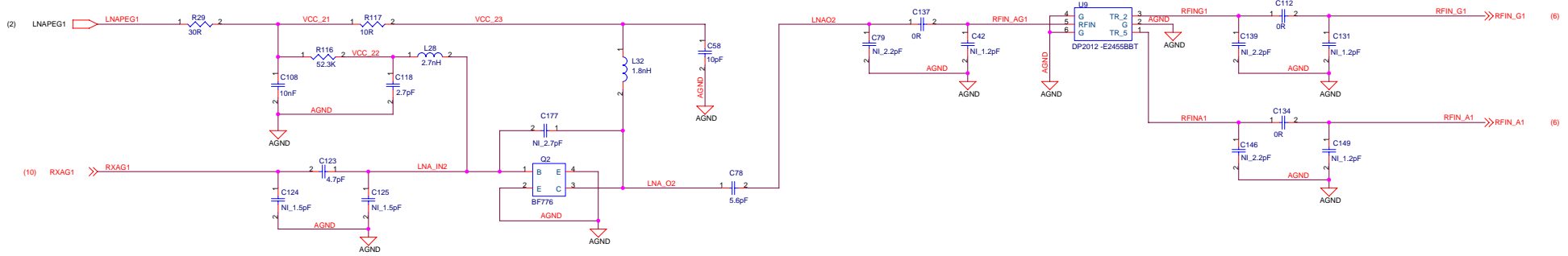
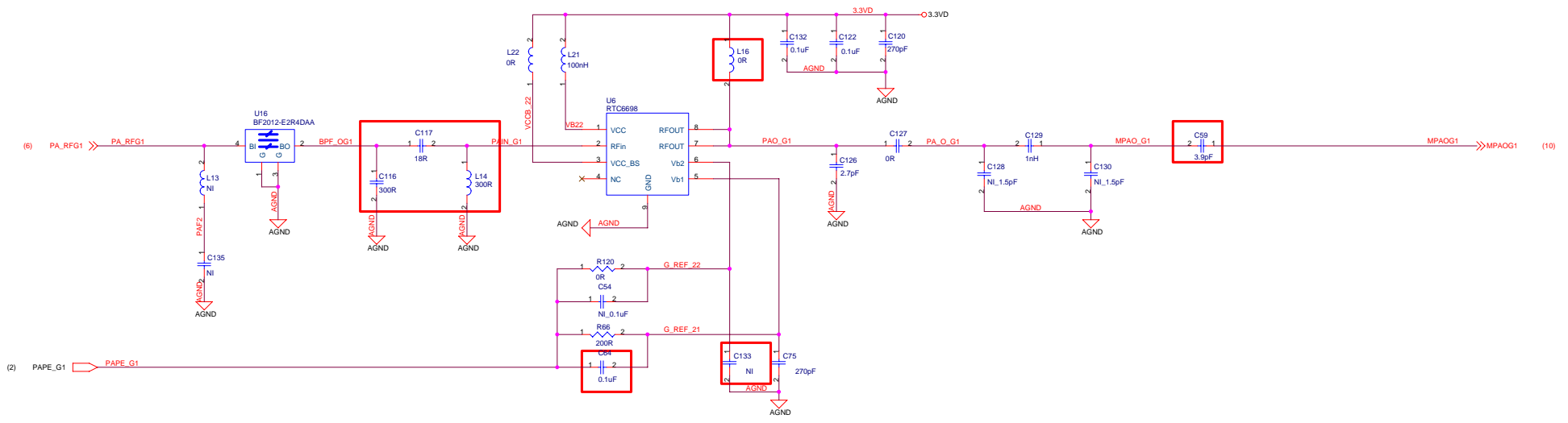
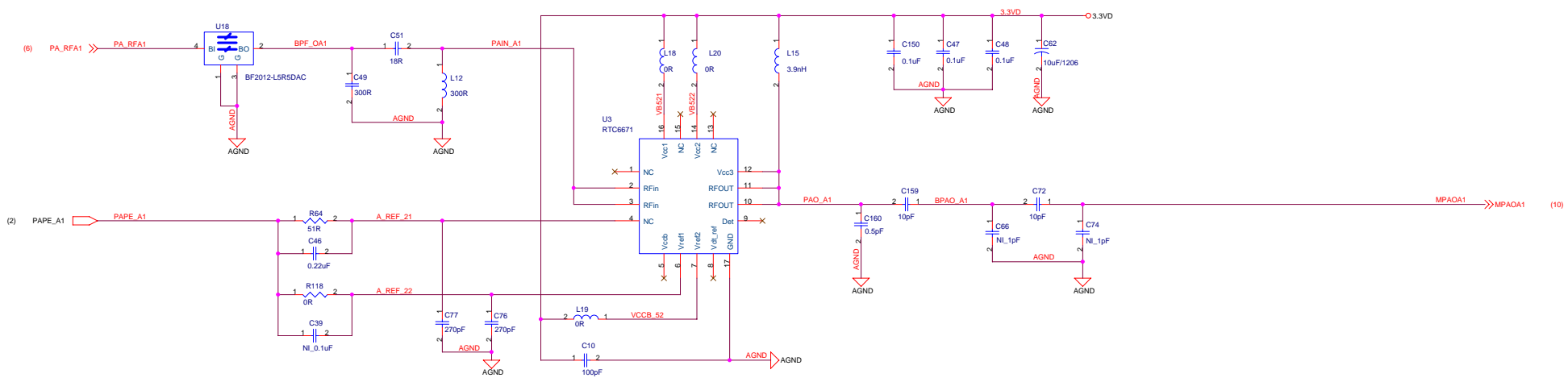
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Size	Document Number	Rev
B	21-FALSH.SCH	V6
Date:	Thursday, October 29, 2009	Sheet 4 of 17

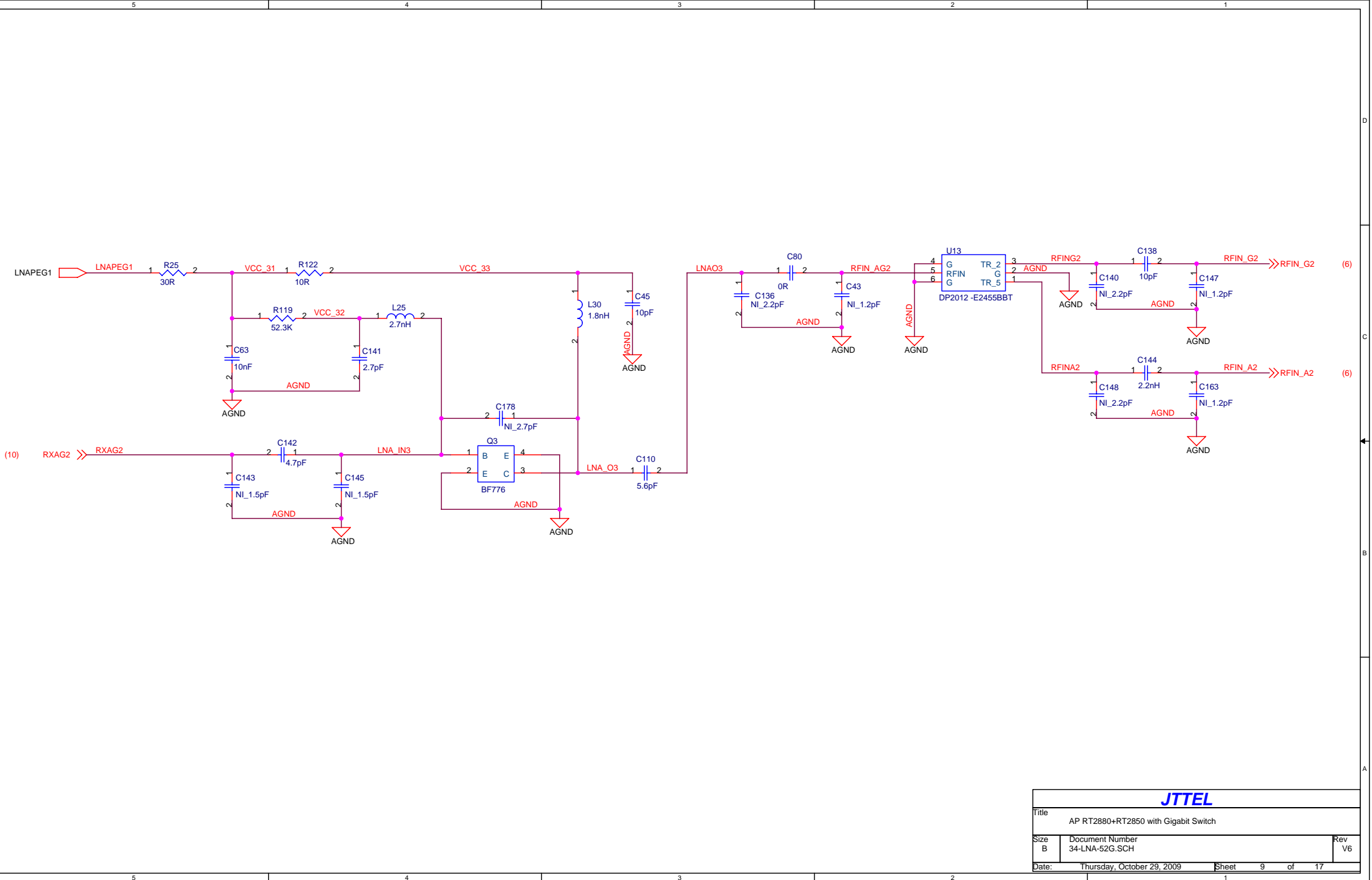


Pin Name	Description	Value=0	Value=1
MA8	Flash	8bit	16bit*
MA9	Boot ROM	ext Flash*	int ROM
MA10	Endian	Little Endian*	Big Endian
MA11	PCI Clock	33MHz*	66MHz
MA[13:12]	CPU clock	00 : 250MHz 01 : 266MHz* 10 : 280MHz 11 : 300MHz	
MA14	Gigabit Enet I/O	3.3V	2.5V*
MA15	Enet TX clock	Input	Output*
MA16	PLL Select	Not Bypass*	Bypass
MA17	PCI Mode	Device	Host*
MA18	Enet RX clock	Input*	Output
MA19	iNIC, SDRAM	2MB*	8MB
MA20	iNIC, Config.	from MA19*	EEPROM

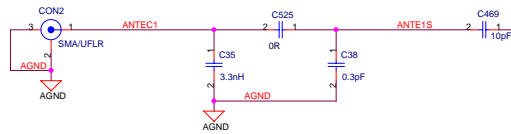




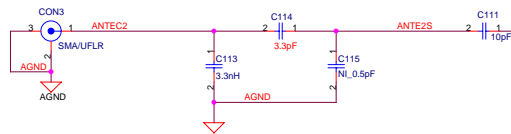




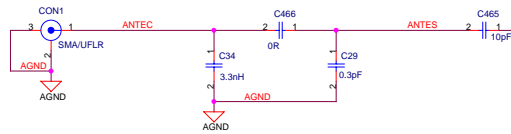
(ANTENNA1)
(TX1/RX1)



(ANTENNA2)
(RX2)



(ANTENNA0)
(TX0/RX0)



(2) TR_SWN0

(2) TR_SW0

JTEL

Title				AP RT2880+RT2850 with Gigabit Switch			
Size		Document Number					Rev
C		35-FrontEnd.SCH					V6
Date:		Thursday, October 29, 2009		Sheet		10 of 17	

For EMI solution

For EMI solution

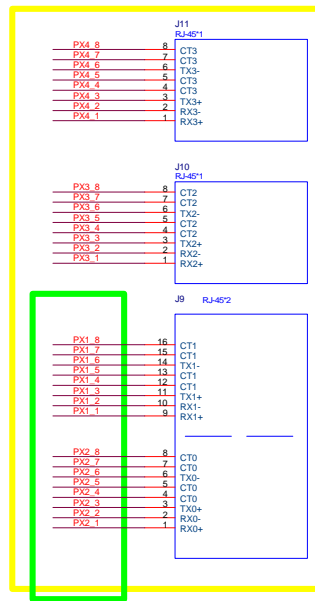
For EMI solution

For EMI solution

For EMI solution

For EMI solution

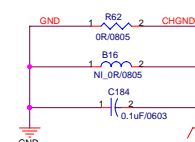
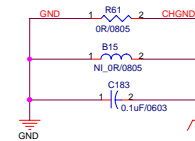
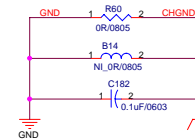
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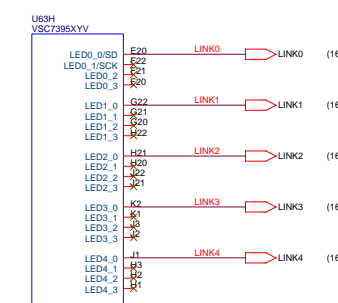
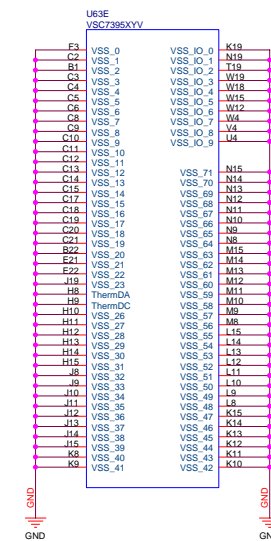
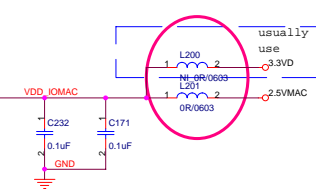
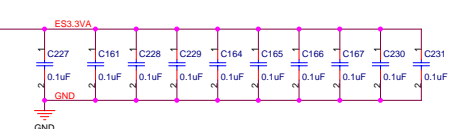
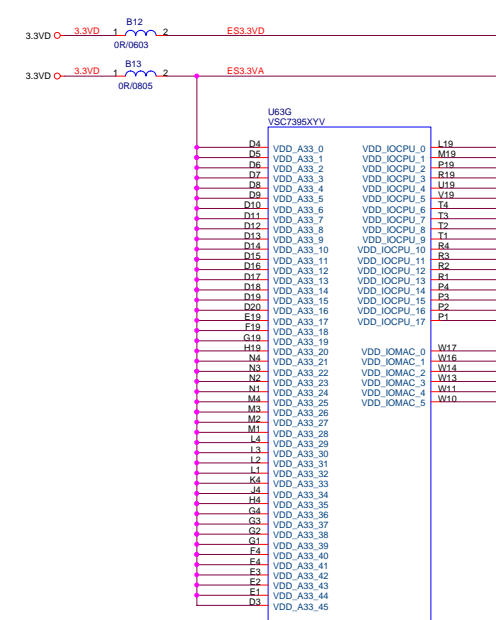
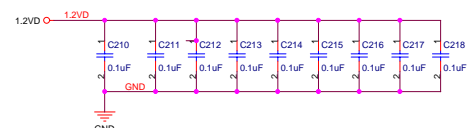
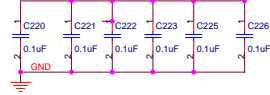
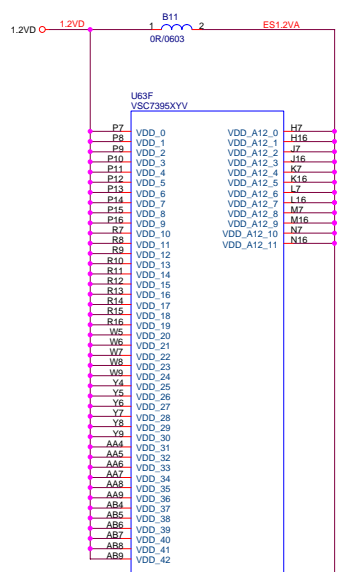
LAN

WAN

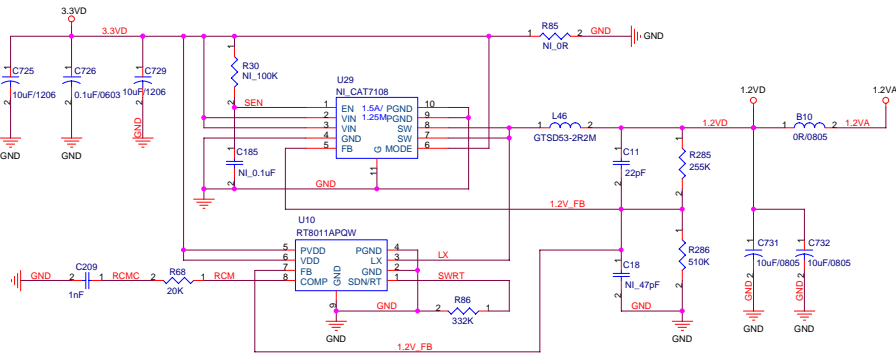
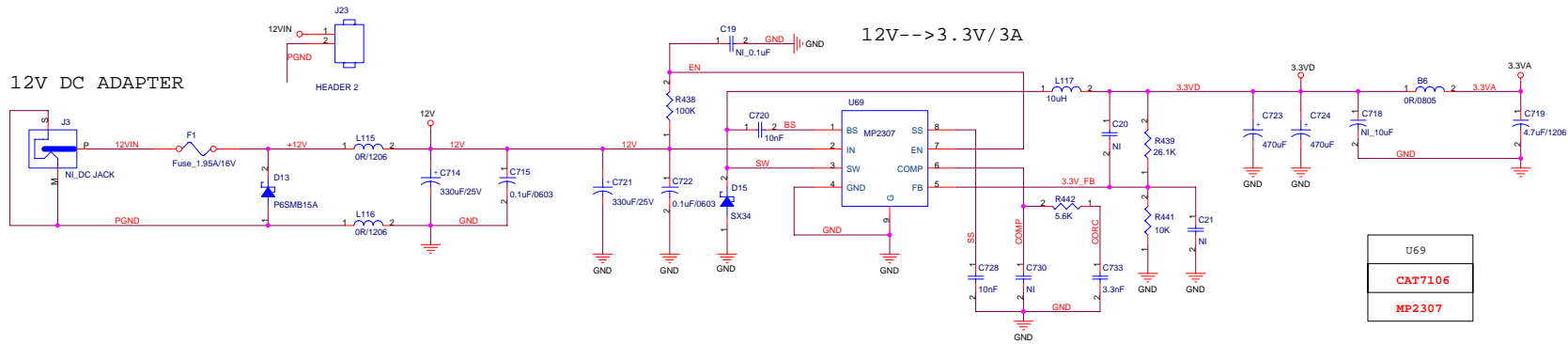
For EMI



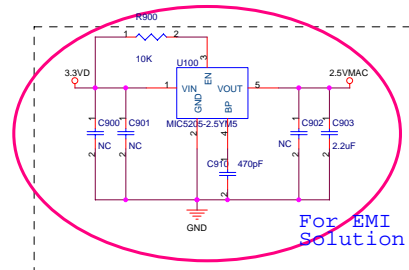
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File	AP RT2880+RT2850 with Gigabit Switch		
Size	Document Number	Rev	
C	42-SW-GB-RJ45.SCH	V6	
Date	Thursday, October 29, 2009	Sheet	12 of 17



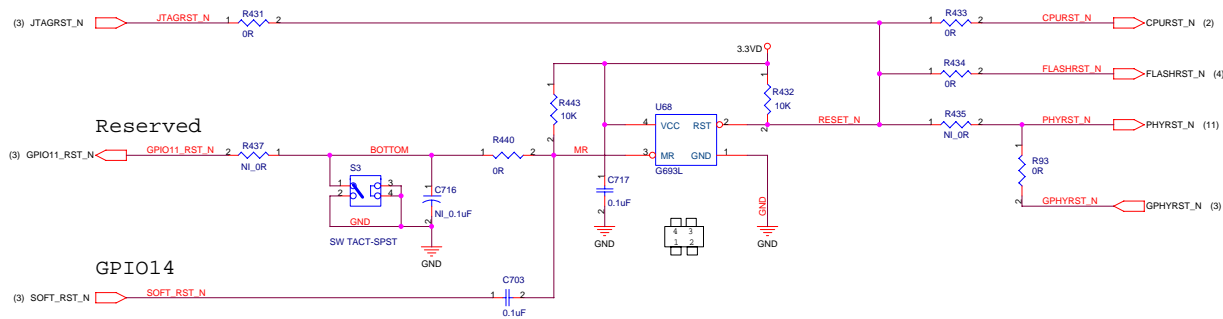
LED Control-
PHY_LED_CTRL (0x1B) PHY Register:
mode 0000

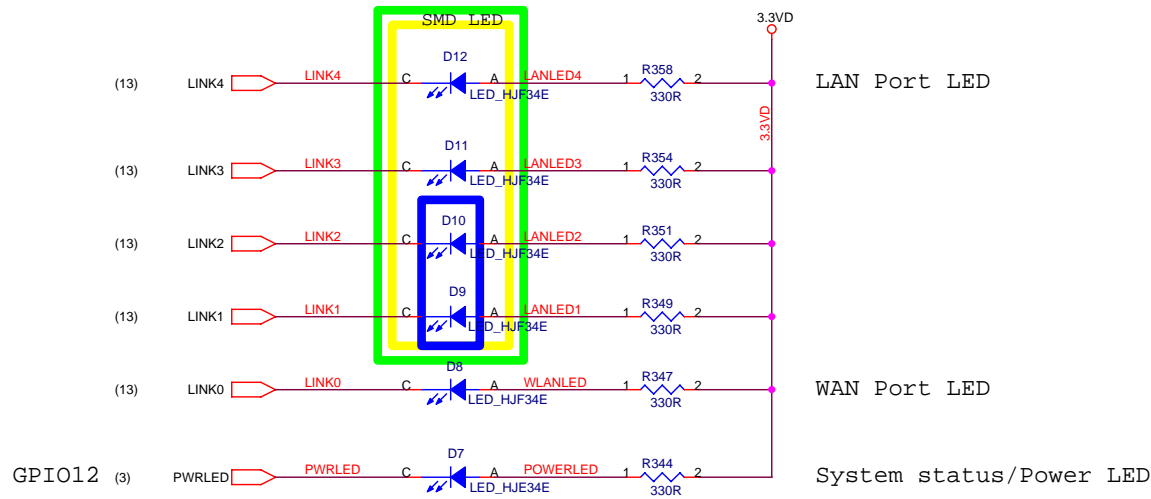
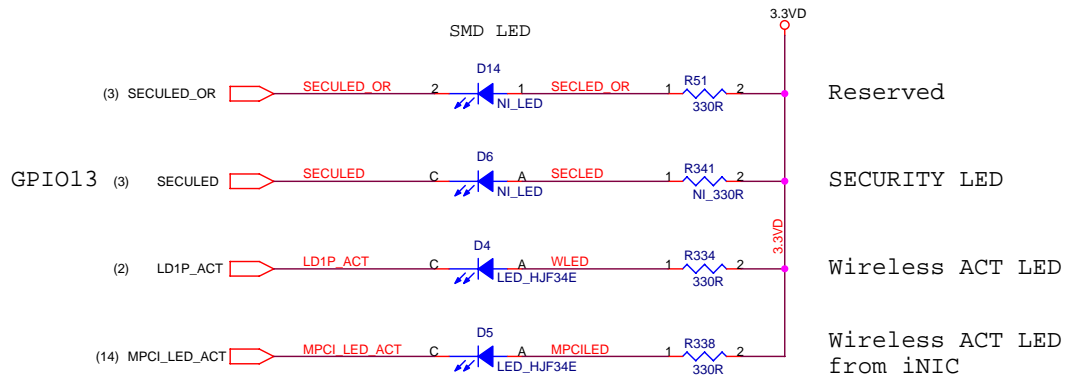


Regulator	Install	Not Install	C11	R285	R286	L46
CAT7108	U29, R30, R285, R286, C185	U10	0.1uF	300K	300K	4.7uH
RT8011A (ADJ)	U10, C11, R285, R286	U29	22pF	255K	510K	2.2uH

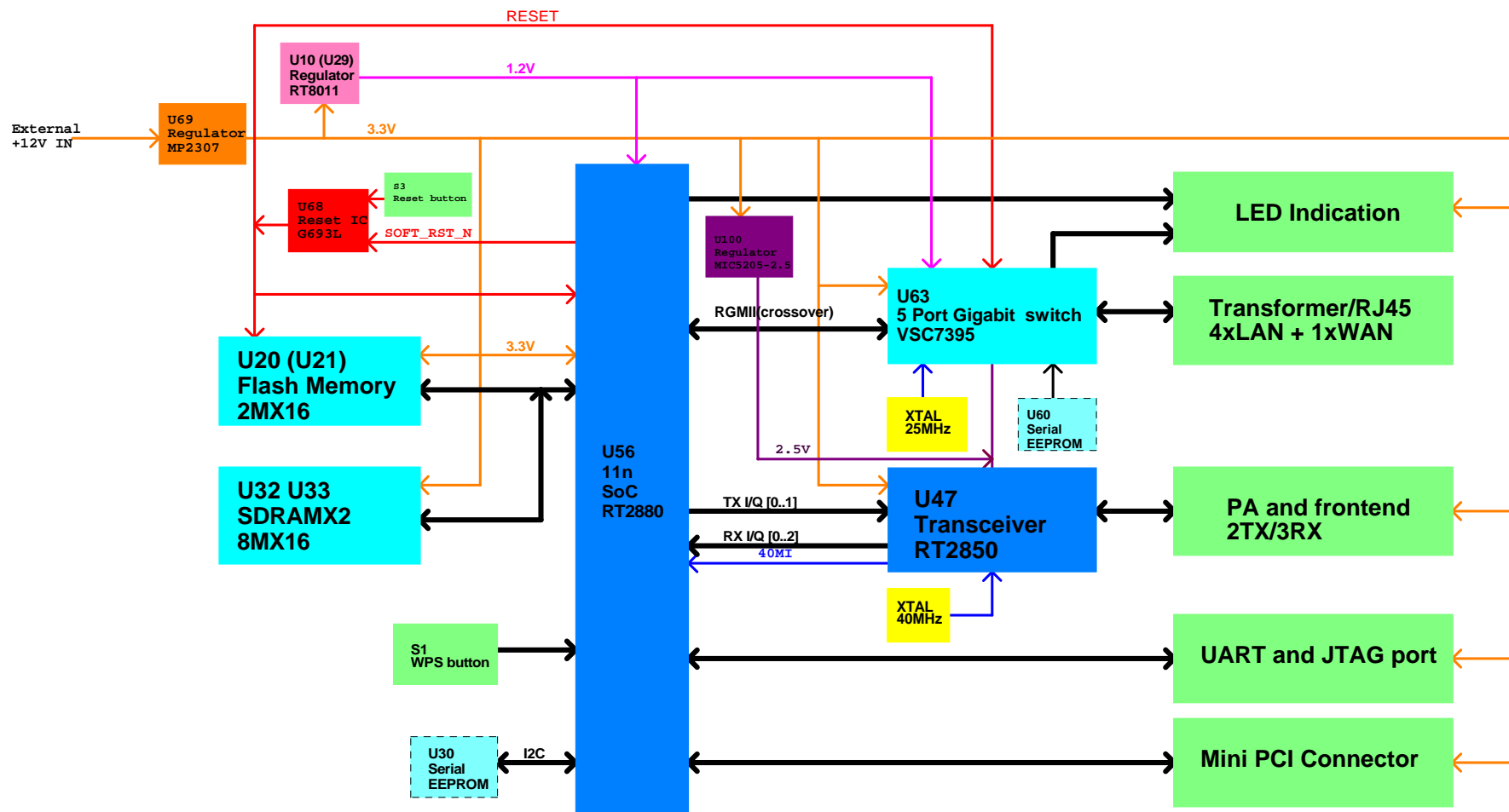


Reset Circuit





JTTEL		
Title AP RT2880+RT2850 with Gigabit Switch		
Size B	Document Number 7-LED.SCH	Rev V6
Date: Thursday, October 29, 2009	Sheet 16	of 17



RF frontend

