Technical Description

The brief circuit description is listed as below:

- 1) U2 acts as 2.4GHz RF Module (BC5).
- 2) U1 acts as Linear Voltage Charging Chip (XT2055).
- 3) U3 acts as LDO Regulator (BL9198).
- 4) X1 acts as a Crystal for U2.
- 5) U4 acts as Audio Amplifier.
- 6) U3 of 2.4GHz RF Module acts as Flash.

Antenna Type: Internal antenna

Antenna Gain: 0dBi

Field strength of production range: 100 to 103.8 dBµV/m at 3m

Part No: BT57E6

BT57E6 Bluetooth Module

SPECIFICATION FOR APPROVAL

Description: BC5 bluetooth module
Revision:V2.1+EDR
Customer:
Customer Approval No:

APPROVED SIGNATURES

Customer:	
APPROVED BY:	APPROVED BY:
DATE:	DATE:
CHOP&SIGNATURES:	CHOP&SIGNATURES:

Bestzlink Technology Ltd 深圳市贝斯兰德电子有限公司 深圳市福田区财富广场B栋7M

TEL: 0755- 21517589

BC5 Bluetooth Module



1. PCBA Features

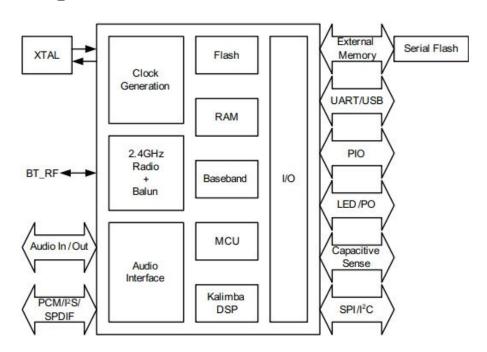
- Fully Qualified Bluetooth V2.1+EDR specification system
- Best in Class Bluetooth Radio with +8dBm Transmit Power and -90dBm Receive Sensitivity
- •64MIPS Kalimba DSP Co-Processor
- ●16-bit Internal Stereo CODEC 95dB SNR for DAC
- Low-Power 1.5V Operation, 1.8V to 3.6V I/O
- Integrated 1.5V and 1.8V Linear Regulators
- Integrated Switched-Mode Regulator
- Integrated Battery Charger
- USB, I2C and UART with Dual Port Bypass Mode to 4Mbits/s
- Supports up to 32Mbits of External Flash Memory (8Mbits Typical Requirement)
- Multi-Configurable I2S, PCM or SPDIF Interface
- Enhanced Audibility and Noise Cancellation
- Support for 802.11 Co-existence
- RoHS Compliant
- Surface-mount, Size: 18.50×13.00×2.00mm max

2.Description

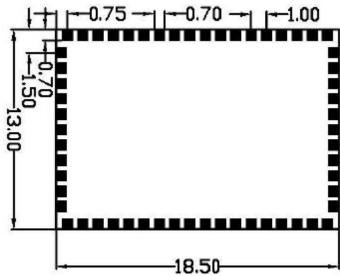
The **BT57E6** is a Bluetooth sub-system using BlueCore5-Multimedia External chipset from leading Bluetooth chipset supplier Cambridge Silicon Radio. The BlueCore5-Multimedia External is a single-chip radio and baseband IC for Bluetooth 2.4GHz systems.

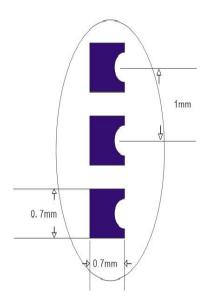
BT57E6 interfaces up to 32Mbit of external Flash memory. When used with CSR Bluetooth stack, it provides a fully compliant Bluetooth system to V2.1+EDR of the specification for data and voice.

3.Diagram



4. Dimensions



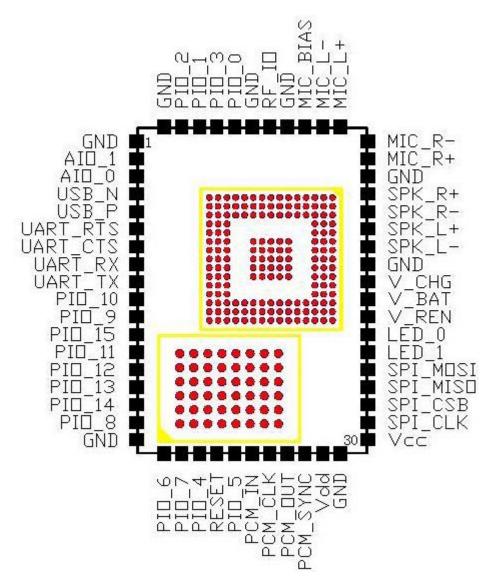


5. Pin Assignment

1	GND	Ground connections			
2	AIO(1)	Analogue programmable input/			
2	1110(1)	output line			
3	AIO(0)	Analogue programmable input/			
3	A10(0)	output line			
4	USB_DN	USB data minus			
5	USB_DP	USB data plus with selectable			
3	USB_DI	internal 1.5kΩ pull-up resistor			
6	UART_RTS	UART request to send active low			
7	UART_CTS	UART clear to send active low			
8	UART_RX	UART data input			
9	UART_TX	UART data output			
10	PIO_10	Programmable input/output line			
11	PIO_9	Programmable input/output line			
12	PIO_15	Programmable input/output line			
13	PIO_11	Programmable input/output line			
14	PIO_12	Programmable input/output line			
15	PIO_13	Programmable input/output line			
16	PIO_14	Programmable input/output line			
17	PIO_8	Programmable input/output line			
18	GND	Ground connections			
19	PIO_6	Programmable input/output line			
20	PIO_7	Programmable input/output line			
21	PIO_4	Programmable input/output line			

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Description I I I I I I I I I I I I I I I I I I I			
RESET	Reset if low. Input de bounced so must be low for >5ms to cause a reset		
DIO 5	Programmable input/output line		
_	Synchronous data input Synchronous data clock		
<u> </u>			
	Synchronous data output		
-	Synchronous data sync		
	+2.8~+3.3V		
	Ground connections		
	+1.8V		
_	SPI clock		
<u> </u>	SPI active low		
<u> </u>	SPI data output		
<u> </u>	SPI data input		
_	LED driver		
LED_0 LED driver			
V REN	Take high to enable high-voltage linear		
	regulator and switch-mode regulator		
	Lithium ion/polymer battery positive		
V_BAT	terminal. Battery charger output and input to		
	switch-mode regulator		
V CHG	Lithium ion/polymer battery charger input		
GND	Ground connection		
	Speaker output negative, left		
-	Speaker output positive, left		
_	Speaker output negative, right		
	Microphone input positive, right		
	Microphone input negative, right		
<u> </u>	Microphone input negative, light		
	Microphone input positive, left		
	Microphone bias		
<u>-</u>	Ground connection		
	50 ohm Rx/Tx connection to antenna		
-	Ground connection		
	Programmable input/output line		
	Programmable input/output line Programmable input/output line		
TIV 3	<u> </u>		
	Drogrammable innut/output line		
PIO_1	Programmable input/output line		
PIO_1 PIO_2	Programmable input/output line Programmable input/output line		
PIO_1 PIO_2 GND Gro			
PIO_1 PIO_2 GND Gro und			
PIO_1 PIO_2 GND Gro			
	PIO_5 PCM_IN PCM_CLK PCM_OUT PCM_SYNC Vdd GND Vcc SPI_CLK SPI_CSB SPI_MISO SPI_MOSI LED_1 LED_0 V_REN V_BAT V_CHG		



6. Specification

6.1 General Specification

Items	Specification		
Operating Frequency Band	2.402GHz-2.480GHz unlicensed ISM		
operating Frequency Band	Band(USA,Spain, France)		
Bluetooth Specification	V2.0/V2.1/V2.1+EDR		
Output Power Class	Class II		
Operating Voltage	+1.8V, +3.3V		
Host interface	UART		
Audio interface	Analog 、 PCM、 I2S、 SPDIF		
Baseband Crystal OSC	26.000MHz		
Output Interface	UART,I2C		

6.2 Electrical Characteristics				
Absolute Maximum Rating				
Rating	Min			Max
Storage Temperature	-25°C			+125°C
Supply Voltage: Vcc	-0.4V			+2.1V
Input I/O Voltage: Vdd	-0.4V			+3.6V
Supply Voltage: V_BAT, V_REN	-0.4V			+4.5V
Supply Voltage: V_CHG	-0.4V		+6.3V	
Recommended Operating Conditions				
Operating Condition	Min		Max	
Operating Temperature Range	-20°C		+70°C	
Supply Voltage: Vcc	+1.75V	,	+1.9V	
Supply Voltage: Vdd	+2.75V	,	+3.5V	
Supply Voltage: V_BAT	+3.1V			+4.3V
Supply Voltage: V_CHG	+5.0V			+6.0V
Input/Output Terminal Characteristic	s			
Linear Regulator	Minimum	Typica	al	Maximum
Output Voltage (Iload = 200mA /	1.70V	1.8V		1.9V
$VREG_IN = 3.0V$				
Maximum Output Current	-			200mA
Crystal frequency		26.000	MHz	
Maximum RF transmit power	-6dBm	-6dBm 0dB		+4dBm
Sensitivity at 0.1% BER for all	-	-84dB		-75dBm
Audio Output power into 32Ω	ower into 32Ω		r	
Typical Average Current Consumption	1			
Mode		Ave	erage	
ACL data transfer 115.2kbps UART no t	raffic (Master)	2	2.5	
ACL data transfer 115.2kbps UART no t	raffic (Slave)		10	
SCO connection HV3 (30ms interval Sniff Mode) (Slave)			13	
SCO connection HV3 (30ms interval Sniff Mode)			14	
SCO connection HV3 (Slave)			17	
SCO connection HV3 (Master)			14	
SCO connection HV1 (Slave)			25	
SCO connection HV1 (Master)			4.5	
Microphone inputs and ADC / channel			1	
DAC and loudspeaker driver, no signal / channel			.5	
Digital audio processing subsystem			8	

Digital audio processing subsystem 8

General conditions: Vcc=1.8V Vdd_Flash=3.3V Temperature = +20°C Output

7 Recommended Reflow Temperature Profile

