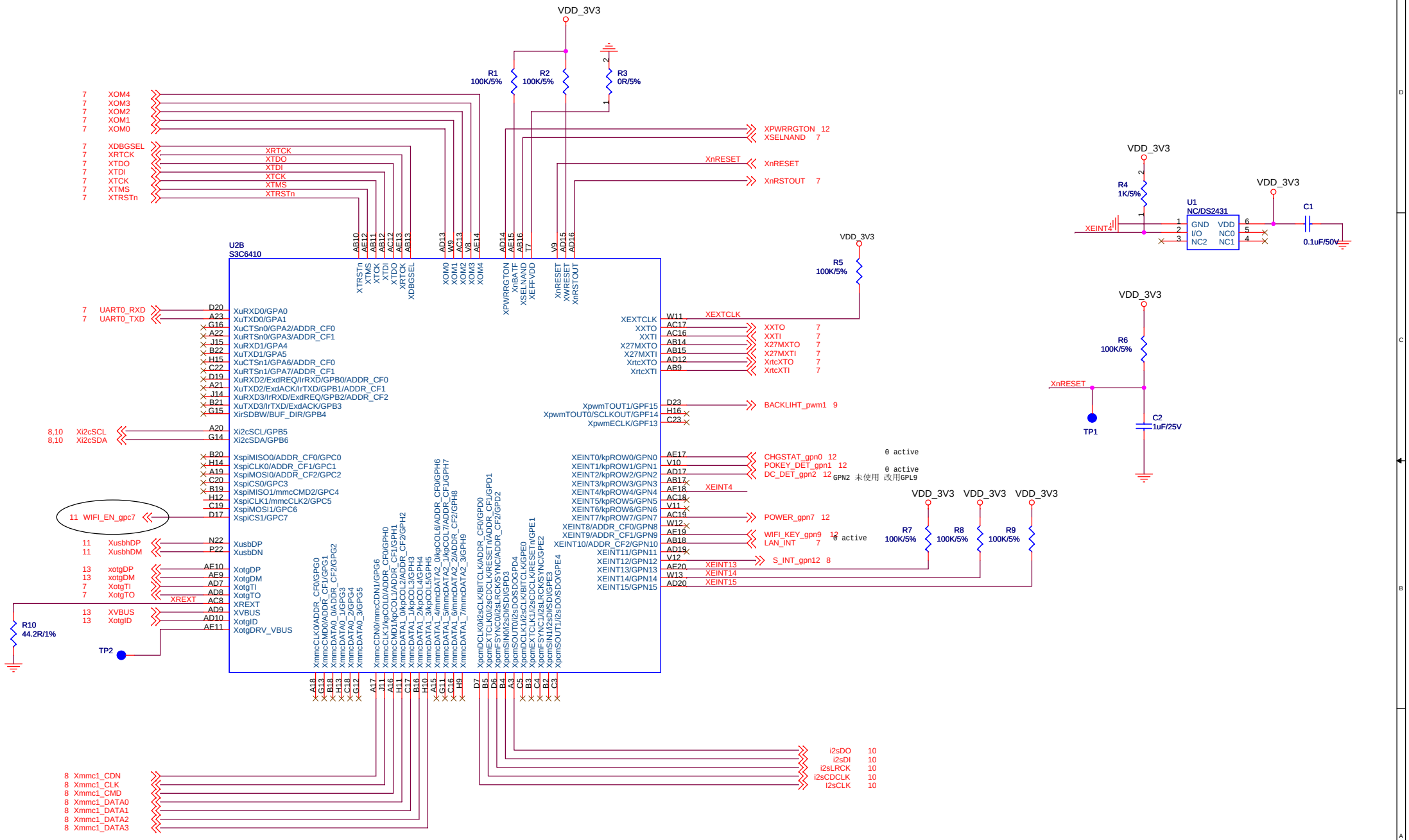
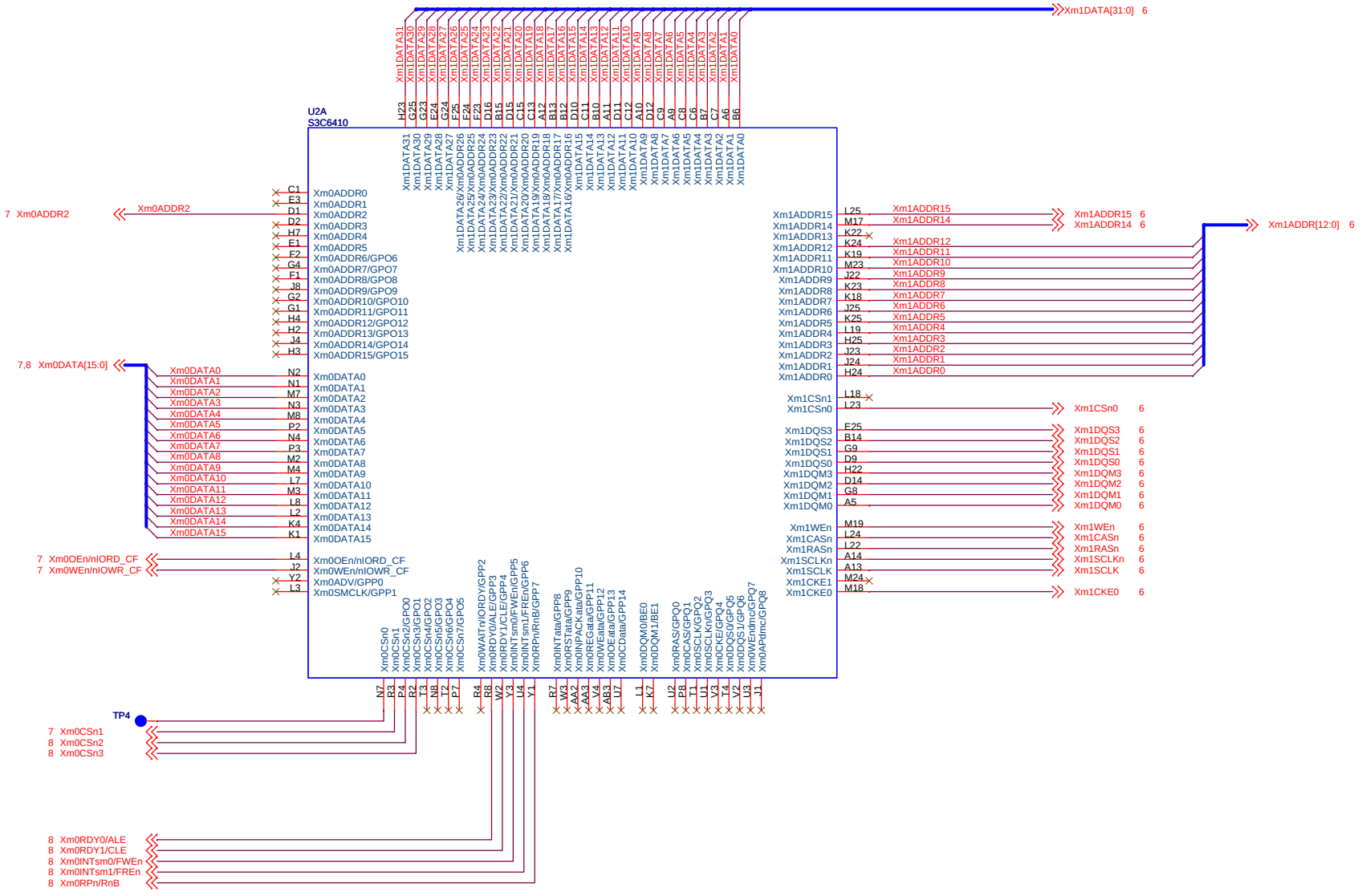
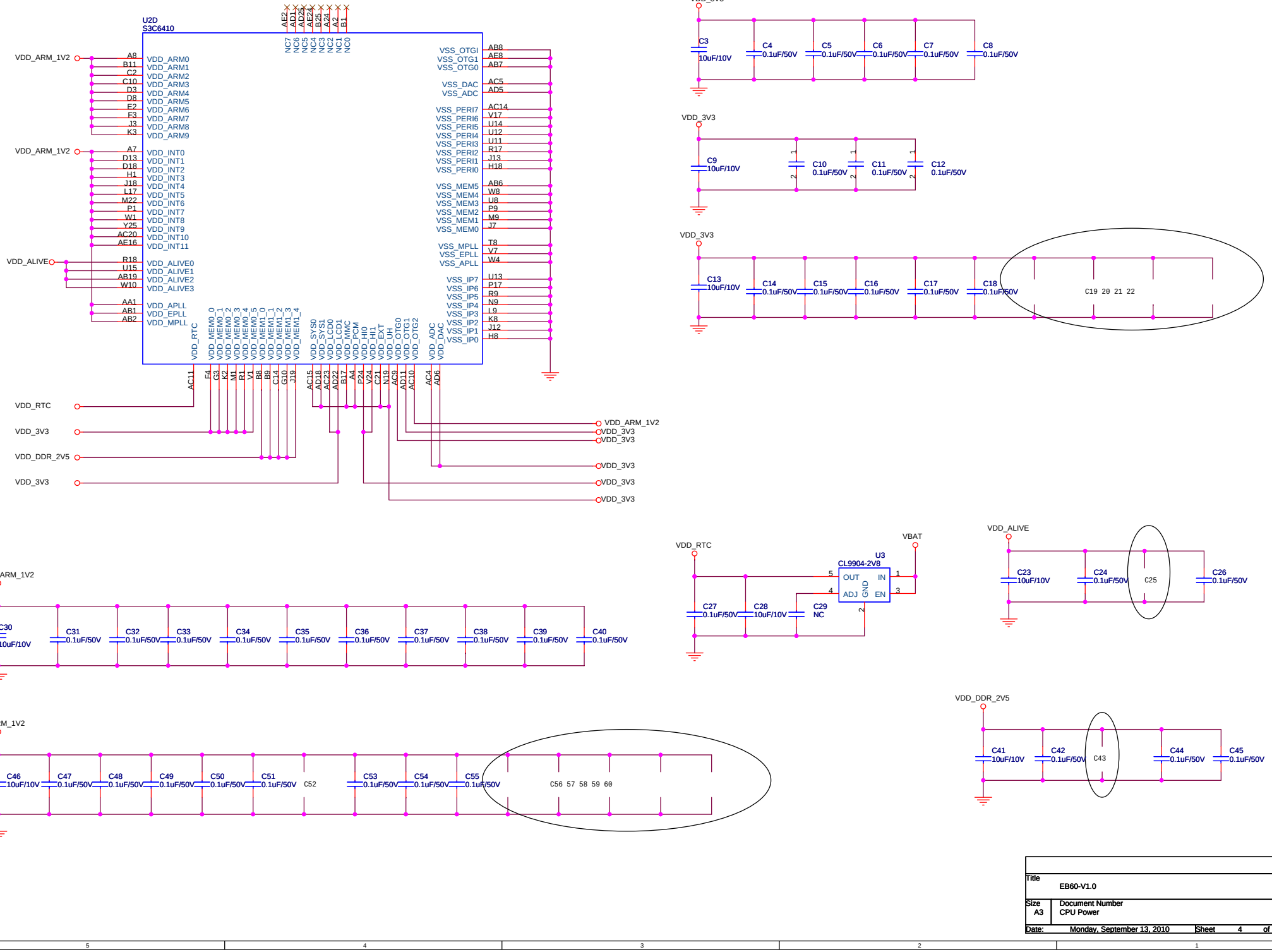


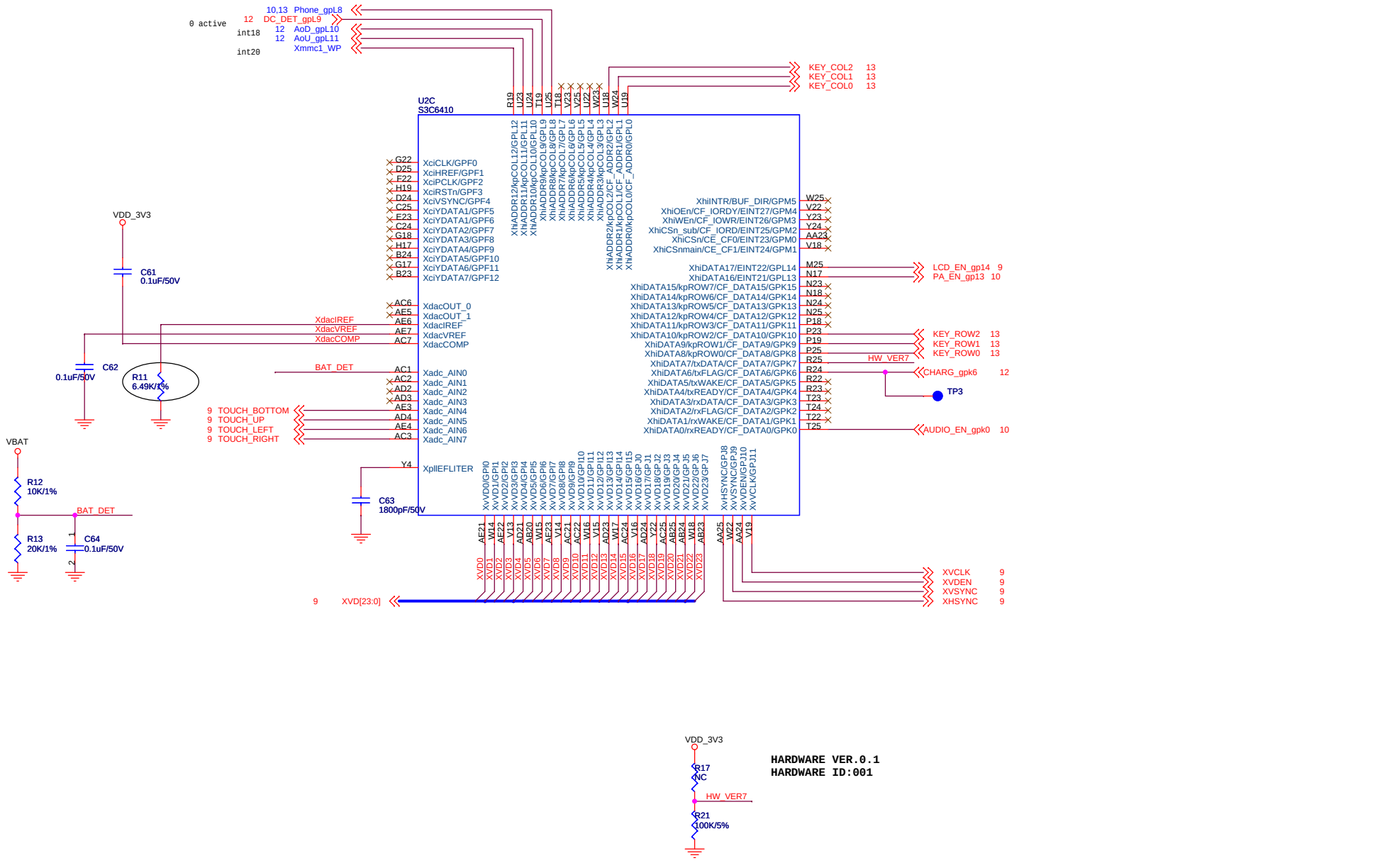
Title	EB60-V1.0		
Size A3	Document Number CPU (IO)		Rev V1.0
Date: Friday, September 10, 2010	Sheet 2 of 13		



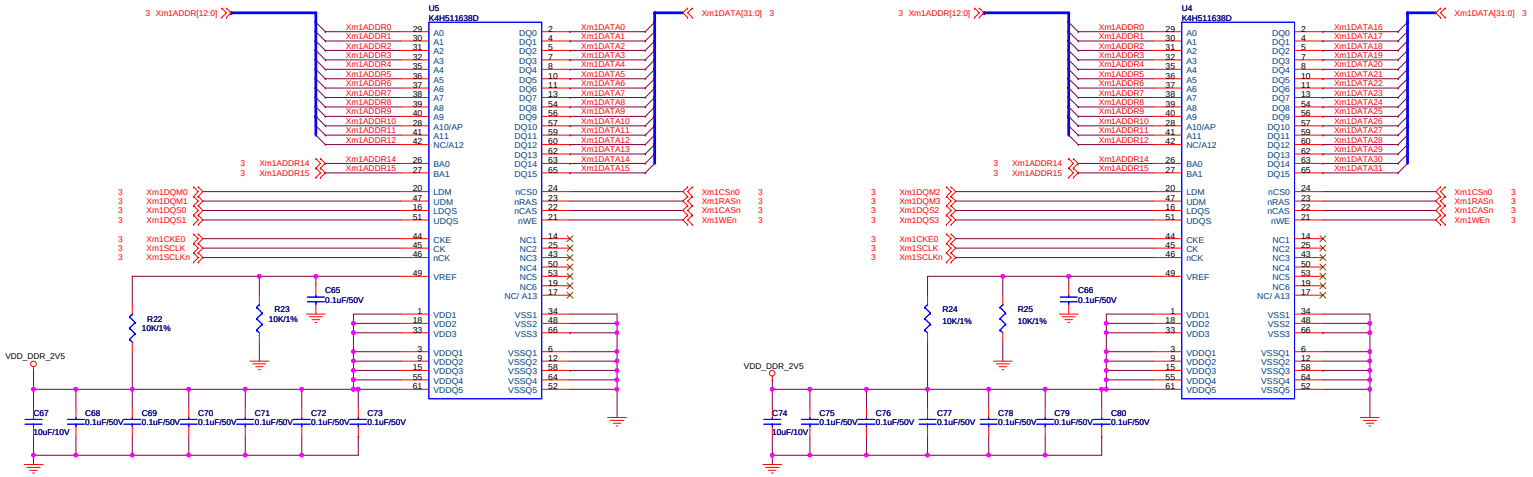
Title	EB60-V1.0		
Size A3	Document Number addr Data		Rev V1.0
Date: Saturday, June 26, 2010	Sheet 3 of 13		



Title	EB60-V1.0	
Size A3	Document Number CPU Power	Rev V1.0
Date: Monday, September 13, 2010	Sheet 4 of 13	

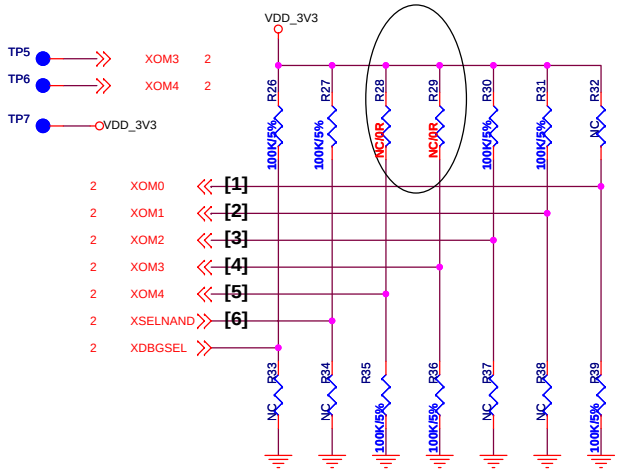


Title	EB60-V1.0	
Size A3	Document Number Function	Rev V1.0
Date: Monday, September 13, 2010	Sheet 5 of 13	

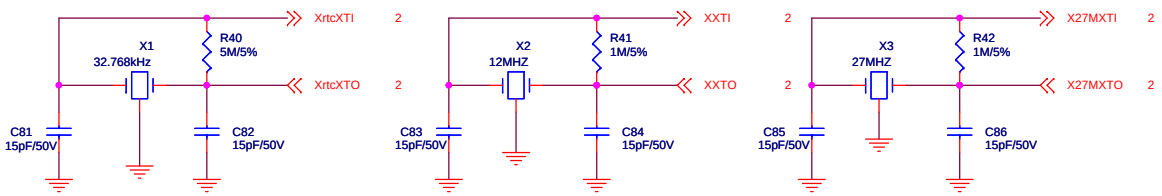


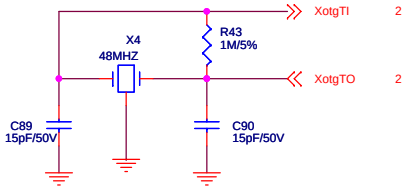
ok !

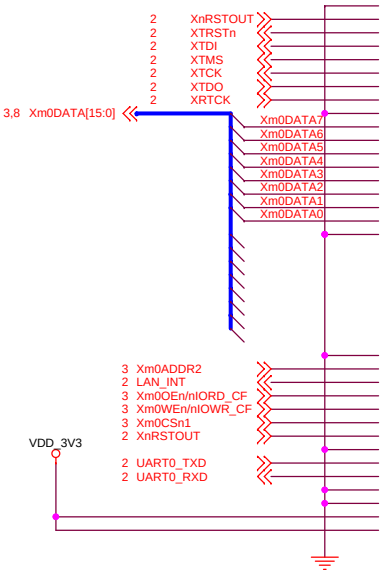
Title	EB60-V1.0	
Size A2	Document Number DDR	Rev V1.0
Date: Saturday, September 04, 2010	Sheet 6 of 13	

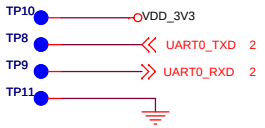


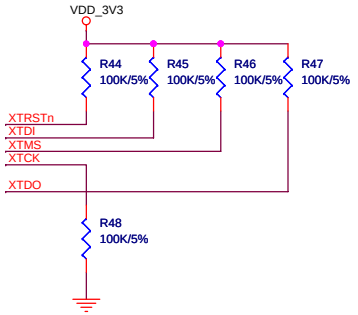
Booting Mode	[6]	[5]	[4]	[3]	[2]	[1]
NAND, 512/3 cycle	1	0	0	0	0	ON :EXTCLK
NAND, 512/4 cycle	1	0	0	0	1	
ANAND, 2K/4 cycle	1	0	0	1	0	
ANAND, 2K/5 cycle	1	0	0	1	1	
NOR/SROM	x	0	1	0	0:8bit 1:16bit	OFF :XTI
OneNAND	0	0	1	1	0	
Modem	x	0	1	1	1	
Internal ROM	x	1	1	1	1	







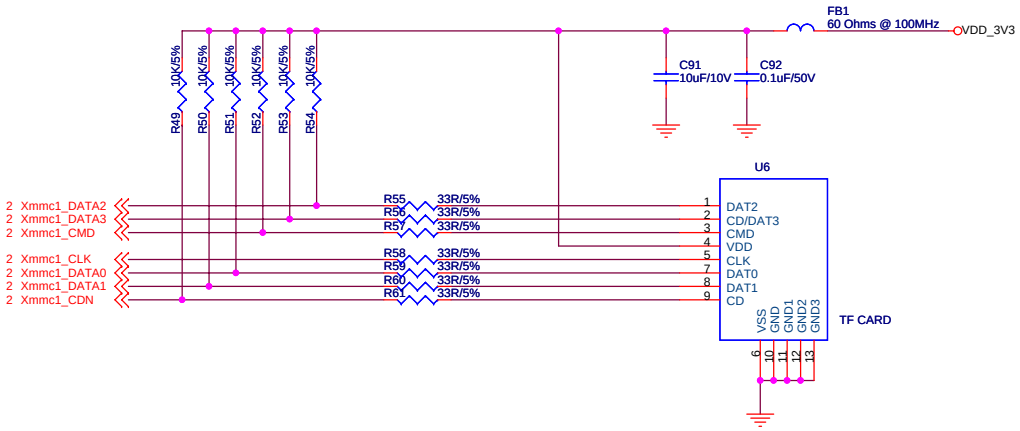


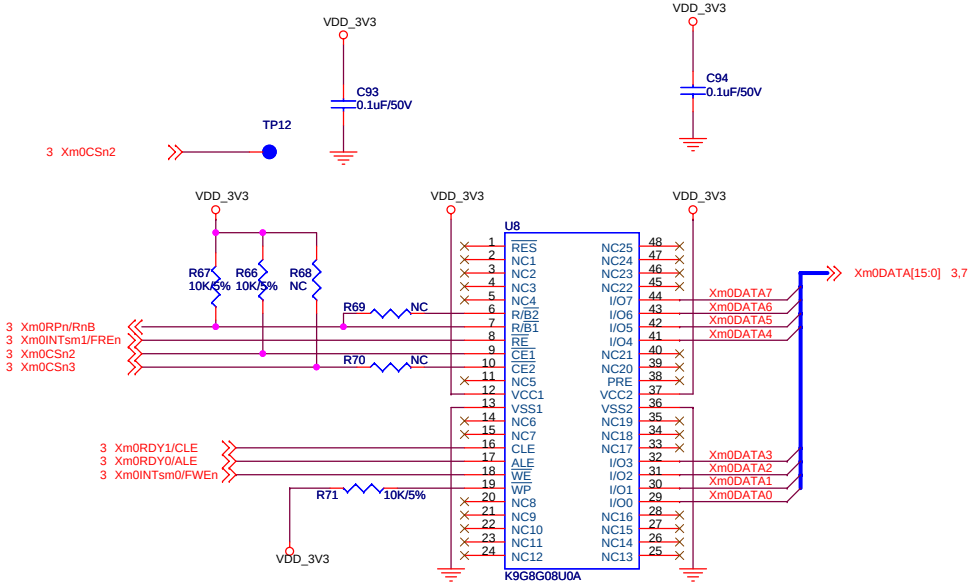


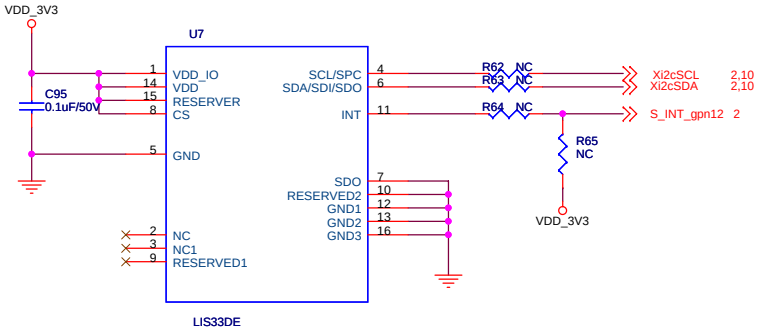
0.5mm 40Pin 下触

OK !

Title	EB60-V1.0		
Size A3	Document Number Boot clock Debug		Rev V1.0
Date: Saturday, September 04, 2010	Sheet 7 of 13		

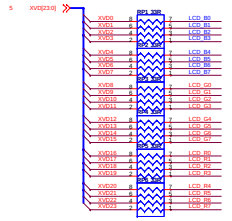


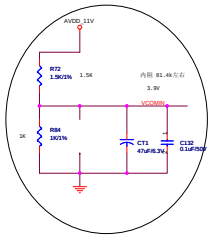


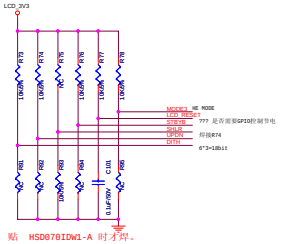


OK!

Title: EB60-V1.0		
Size: A3	Document Number: TF NAND USB	Rev: V1.0
Date: Thursday, April 29, 2010	Sheet 8 of 13	

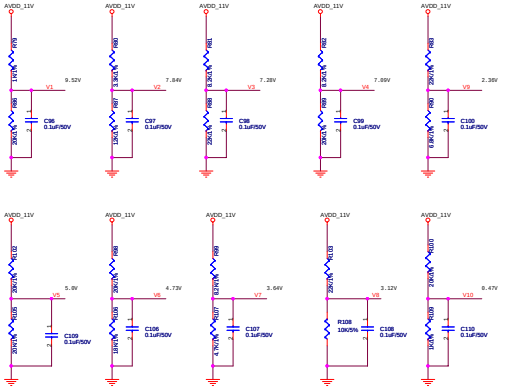






AO70VW04的VCOM电压为3.3—3.9V
HSD070IDW1-A的VCOM电压为3.9V
AT070TN82 的VCOM电压为3.9—4.3V

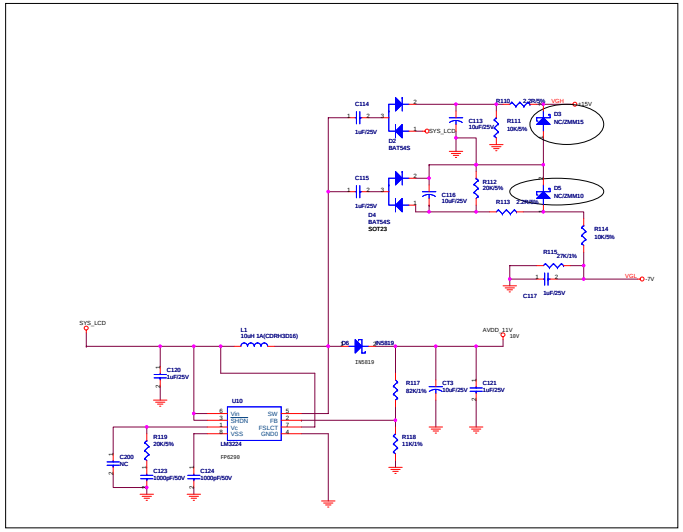
贴 HSD070IDW1-A 时才焊。

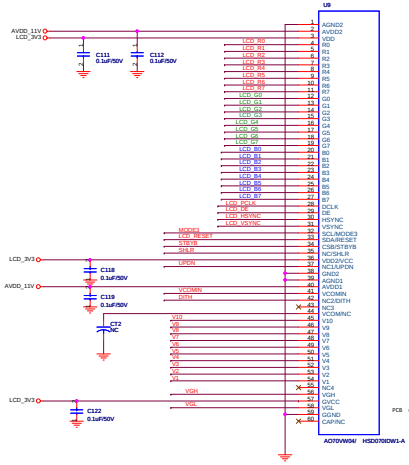


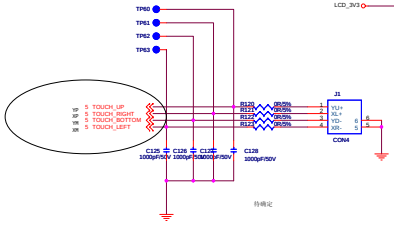
Item	Symbol	Min.	Typ.	Max.	Unit	Note
Supply Voltage	Vcc	2.7	3.0	3.5	V	
	V_{GH}	14.5	15	20	V	
	V_{GL}	-10	-7	-6.5	V	
	AV_{DD}	9.85	10	10.15	V	
VCOM	V_{COM}	-	3.9	-	V	
Input signal voltage	V_{IH}	0.7 Vcc	-	Vcc	V	Note (1)
	V_{IL}	0	-	0.3 Vcc	V	
Current of power supply	I_{DD}	-	5.426	-	mA	Vcc =3.0V
	I_{ADD}	-	24.1	-	mA	AVDD=10 V(Black)
	I_{GH}	-	0.128	-	mA	V_{GH}=15V
	I_{GL}	-	0.344	-	mA	V_{GL}= 7V

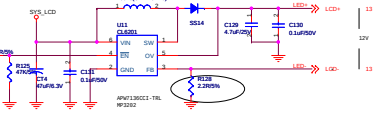
Power On Sequence: VCC-> AVDD -> VGL -> VGH -> Data -> B/L
Power Off Sequence: B/L-> Data -> VGH -> VGL -> AVDD -> VCC

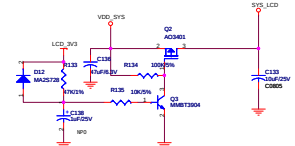
VCC	AVDD	VGL	VGH	DATA	BL
16MS	6MS	10MS	10MS	50MS	

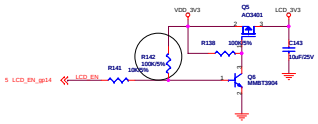






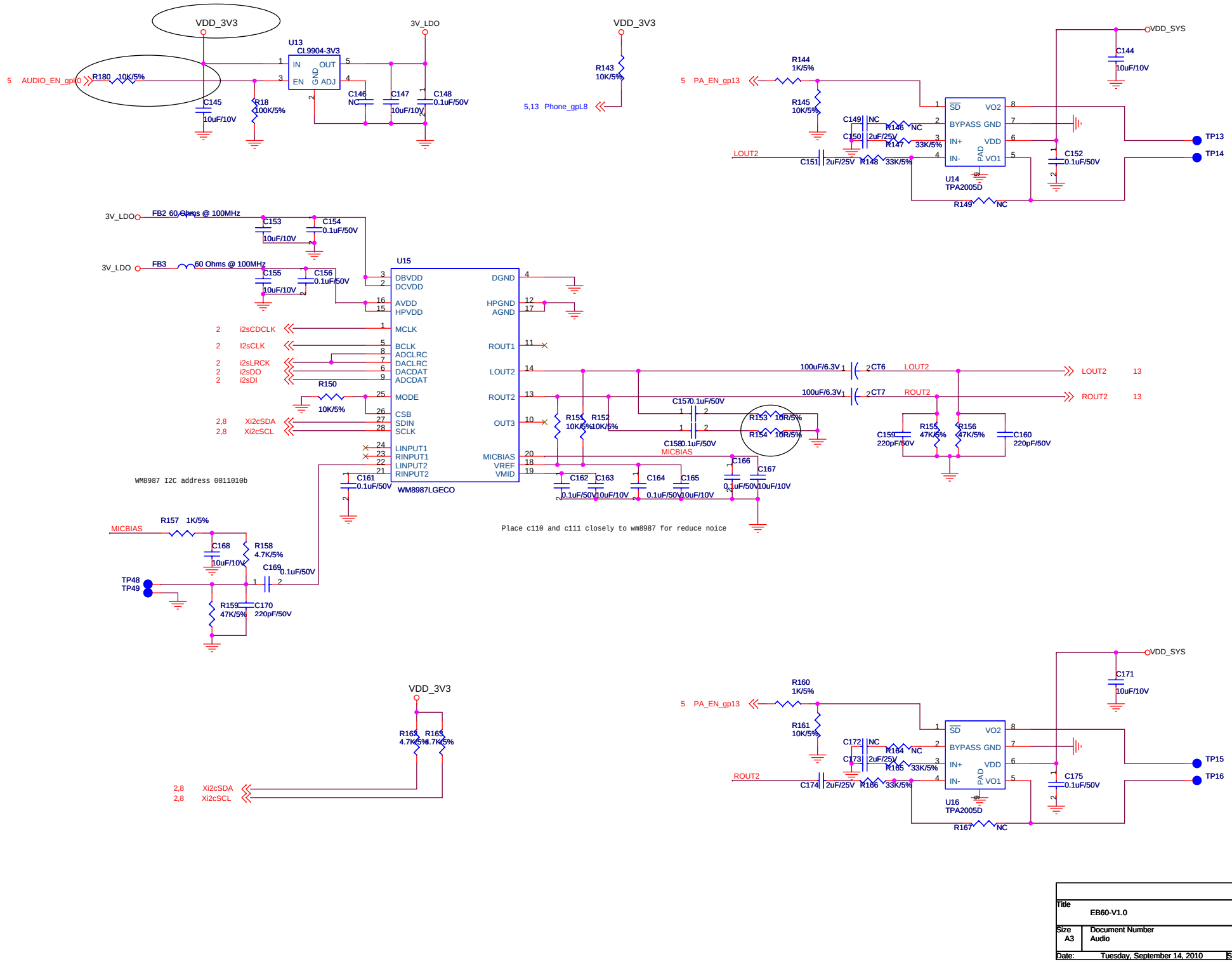






0625 ok!

Title	E860-V1.0	
Size: A1	Document Number: TFT LCD	Rev: V1.0
Date: Tuesday, September 14, 2010	Sheet 9 of 13	


VDD_3V3
U13
CL9904-3V3
IN
OUT
EN
GND
ADJ
3V_LDO
5 AUDIO_EN_gpl0
R180 10K/5%
C145 10uF/10V
R18 100K/5%
C146 NC
C147 10uF/10V
C148 0.1uF/50V
VDD_3V3
R143 10K/5%
5,13 Phone_gpL8
5 PA_EN_gp13
R144 1K/5%
R145 10K/5%
C149 NC
R146 NC
C150 2uF/25V
R147 33K/5%
LOUT2
C151 2uF/25V
R148 33K/5%
U14
TPA2005D
SD
BYPASS
IN+
IN-
VO2
GND
VDD
VO1
PAD
R149 NC
VDD_SYS
C144 10uF/10V
C152 0.1uF/50V
TP13
TP14
3V_LDO
FB2 60 Ohms @ 100MHz
C153 10uF/10V
C154 0.1uF/50V
FB3 60 Ohms @ 100MHz
C155 10uF/10V
C156 0.1uF/50V
U15
WM8987LGECO
DBVDD
DCVDD
AVDD
HPVDD
DGND
HPGND
AGND
MCLK
BCLK
ADCLRC
DACLRC
DACDAT
ADCDAT
MODE
CSB
SDIN
SCLK
LINPUT1
RINPUT1
LINPUT2
RINPUT2
ROUT1
LOUT2
ROUT2
OUT3
MICBIAS
VREF
VMID
2 i2sCDCLK
2 I2sCLK
2 i2sLRCK
2 i2sDO
2 i2sDI
R150 10K/5%
2,8 Xi2cSDA
2,8 Xi2cSCL
C161 0.1uF/50V
R151 10K/5%
R152 10K/5%
C157 0.1uF/50V
C158 0.1uF/50V
R153 10R/5%
R154 10R/5%
100uF/6.3V
CT6
CT7
LOUT2
ROUT2
13
C159 220pF/50V
R155 47K/5%
R156 47K/5%
C160 220pF/50V
C162 0.1uF/50V
C163 10uF/10V
C164 0.1uF/50V
C165 10uF/10V
C166 0.1uF/50V
C167 10uF/10V
WM8987 I2C address 0011010b
Place c110 and c111 closely to wm8987 for reduce noice
MICBIAS
R157 1K/5%
C168 10uF/10V
R158 4.7K/5%
C169 0.1uF/50V
TP48
TP49
R159 47K/5%
C170 220pF/50V
VDD_3V3
R162 4.7K/5%
R163 4.7K/5%
2,8 Xi2cSDA
2,8 Xi2cSCL
5 PA_EN_gp13
R160 1K/5%
R161 10K/5%
C172 NC
R164 NC
C173 2uF/25V
R165 33K/5%
ROUT2
C174 2uF/25V
R166 33K/5%
U16
TPA2005D
R167 NC
VDD_SYS
C171 10uF/10V
C175 0.1uF/50V
TP15
TP16
ok!
Title
EB60-V1.0
Size
A3
Document Number
Audio
Rev
V1.0
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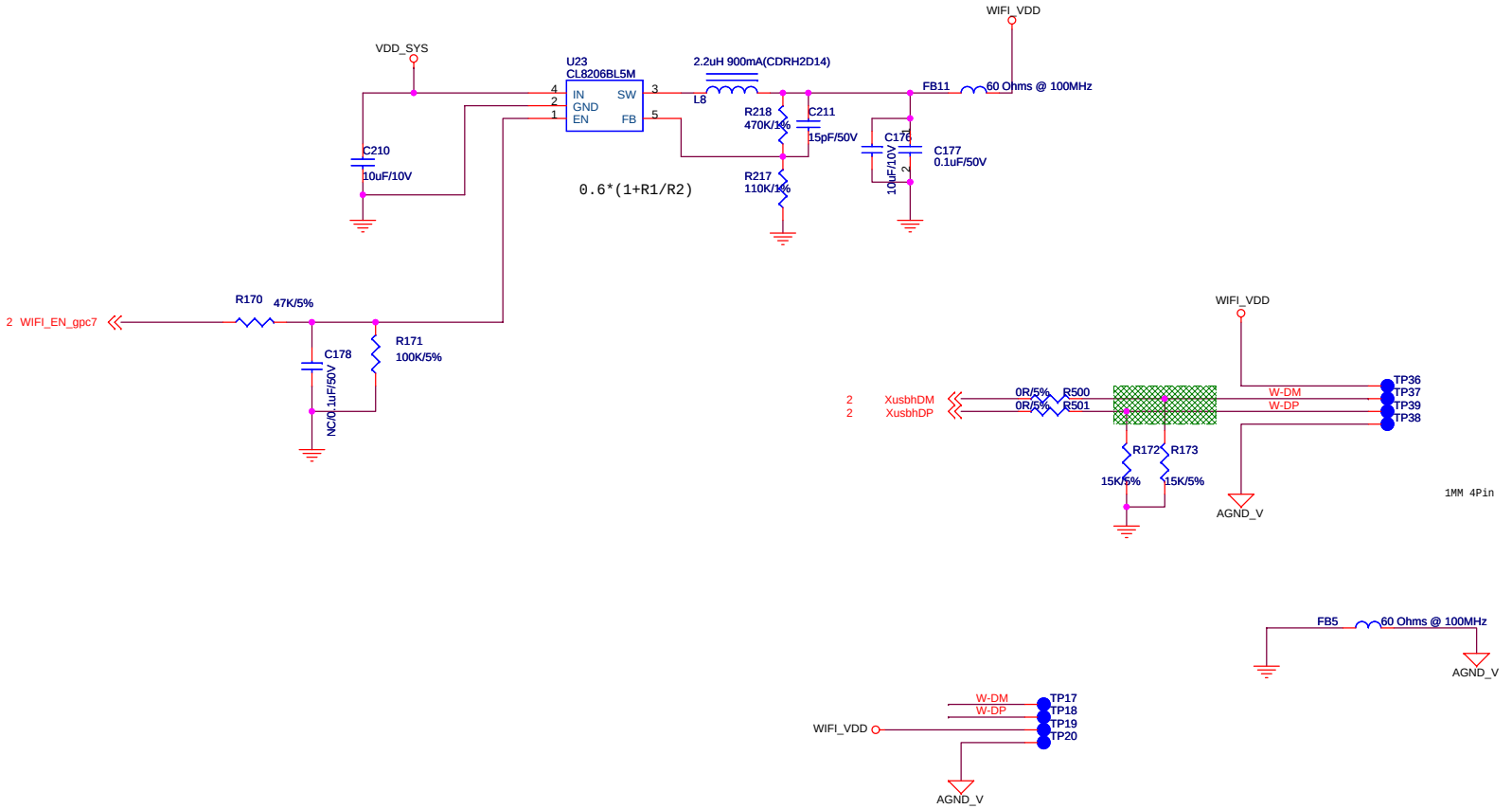
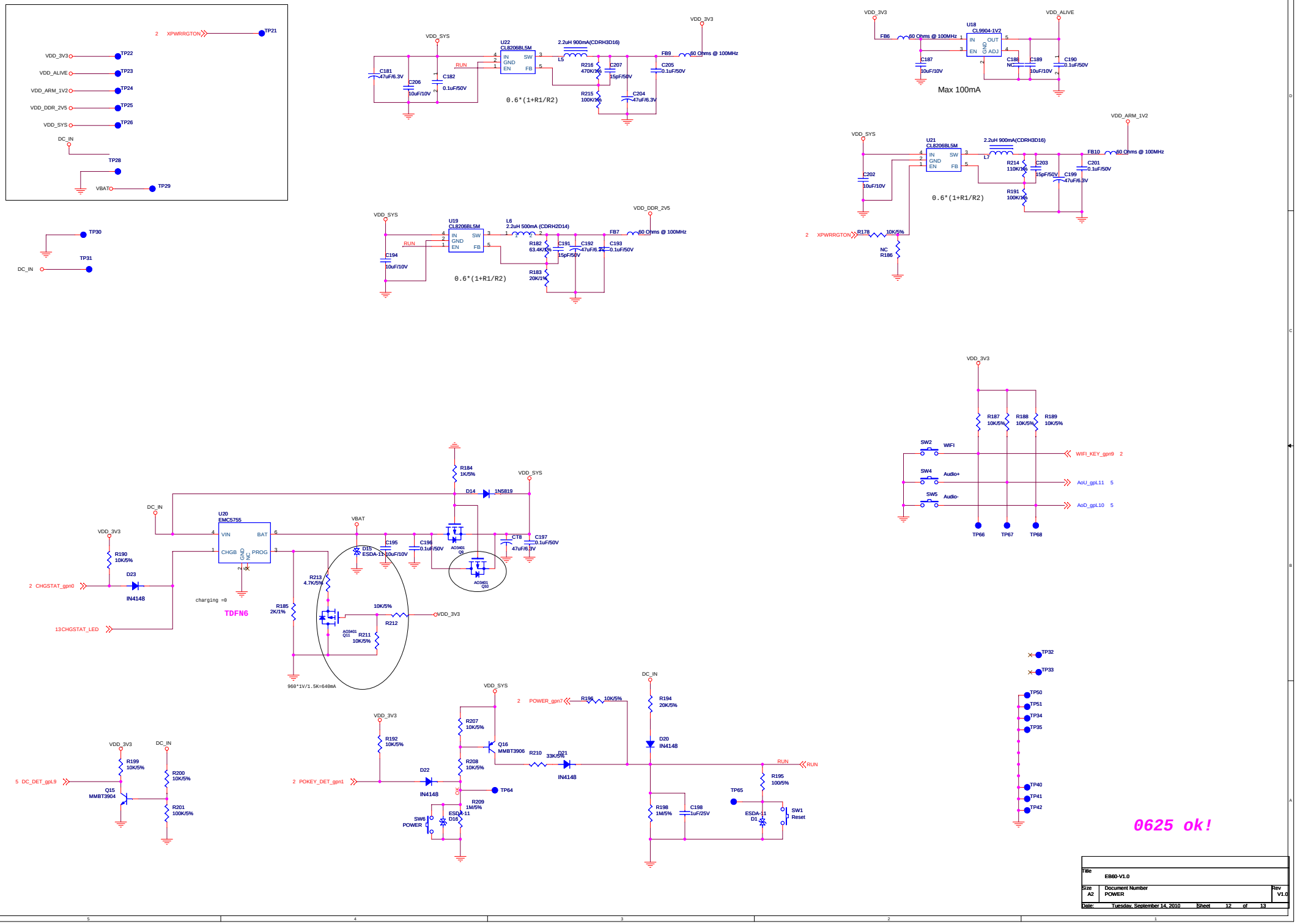
13



RF Microstrip
Z0= 50 ohm

Title	EB60-V1.0	
Size A3	Document Number WIFI	Rev V1.0
Date: Saturday, September 04, 2010	Sheet 11 of 13	


2 XPWRRGTON
TP21
VDD_3V3
TP22
VDD_ALIVE
TP23
VDD_ARM_1V2
TP24
VDD_DDR_2V5
TP25
VDD_SYS
TP26
DC_IN
TP28
VBAT
TP29
TP30
DC_IN
TP31
VDD_SYS
U22
CL8206BL5M
IN
GND
EN
SW
FB
RUN
2.2uH 900mA(CDRH3D16)
L5
C181
47uF/6.3V
C206
10uF/10V
C182
0.1uF/50V
R216
470K/1%
C207
15pF/50V
R215
100K/1%
C204
47uF/6.3V
C205
0.1uF/50V
FB9
60 Ohms @ 100MHz
VDD_3V3
0.6*(1+R1/R2)
VDD_3V3
FB6
60 Ohms @ 100MHz
U18
CL9904-1V2
IN
OUT
EN
ADJ
VDD_ALIVE
C187
10uF/10V
C186
NC
C189
10uF/10V
C190
0.1uF/50V
Max 100mA
VDD_SYS
U21
CL8206BL5M
2.2uH 900mA(CDRH3D16)
L7
FB10
60 Ohms @ 100MHz
VDD_ARM_1V2
C202
10uF/10V
R214
110K/1%
C203
15pF/50V
C199
47uF/6.3V
C201
0.1uF/50V
R191
100K/1%
0.6*(1+R1/R2)
2 XPWRRGTON
R178
10K/5%
NC
R186
VDD_SYS
U19
CL8206BL5M
L6
2.2uH 500mA (CDRH2D14)
FB7
60 Ohms @ 100MHz
VDD_DDR_2V5
RUN
C194
10uF/10V
R182
63.4K/1%
C191
15pF/50V
C192
47uF/6.3V
C193
0.1uF/50V
R183
20K/1%
0.6*(1+R1/R2)
VDD_3V3
R187
10K/5%
R188
10K/5%
R189
10K/5%
SW2
WIFI
WIFI_KEY_gpn9 2
SW4
Audio+
AoU_gpL11 5
SW5
Audio-
AoD_gpL10 5
TP66
TP67
TP68
DC_IN
VDD_3V3
R190
10K/5%
2 CHGSTAT_gpn0
D23
IN4148
13 CHGSTAT_LED
U20
EMC5755
VIN
BAT
CHGB
GND
PROG
NC
charging =0
TDFN6
R184
1K/5%
D14
1N5819
VDD_SYS
VBAT
D15
ESDA-11-1
C195
10uF/10V
C196
0.1uF/50V
AO3401
Q9
C78
47uF/6.3V
C197
0.1uF/50V
R213
4.7K/5%
R185
2K/1%
AO3401
Q11
R211
10K/5%
10K/5%
R212
VDD_3V3
960*1V/1.5K=640mA
TP32
TP33
TP50
TP51
TP34
TP35
TP40
TP41
TP42
VDD_3V3
DC_IN
5 DC_DET_gpL9
R199
10K/5%
Q15
MMBT3904
R200
10K/5%
R201
100K/5%
VDD_3V3
R192
10K/5%
2 POKEY_DET_gpn1
D22
IN4148
TP64
R209
1M/5%
SW6
POWER
ESDA-11
D16
VDD_SYS
R207
10K/5%
R208
10K/5%
Q16
MMBT3906
2 POWER_gpn7
R196
10K/5%
R210
33K/5%
D21
IN4148
DC_IN
R194
20K/5%
D20
IN4148
RUN
RUN
R195
100/5%
R198
1M/5%
C198
1uF/25V
TP65
ESDA-11
D1
SW1
Reset
0625 ok!
Title
EB60-V1.0
Size
A2
Document Number
POWER
Rev
V1.0
Date:
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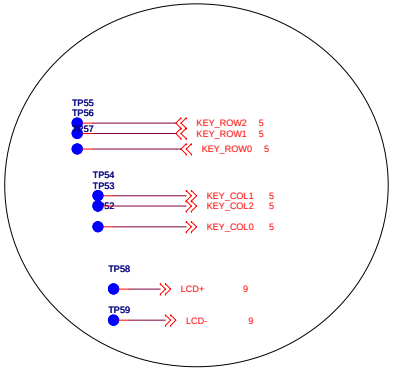
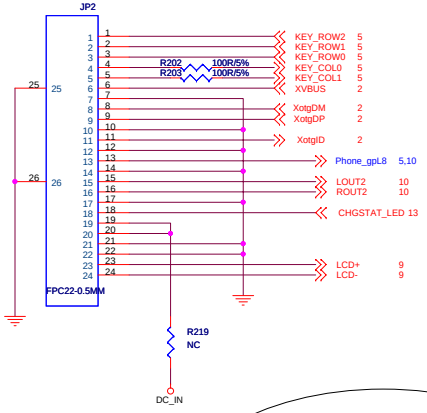
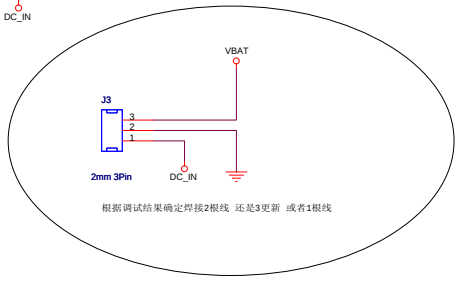
LEFT

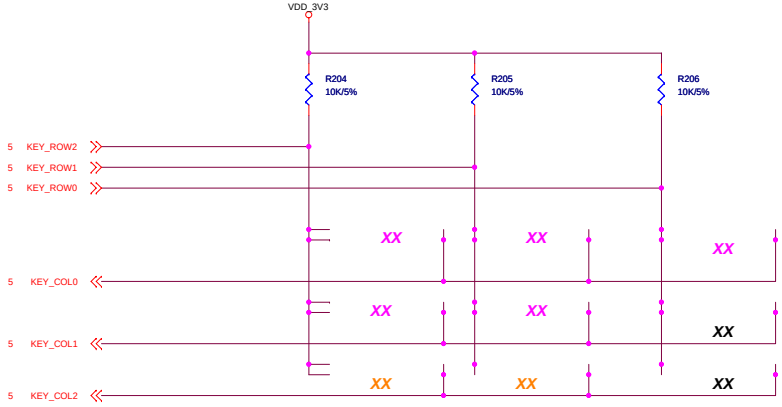
FCON3-2MM

RIGHT









Title: EB60-V1.0		
Size: C	Document Number: KEY Connect	Rev: V1
Date: Monday, September 13, 2010	Sheet 13 of 13	