

+1.8V

CPU_VDD

wifi - 230 mA

CPU - 1500 mA pk

DDR - 440 mA pk; 320 mA avg

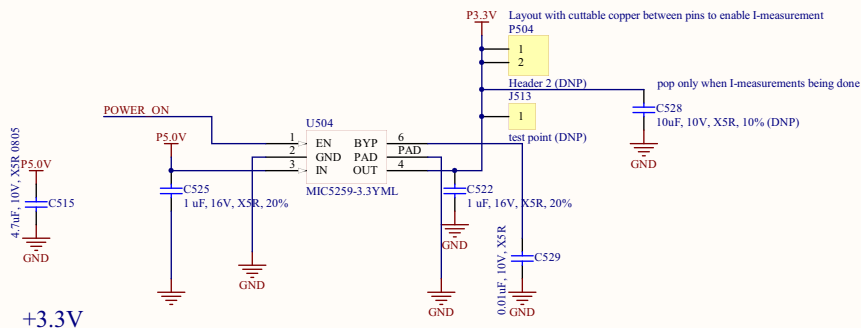
CPU - 550 mA pk

Power on sequence shall be as follows:

3.3V comes up first. AVDD PLL and AVDD for codec is also turned on now.

Then CPU VDD (P1.0V) and P1.8V comes up next, ideally simultaneously. Camera is also powered on at this time.

Then USB 5.0V rail is powered up.



+3.3V

wifi - 200 mA

CPU - 100 mA pk

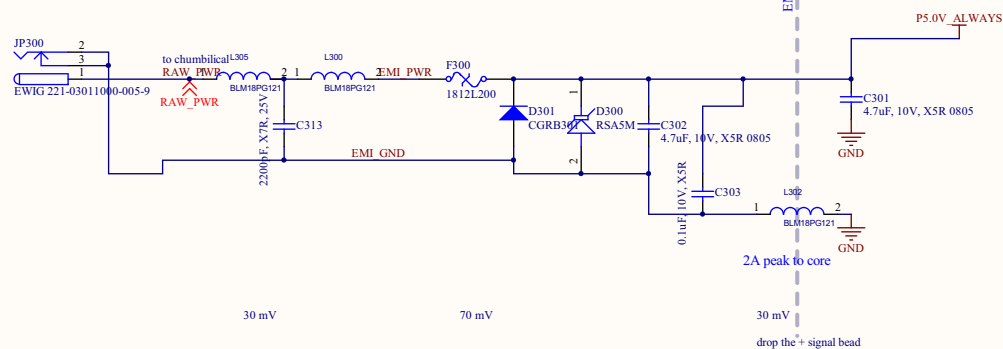
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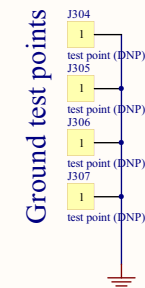
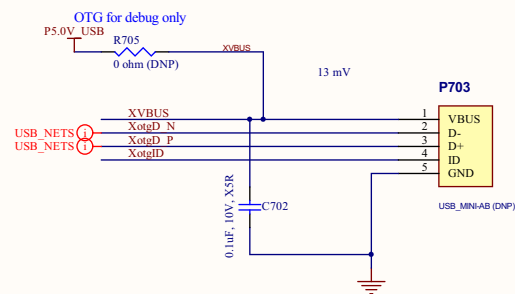
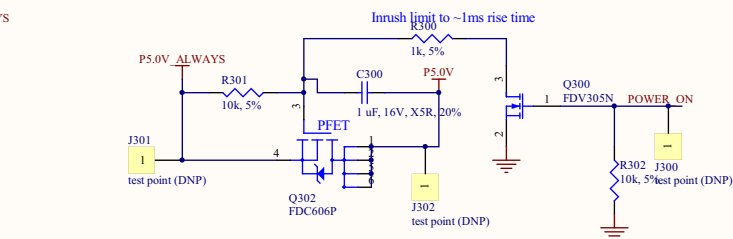
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Silvermoon OEM ref v3

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200 ohm @ 100 MHz, 1-turn ferrite core required on cable for EMC
 5V @ 2.5A peak +/- 5% from AC



Main supply switch

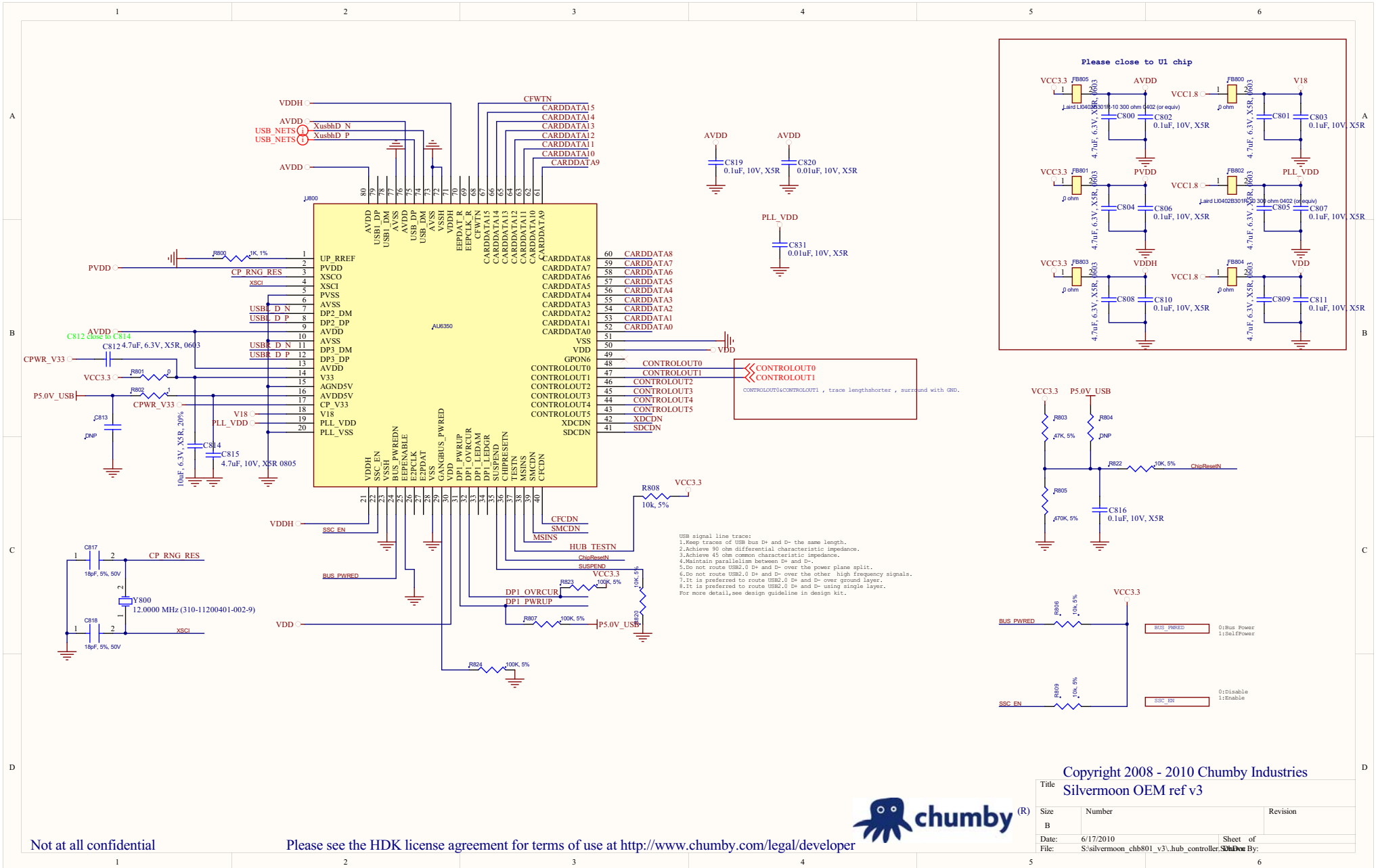


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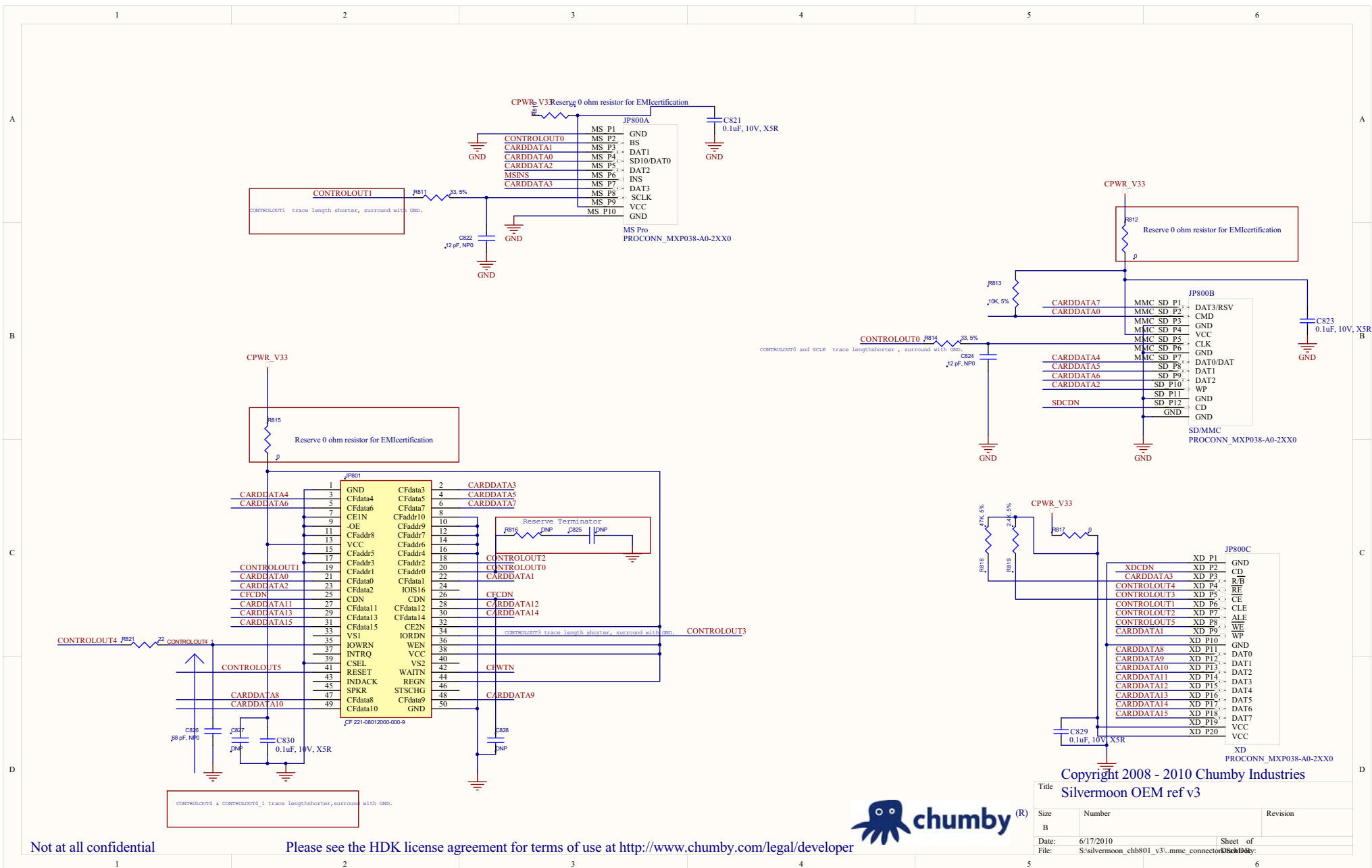


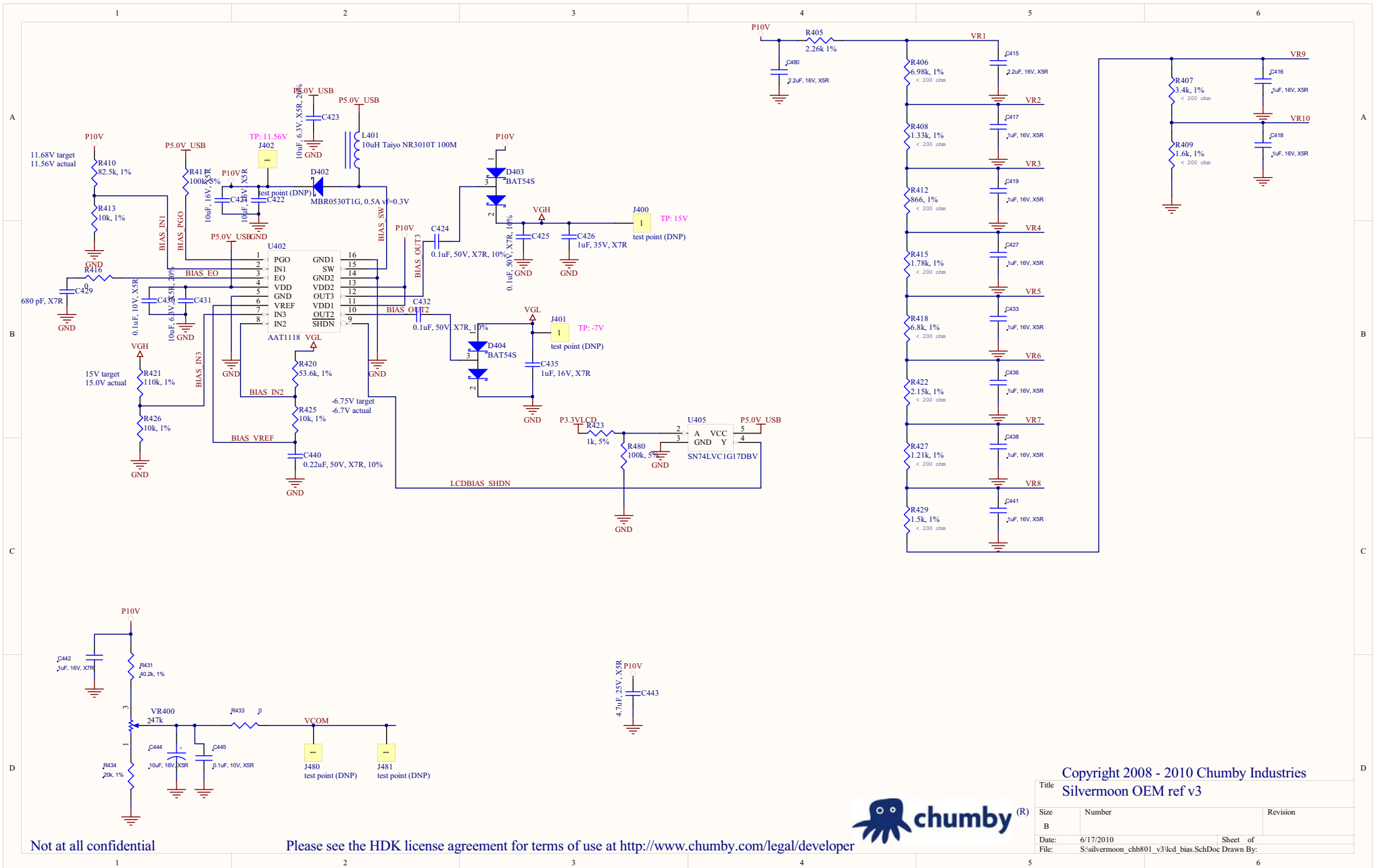
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