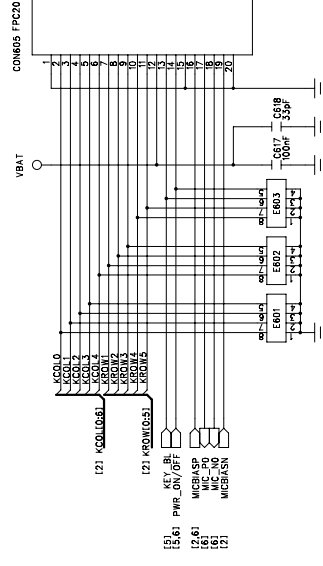
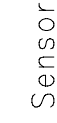
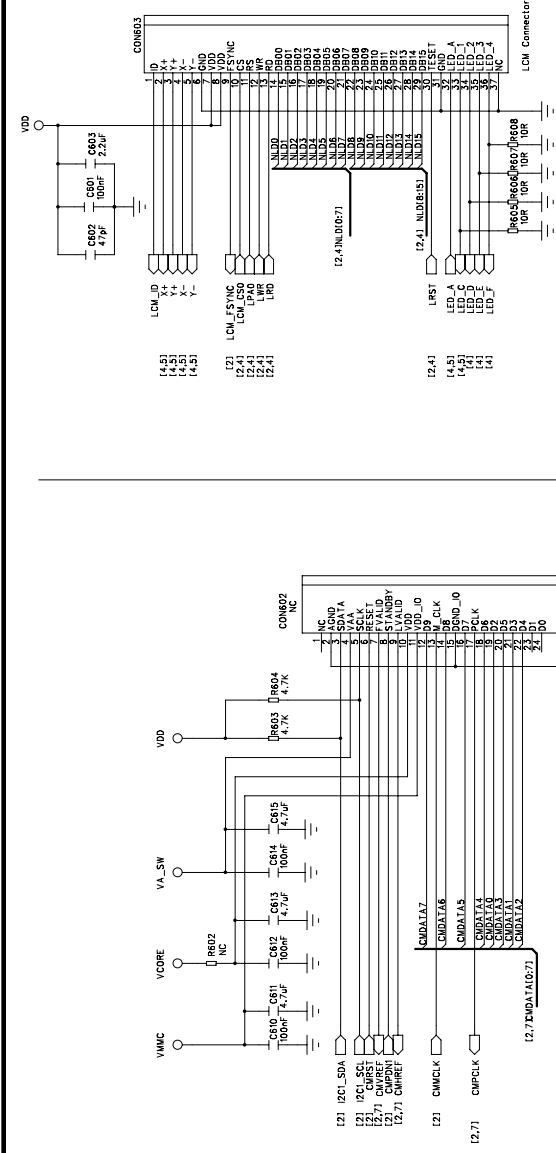
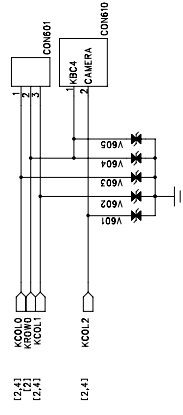
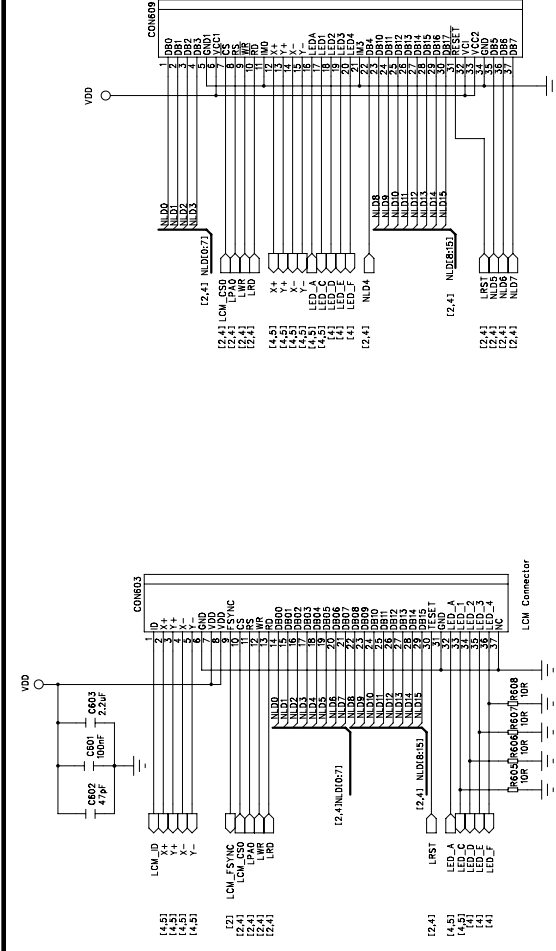
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KEY CONNECTOR

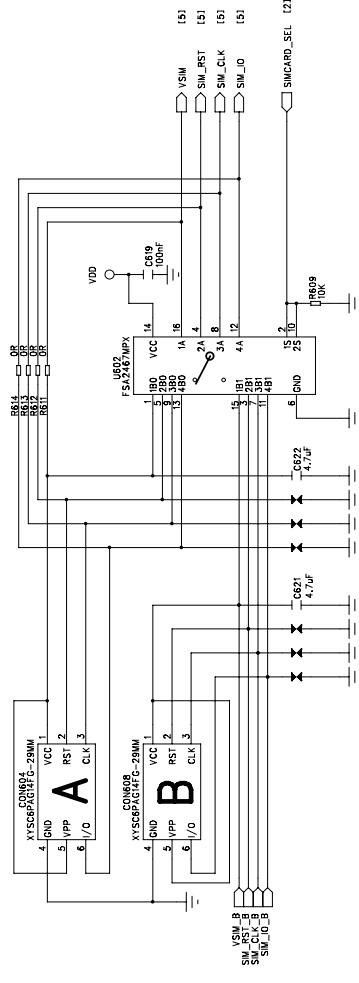


SideKey



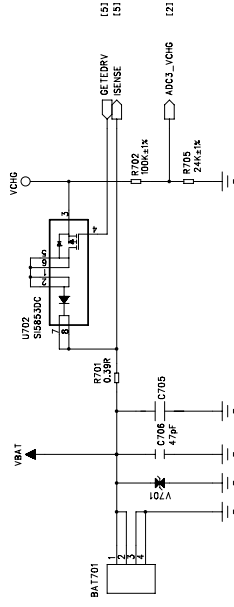
2.8(3.0)WQVGA

2.8QVGA

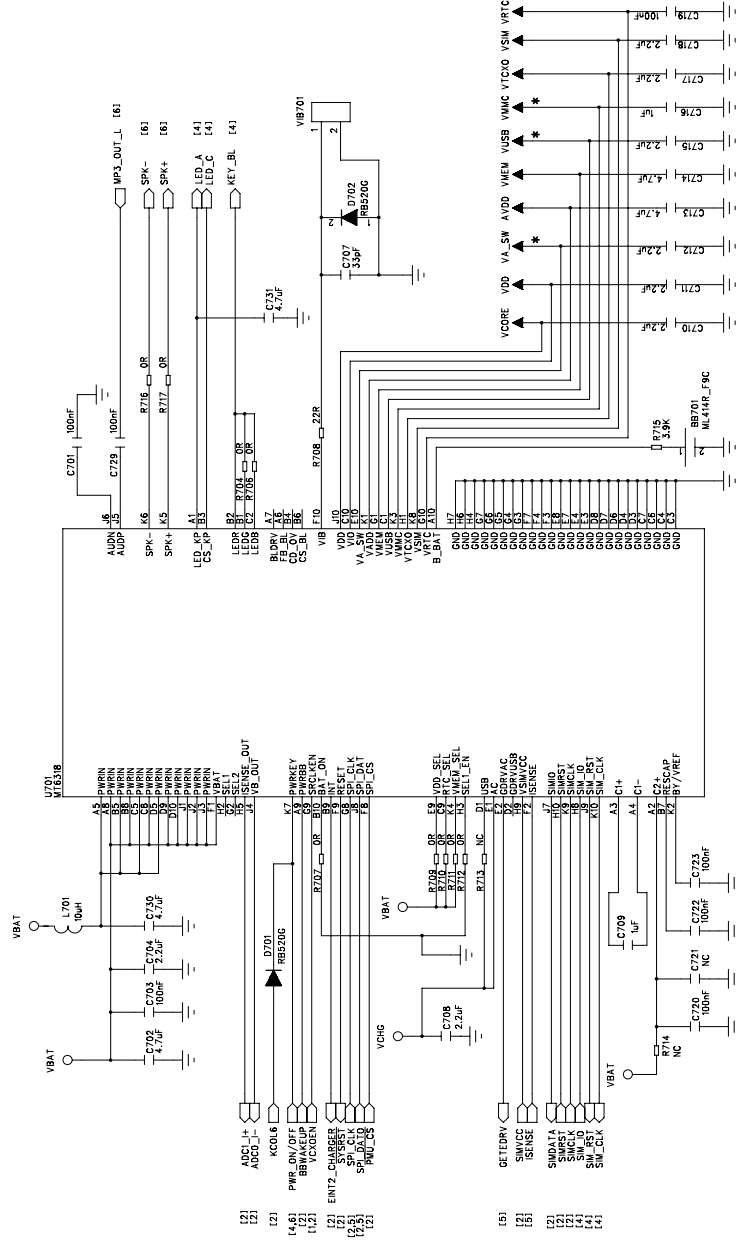


SIM Connector

			DRAWN	ZHANG QS	DATE	2008-01-20	MODEL:	E880	SHEET 4 OF 8
			CHECKED		TIME		TITLE:	CONNECT	V2.0

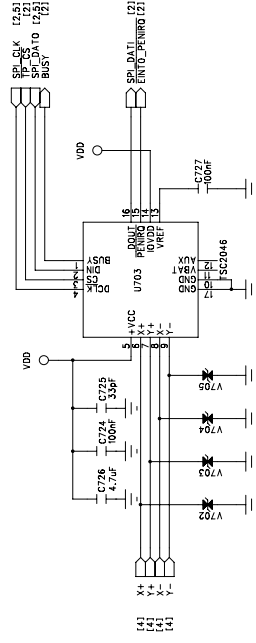


Battery Charge



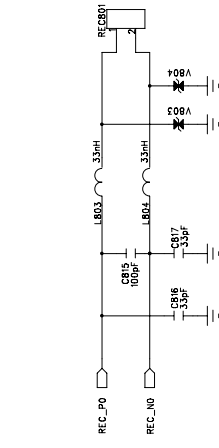
Power Management

Regulator	Voltage		Current
Vcore	VDD_SEL=0	1.2V / 0.9V	200mA
	VDD_SEL=1	1.8V / 1.5V	200mA
VMEM	VMEM_SEL=0	1.8V	150mA
	VMEM_SEL=1	2.8V	150mA
VRTC	RTC_SEL=0	1.2V	0.6mA
	RTC_SEL=1	1.5V	0.6mA
VDD	2.8V		100mA
AVDD	2.8V		150mA
VA_SW	2.8V / 3.0V		50mA
VUSB	3.3V		20mA
VMMC	2.8V / 3.3V		250mA
VTCXO	2.8V		20mA
VSIM	1.8V / 3.0V		20mA

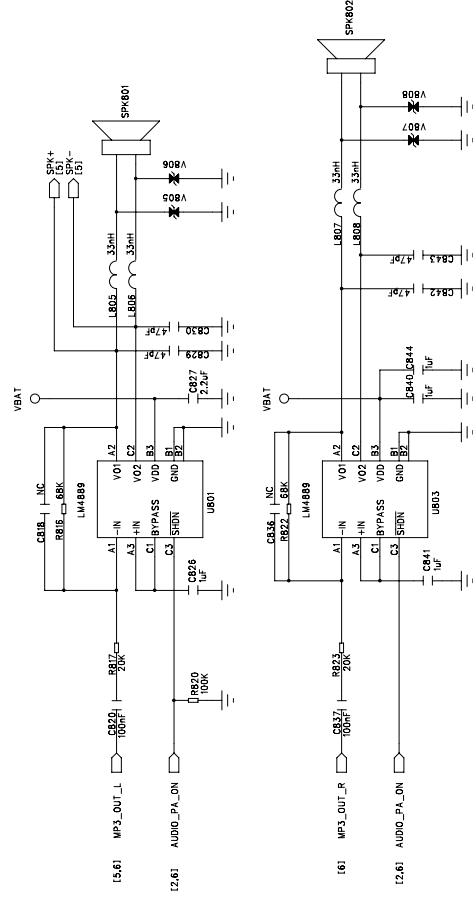


Touch Panel

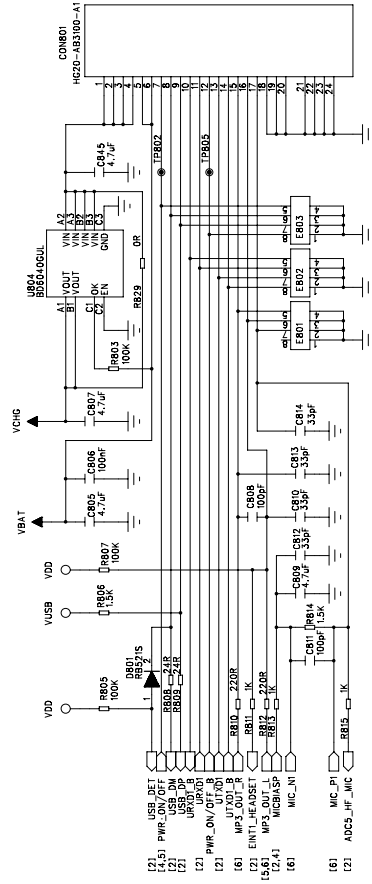
				DRAWN	ZHANG QS	DATE	2008-01-20	MODEL:	E880	SHEET 5 OF 8
				CHECKED		TIME		TITLE:	MT6318	VERSION V2.0



REC

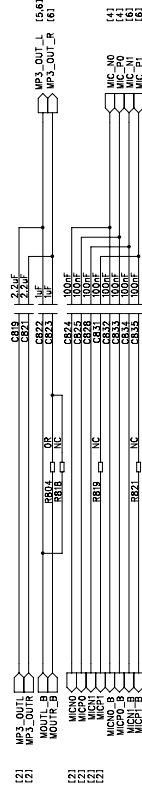


SPK



☐ F1801
☐ F1802
☐ F1803
☐ F1804

Headset circuit



Audio Analog Switch

				DRAWN	ZHANG QS	DATE	2008-01-20	MODEL:	E880	SHEET 6 OF 8
				CHECKED		TIME		TITLE:	AUDIO	V2.0

