PartType	Value	RefDes	PartDecal	Pins	Layer	Orient.	X	Y	SMD	Glued
CAP_JENNIC	1,8pF	C1	0402C_JENNIC	2	Top	0	7.063	12.515	Yes	No
CAP_JENNIC	10nF	C2	0402C_JENNIC	2	Top	270	12.23	11.911	Yes	No
CAP_JENNIC	100nF	C3	0402C_JENNIC	2	Top	180	9.209	14.426	Yes	No
CAP_JENNIC	1pF	C4	0402C_JENNIC	2	Top	180	5.603	14.518	Yes	No
CAP_JENNIC	NC	C6	0402C_JENNIC	2	Top	90	2.067	7.685	Yes	No
CAP_JENNIC	100nF	C7	0402C_JENNIC	2	Top	180	8.372	2.734	Yes	No
CAP_JENNIC	12pF	C10	0402C_JENNIC	2	Top	0	13.267	5.012	Yes	No
CAP_JENNIC	12pF	C11	0402C_JENNIC	2	Top	180	13.452	10.125	Yes	No
CAP_JENNIC	47pF	C12	0402C_JENNIC	2	Top	270	9.77	12.844	Yes	No
CAP_JENNIC	10uF	C13	0805C_JENNIC	2	Top	0	12.662	2.955	Yes	No
CAP_JENNIC	100nF	C14	0402C_JENNIC	2	Top	90	11.275	11.633	Yes	No
CAP_JENNIC	100nF	C15	0402C_JENNIC	2	Top	270	13.278	11.64	Yes	No
CAP_JENNIC	100nF	C16	0402C_JENNIC	2	Top	0	4.451	2.938	Yes	No
EDGE_CON_JENNIC	<NONE>	EC1	PAD_1.0_0.7_JENNIC	1	Top	0	0	12.7	No	No
EDGE_CON_JENNIC	<NONE>	EC2	PAD_1.0_0.7_JENNIC	1	Top	0	0	11.43	No	No
EDGE_CON_JENNIC	<NONE>	EC3	PAD_1.0_0.7_JENNIC	1	Top	0	0	10.16	No	No
EDGE_CON_JENNIC	<NONE>	EC4	PAD_1.0_0.7_JENNIC	1	Top	0	0	8.89	No	No
EDGE_CON_JENNIC	<NONE>	EC5	PAD_1.0_0.7_JENNIC	1	Top	0	0	7.62	No	No
EDGE_CON_JENNIC	<NONE>	EC6	PAD_1.0_0.7_JENNIC	1	Top	0	0	6.35	No	No
EDGE_CON_JENNIC	<NONE>	EC7	PAD_1.0_0.7_JENNIC	1	Top	0	0	5.08	No	No
EDGE_CON_JENNIC	<NONE>	EC8	PAD_1.0_0.7_JENNIC	1	Top	0	0	3.81	No	No
EDGE_CON_JENNIC	<NONE>	EC9	PAD_1.0_0.7_JENNIC	1	Top	0	0	2.54	No	No
EDGE_CON_JENNIC	<NONE>	EC10	PAD_1.0_0.7_JENNIC	1	Top	90	2.54	0	No	No
EDGE_CON_JENNIC	<NONE>	EC11	PAD_1.0_0.7_JENNIC	1	Top	90	3.81	0	No	No
EDGE_CON_JENNIC	<NONE>	EC12	PAD_1.0_0.7_JENNIC	1	Top	90	5.08	0	No	No
EDGE_CON_JENNIC	<NONE>	EC13	PAD_1.0_0.7_JENNIC	1	Top	90	6.35	0	No	No
EDGE_CON_JENNIC	<NONE>	EC14	PAD_1.0_0.7_JENNIC	1	Top	90	7.62	0	No	No
EDGE_CON_JENNIC	<NONE>	EC15	PAD_1.0_0.7_JENNIC	1	Top	90	8.89	0	No	No
EDGE_CON_JENNIC	<NONE>	EC16	PAD_1.0_0.7_JENNIC	1	Top	90	10.16	0	No	No
EDGE_CON_JENNIC	<NONE>	EC17	PAD_1.0_0.7_JENNIC	1	Top	90	11.43	0	No	No
EDGE_CON_JENNIC	<NONE>	EC18	PAD_1.0_0.7_JENNIC	1	Top	90	12.7	0	No	No
EDGE_CON_JENNIC	<NONE>	EC19	PAD_1.0_0.7_JENNIC	1	Top	0	16	2.54	No	No
EDGE_CON_JENNIC	<NONE>	EC20	PAD_1.0_0.7_JENNIC	1	Top	180	16	3.81	No	No
EDGE_CON_JENNIC	<NONE>	EC21	PAD_1.0_0.7_JENNIC	1	Top	0	16	5.08	No	No
EDGE_CON_JENNIC	<NONE>	EC22	PAD_1.0_0.7_JENNIC	1	Top	0	16	6.35	No	No
EDGE_CON_JENNIC	<NONE>	EC23	PAD_1.0_0.7_JENNIC	1	Top	0	16	7.62	No	No
EDGE_CON_JENNIC	<NONE>	EC24	PAD_1.0_0.7_JENNIC	1	Top	180	16	8.89	No	No
EDGE_CON_JENNIC	<NONE>	EC25	PAD_1.0_0.7_JENNIC	1	Top	0	16	10.16	No	No
EDGE_CON_JENNIC	<NONE>	EC26	PAD_1.0_0.7_JENNIC	1	Top	0	16	11.43	No	No
EDGE_CON_JENNIC	<NONE>	EC27	PAD_1.0_0.7_JENNIC	1	Top	0	16	12.7	No	No
PCB_ANTENNA_APPS	<NONE>	J1	PCB_ANT_APPS	2	Top	0	2.657	24.018	Yes	No
SCREEN_LUG2_JENNIC	<NONE>	J2	SCREEN_LUG3_JENNIC	2	Top	90	14.579	1.456	Yes	No
SCREEN_LUG2_JENNIC	<NONE>	J3	SCREEN_LUG3_JENNIC	2	Top	180	14.579	14.939	Yes	No
SCREEN_LUG2_JENNIC	<NONE>	J4	SCREEN_LUG3_JENNIC	2	Top	270	1.456	14.939	Yes	No
SCREEN_LUG2_JENNIC	<NONE>	J5	SCREEN_LUG3_JENNIC	2	Top	0	1.456	1.456	Yes	No
IND	4n3H	L1	0402C	2	Top	0	5.618	13.42	Yes	No
IND	3nH	L2	0402C	2	Top	90	8.619	12.781	Yes	No
RES_JENNIC	43k	R1	0402C_JENNIC	2	Top	180	11.826	13.709	Yes	No
JN516X_QFN40_JENNIC	JN516X	U1	QFN40_JENNIC	41	Top	180	6.832	7.683	Yes	No
TSX_JENNIC	32MHz	Y1	TSX-3225_JENNIC	4	Top	90	12.873	7.53	Yes	No