

Intel CedarView-D/M Processor + NM10

DESIGN Name : IC-10

Revision : C

Date : 02/05/2013

Specification

Intel CedarView+NM10

Two DDR3 SO-DIMMs

I/O Chips ITE 8712

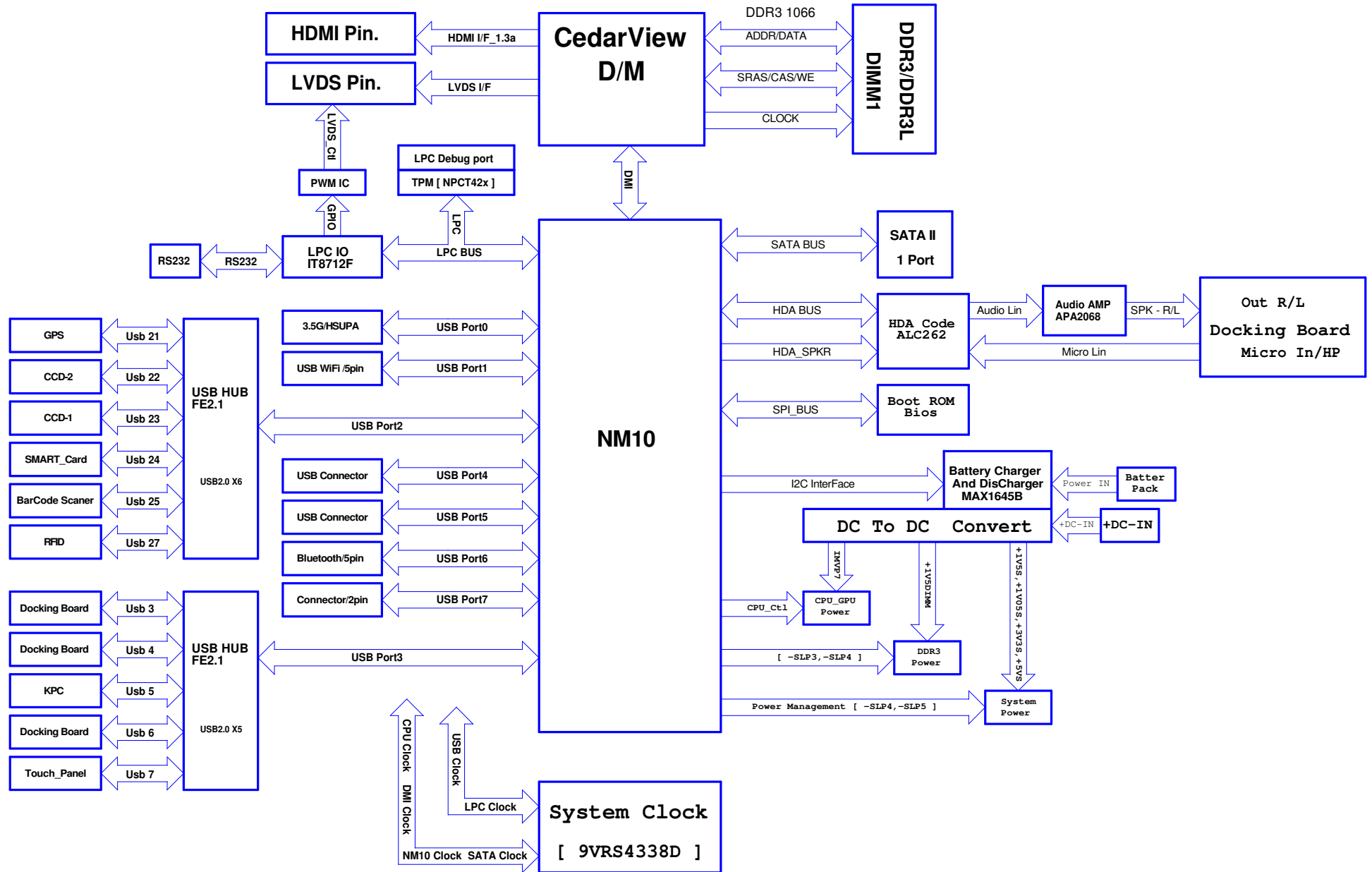
Two Serial Ports Support RS232

LVDS Port x 1

| PAGE | TITLE |
|------|----------------------------|
| 01 | Cover Sheet |
| 02 | System Block Diagram |
| 03 | Power_On Sequencing |
| 04 | IMVP7_CPU_GPU-Power |
| 05 | Clock_Generation |
| 06 | CedarView_HOST_LVDS_HDMI |
| 07 | CedarView_DDR3_SO_DIMM-A |
| 08 | CedarView_Power_Gnd |
| 09 | CedarView_NM10_DMI_USB_I/F |
| 10 | NM10_SATA-I/F_And- [KPC] |
| 11 | NM10_GPIO |
| 12 | NM10_Power_Gnd-And [+1V8S] |
| 13 | SATA_HDD,USBX2,m-PCIE |
| 14 | Touch_Panel-[HDMI_Chips] |
| 15 | LPC_IO[ITE8712]-And_FAN |
| 16 | USB_HUBx2 And IO_CONNs |
| 17 | Audio_Codec And - TPM |
| 18 | Power_ON- [+5VA,+3V3A] |
| 19 | Power_ [+5VS,+3V3S] |
| 20 | Power_ [+1V5DIMM,+0V75] |
| 21 | Power_ [+1V5S,+1V05S] |
| 22 | DUAL_Battery_Charger |

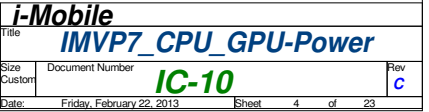
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| 23 | History_[01] |
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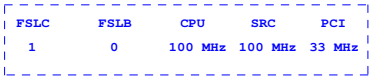
BLOCK DIAGRAM

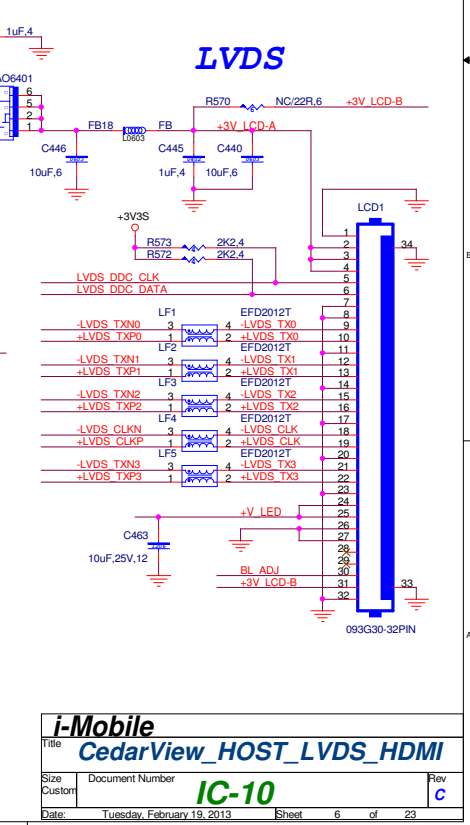
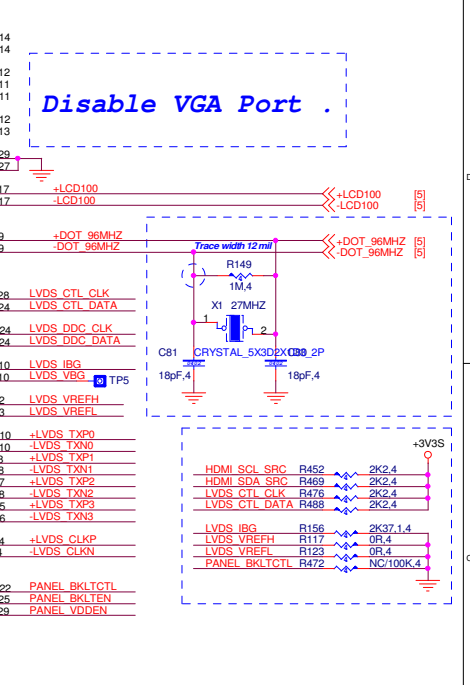
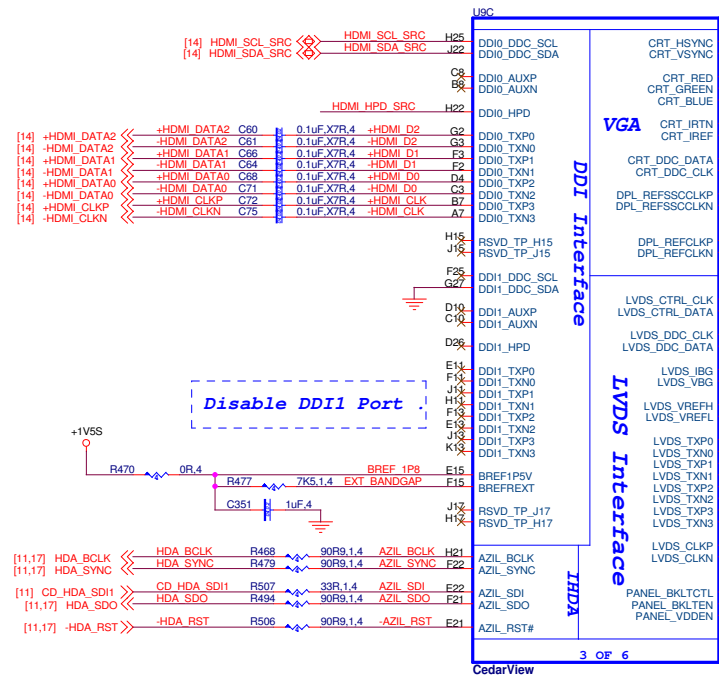
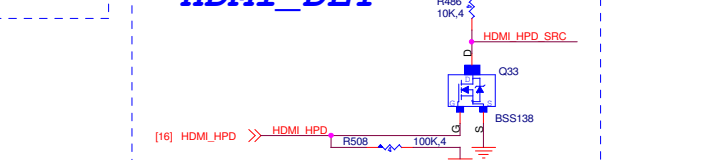
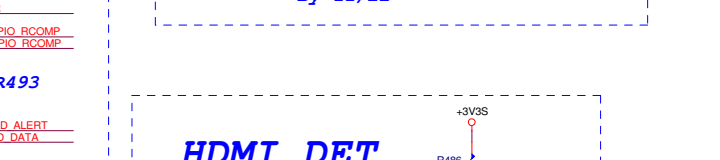
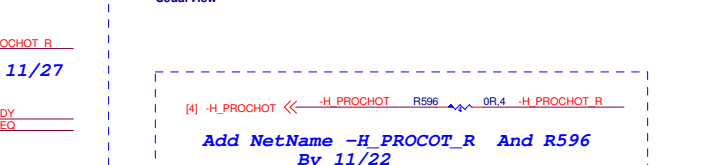




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|-----------------------------------|----------------------------|-------|-----------------|
| <i>i-Mobile</i> | | | |
| Title | | | |
| <i>Power_On_Sequencing</i> | | | |
| Size | Document Number | | Rev |
| Custom | <i>IC-10</i> | | <i>C</i> |
| Date: | Tuesday, February 19, 2013 | Sheet | 3 of 23 |

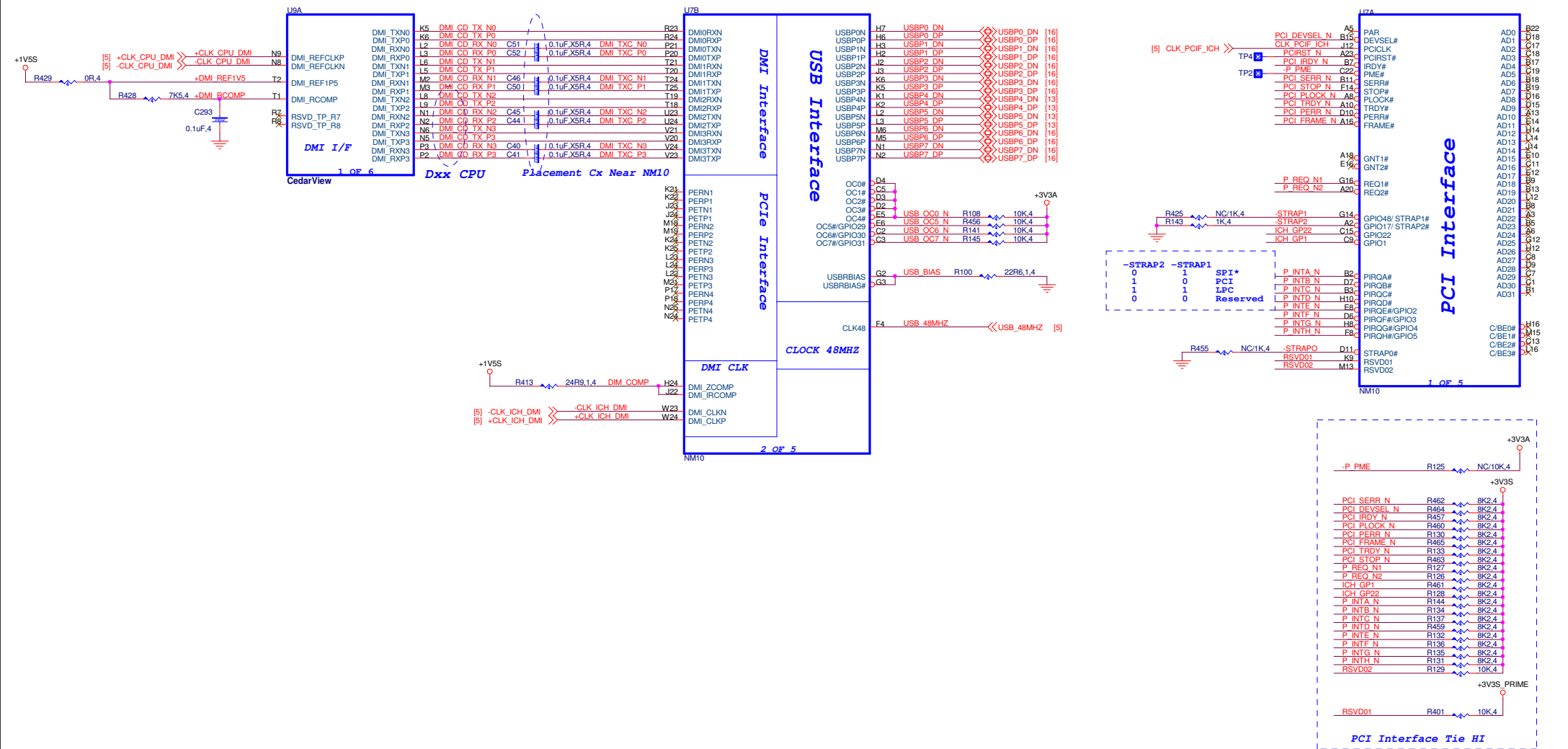


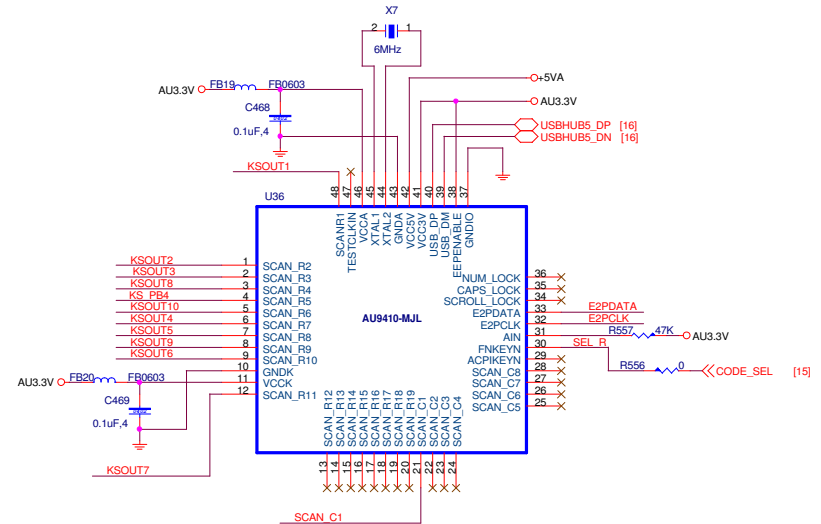
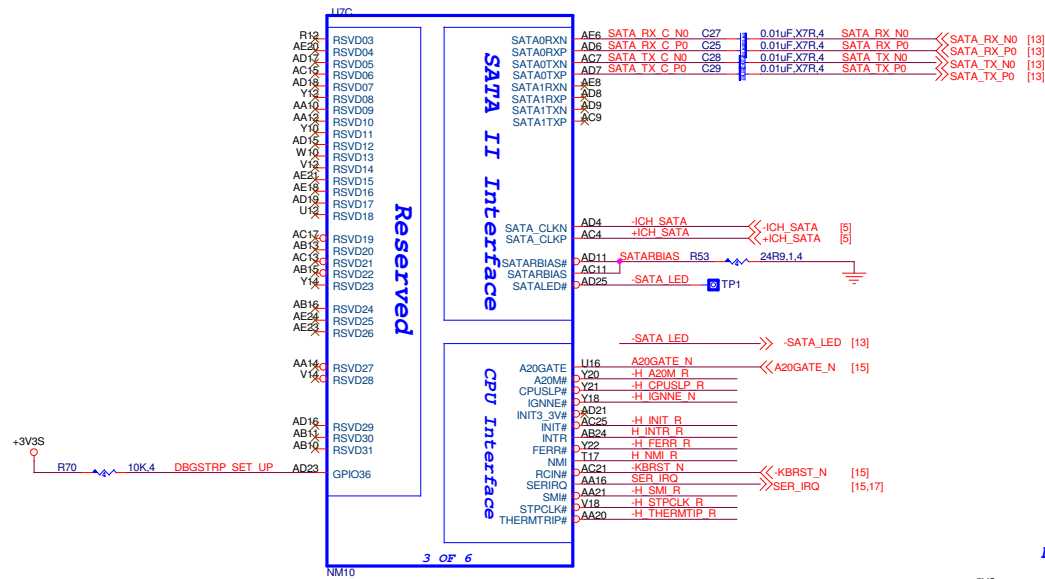




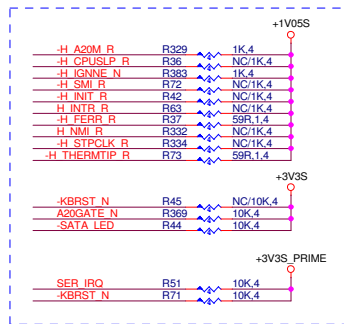
Swap [-M_CAS], [-M_RAS]
By 11/20

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| MA1 | AK16 | DDR3_MA1 | DDR3_DO1 | Y29 | M_DO1 |
| MA2 | AJ14 | DDR3_MA2 | DDR3_DO2 | AC30 | M_DO2 |
| MA3 | AJ16 | DDR3_MA3 | DDR3_DO3 | AC31 | M_DO3 |
| MA4 | AK18 | DDR3_MA4 | DDR3_DO4 | W31 | M_DO4 |
| MA5 | AK18 | DDR3_MA5 | DDR3_DO5 | W28 | M_DO5 |
| MA6 | AH18 | DDR3_MA6 | DDR3_DO6 | AB28 | M_DO6 |
| MA7 | AK20 | DDR3_MA7 | DDR3_DO7 | AB30 | M_DO7 |
| MA8 | AJ20 | DDR3_MA8 | DDR3_DO8 | AA24 | M_DO8 |
| MA9 | AH20 | DDR3_MA9 | DDR3_DO9 | AA22 | M_DO9 |
| MA10 | AJ12 | DDR3_MA10 | DDR3_DO10 | AE27 | M_DO10 |
| MA11 | AK21 | DDR3_MA11 | DDR3_DO11 | AE26 | M_DO11 |
| MA12 | AJ12 | DDR3_MA12 | DDR3_DO12 | AE27 | M_DO12 |
| MA13 | AJ18 | DDR3_MA13 | DDR3_DO13 | AA25 | M_DO13 |
| MA14 | AH22 | DDR3_MA14 | DDR3_DO14 | AD25 | M_DO14 |
| MA15 | AJ22 | DDR3_MA15 | DDR3_DO15 | AD27 | M_DO15 |
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| | | | DDR3_DO17 | AE28 | M_DO17 |
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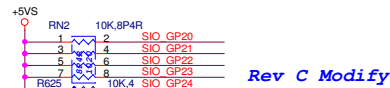
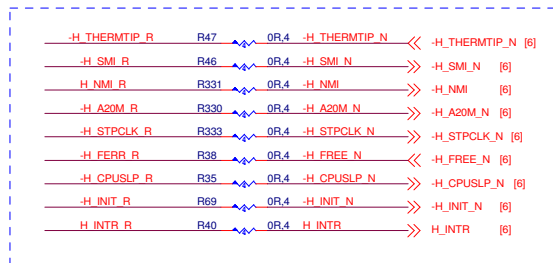




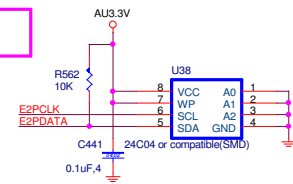
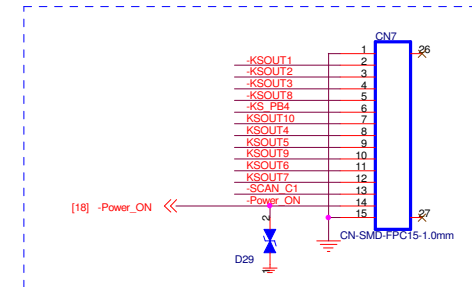
Modify By Perry 0827_12



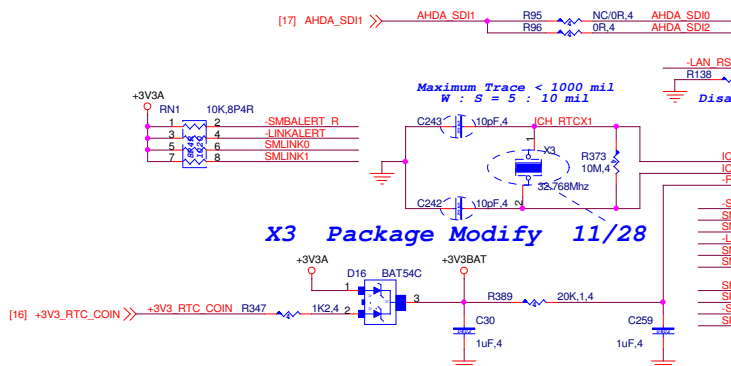
Remove R36, R42, R45, R63, R72, R332, R334 By 11/17/12



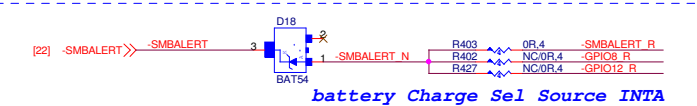
Modify Add [SIO_GP24] By Perry 09/06/12



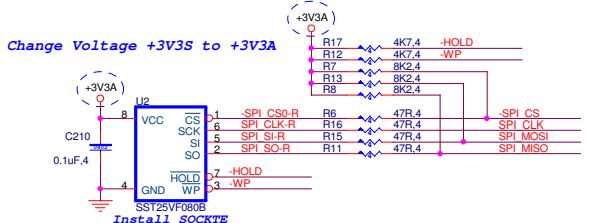
X3 Package Modify 11/28



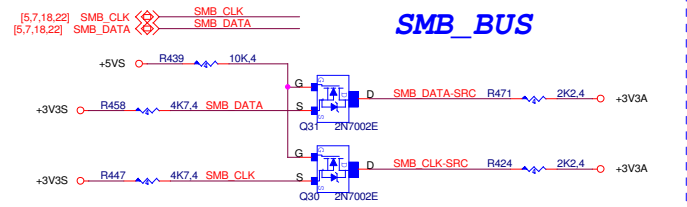
battery Charge Sel Source INTA

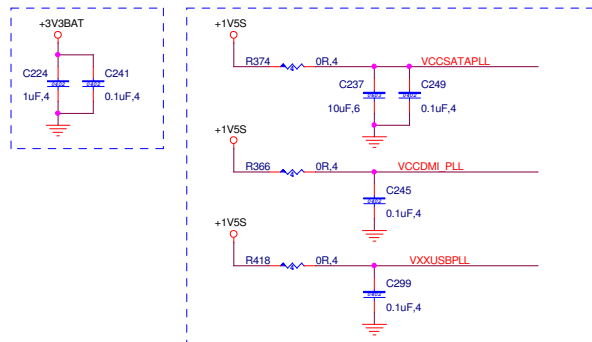
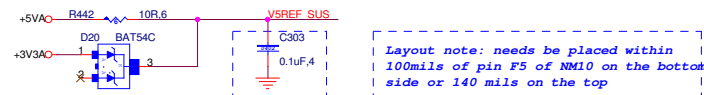
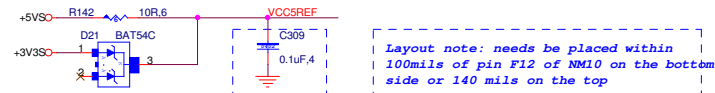


System Boot ROM

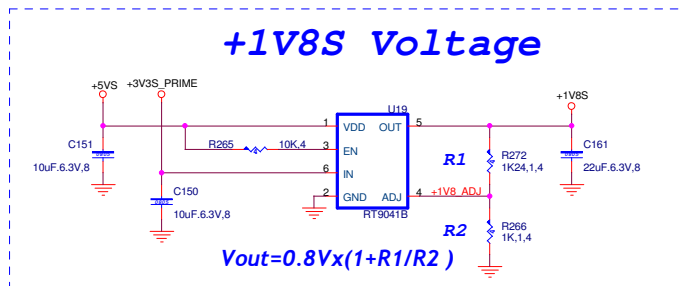
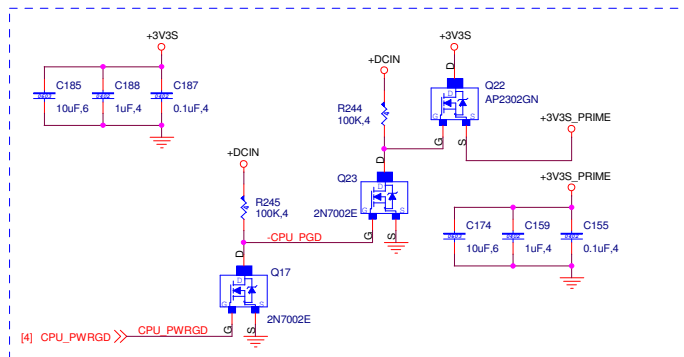


SMB_BUS



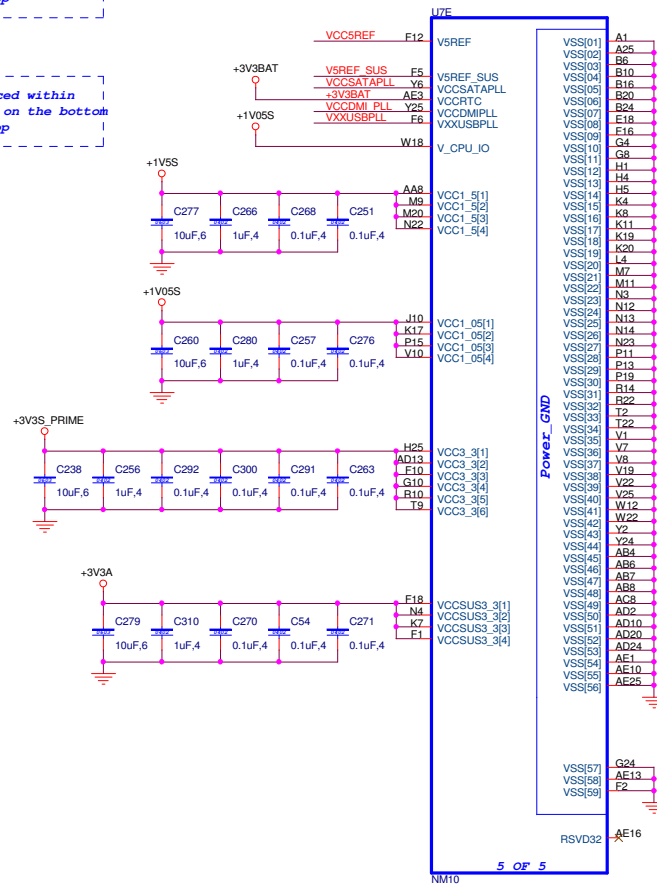


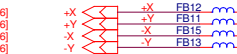
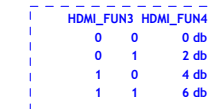
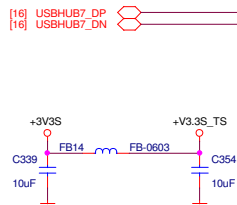
BOM Change R366,R418 ,150R to 0R By 11/14

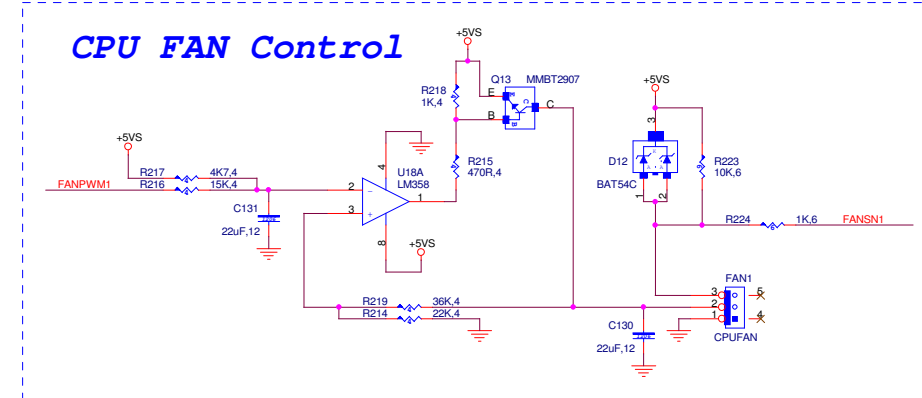
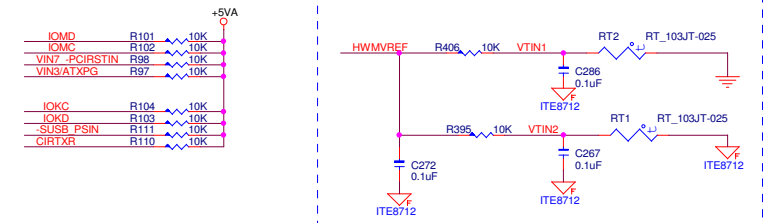
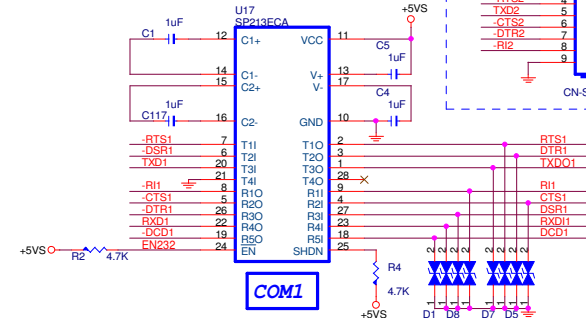
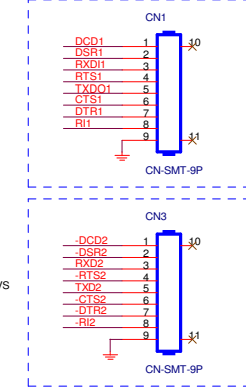
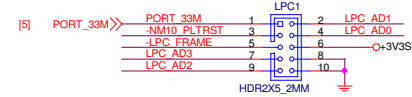
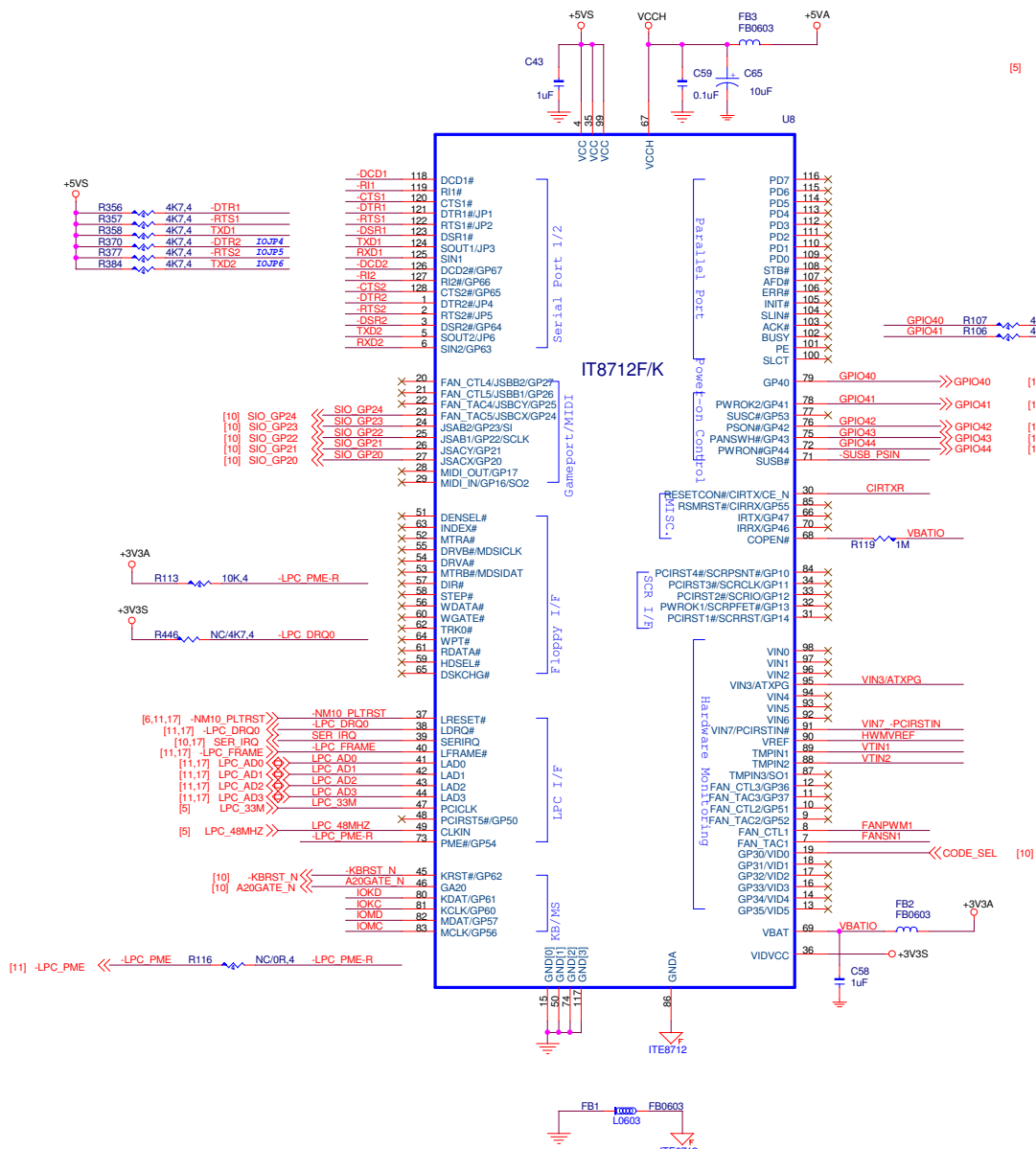


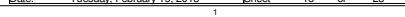
+1V8S Voltage

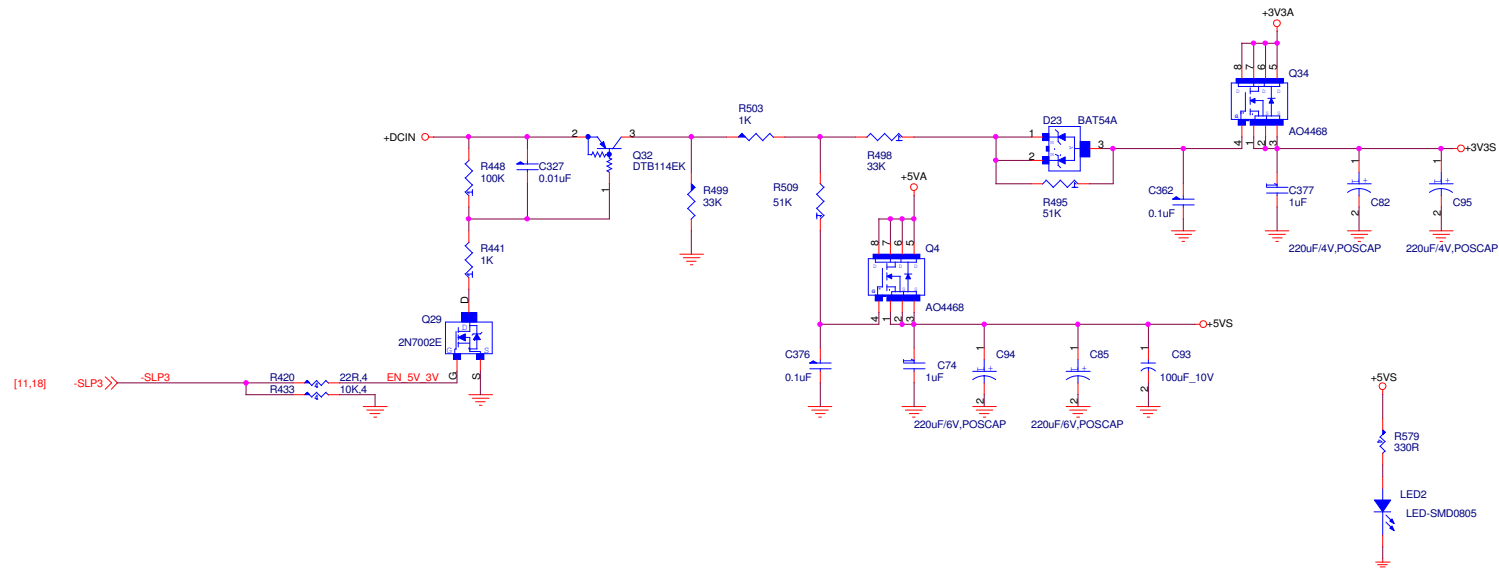
$$V_{out} = 0.8V_x(1 + R1/R2)$$

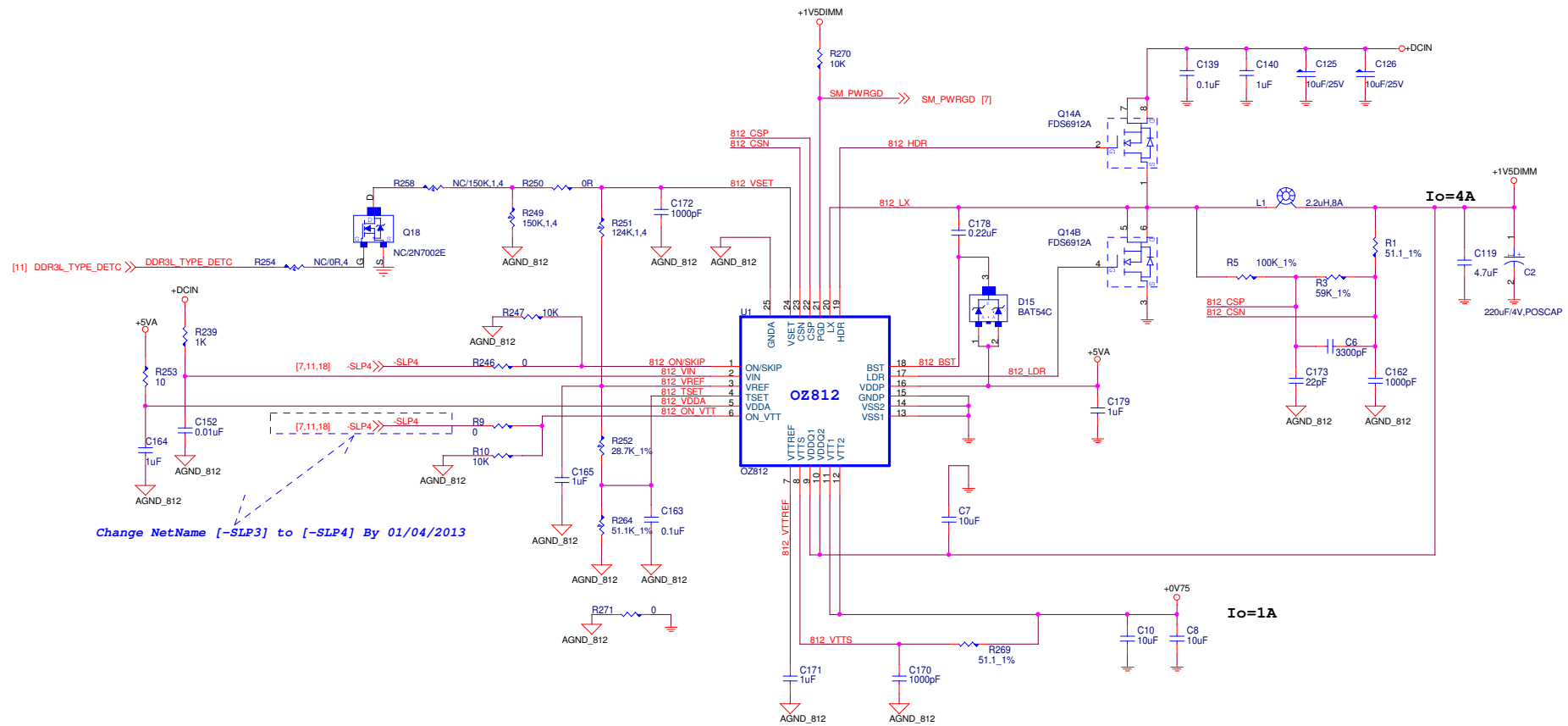




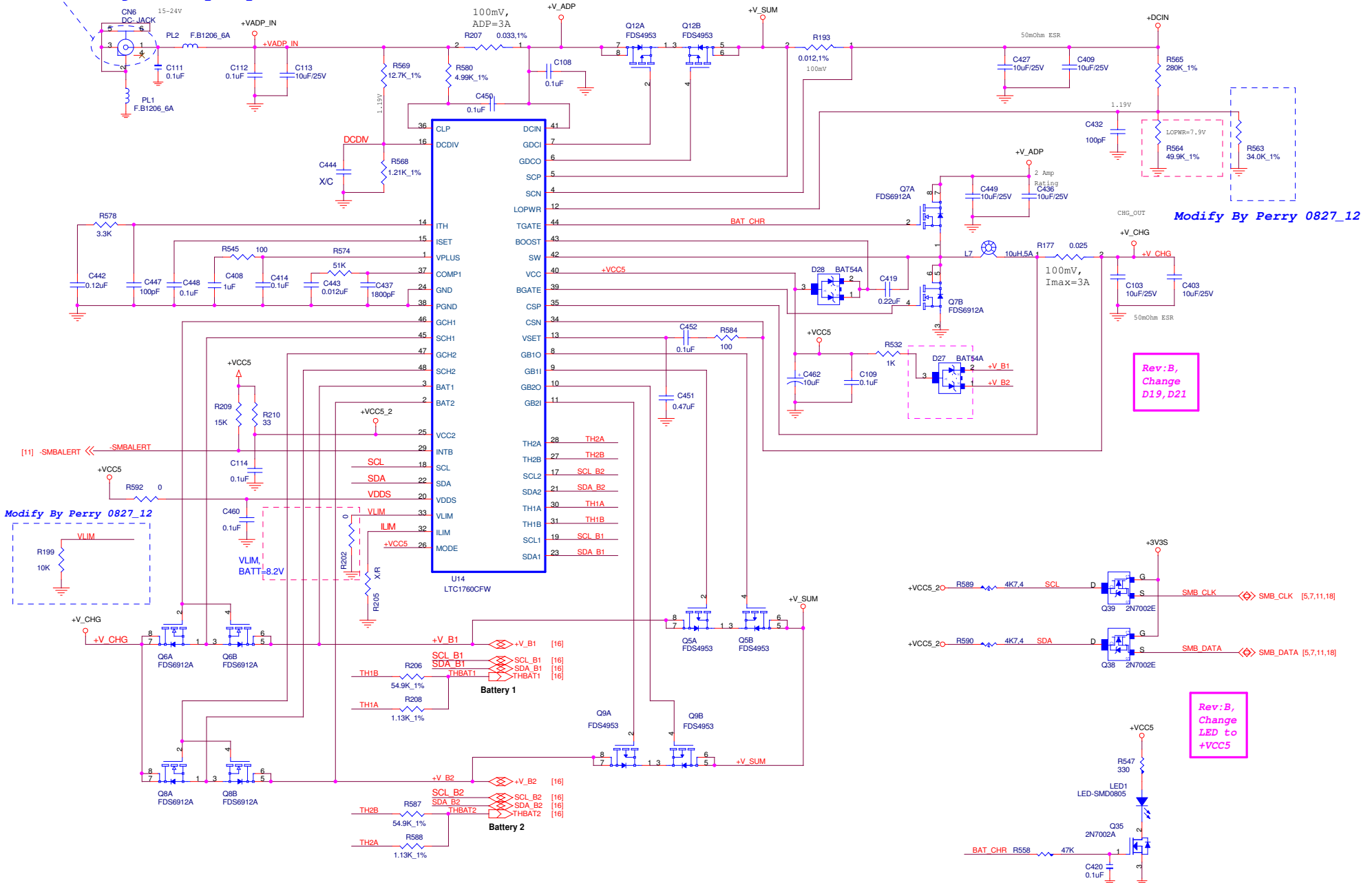








DC_JACK Package Modify Layout 10/26



| | Date | Descriptions |
|-------|------------|--------------------------------|
| | xx/xx/2012 | <div>■</div> 1st. release sch. |
| Rev.1 | xx/xx/2012 | |