



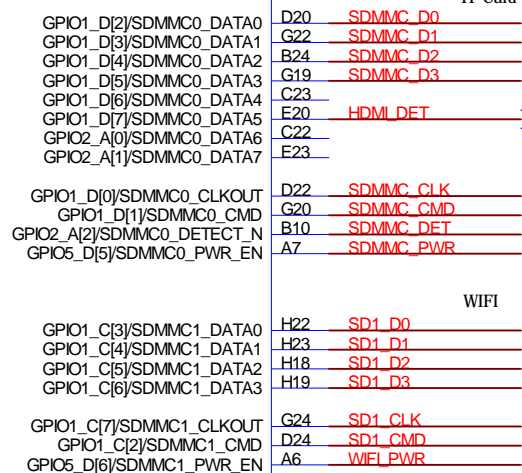
RK2918-F

RK2918-G

THE 1th FLASH

U1-F

SDMMC Module

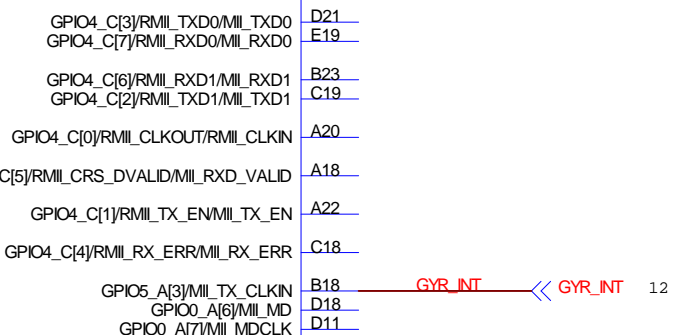


TF Card



U1-G

RMII/MII/JTAG Module



RK2918

TRST\_N K5  
TCK L5  
TDI L6  
TDO L7  
TMS L6

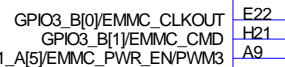
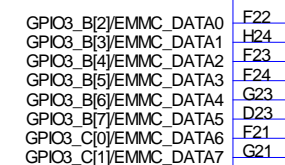
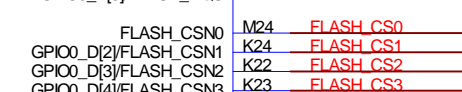
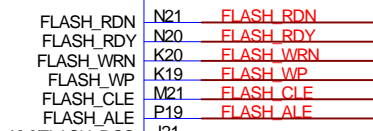
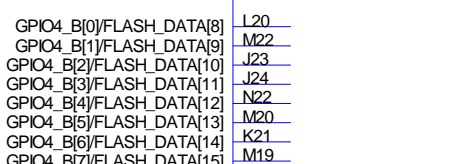
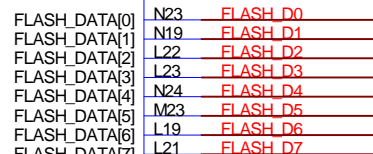
JTAG

RK2918-E

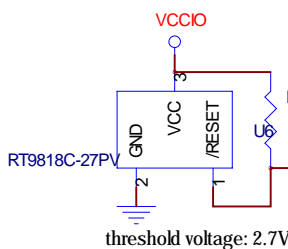
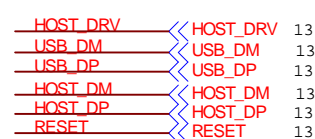
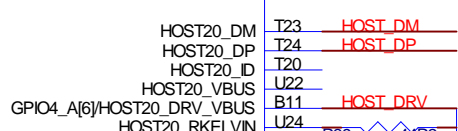
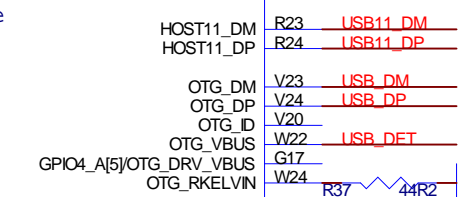
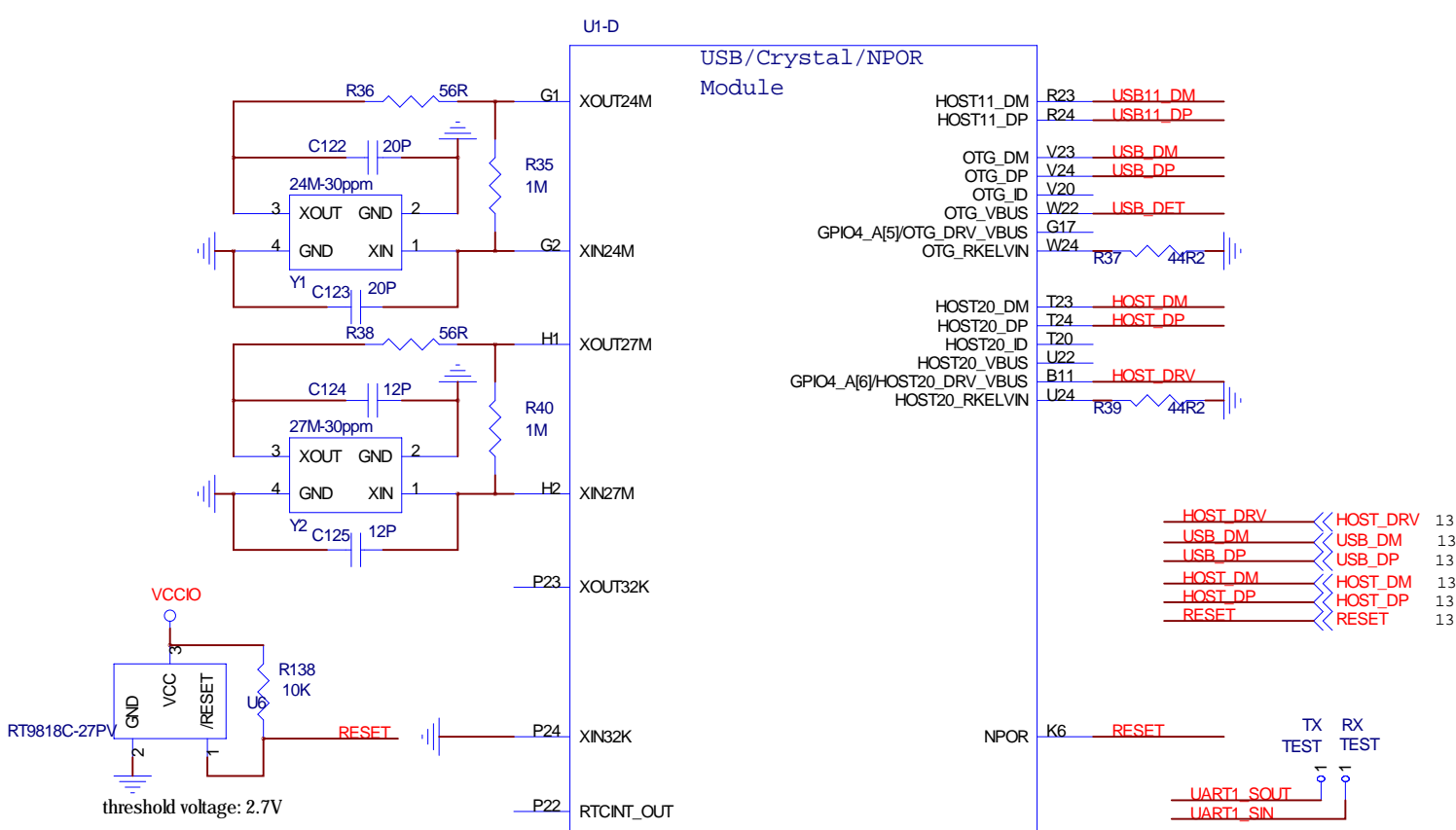
RK2918-D

U1-E

NAND FLASH/EMMC Module

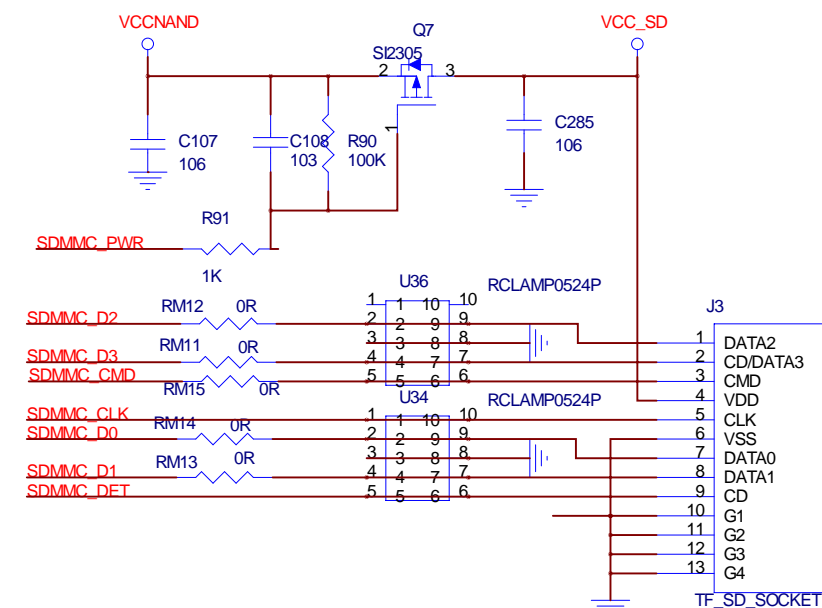


RK2918



Note:The reset time must be more than 200mS.

SD CARD



E-SMART

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Title:

File:

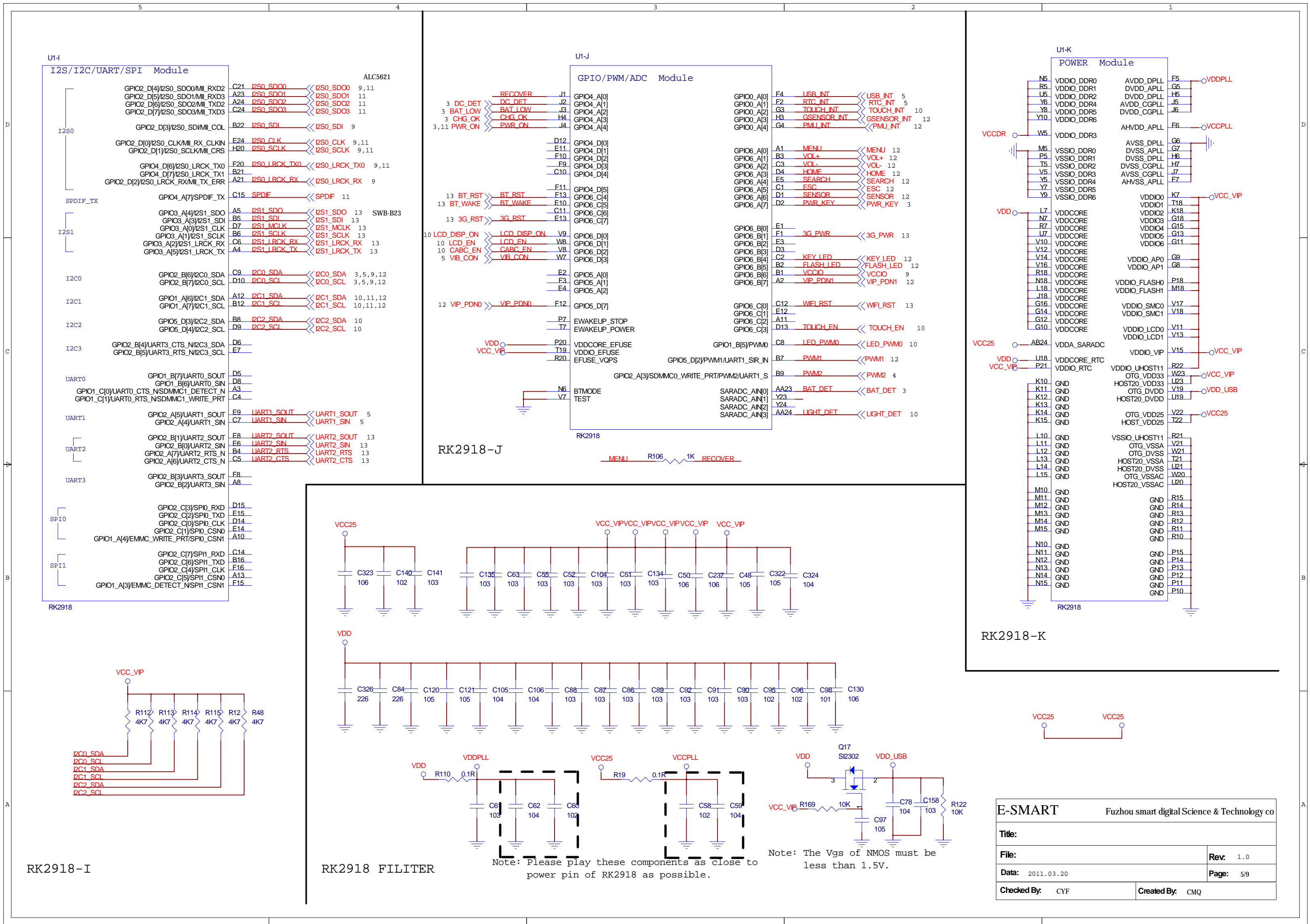
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Checked By: CYF

Rev: 1.0

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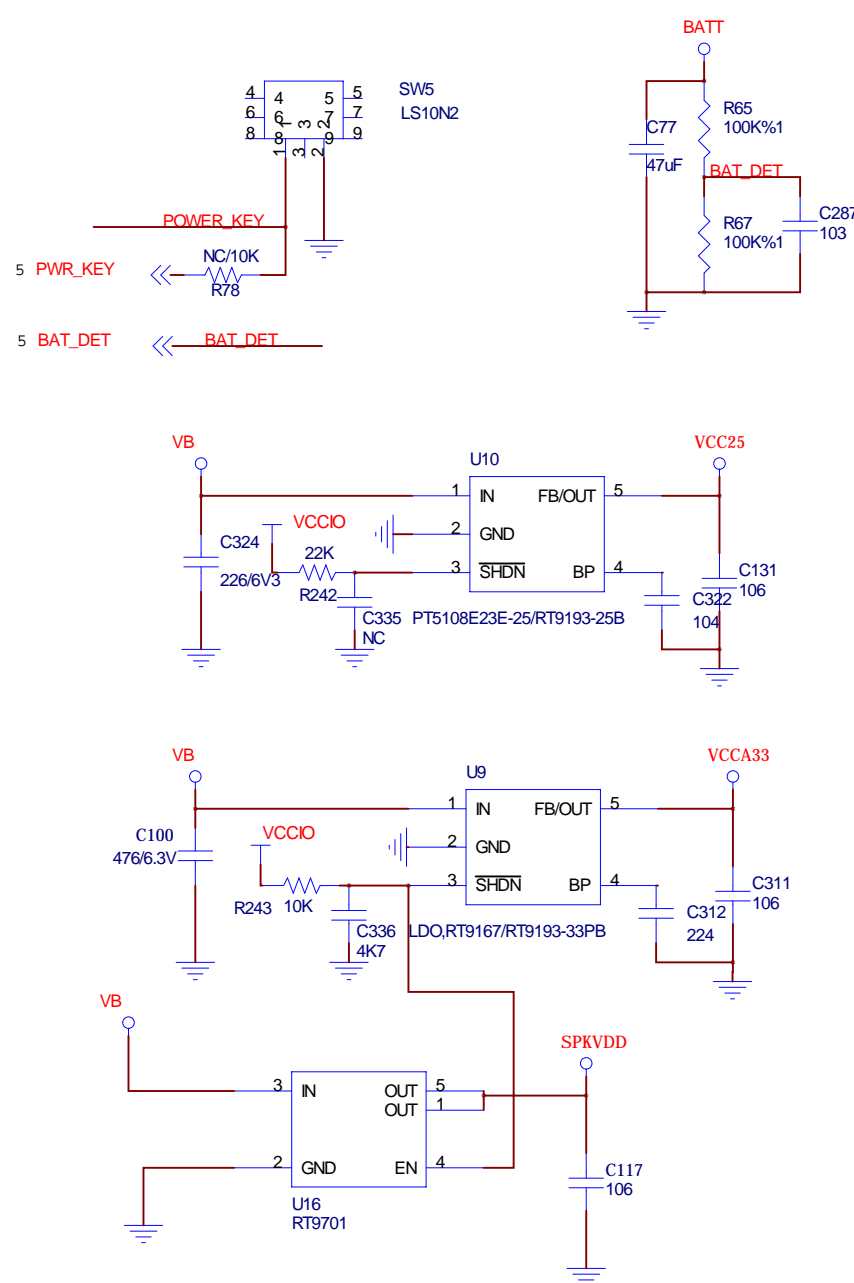
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The schematic diagram illustrates the core power section of the RK2918. It features a DC-DC converter U53 (SY8009A1.5A) with a feedback network. The feedback trace is connected to the power pin of RK2918. A note specifies that the feedback trace must be connected to the power pin of RK2918.

Note:  
The feedback trace must be connected to the power pin of RK2918.

**BATTER**

[illegible]

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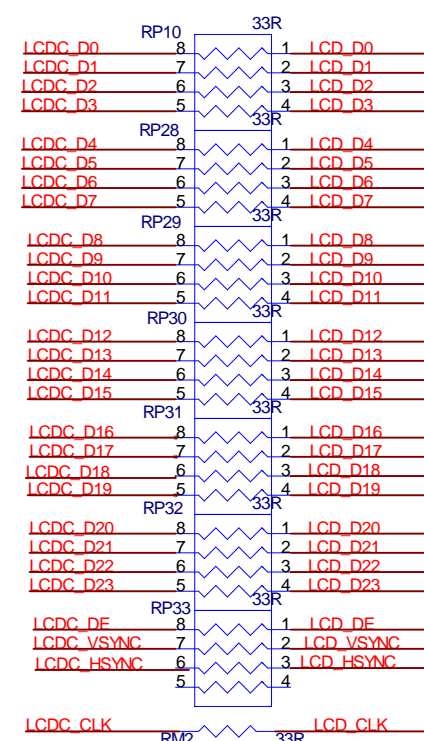
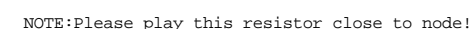
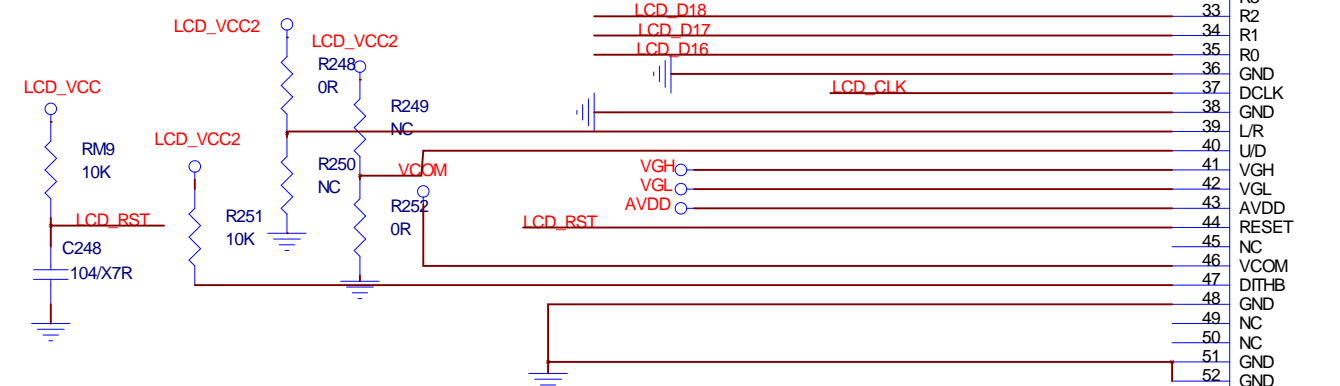
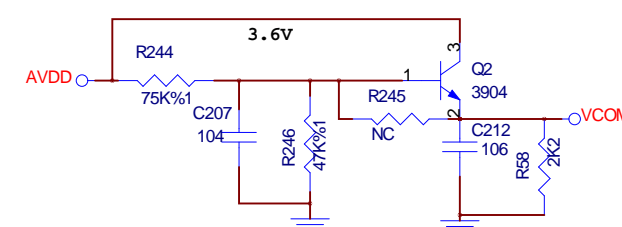
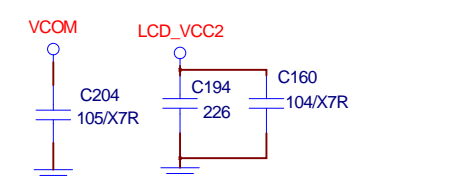
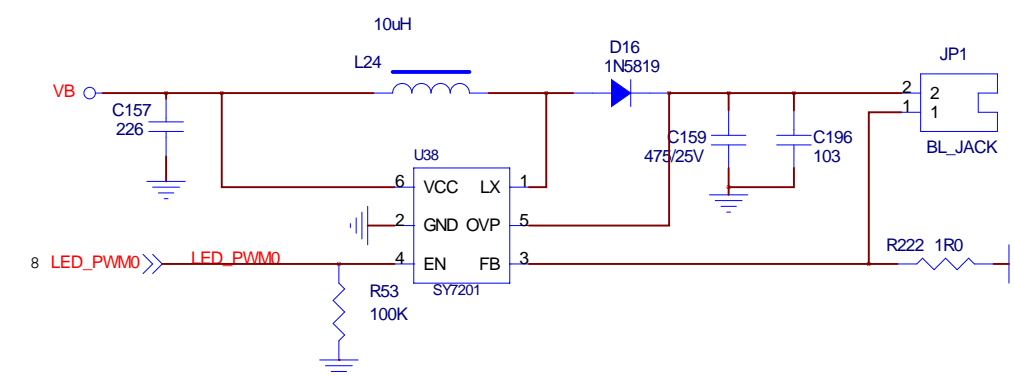
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Q

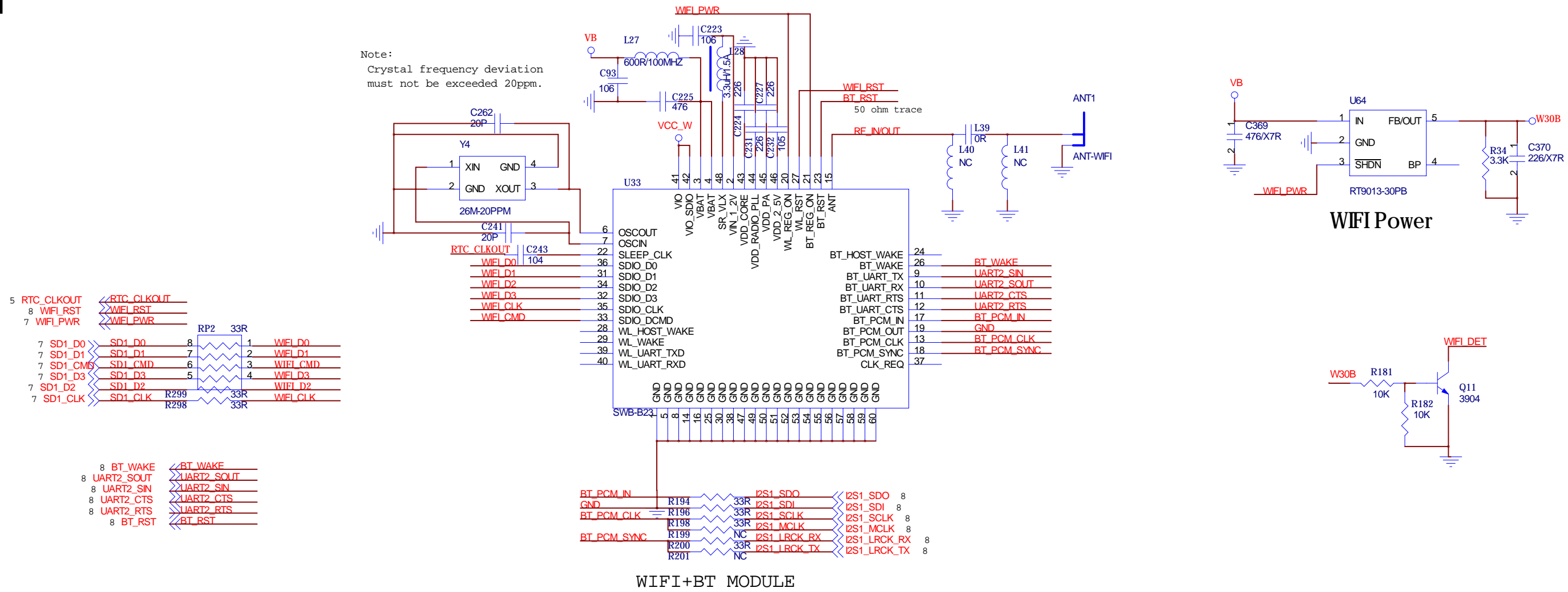
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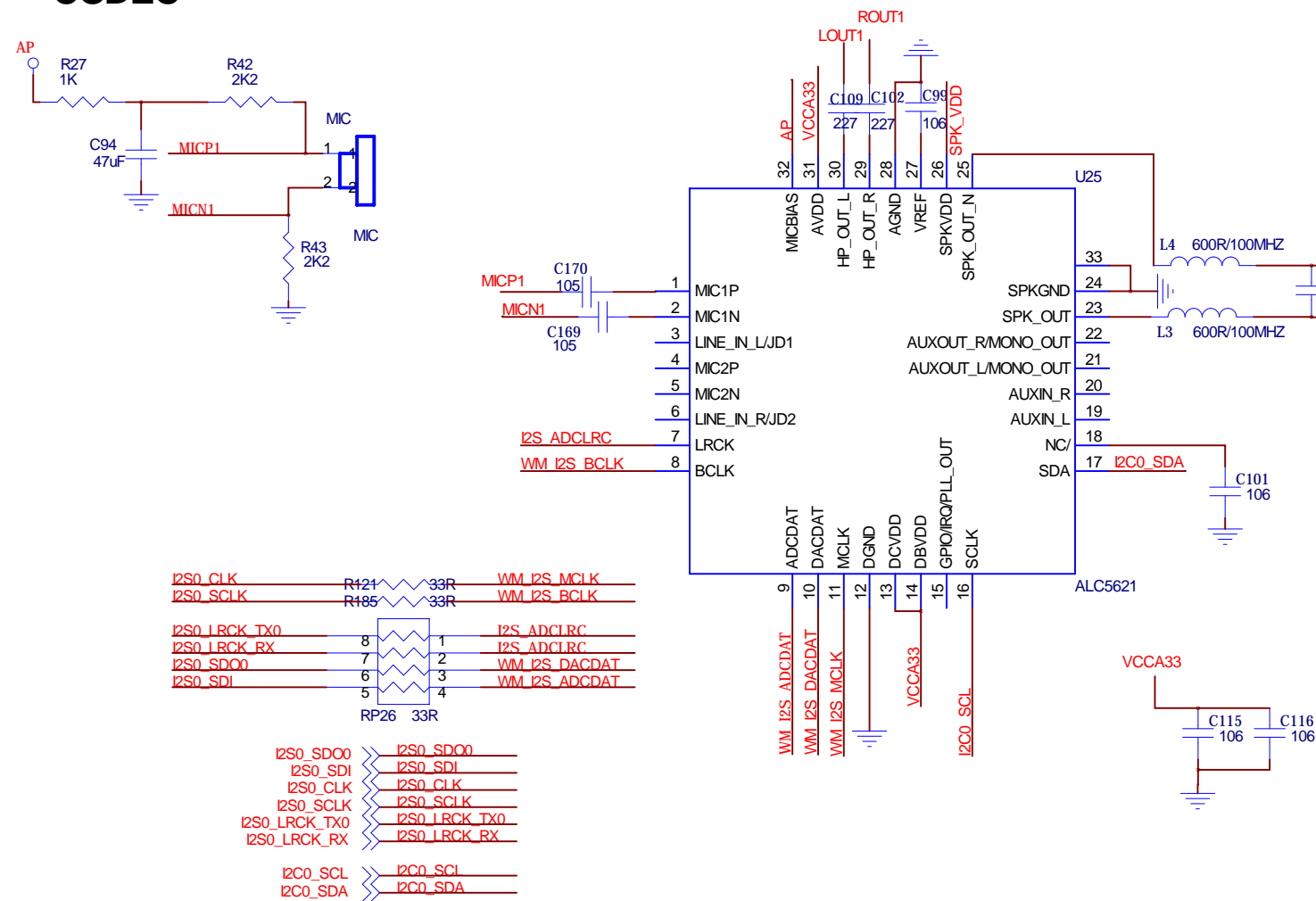


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## WIFI



## CODEC



## HEADPHONE

