Modify the record: with software changes, New connection method, the signal line WAKEUP_KEY to U4.P33 (P1_1), Old connection method, the signal line WAKEUP_KEY to U4.P35 (P3_5). Signal line WAKEUP_KEY increase pull-up resistor. V30 S4 U4 BP2042S 39 GND4 38 P4 4 37 GND3 36 GND3 35 P4 5 34 P1_1 33 P4_0 R22 R18 47K 510K PAIR V30 0 XC61CC2702MRN C17 10uF/16V R19 10 1P8V P2_ P3_ OUT VSS V30 ○ C9 **-**○ ∨30 C24 | C25 0.01uF 0.1uF | 0.1uF C22 47uF/6.3V VBAT R1 47K SSSS820501 47uF/6.3V C20 MAC OFF V30 РС В1 S5 R20 10K 1.5VAAAx2 C11 0.1uF C26 0.1uF R21 510K Program S6 RXD (TP) TXD (TP C23 0.01uF GND (TP) RESET RESET WAKEUP_PAO CLK
DATA
WAKFUP_KEY
CHECKPAO
CONNECT **VBAT** Model: X Mark 1K BT R2 QR вт SCH: V2.0 Size: Α4 CON2 PCB: PB-1015102 1 of Sheet Drawn By: YuJinTao Check By: Comfirm By:

2011-04-19







