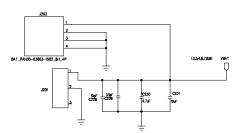
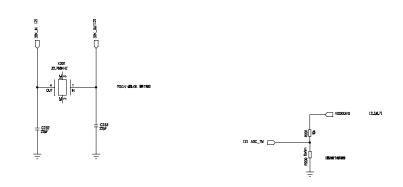
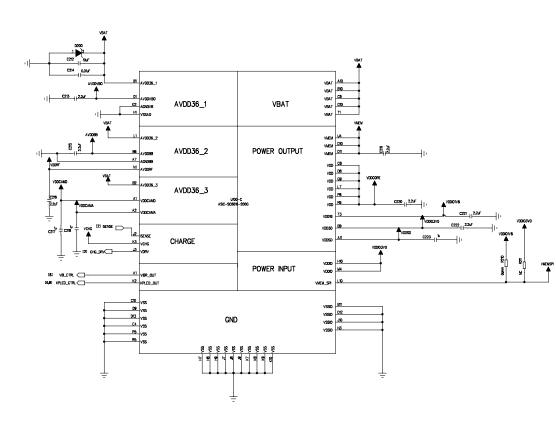


All trace connectted to the MOSFET should be as wide as possible

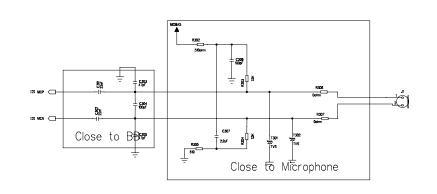
## BAT CON.

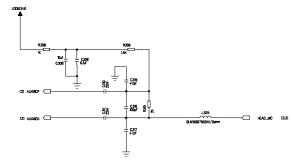


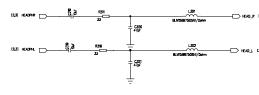




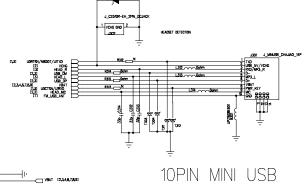


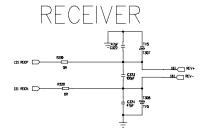


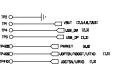


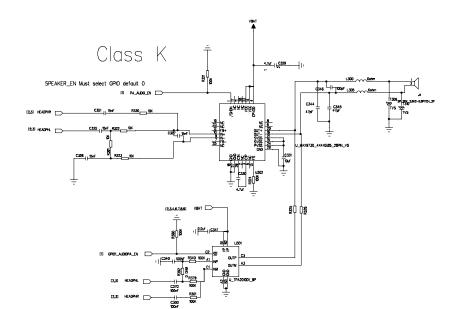


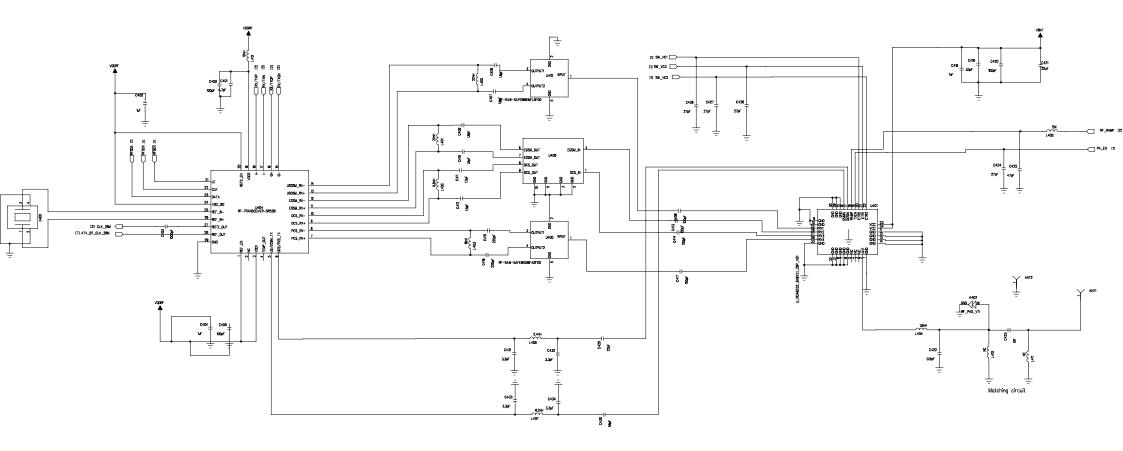


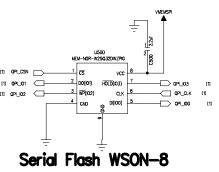


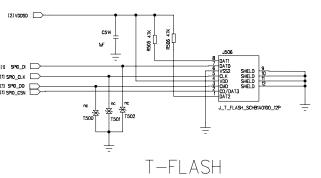


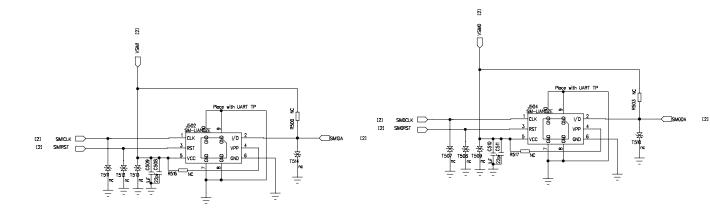












SIM1 SIM2

