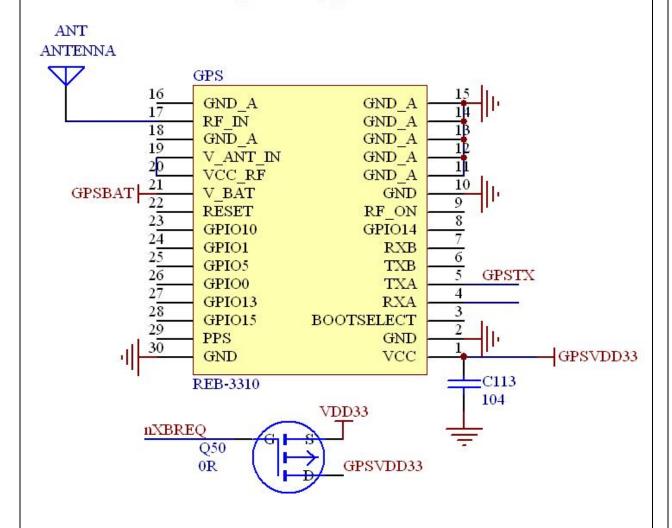
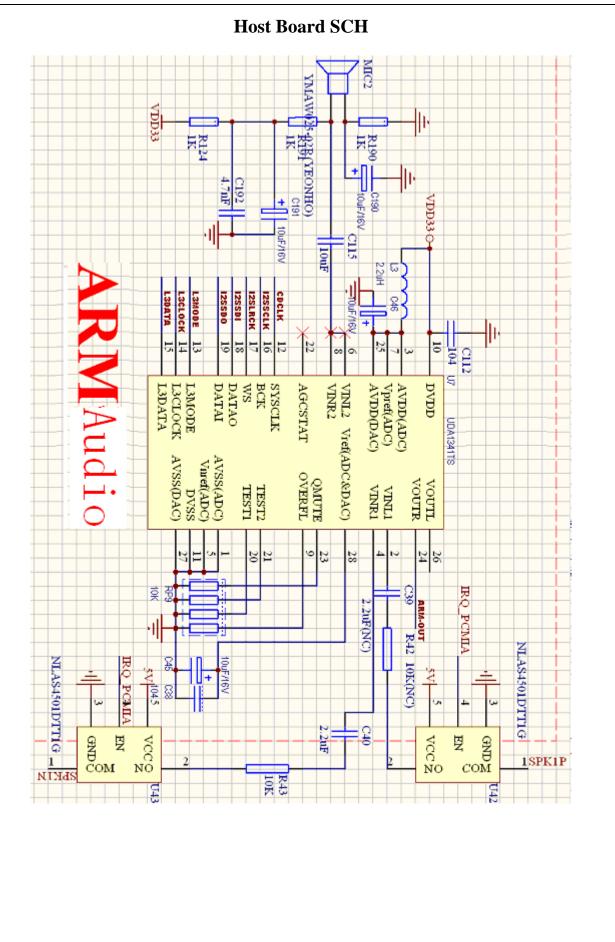


Host Board SCH

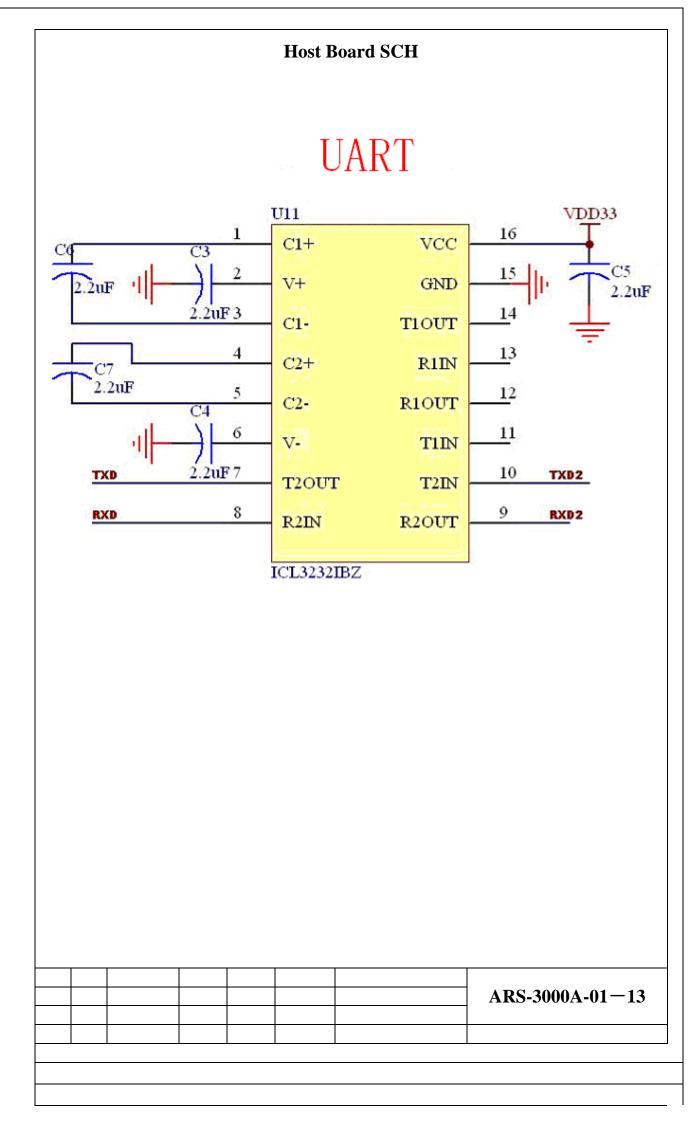
GPS

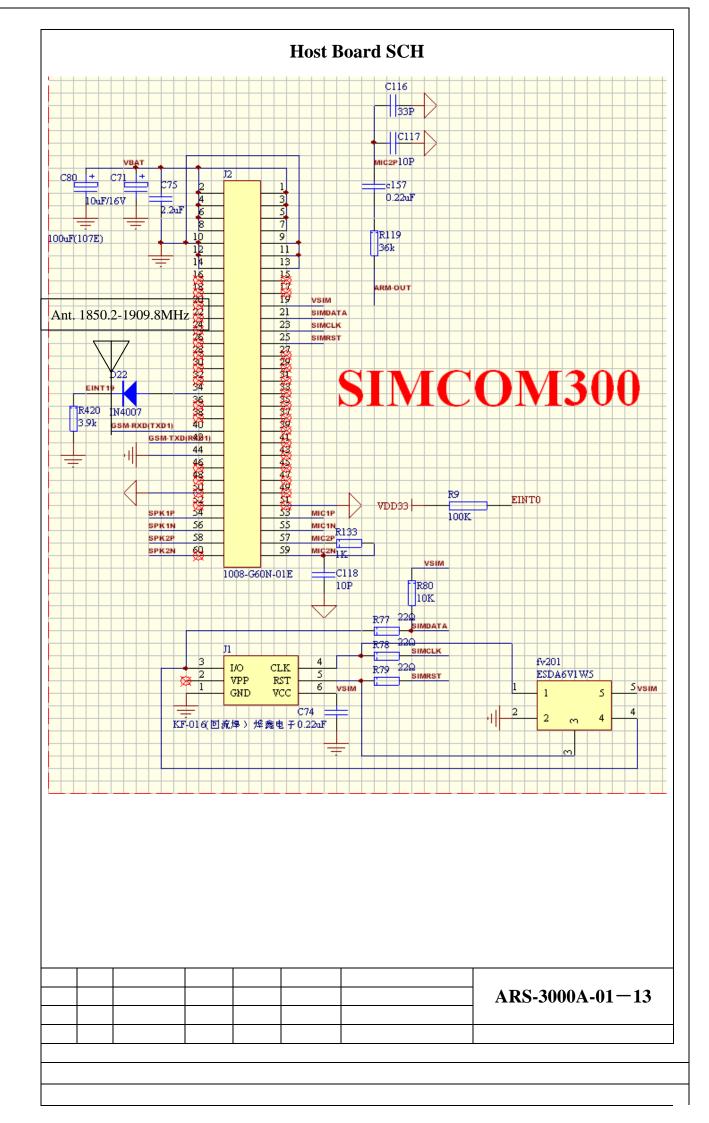


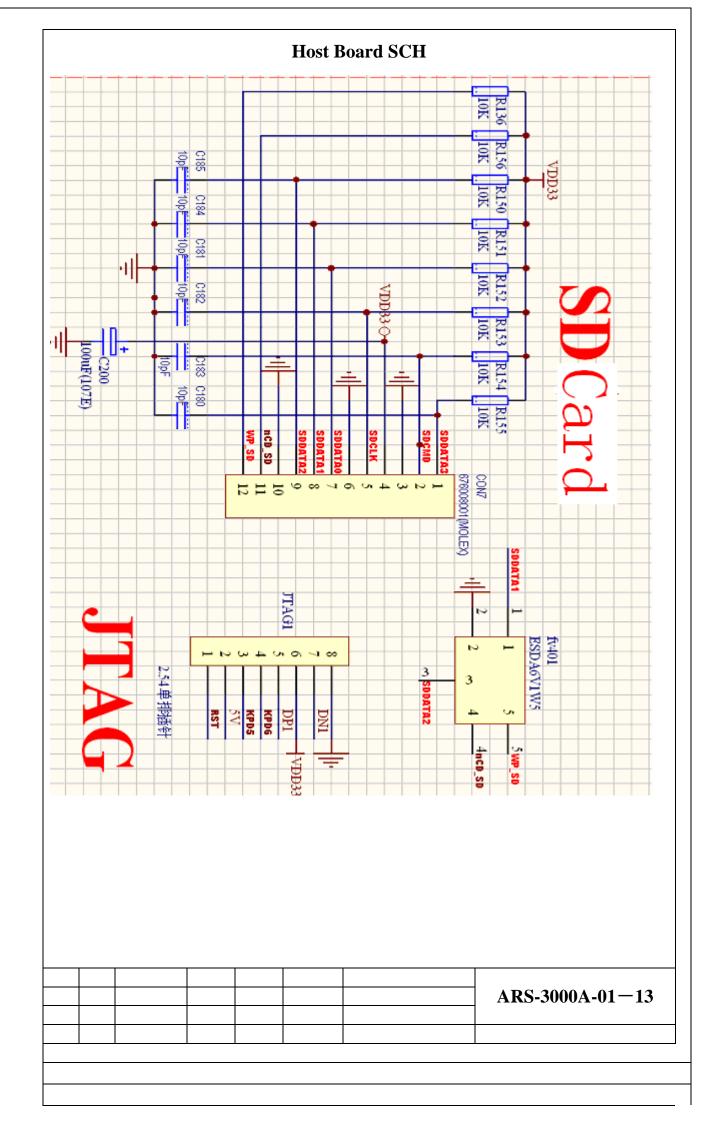
				ARS-3000A-01—13	

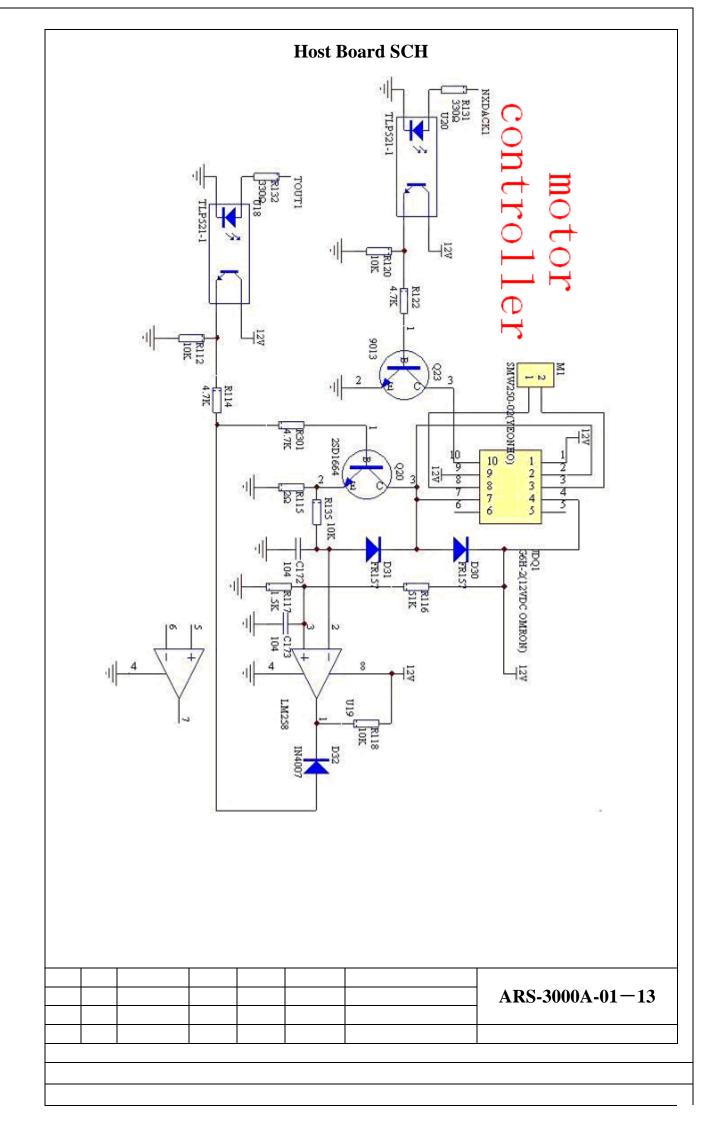


ARS-3000A-01-13					
	ARS-3000A-01—13				





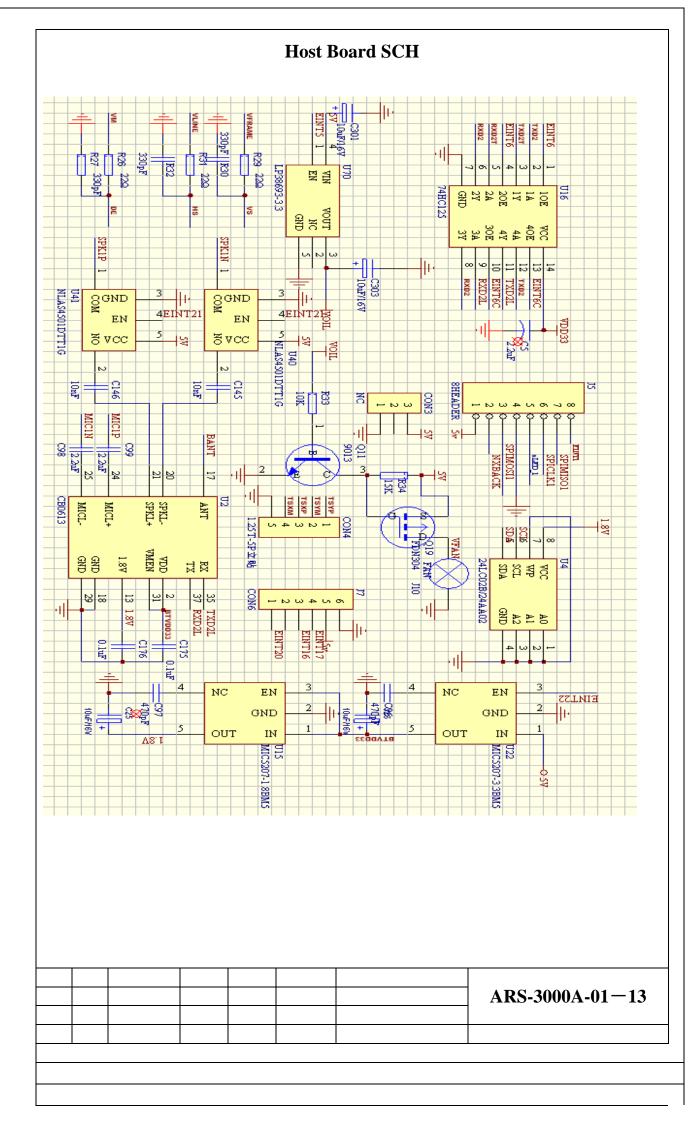


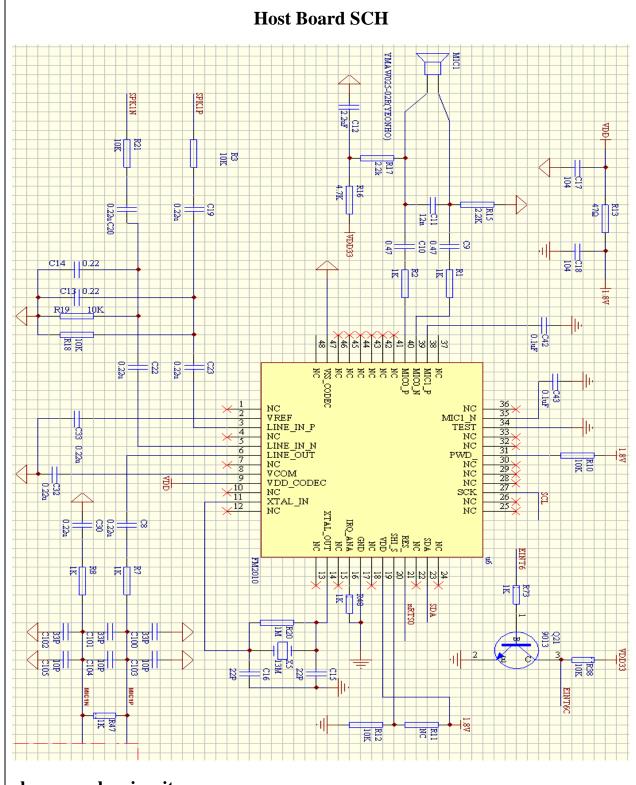


Host Board SCH

IND	1	VSS	CON45 05003HR-45A01(YENOHO)	GND	1	A1	В1 –	100	VDD3
SND	2	VSS	03 2	GND	2	A1 A2	B2	99	VDD3
VDD33	3 3	VDD	自ち	GND	3	A3	B3	98	VDD3
VDD33	3 4	VDD	2	GND	4	A3 A4	B4	97	VDD3
SND	5	B0	5 A	RST1	5	A5	B5	96	RXD2
SND	6	B1	01	VDDRTC	6	A6 A6	B6	95	TXD2
SND	7	B3	3	IRQ_PCMI	д 7	A7	B7	9 4s	M-TXD(R
/D3	8	R3	园	nXBREQ	8	A8	B8	9 3:s	M-RXD(T
/D4	9	B4	0	EINT19	9	A9	В9	92	TXDO
/D5	10	B5	OF	EINT11	10	A10		91	RXDO
/D6	11	B6	\sim	EINTO	11		B10 -	90	
/D7	12			AVref	12	A11	B11 -	89	DP1
SND	13	B7		AIN3	13	A12	B12 -	88	DN1
SND	14	G0		AIN2	14	A13	B13 -	87	SPICLK
/D 10	15	G1		OILTEST	15	A14	B14 -	86	EINT16
/D 11	16	G2		AIN1	16	A15	B15	85	EINT17
/D 12	17	G3		IICSDA	17	A16	B16 -	84	SPIMISO
/D 13	18	G4		HCSCL	18	A17	B17 -		SPIMOS
/D 14	19	G5		EINT20	19	A18	B18 -	82	VD3
/D 15	20	G6		nLED_2	20	A19	B19	81	VD4
SND	21	G-7		nLED 1	21	A20	B20 -	80	VD5
SND	22	R0		nRTSO	22	A21	B21 -	79	VD6
SND	23	R1		EINT1	23	A22	B22 -	78	VD7
/D 19	24	R2		EINT6	24	A23	B23 -	77	VD 10
/D20	25	R3	1 <u>6</u> 11 63	EINT5	25	A24	B24 -	76	VD 11
/D21	26	R4	C35	EINT21	26	A25	B25 -		VD 12
/D22	27	R5		EINT22	27	A26	B26 -		VD 13
/D23	28	B6		TSYP	28	A27	B27 -	73	VD 14
SND	29	B7	<u>Q</u>	TSYM	29	A28	B28 -	72	VD 15
/CLK	30	VSS	1.41	TSXP	30	A29	B29	71	VD 19
CD	BWRE	PCLK	1011 36	TSXM	31	A30	B30 -	70	VD20
15	32	DISP		nSS_SPI	32	A31	B31 -	69	VD21
/S	33	HSYNC	10µF	SPICLK	33	A32	B32 -	68	VD22
ÞΕ	34	VSYNC	튀 3	SPIMOSI	34	A33	B33 -	67	VD23
VDD33	335	DE	1,	SPIMISO	35	A34	B34 -	66	LCD_PWI
VDD33	3 36	AVDD5		CDCLK	36	A35	B35 -		VM
IND	37	AVDD5		12SSCLK	37	A36	B36 -	64	VFRAME
IND	38	VSS		12SLRCK	38	A37	B37 -		VLINE
IND	39	VSS		IZSSDI	39	A38	B38 -		VCLK
īV	40	VLED-		IZSSDO	40	A39	B39		WP_SD
īV	41	VLED+		T3CTOCK	41	A40	B40 -		SDDATA
	42	41		L3DATA	42	A41	B41 -		SDDATA
	43	42		L3MODE	43	A42	B42 -		SDCLK
	44	43		NXBACK	44	A43	B43 -		SDCMD
COUTI		44		LEND	45	A44	B44 -		SDDATA:
		45		NXDREQ:		A45	B45 -		SDDATA
				NXDACK		A46	B46 -		nCD_SD
				TOUT1	48	A47	B47 -		CLKOUT
				TOUT0	49	A48	B48 -		DPO
				EINT2	50	A49	B49 -		DNO
					50	A50	B50 -		-110
							100		
							CON100		
							44		

			•	•			
ARS-3000A-01-13							





clear up echo circuitry

ARS-3000A-01—13				
			 •	

4.3"LCD drive Board SCH 05003HR-45A01(YENOHO) CON1 CON2 VSS 1 VSS VSS 3 2 LEDA VSS VDD 3 VDD VDD 4 VDD 6 RI SYNC/DE mode В3 GHD R2 R3 VC19 R3 B4 9 LV0033 10 VC20 B5 R4 10 LV0033 11 VC21 R5 B6 CON6 12 Ú1 В7 R6 13 12 TSXN VV\$ G0 R7 + 5V 100uF(107E)U30 14 13 TSYN 2 G1 G014 PWREN TSXP VOUT GI VIN 3 G2 2 15 16 POLK VDD33 G2 NC G3 5 17 VC11 16 G3 GND G4 18 VC12 17 VC23 G4 G5 LP38693MP-3.3 19 V013 18 VC22 G5 G6 TSP X vc21 19 20 VC14 G6 G7 21 VC15 20 VC20 G7 R021 22 CHD VC13 B0 R1 22 GND 23 GHD ΒI R2 R145 GHD 23 GND 24 R3 B2 D4 330 L2 10uH 25 VC3 24 VDD33 GND R4 B3 26 V04 25 VC15 R5 B4 C37 + VCS 26 VC14 27 B5 B6 R24 U33 VC6 27 5822 VC13 28 В7 B6 2.2uF 220K 120PF V07 28 V012 29 ğ В7 VSS 29 VC11 30 VSS PCLK 30 31 PCLK VC10 PCLK FBONS DISP GND PWIRE TOUT1 32 SHUT DISP HSYNC 32 33 GND HSYNC VSYNC R25 33 C34:7 34 vvs VSYNC DE 10K 34 LM2733YMF 10u**?c**s 35 AVDD5 DE ves 36 O PWRSEL AVDD5 36 37 NC VSS R109 VC3 VSS VSS 3.3K VLED-VSS 39 40 VLED-VLED+ 40 VLED+ 41 42 05004HR-40A01(YENOHO) 43 44 GND 45 **ARS-3000A Auto Remote** ARS-3000A-01-14 **Service System Unit**

4.3"LCD drive Board SCH

