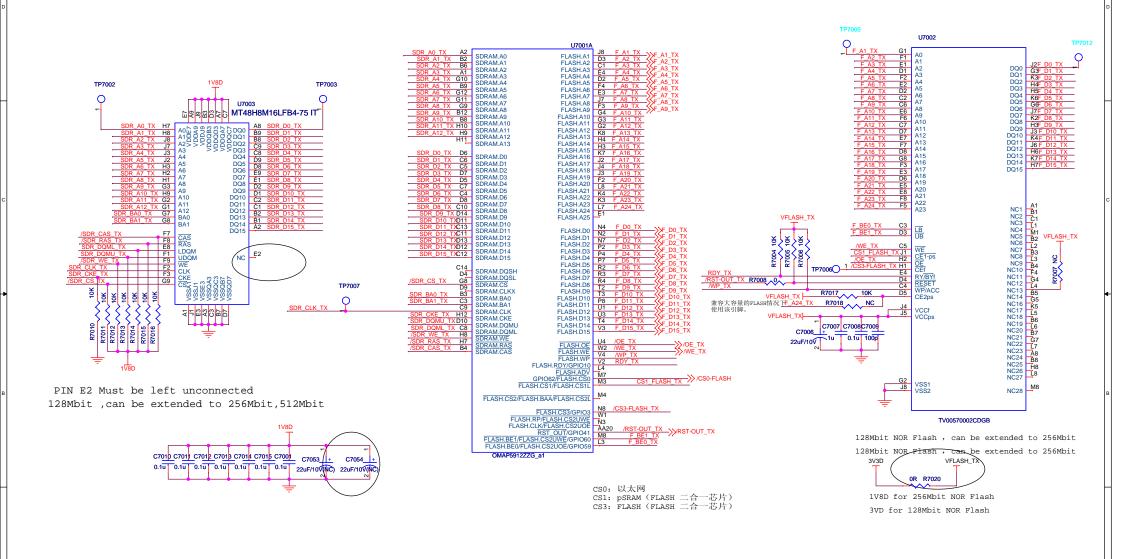




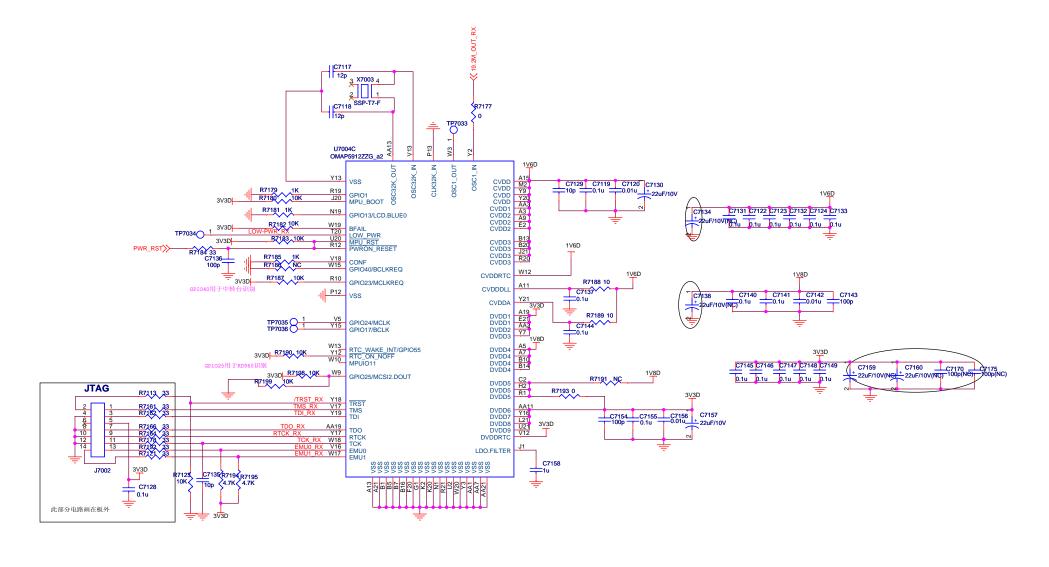
3VD for 128Mbit NOR Flash

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| Part Name: | Memory_RX | Date: | Wednesday, July 03, 2013 | | | | |
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| Prepare: | Check: | | Approve: | • | | | |



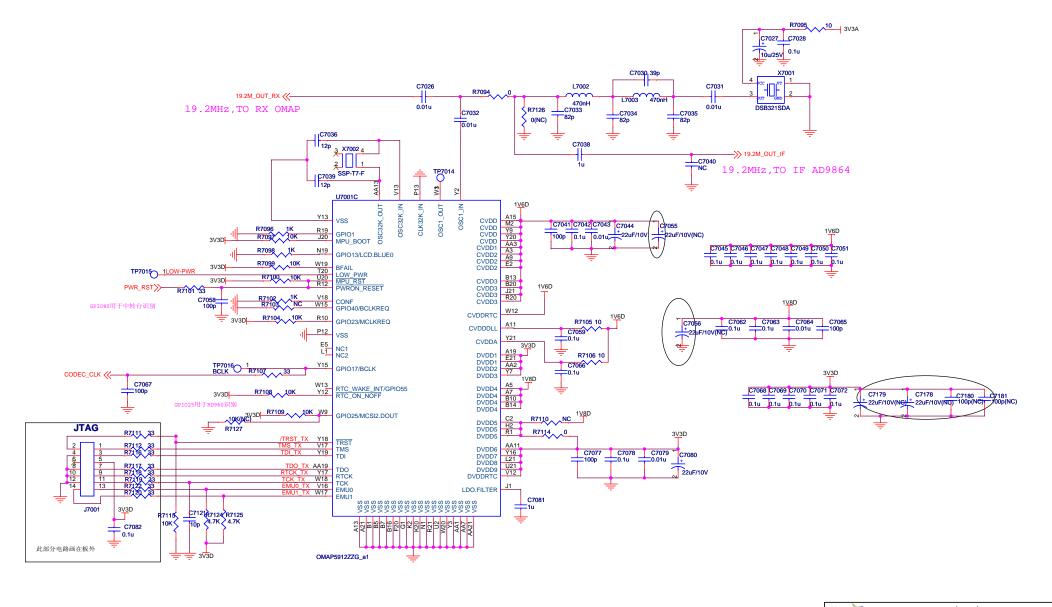
| Hytera | Hytera Communications Co.Ltd. | | | | | |
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| Model Name: | DMR RD620 U1 | Page: | : 09 of 19 | | | |
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OMAP_Core_RX



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| OMAP_Core_R | K Date: | Wednesday, July 03, 2013 | | | | | | |
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| | DMR RD620 U1 OMAP_Core_R | DMR RD620 U1 Page: OMAP_Core_RX Date: | DMR RD620 U1 Page: 10 of OMAP_Core_RX Date: Wednesday. Rev: | DMR RD620 U1 Page: 10 of 15 OMAP_Core_RX Date: Wednesday, July 05 Rev: | | | | |

OMAP_Core_TX

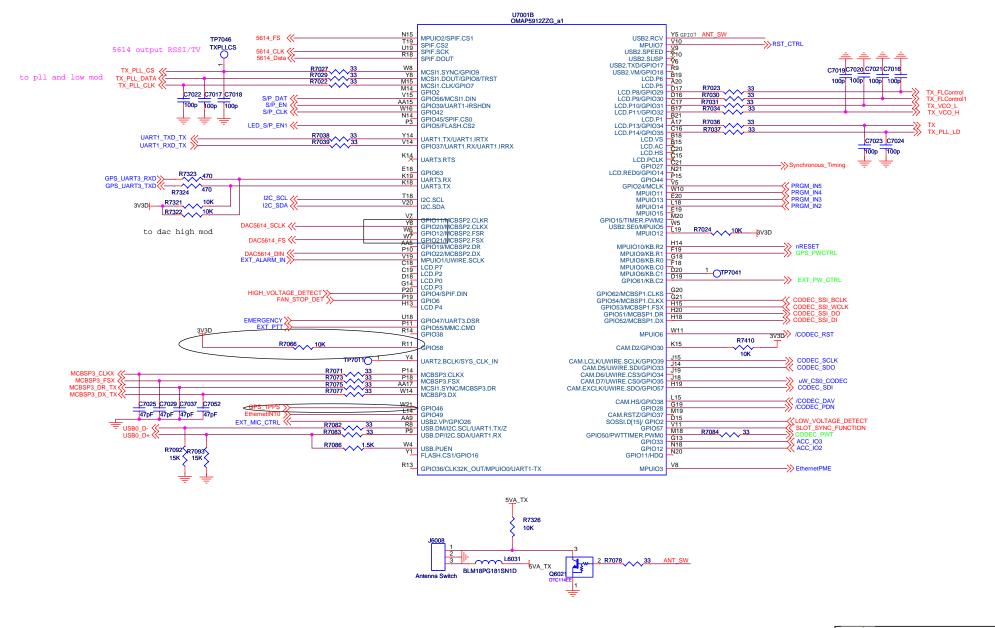


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Omap connect Peripheral_RX U7004B OMAP5912ZZG_a2 5 R7147 N14 SPIF.CS0 LCD.P0 B21 LCD.P1 B21 LCD.P1 C10 LCD.P2 C14 LCD.P3 C14 LCD.P3 C14 LCD.P4 C10 LCD.P6 C18 LCD.P6 C18 LCD.P7 D17 LCD.P8(P1029 D16 LCD.P9(P1030 C17 LCD.P11/GP1031 B17 LCD.P13/GP1034 C16 LCD.P13/GP1034 C16 LCD.P14/GP1035 R13 GP1036 M19 CAM.RSIZ/GP1037 L16 CAM.RSIZ/GP1037 L16 N15 SPIF.CS0 T19 SPIF.CS1 U19 SPIF.CS2 SPIF.SCK SPIF.DOUT SPIF.DIN V11 GPIO57/MMC.CLK V6 R9 GPIO17/UART2.TX C7104 GPIO18/UART2.RX RX_PLL_CLK RX_PLL_CS RX_PLL_DATA GPIO7/MCSI1.CLK GPIO9/MCSI1.SYNC MCSI1.DOUT/GPIO8/TRST UART1_TXD_RX UART1_RXD_RX HART1 TX/HART1 IRTX GPIO37/UART1.RX/UART1.IRRX CAM.RSTZ/GPIO37 CAM.HS/GPIO38 AA15 Synchronous_Timing GPIO2 CAM.HS/GPIO38 AAIS (PIO39 CONTROL OF CONTROL UART3.RTS UART3.DSR UART3.DTR K19 UART3.DTK UART3.RX UART3.TX C7105 C7106 C7107 I2C.SCL 100p 100p 100p GPIO48/I2C.SDA R7157 98_CLKOUT >> GPIO11/MCBSP2.CLKR GPIO20/MCBSP2.CLKX GPIO12/MCBSP2.FSR R7158 33 98_FS >> GPIO21/MCBSP2.FSX KB.C0/MPUIO0 KB.C1/MPUIO6 R7159 33 98 DOUTA >> GPIO19/MCBSP2.DR GPIO22/MCBSP2.DX V19 MPUIO1 MPUIO5/MPUIO12 MPUIO3/MMC2.DAT1 KB.C5/GPIO28 G19 GPIO7/MCSI2.SYNC GPIO62/MCBSP1.CLKS GPIO62/MCBSP1.CLKS GPIO54/MCBSP1.CLKX GPIO53/MCBSP1.FSX GPIO54/MCBSP1.FSX H20 D7015 KPT-1608SGC TP7044 () R7163 2.2K GPIO51/MCBSP1.DR GPIO52/MCBSP1.DX GPIO26/MCSI2.DIN R7165 GPIO27 P11 W11 W10 MPUIO9/MPUIO6 R14 GPIO27/MCSI2.CLK GPIO38/UART1-IRSEL TP7045 🔾 D7016CPT-1608SGC CAM.D0/MPUIO12 L19 CAM.D2/GPIO30 K15 CAM.LCLK/UWIRE.SCLK/GPIO39 J14 CAM.D5/UWIRE.SDI/GPIO33 J19 Y4 TP7030 1 UART2.BCLK/SYS_CLK_IN P14 MCBSP3.CLKX/GPIO43 MCBSP3.FSX/GPIO3/SPI.CS3 CAM.D5/UWIRE.CS3/GPIO34 CAM.D6/UWIRE.CS3/GPIO34 CAM.D7/UWIRE.CS0/GPIO35 RX_FLControl MCBSP3 CLKX (MCBSP3_FSX (/WIRE.CS0/GPIU050 CAM.VS/MPUI014 H19 GPI057 R11 W14 MCSI1.SYNC/MCBSP3.DR MCBSP3.DX R7171 33 ->> RX_PLL_LD R7171 GPIO58 TP7031 TP7032 GPIO6/MCSI1.DIN/TIMER.EVENT3 GPIO42/MCBSP3.CLKX GPIO12 GPIO56 D19 V15 R7172 33 R7174 33 RX_VCO_H USB.DM/I2C.SCL/UART1.TX/Z USB.DP/I2C.SDA/UART1.RX GPIO50/PWTTIMER.PWM0 L14 C7113C7110C7111C7112 C7114C7125 GPIO50/PWTTIMER.PWM1 N20 GPIO11/HDQ Y5 R7176 1.5K W4 C7115 C7116 USB.PUEN 100p 100p 100p 100p 100p NC L1 E5 GPIO1/JUART2.RCV GPIO15/PWTTIMER.PWM2 GPIO44/UWIRE.CS3/SPIF.CS3 NC NC1

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| Model Name: | DMR RD620 U1 Pa | | | Page: | 12 of | 19 | 1 | |
| Part Name: | Omap_Connect_Peripheral_RX | | | Date: | Wednesday, July 03, 2013 | | | |
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Omap connect Peripheral_TX



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| Model Name: | DMR RD620 U1 Page: 13 of | | | 19 |) | | | |
| Part Name: | Omap_Connect_Peripheral_TX D | | | Date: | Wednesday, July 03, 2013 | | | |
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| Prepare: | | Check: | | | Approve: | | | |

