

Name	Description	Voltage(normal)
DVDD018	1.8V Logic Supply	1.8
DVDD3318_A	Power-on Reset A,B,C	
DVDD3318_B	Dual-voltage IO Supply	3.3
DVDD3318_C	Voltage	
DDR_DVDD18	DDR2 PHY Supply Voltage	1.8
PILLO_VDDA	PILLO Supply Voltage	1.2
PLL1_VDDA	PLL1 Supply Voltage	1.2
CVD2	Core Logic Supply Voltage (variable)	1.2
RVDD	Internal RAM Supply Voltage	1.2
USB0_VDDA33	USB0 PHY Supply Voltage	3.3
USB0_VDDA18	USB0 PHY Supply Voltage	1.8
USB0_VDDA1	USB0 PHY 1.2-V LDO output	---
USI1_CVDD	USI1 PHY Core Logic Supply Voltage	1.2
USI1_VDDA33	USI1 PHY Supply Voltage	3.3
USI1_VDDA18	USI1 PHY Supply Voltage	1.8
SATA_VDDA	SATA Core Logic Supply Voltage	1.2
SATA_VDDR	SATA Internal Regulator Supply Voltage	1.8
SATA_REG	SATA PHY Regulator output	---
DDR_VREF2	DDR2/DDDR reference voltage	0.9
RTC_CVDD	RTC Core Logic Supply Voltage	1.2

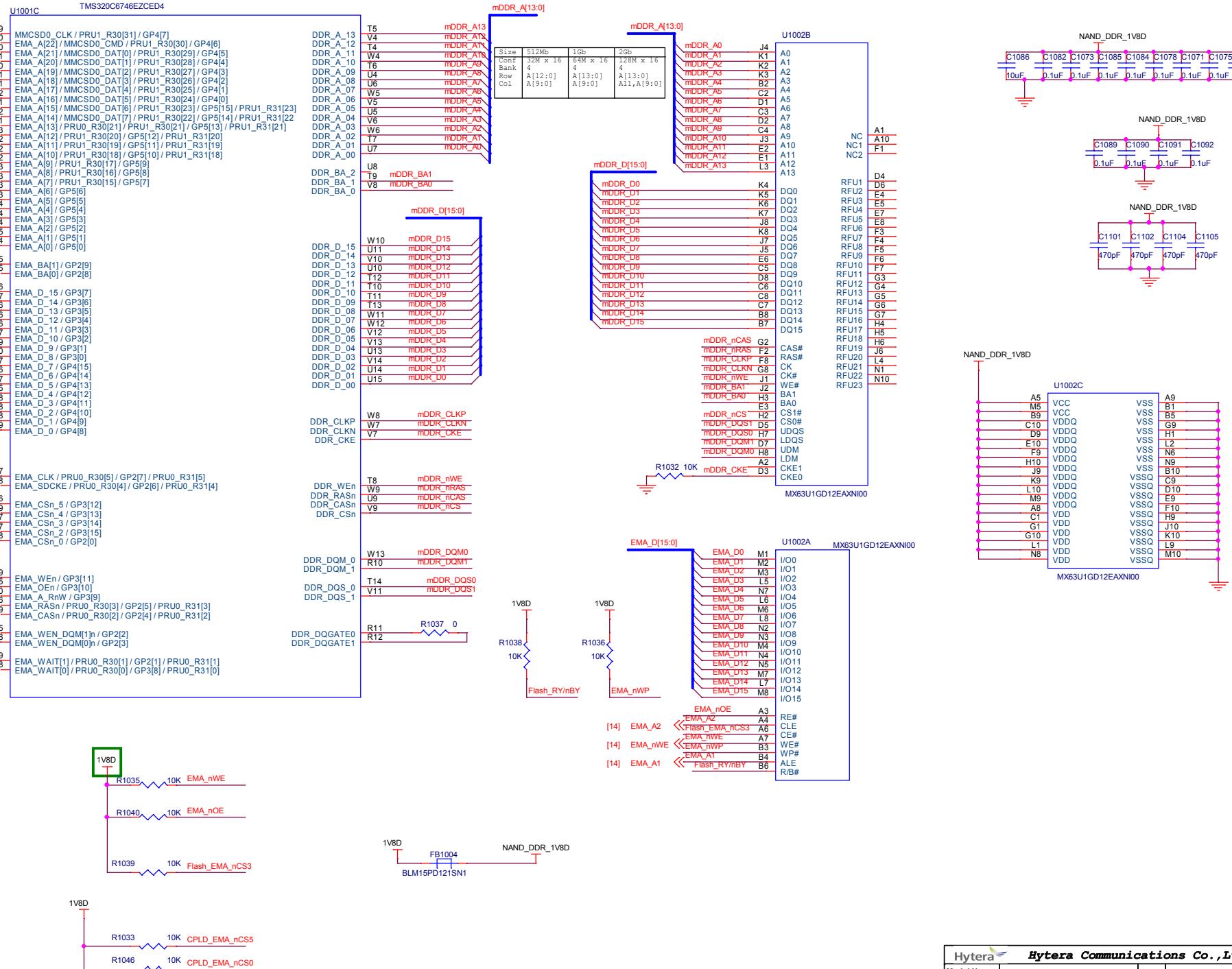
<b>Hytera</b> <i>Hytera Communications Co.,Ltd.</i>			
<b>Model Name:</b>	PTC680	<b>Page:</b>	3 of 68
<b>Part Name:</b>	1.BB_DSP: POWER	<b>Date:</b>	Friday, May 10, 2019
<b>File No.:</b>		<b>Rev:</b>	A
<b>Prepare:</b>	<b>Check:</b>	<b>Approve:</b>	

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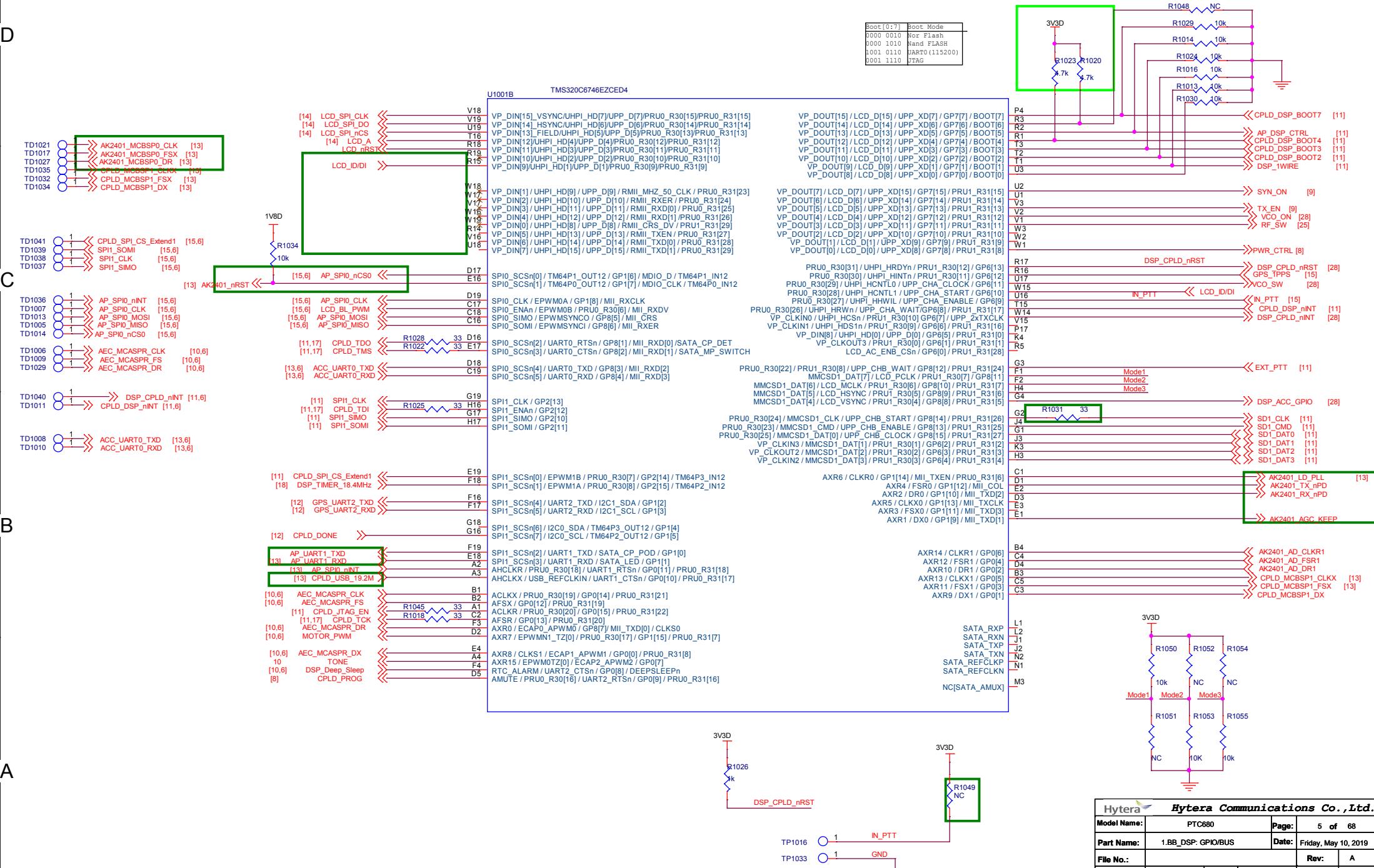
5

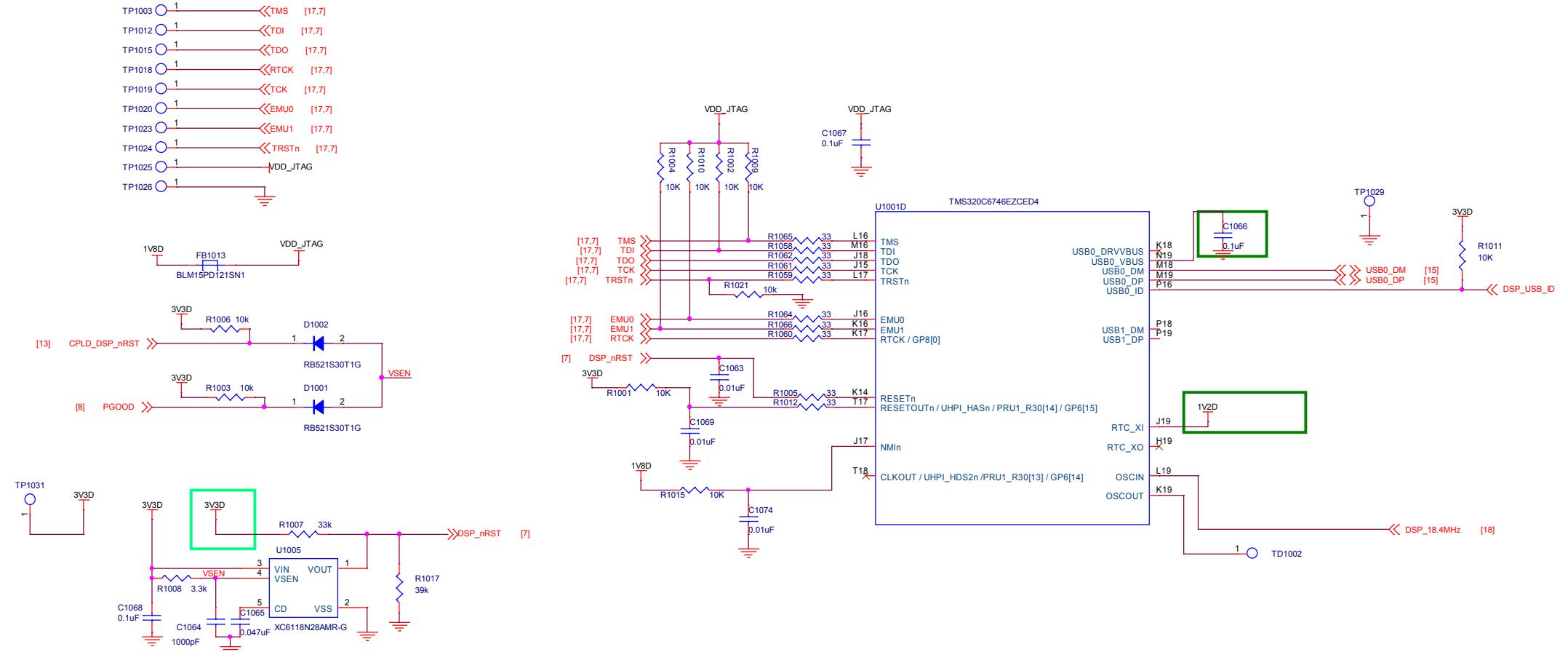
4

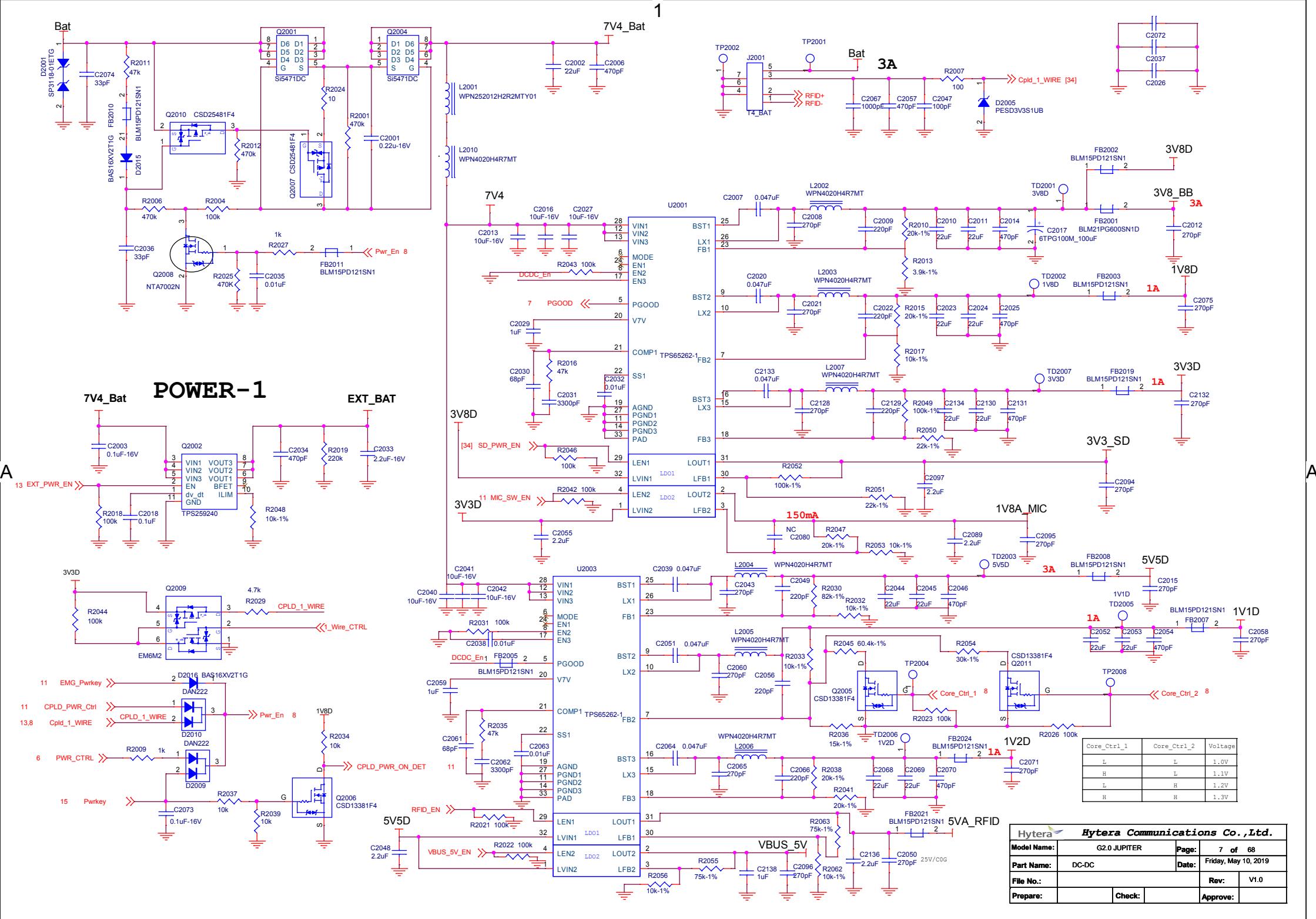
3

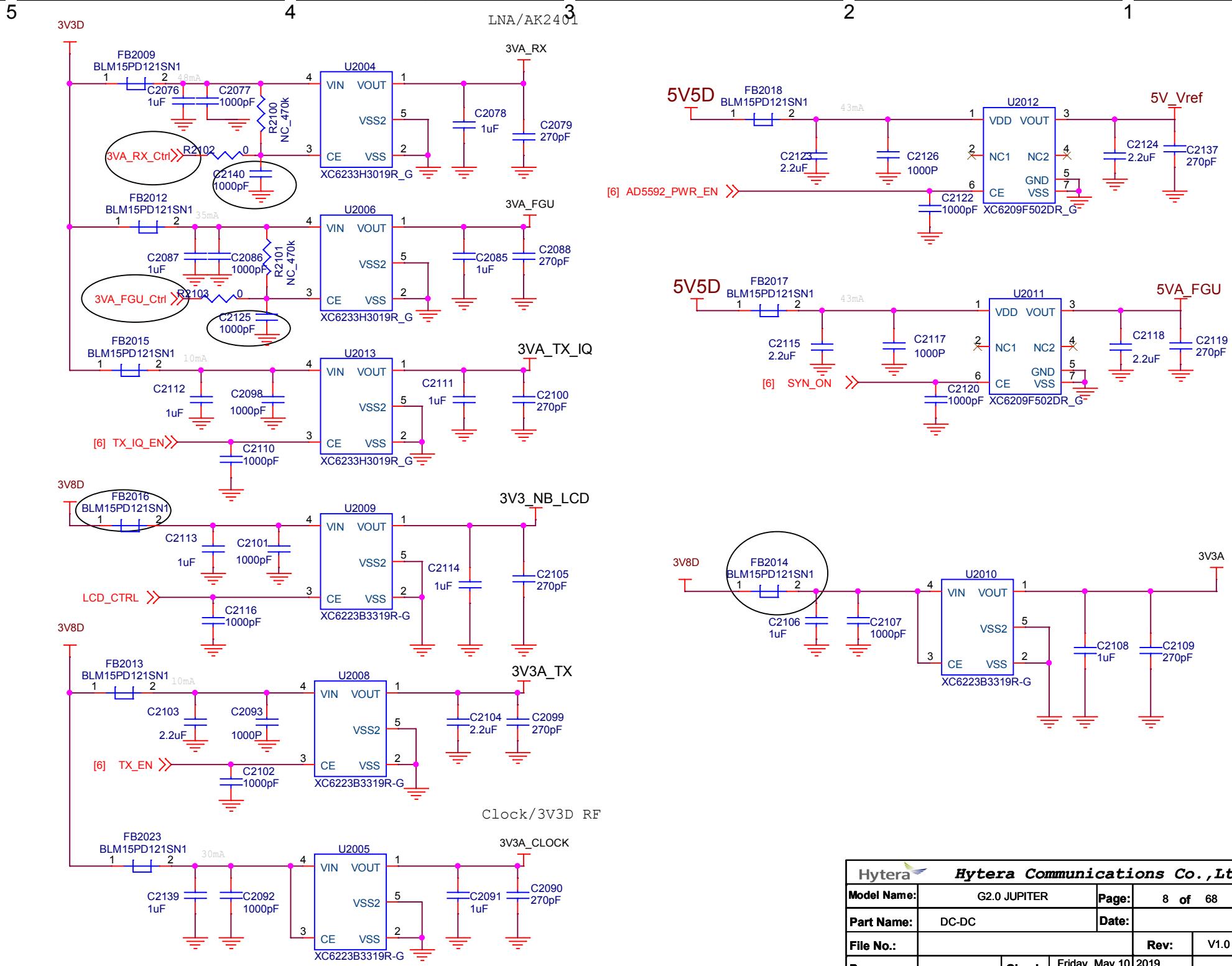
2

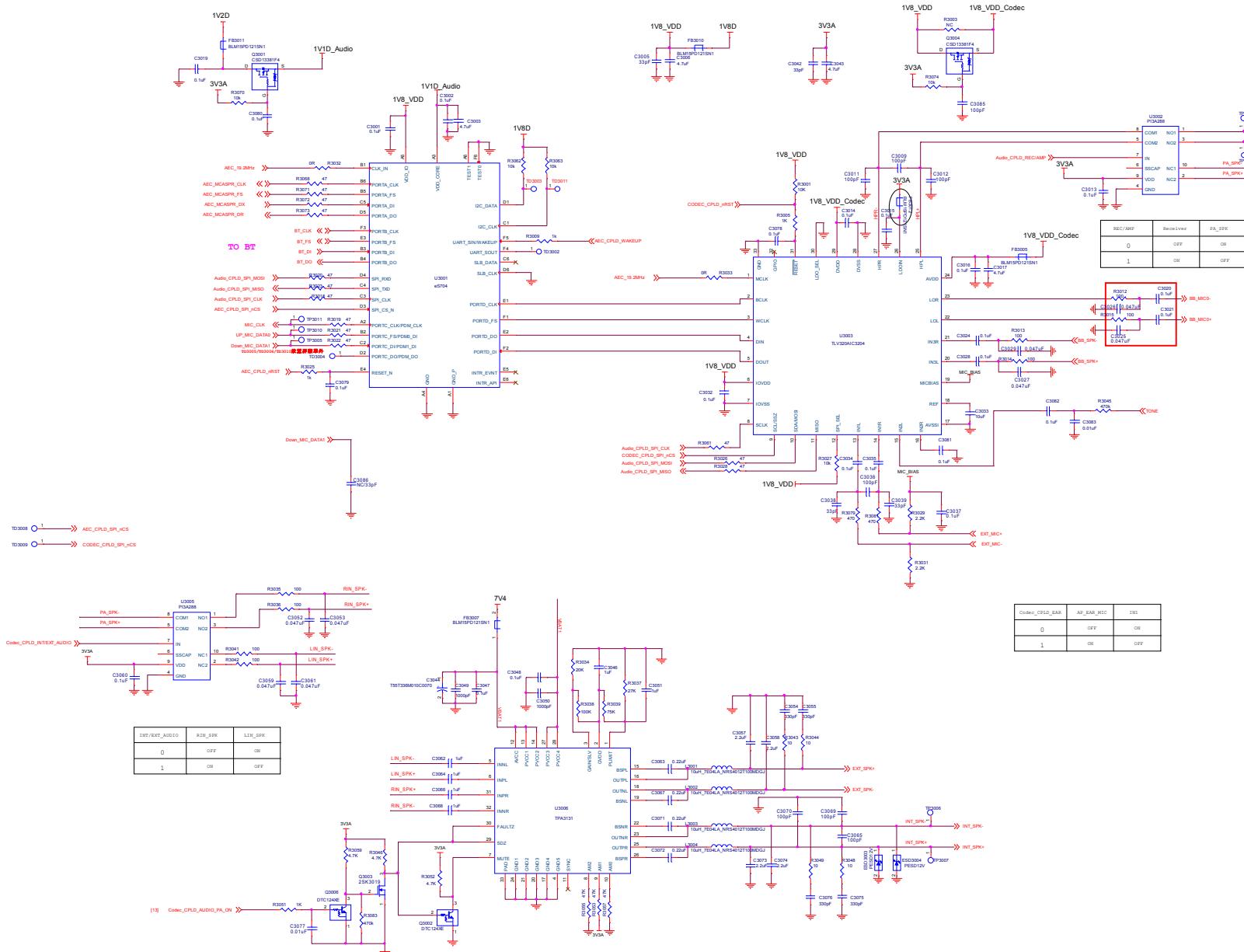
1



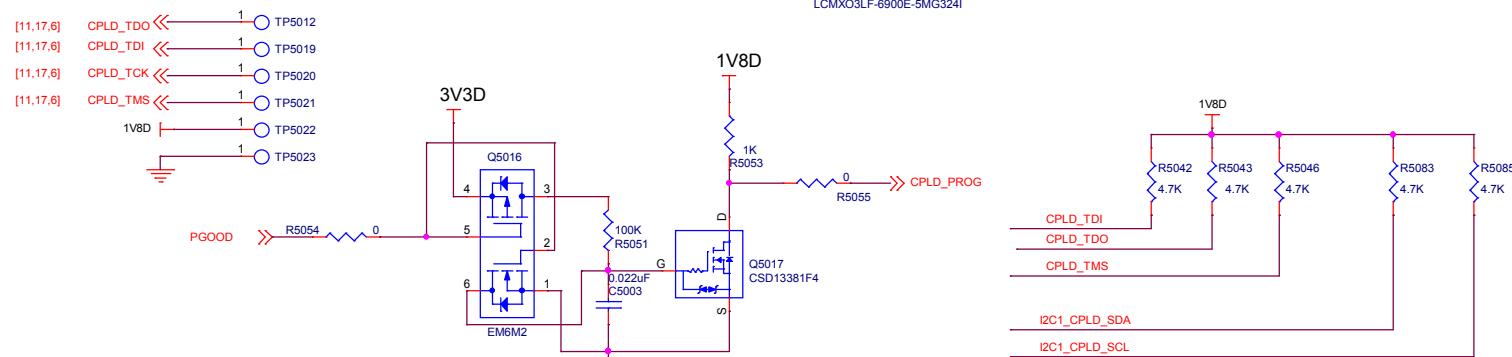
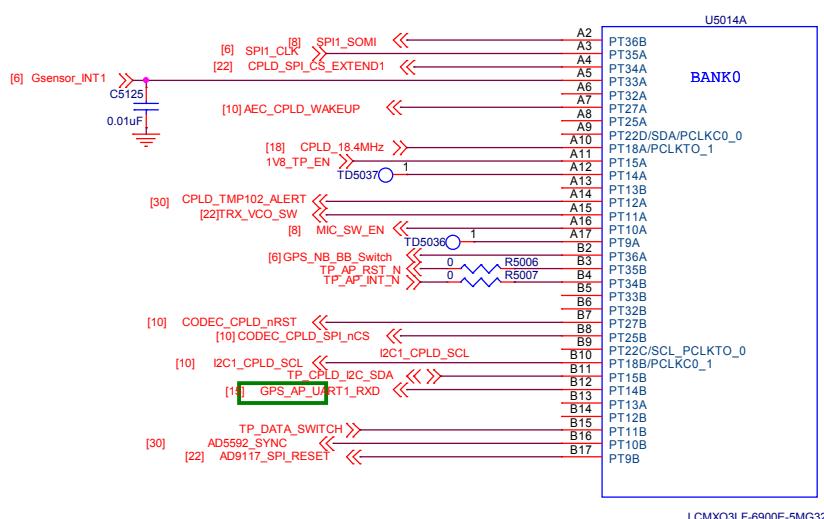
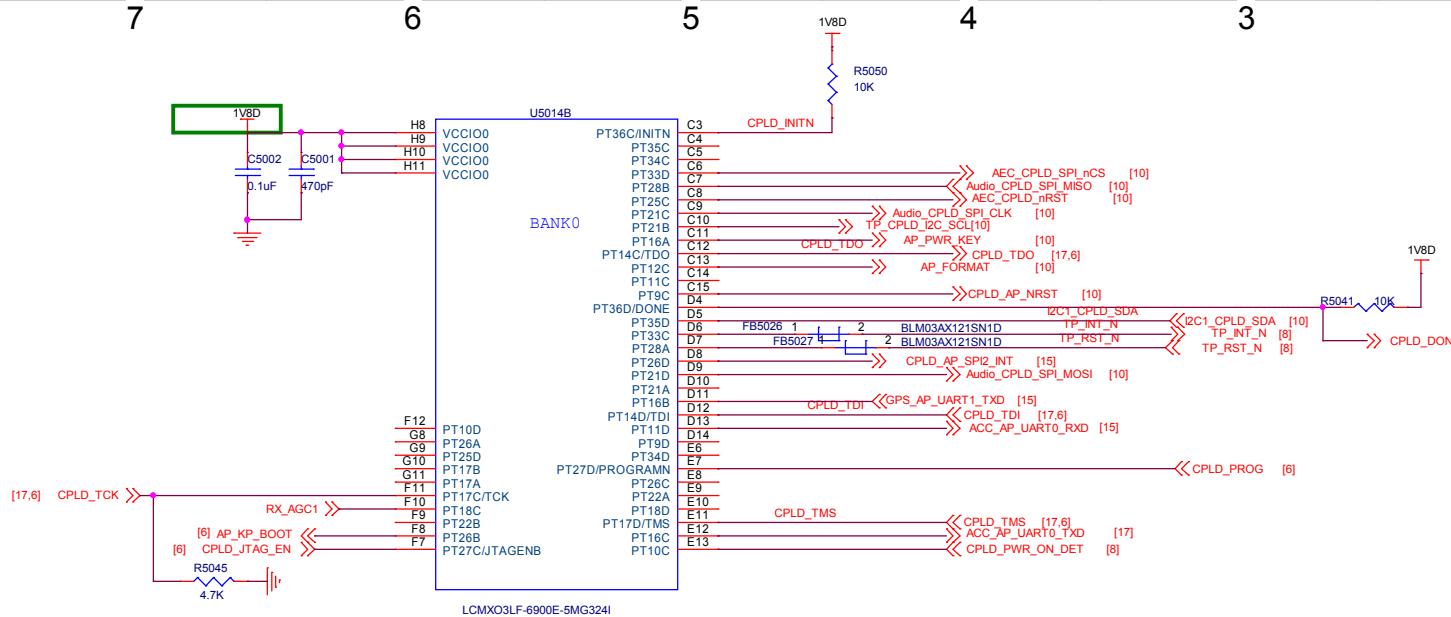


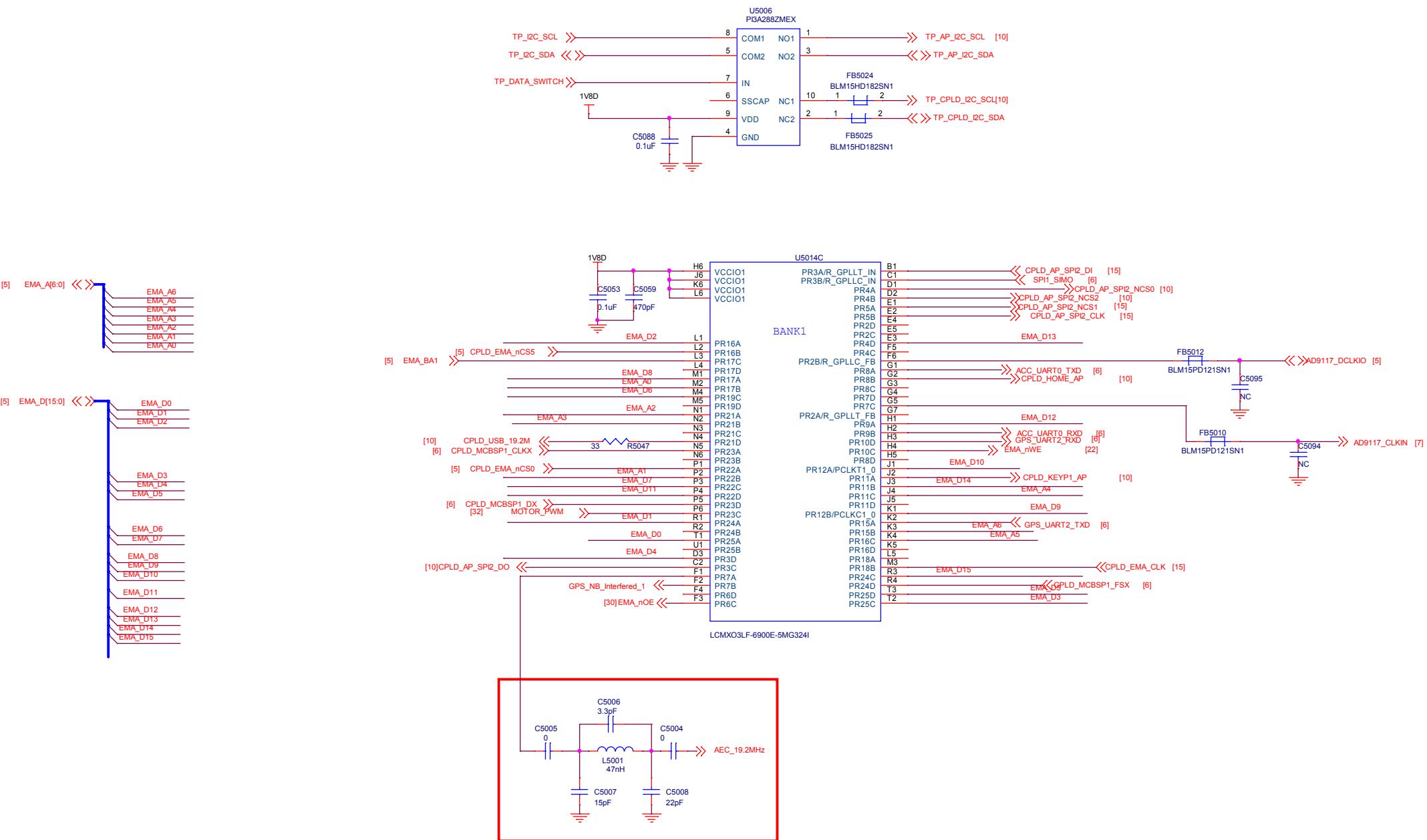


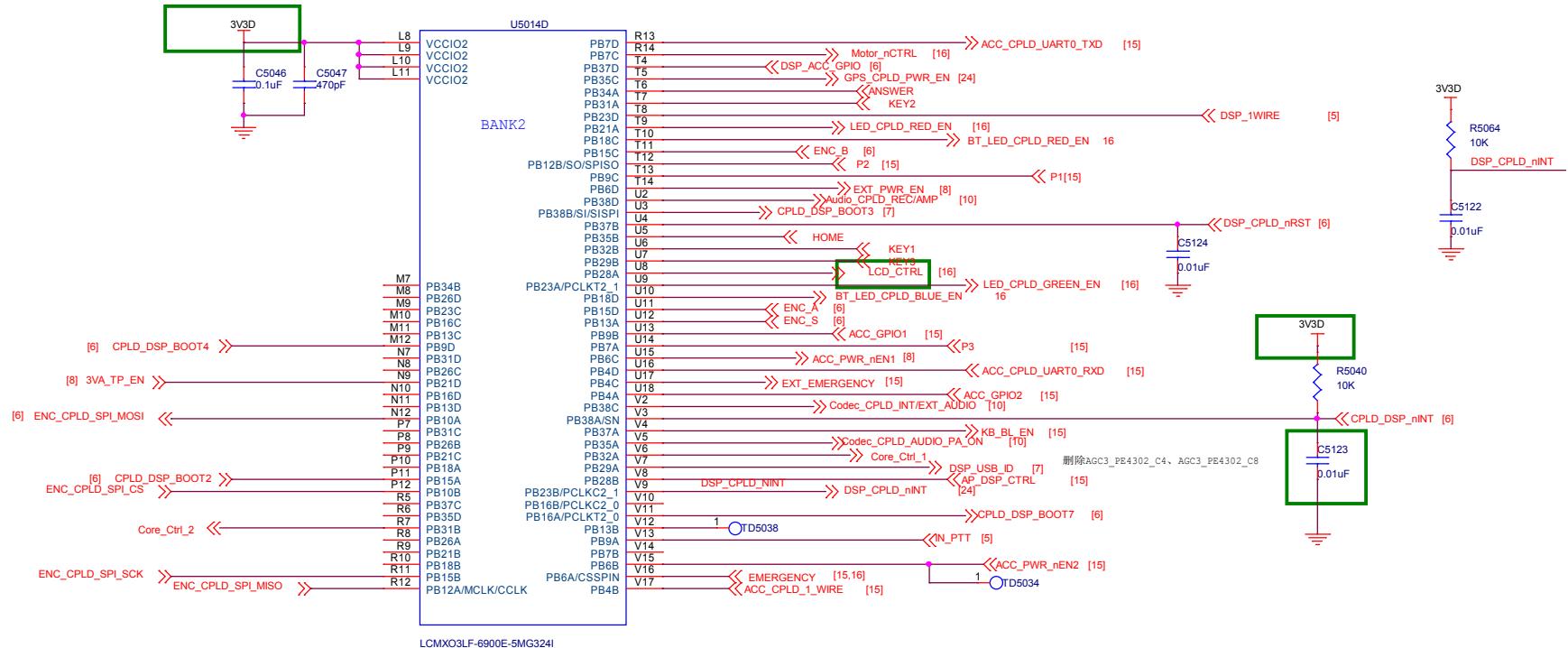


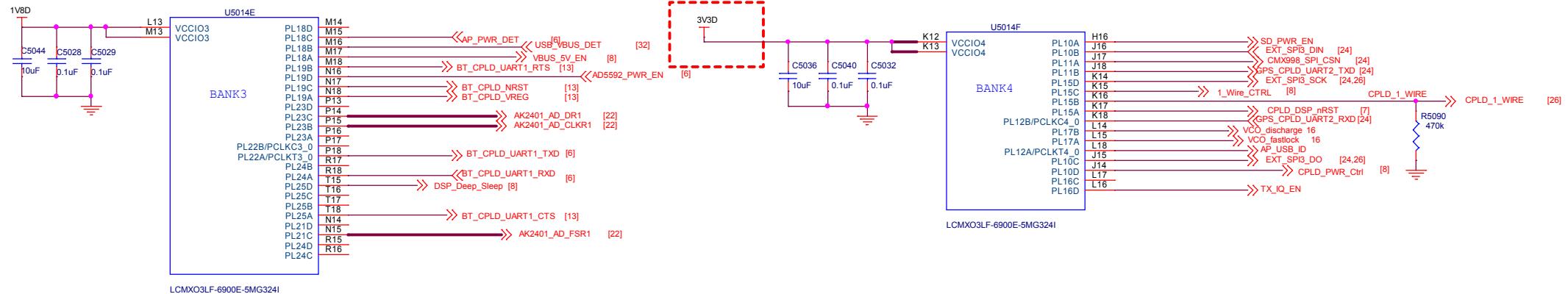


<b>Hytera Communications Co.,Ltd.</b>			
<b>Model Name:</b>	PDC680 NB	<b>Page:</b>	9 of 68
<b>Part Name:</b>	BB_Audio	<b>Date:</b>	Friday, May 10, 2019
<b>File No.:</b>			<b>Rev.:</b> A3
<b>Prepare:</b>	FJT	<b>Check:</b>	<b>Approve:</b>

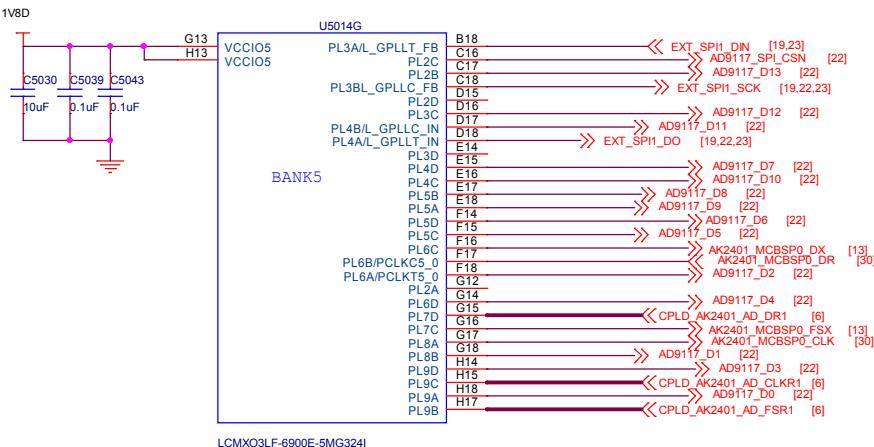




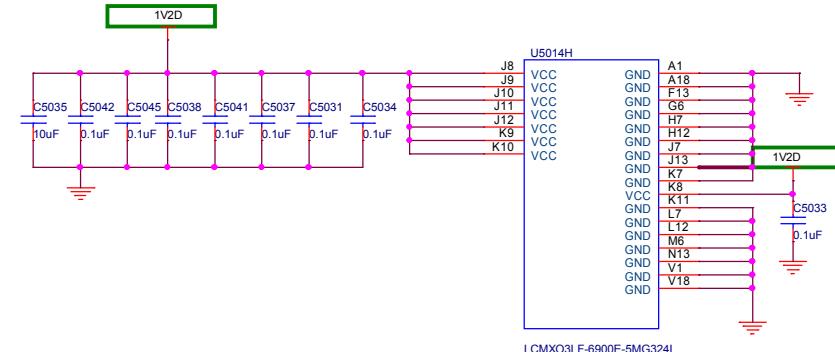


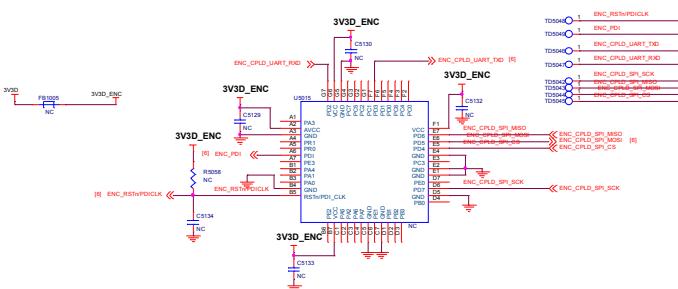


LCMXO3LF-6900E-5MG324I

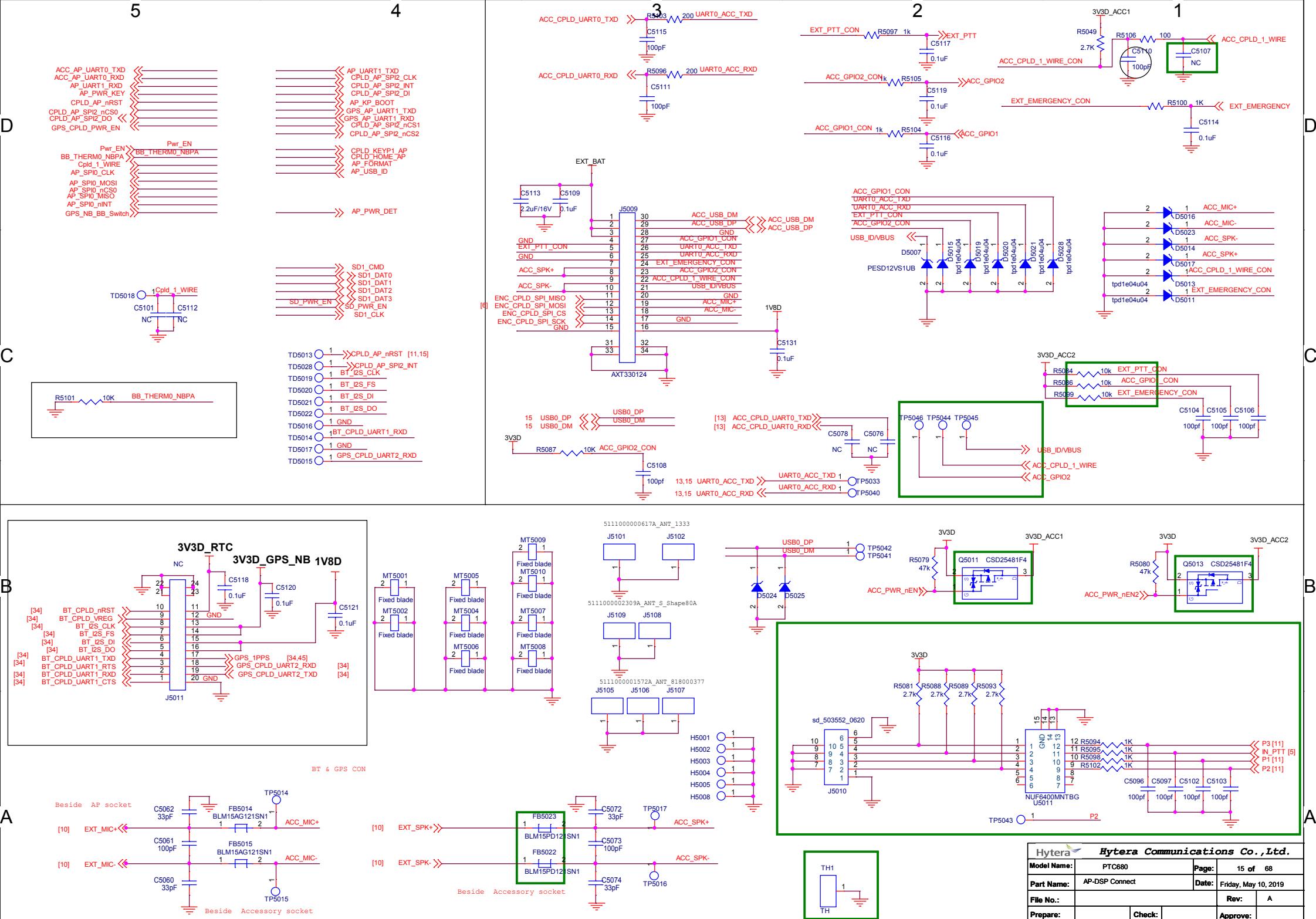


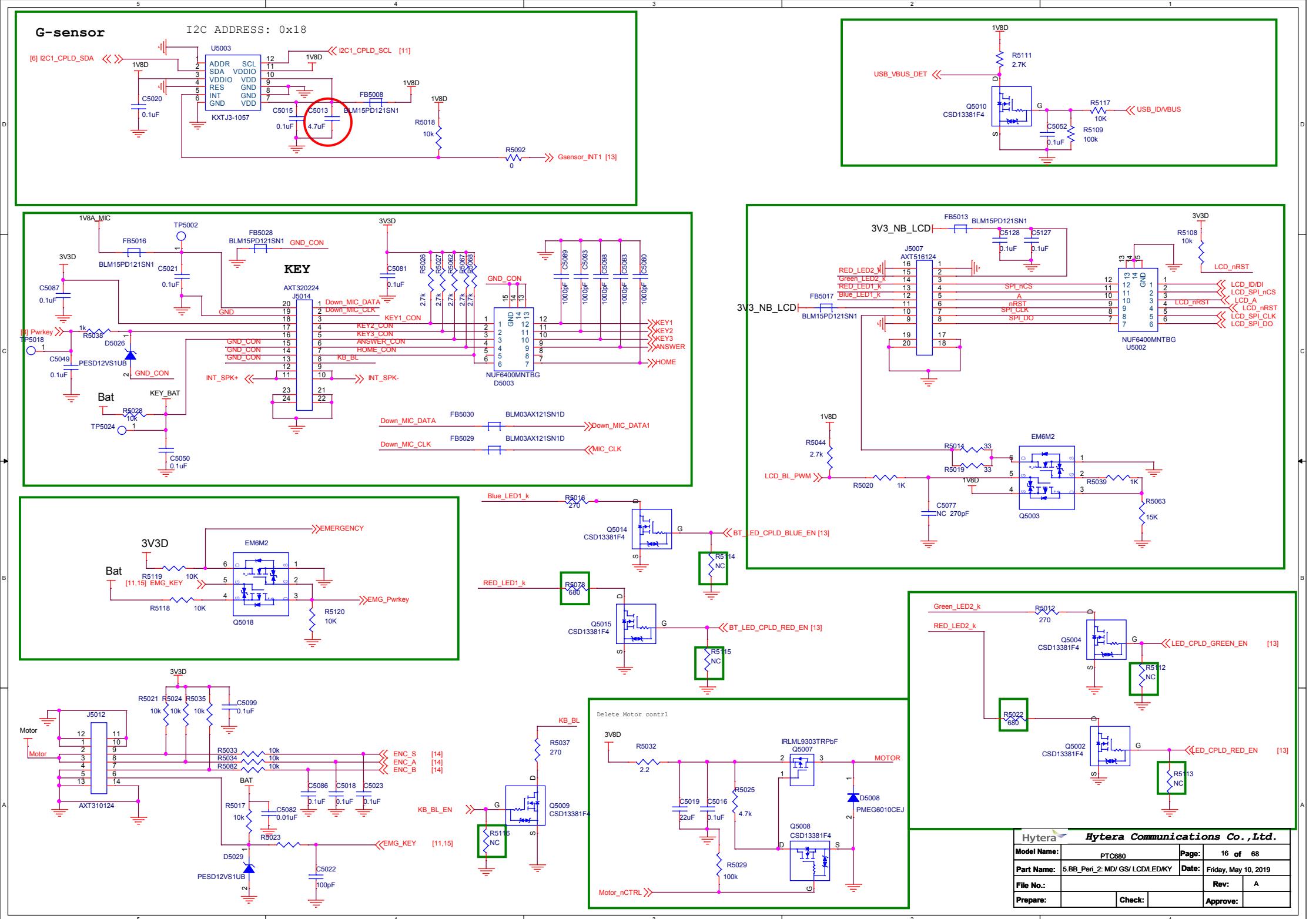
LCMXO3LF-6900E-5MG324I

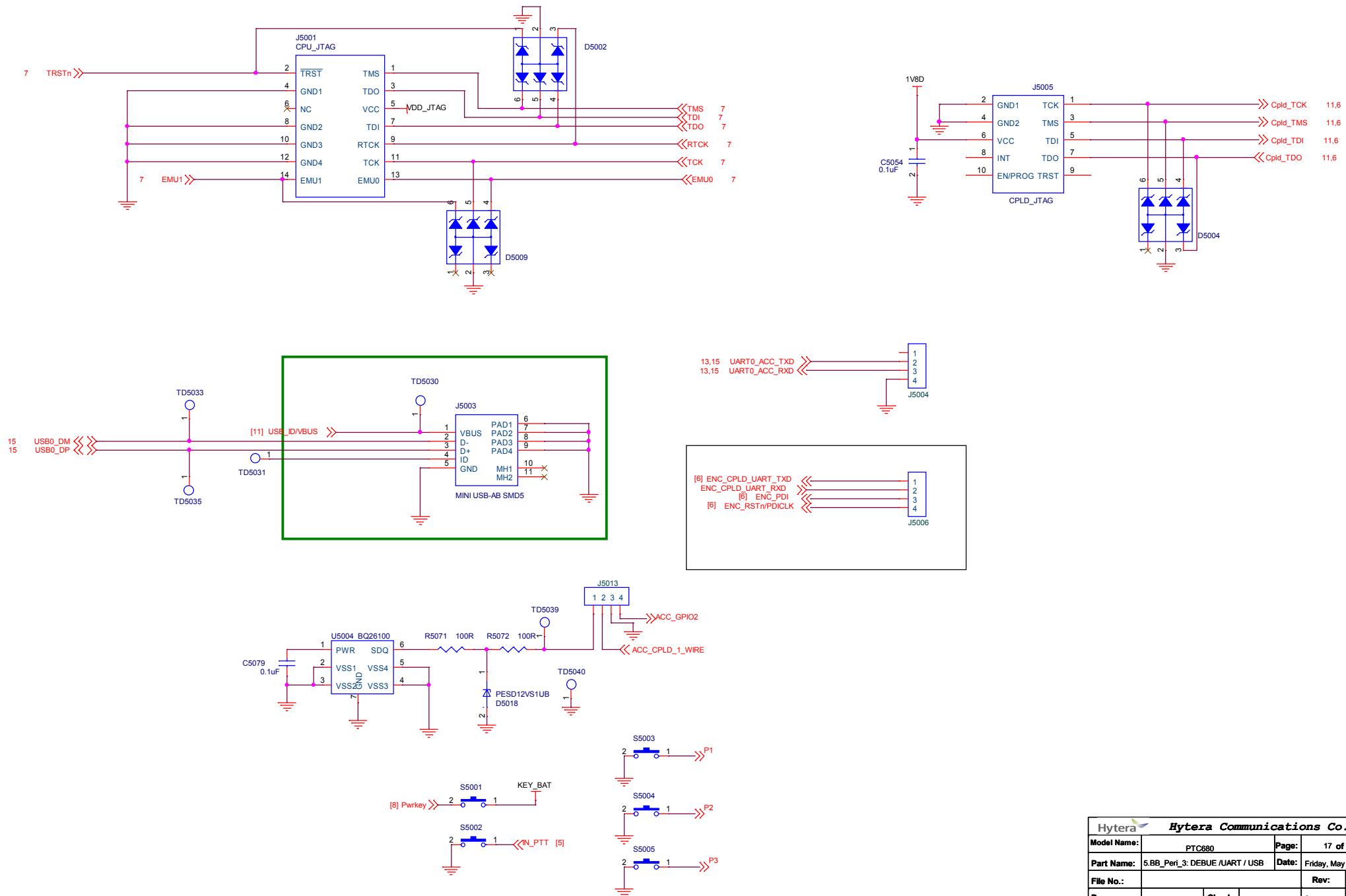




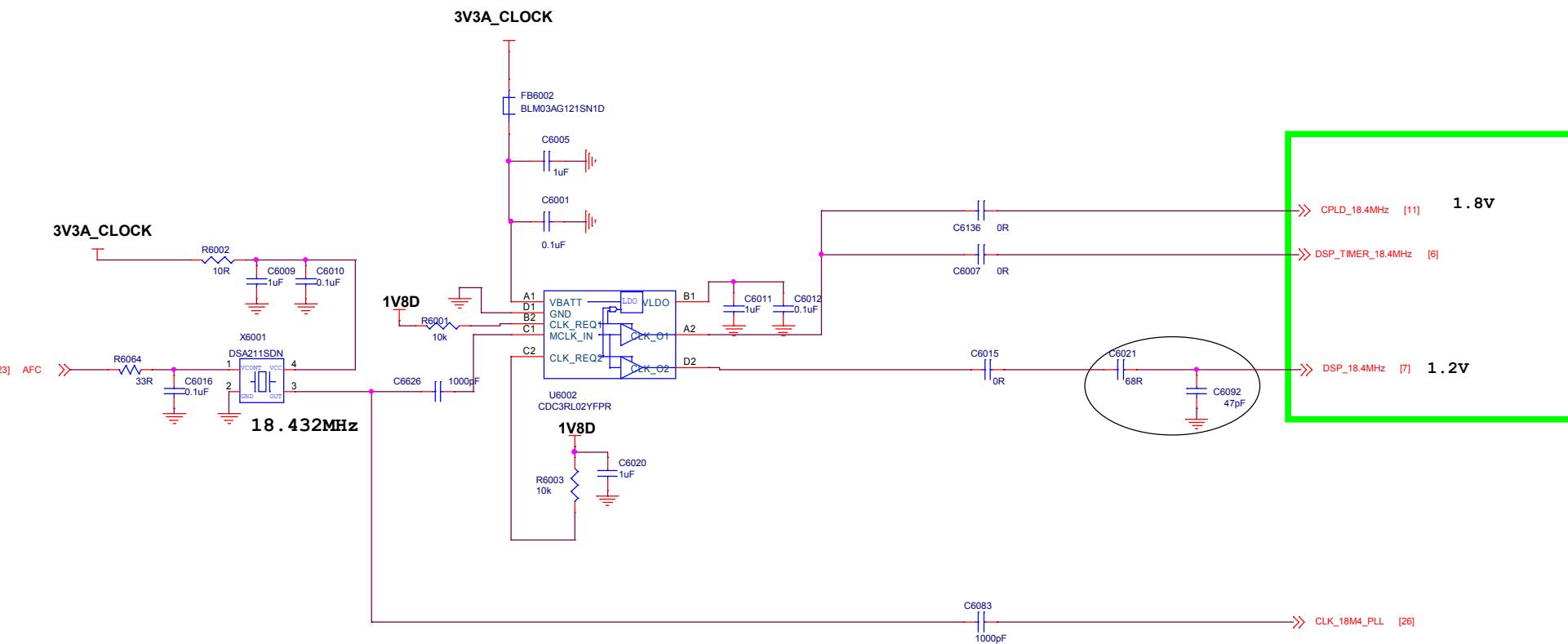
Hytera Communications Co.,Ltd.			
Model Name:	PTC680	Page:	14 of 68
Part Name:	5.BB_MAXQ1850	Date:	Friday, May 10, 2019
File No.:		Rev.:	A
Prepare:		Check:	Approve:

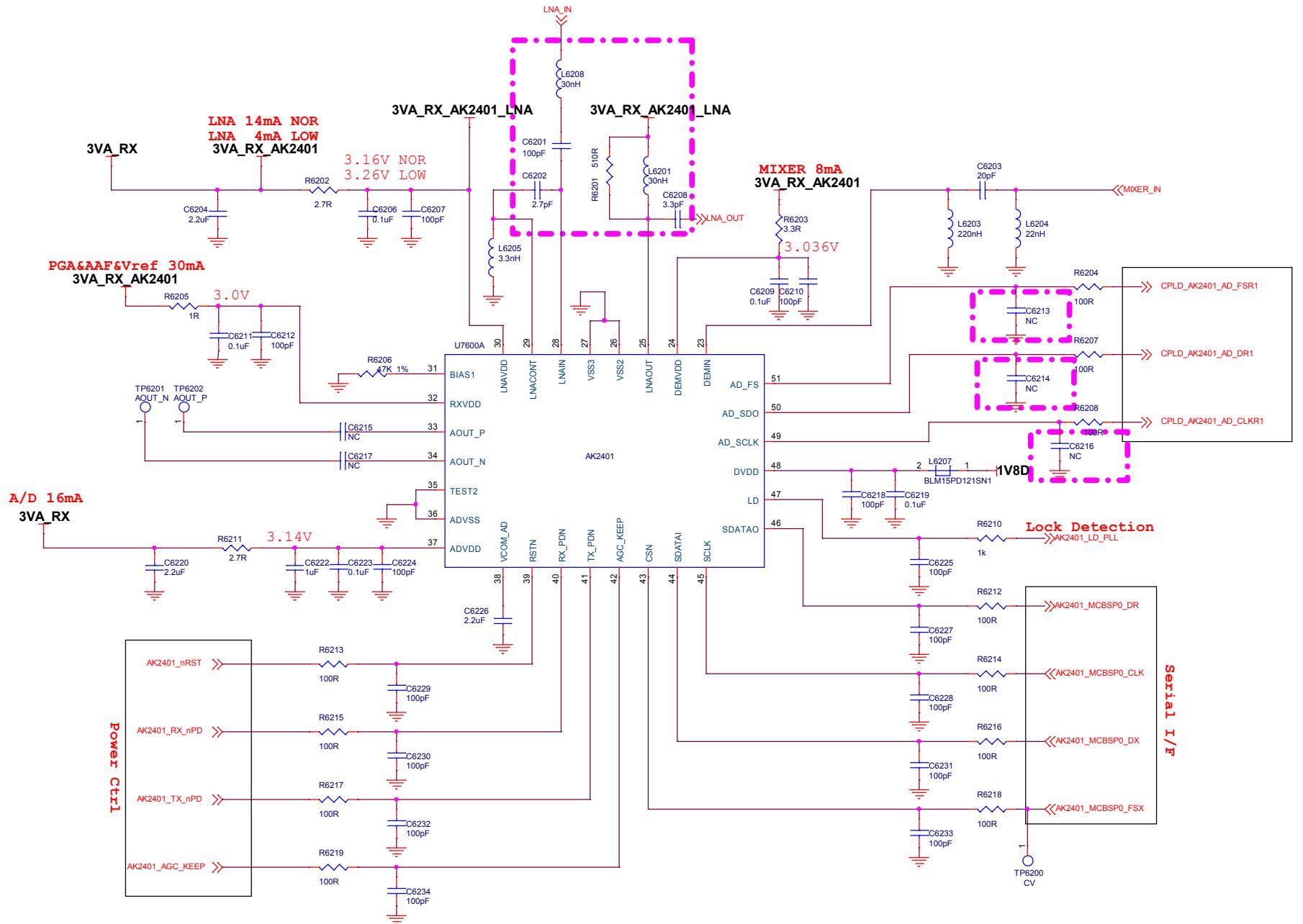






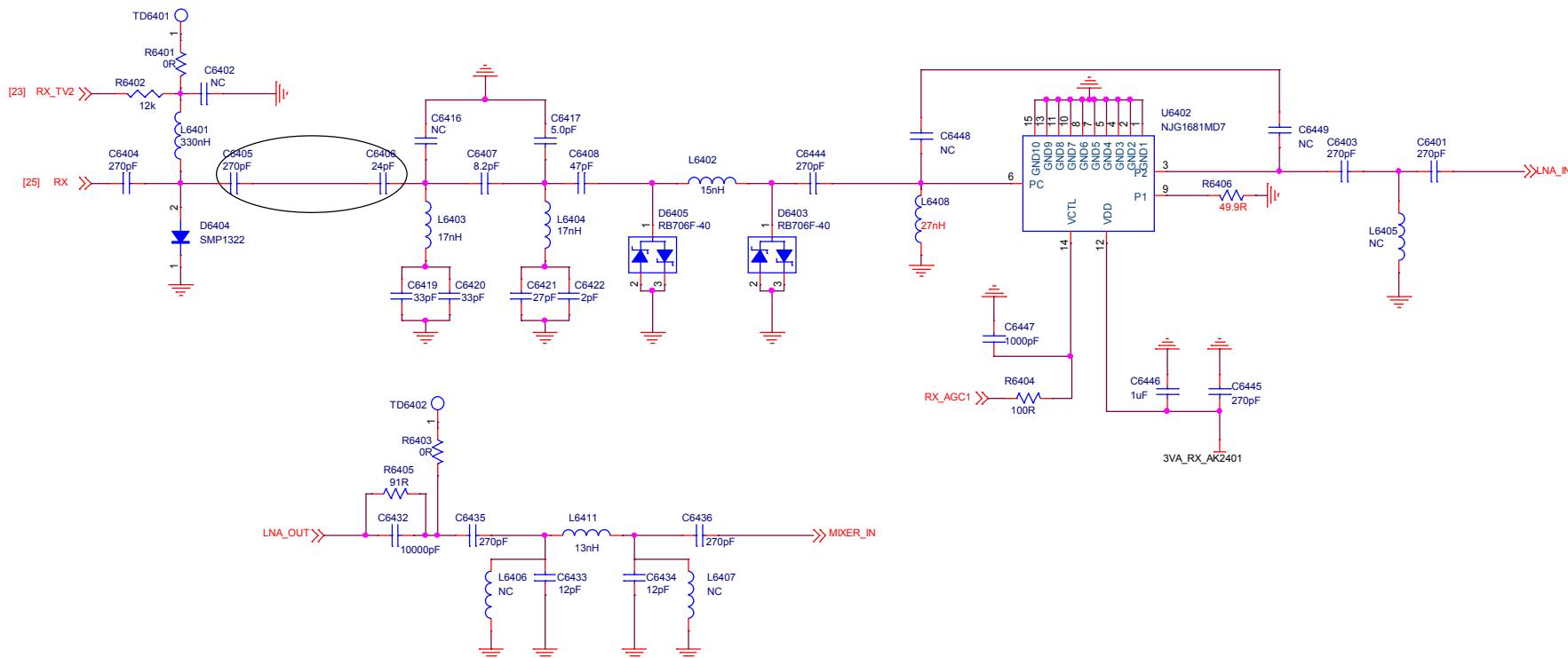
D

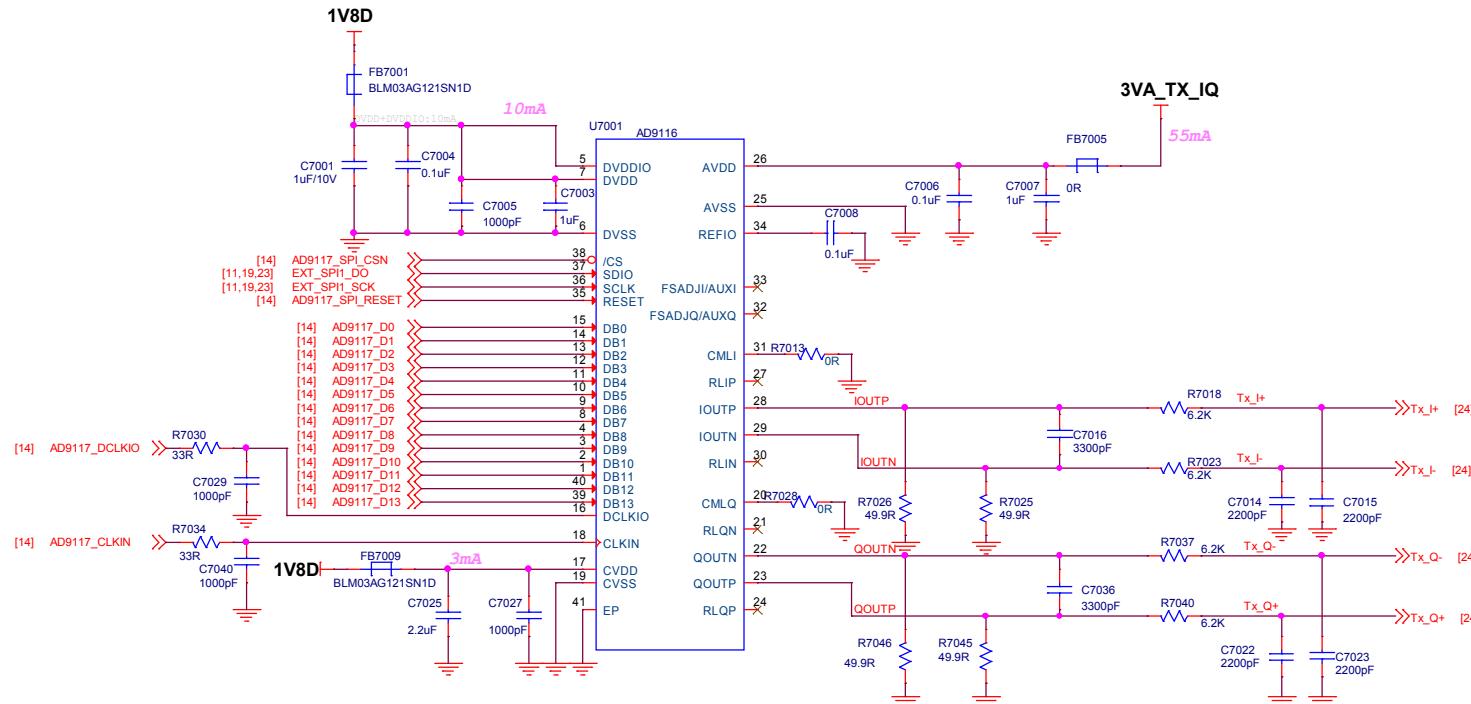




Hytera Communications Co.,Ltd.			
Model Name:	G2.0	Page:	19 of 68
Part Name:	RX-BACK-END	Date:	Friday, May 10, 2019
File No.:	<orgaddr>	Rev:	A01
Prepare:		Check:	
Approve:			

# RX-FRONT-END





Hytera Communications Co.,Ltd.			
Model Name:	G2.0 JUPITER	Page:	21 of 68
Part Name:	AD9117	Date:	Friday, May 10, 2019
File No.:	<orgaddr>	Rev:	A01
Prepare:	Check:	Approve:	

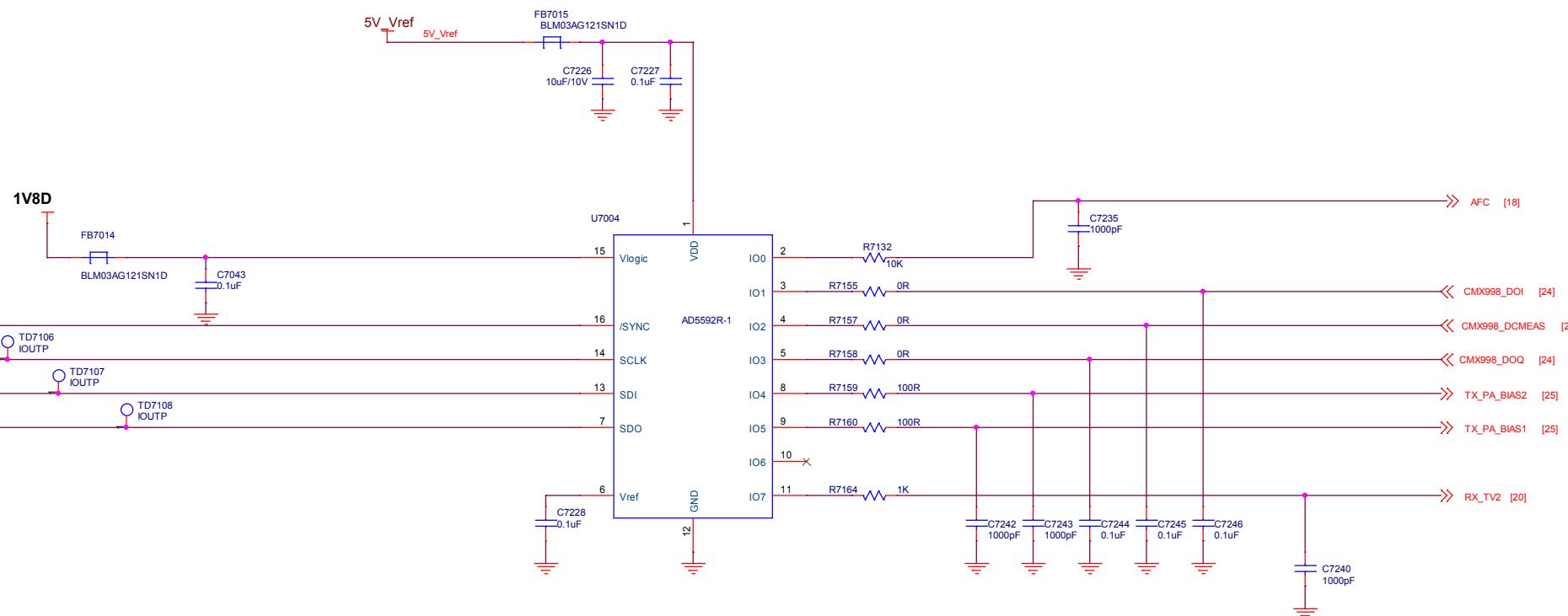
5

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1



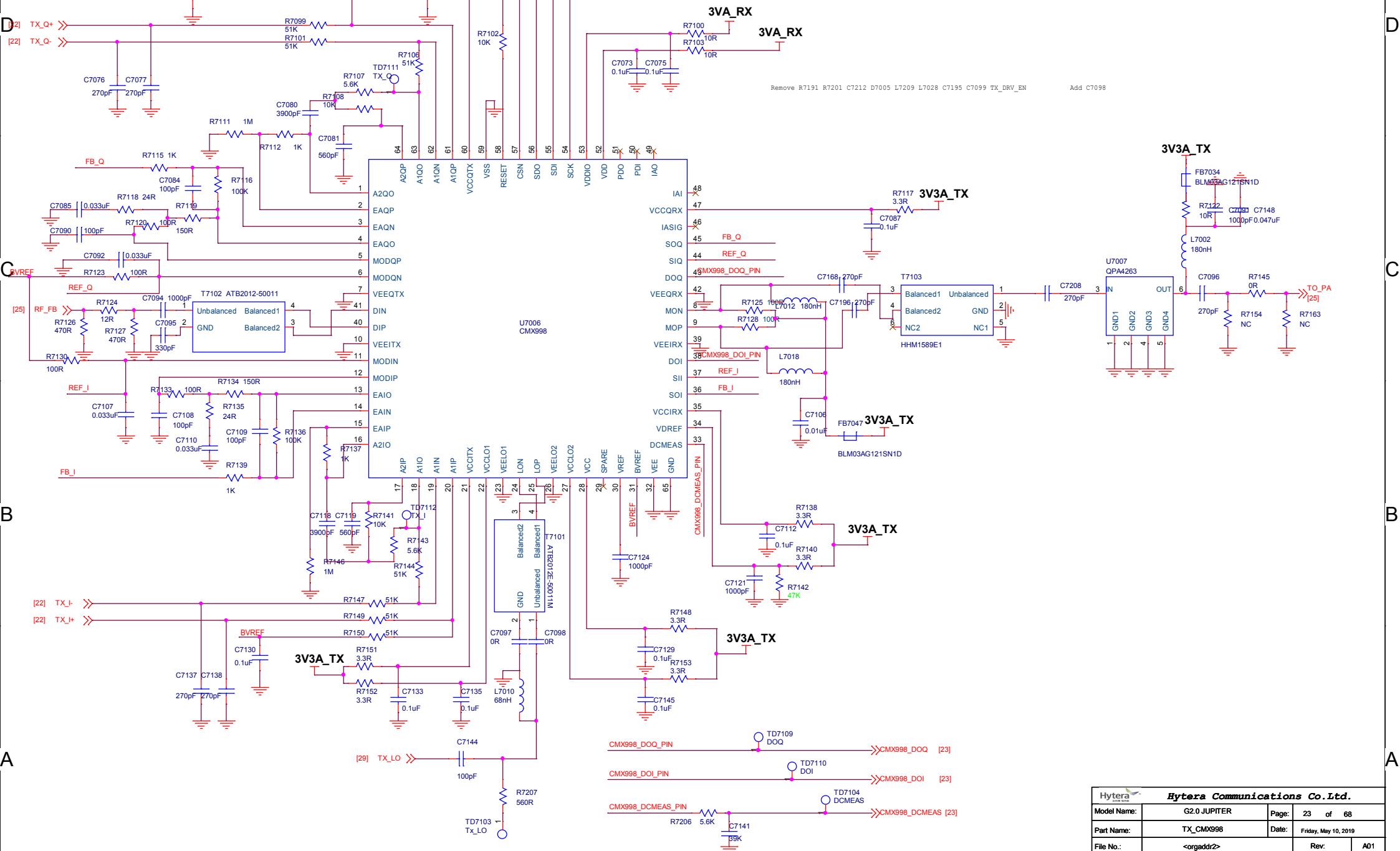
5

4

3

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1



Hytera Communications Co., Ltd.			
Model Name:	G2.0 JUPITER	Page:	23 of 68
Part Name:	TX_CMX998	Date:	Friday, May 10, 2019
File No.:	<orgaddr>	Rev:	A01
Prepare:	Check:	Approve:	

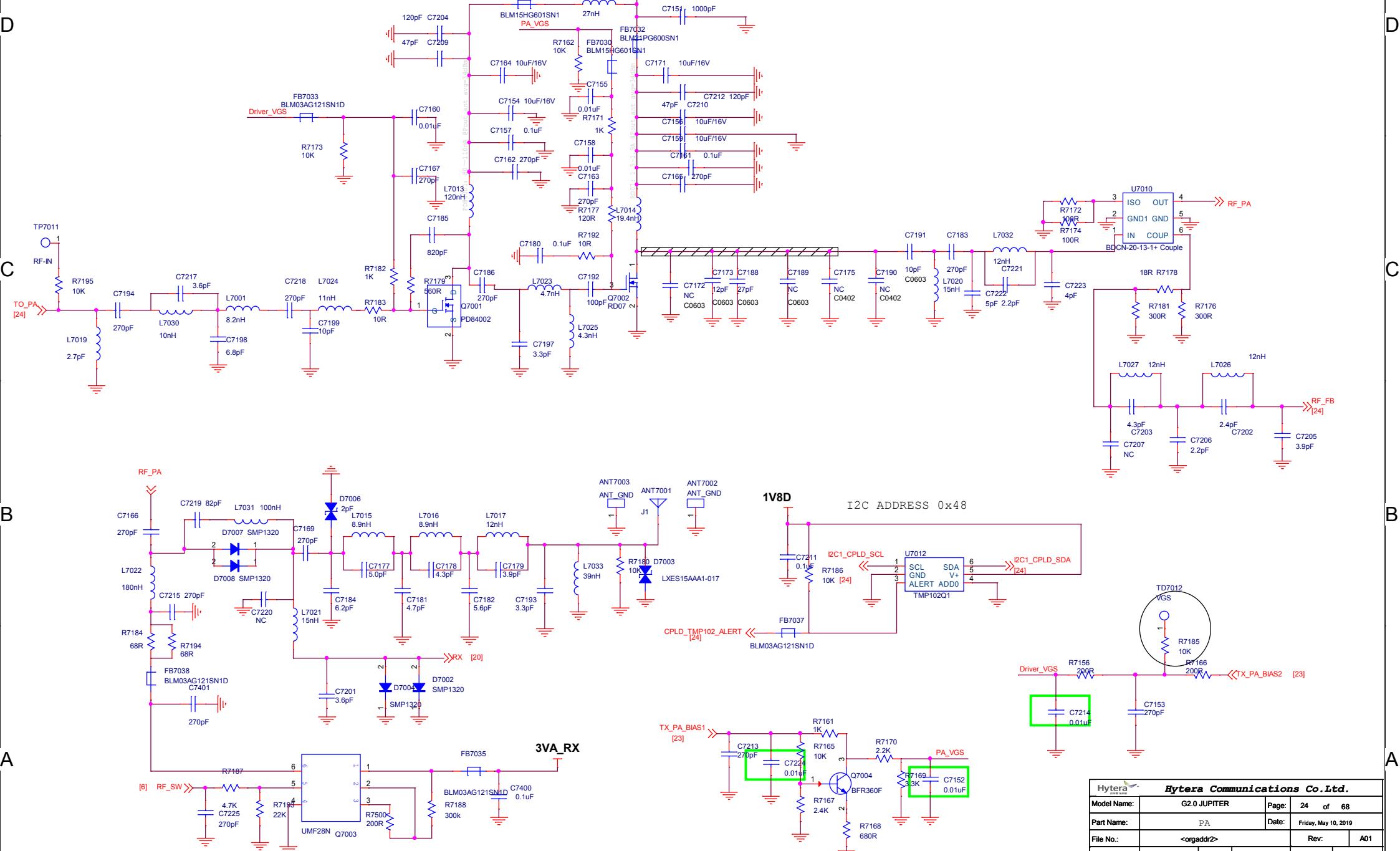
5

4

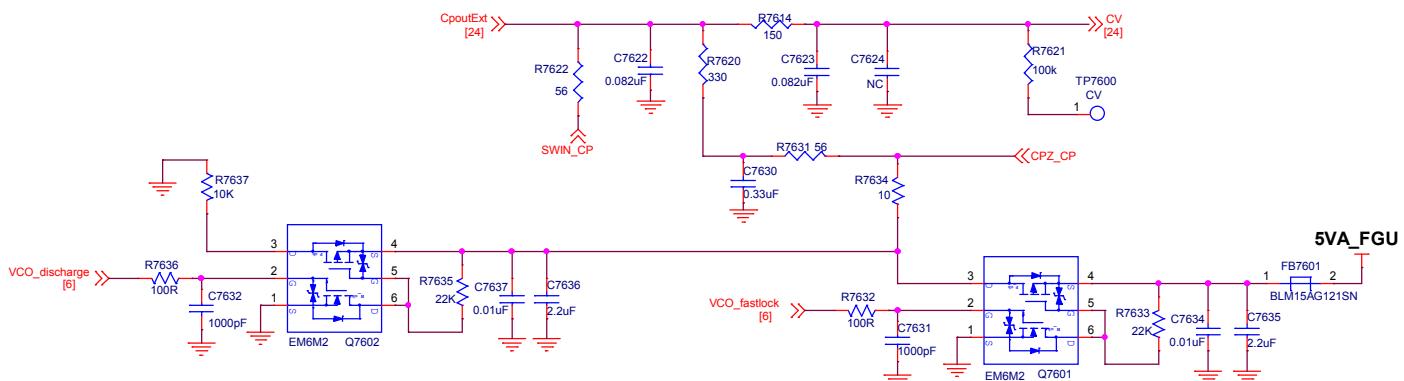
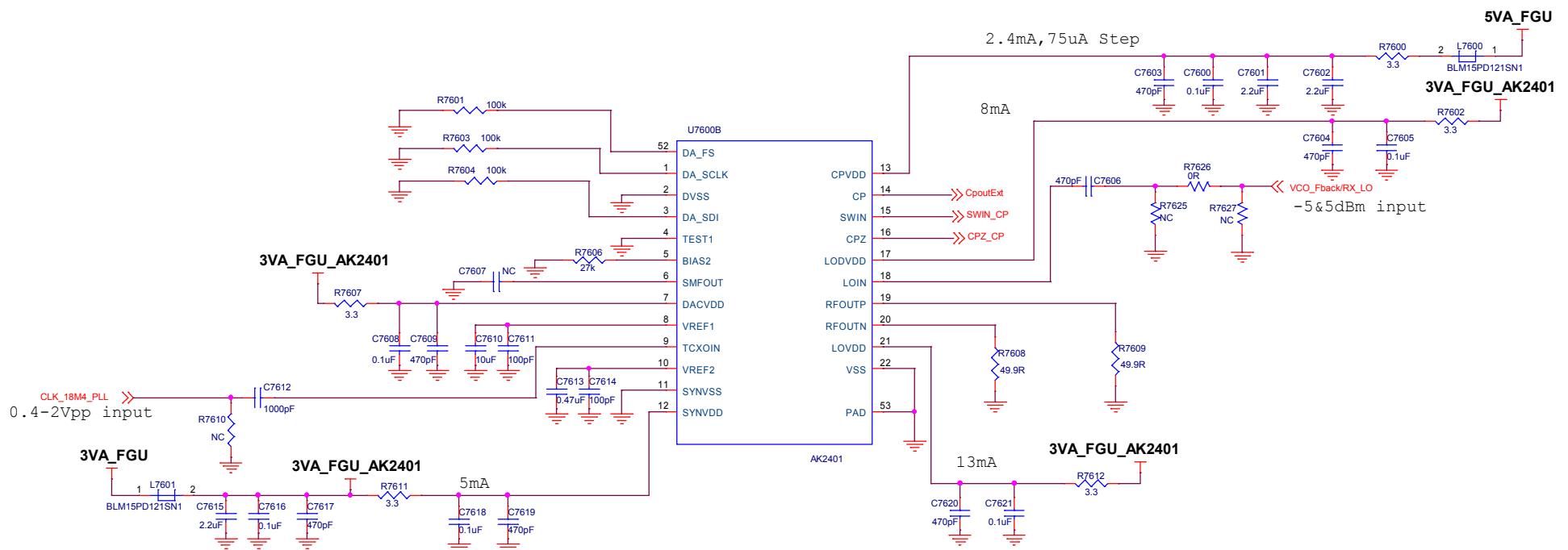
3

2

1



<b>Hytera Communications Co.,Ltd.</b>			
<b>Model Name:</b>	G2.0 JUPITER	<b>Page:</b>	24 of 68
<b>Part Name:</b>	PA	<b>Date:</b>	Friday, May 10, 2019
<b>File No.:</b>	<orgaddr>		<b>Rev:</b> A01
<b>Prepare:</b>	Check:	Approve:	



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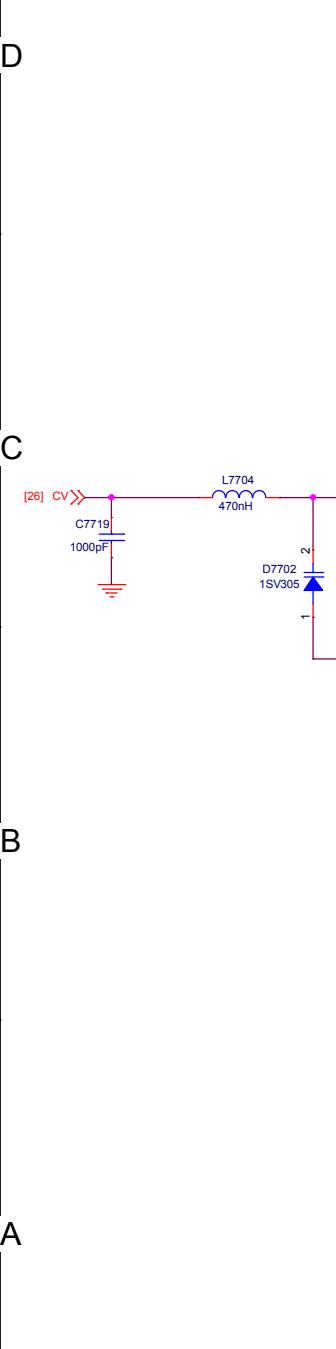
5

4

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2

1



Hytera Communications Co.,Ltd.			
Model Name:	G2.0 JUPITER	Page:	26 of 68
Part Name:	RVCO	Date:	Friday, May 10, 2019
File No.:		Rev:	A01
Prepare:	Check:	Approve:	

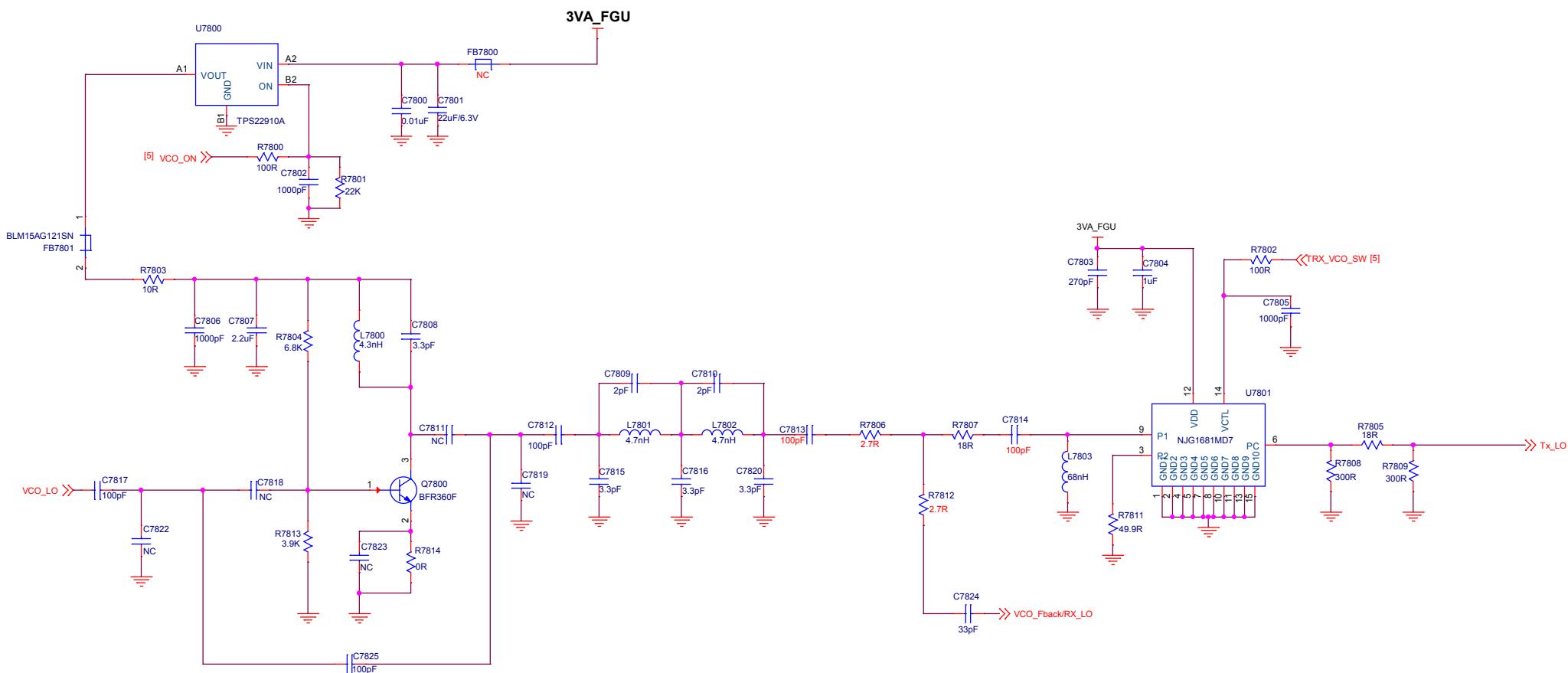
5

4

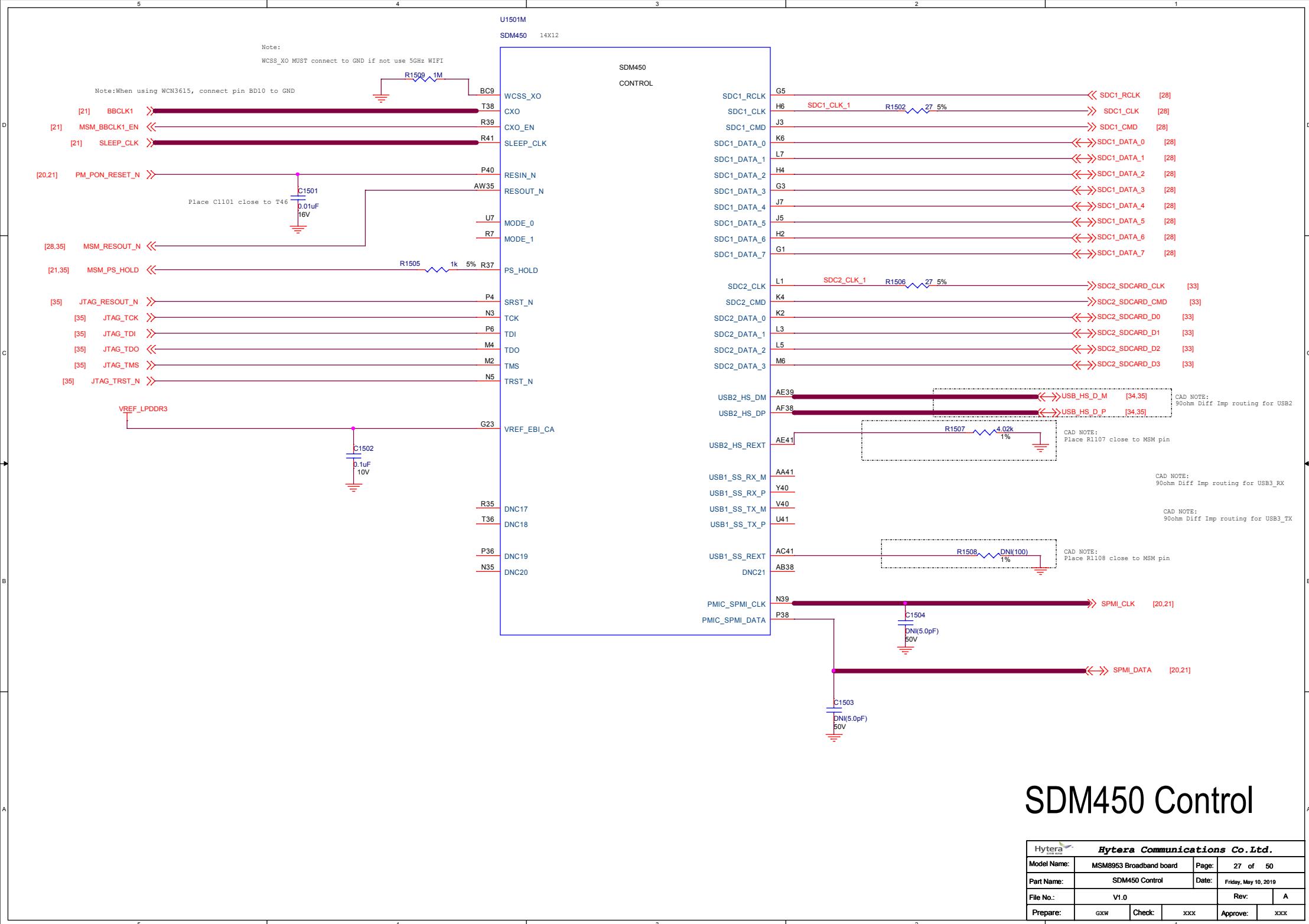
3

2

1



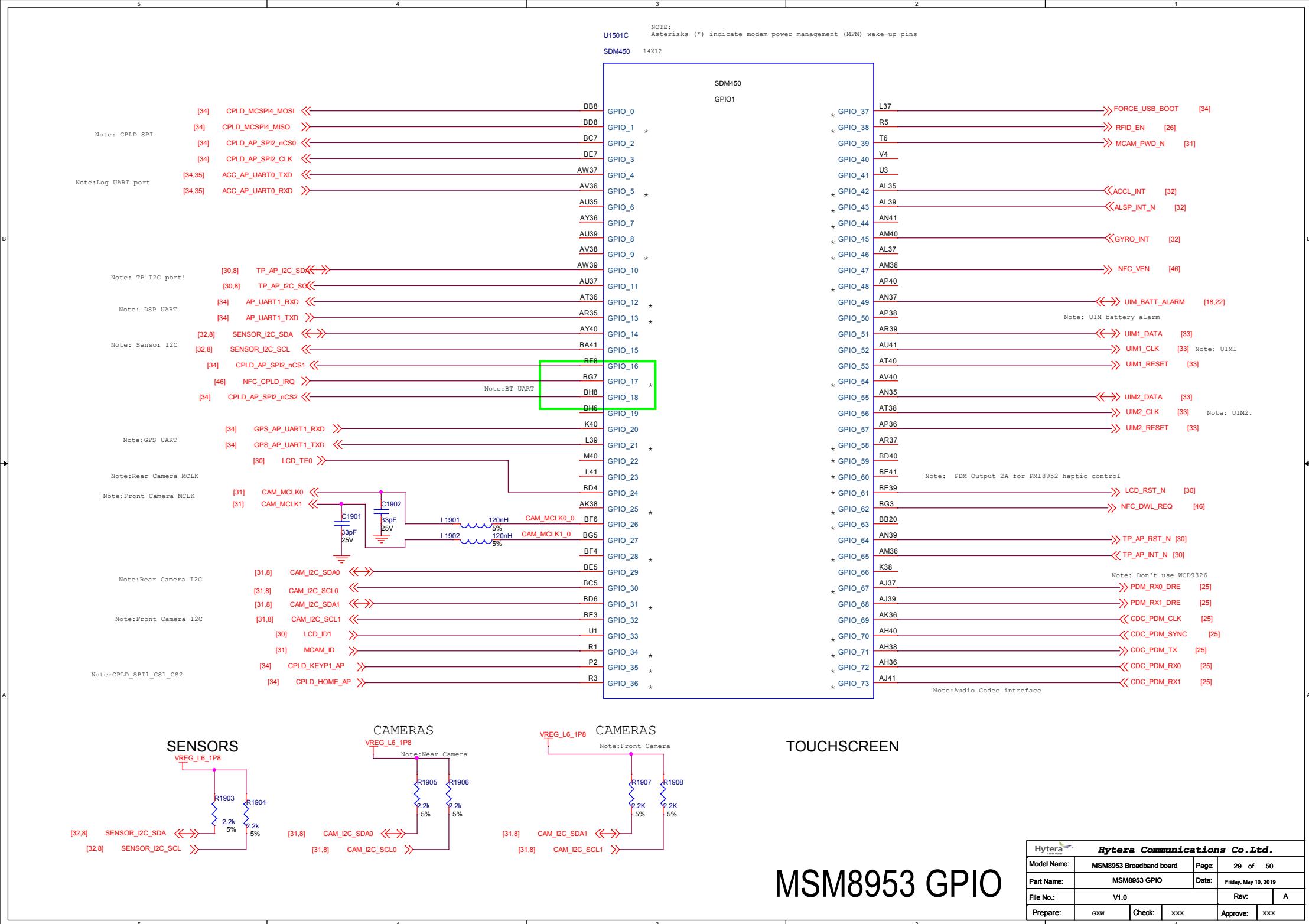
Hytera Communications Co.,Ltd.			
Model Name:	G2.0 JUPITER	Page:	27 of 68
Part Name:	TVCO	Date:	Friday, May 10, 2019
File No.:	<orgaddr>	Rev:	A01
Prepare:	Check:	Approve:	

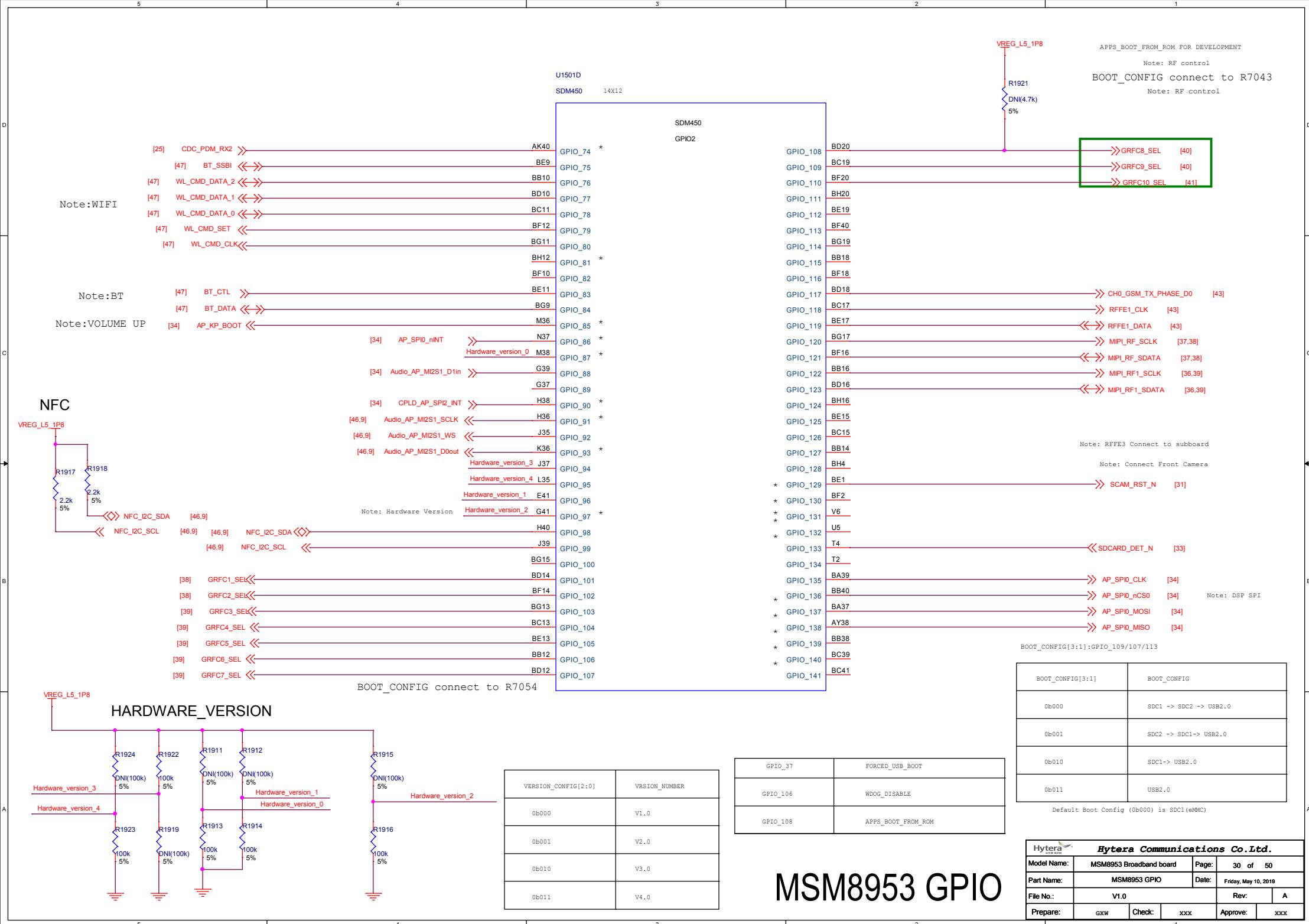




# MSM8953 EBI

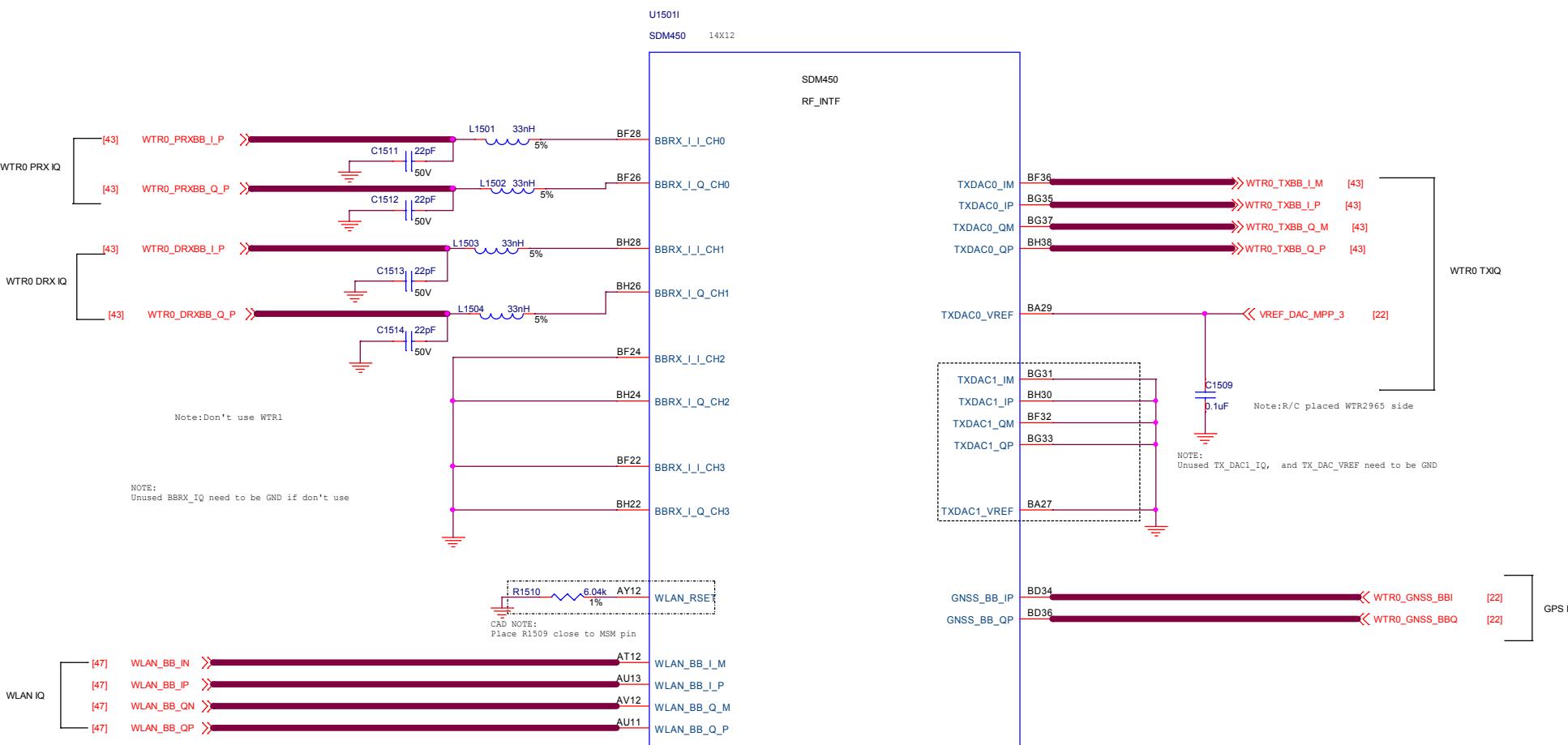
Hytera Communications Co. Ltd.			
Model Name:	MSM8953 Broadband board	Page:	28 of 50
Part Name:	MSM8953 EBI	Date:	Friday, May 10, 2019
File No.:	V1.0	Rev:	A
Prepare:	GXW	Check:	xxx
Approve:	xxx		





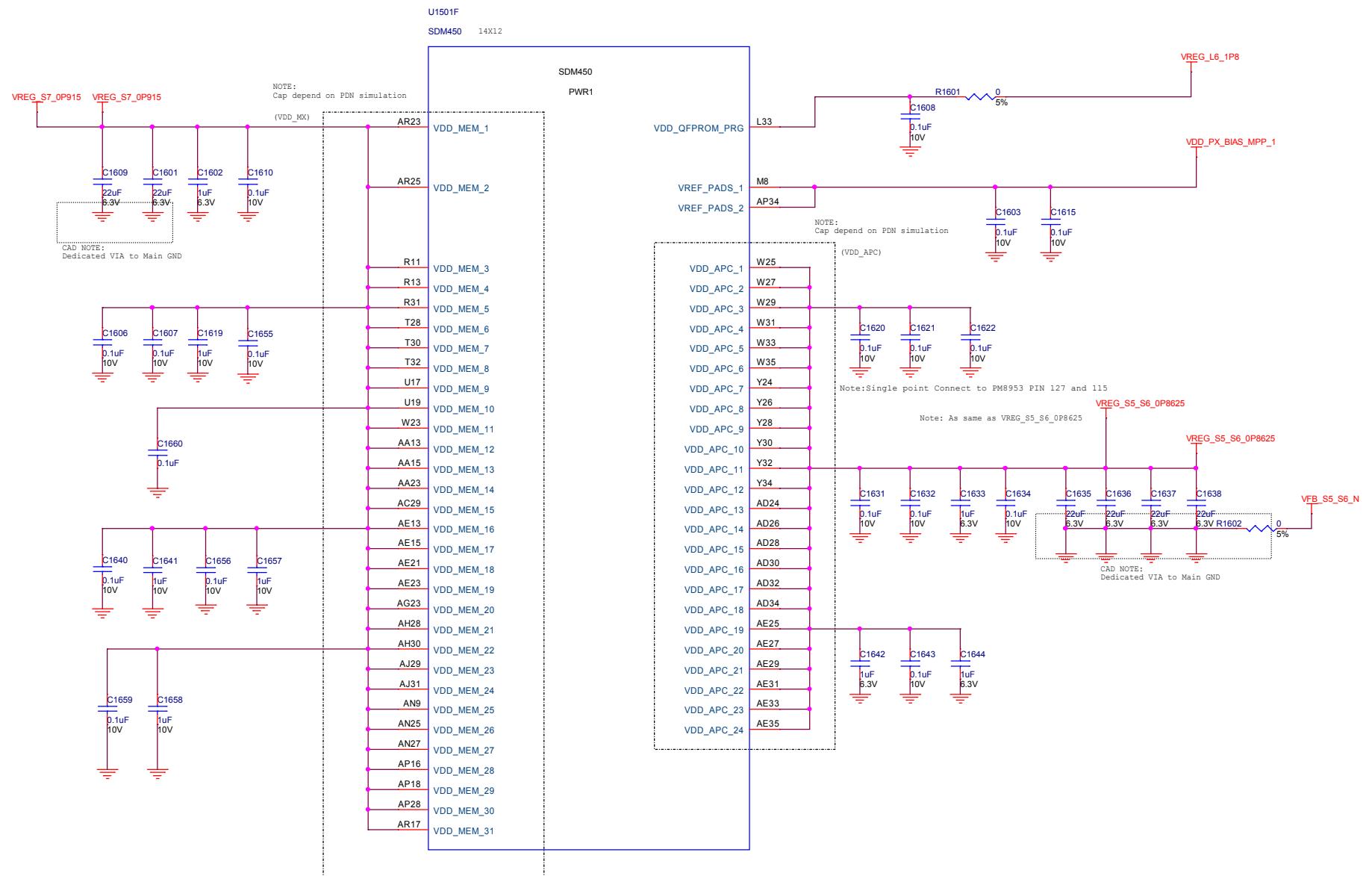


Hytera Communications Co. Ltd.			
Model Name:	MSM8953 Broadband board	Page:	31 of 50
Part Name:	MSM8953 MIPI	Date:	Friday, May 10, 2019
File No.:	V1.0	Rev:	A
Prepare:	GXW	Check:	xxx
Approve:	xxx		



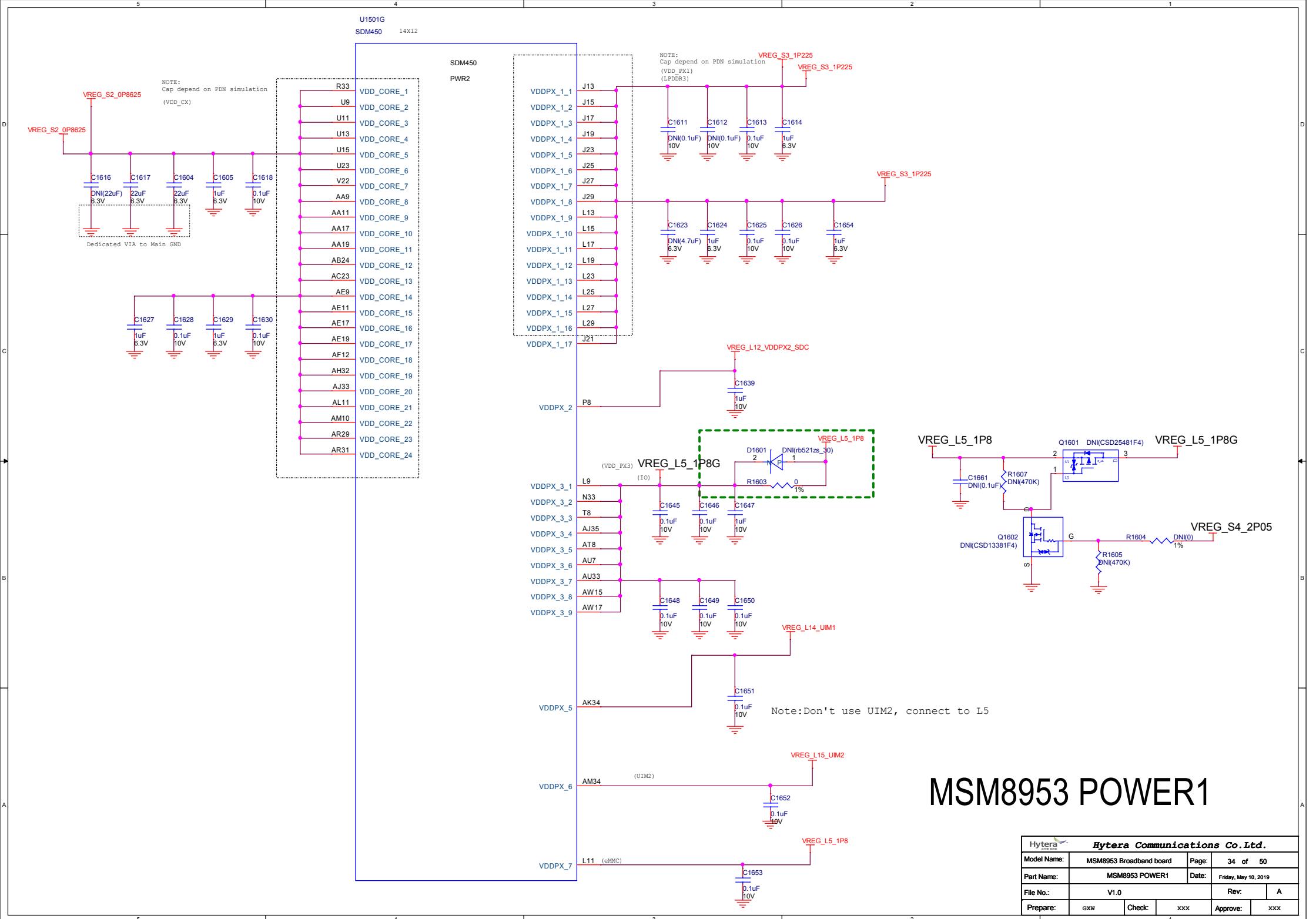
# MSM8953 RF

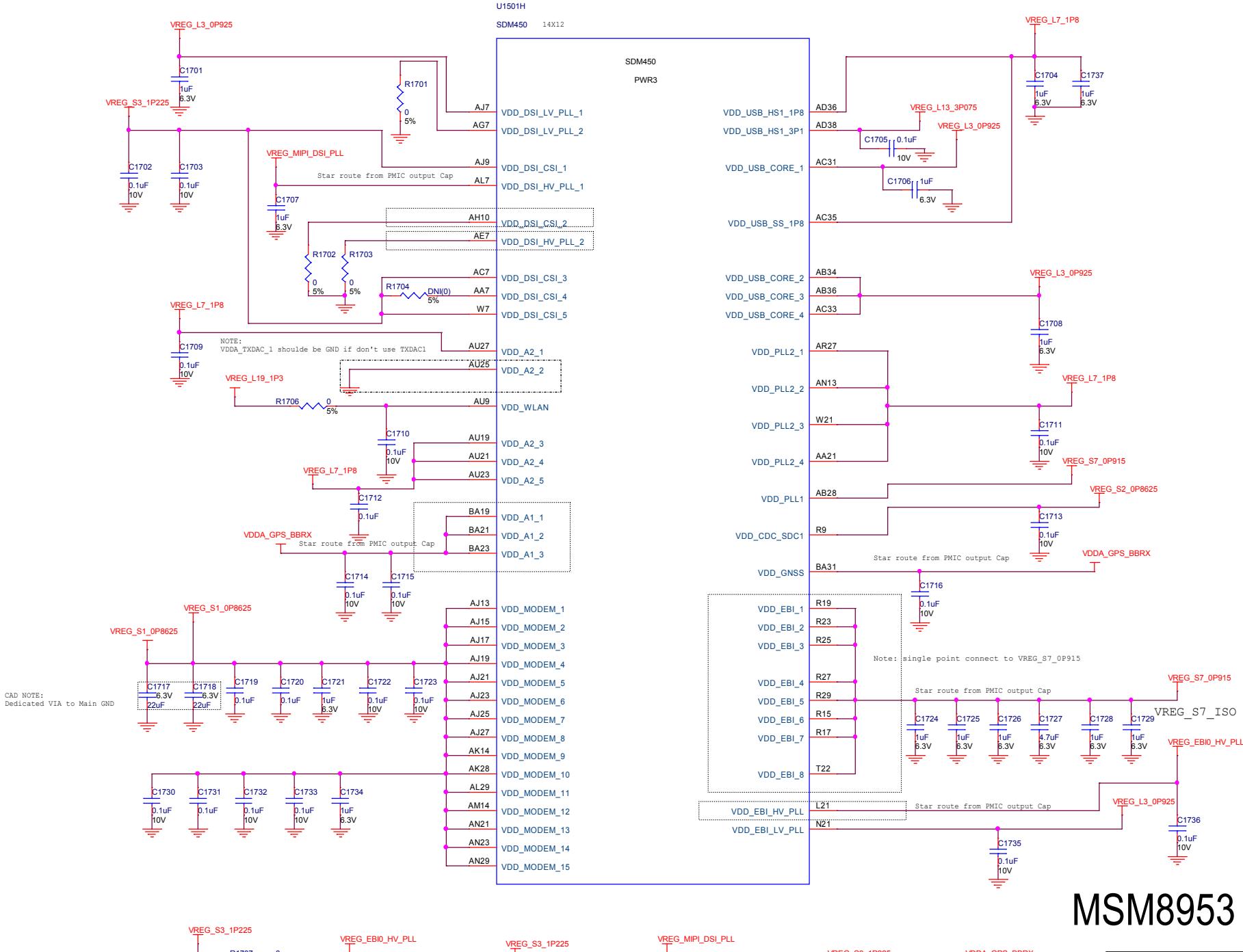
Hytera Communications Co.Ltd.			
Model Name:	MSM8953 Broadband board	Page:	32 of 50
Part Name:	MSM8953 RF	Date:	Friday, May 10, 2019
File No.:	V1.0	Rev:	A
Prepare:	GXW	Check:	xxx
Approve:	xxx		



# MSM8953 POWER1

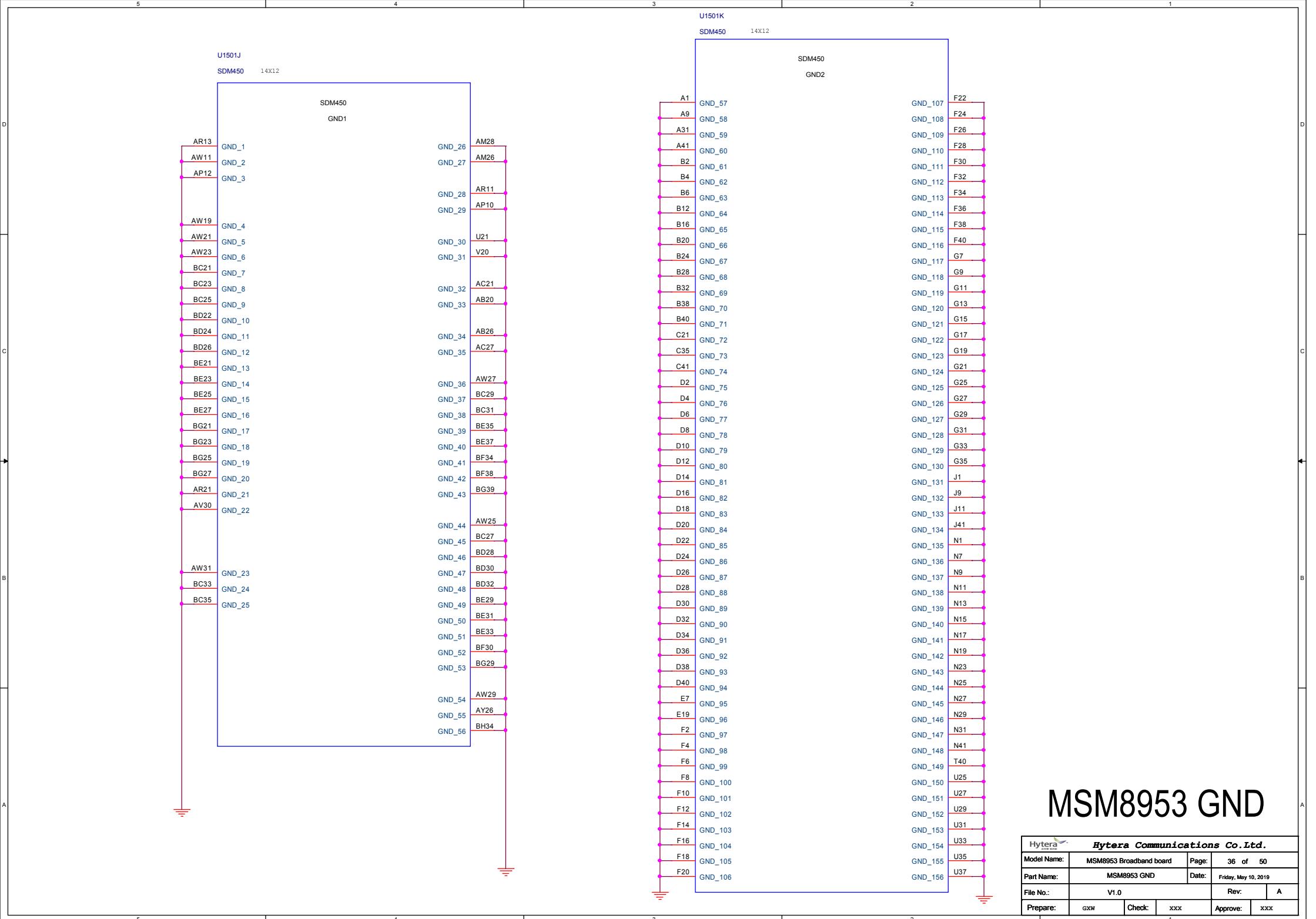
	<b>Hytera Communications Co.,Ltd.</b>			
Model Name:	MSM8953 Broadband board	Page:	33	of 50
Part Name:	MSM8953 POWER1	Date:	Friday, May 10, 2019	
File No.:	V1.0	Rev:		A
Prepare:	GXW	Check:	XXX	Approve: XXX

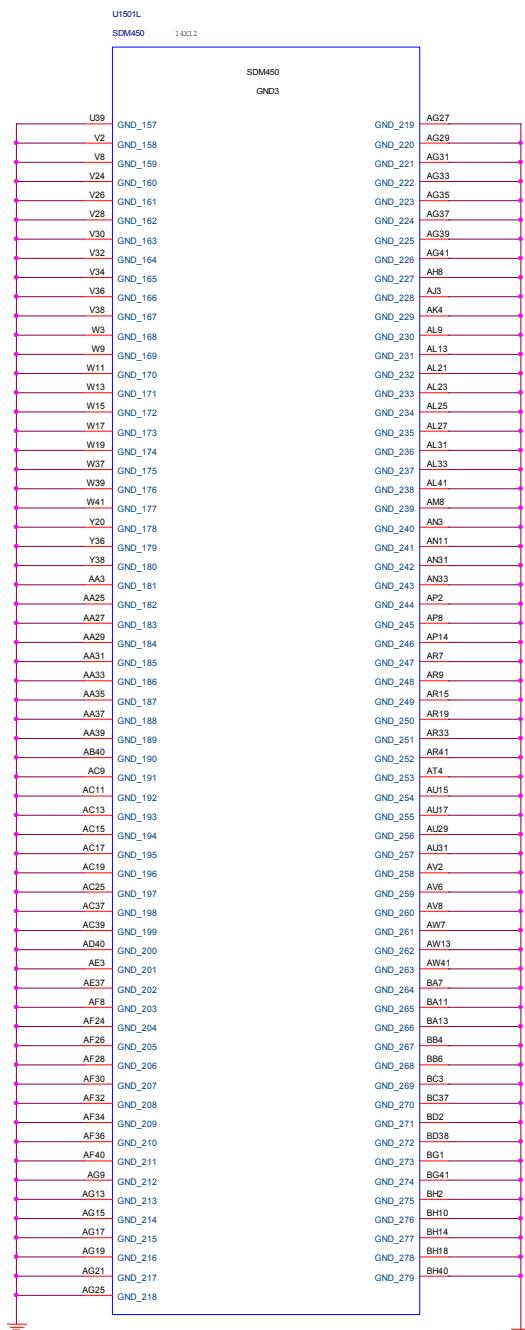




MSM8953 POWER2

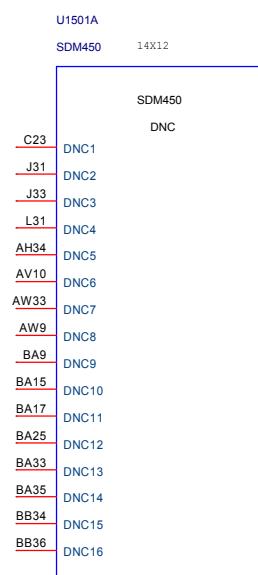
Hytera Communications Co.,Ltd.					
Model Name:	MSM8953 Broadband board		Page:	35 of 50	
Part Name:	MSM8953 POWER2		Date:	Friday, May 10, 2019	
File No.:	V1.0		Rev:	A	
Prepare:	GXW	Check:	XXX	Approve:	XXX





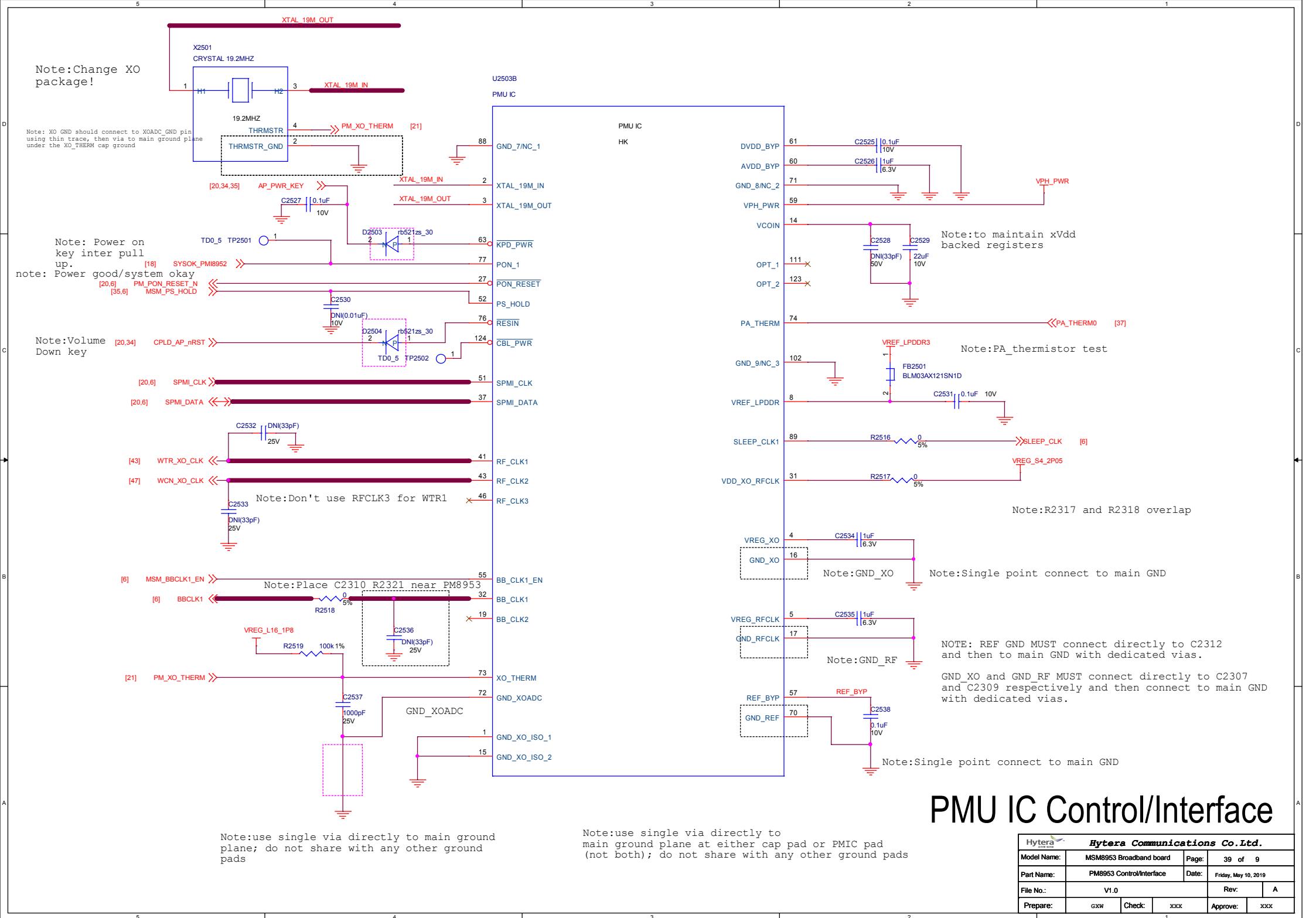
MSM8953 GND

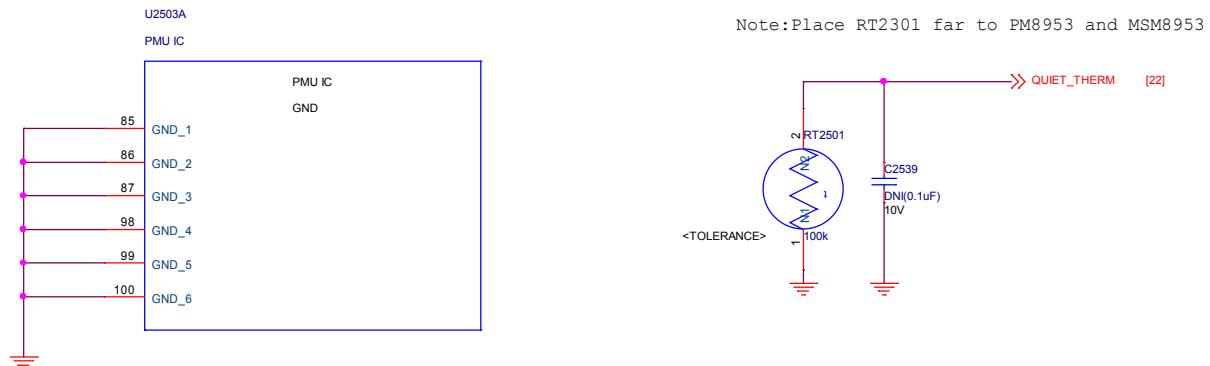
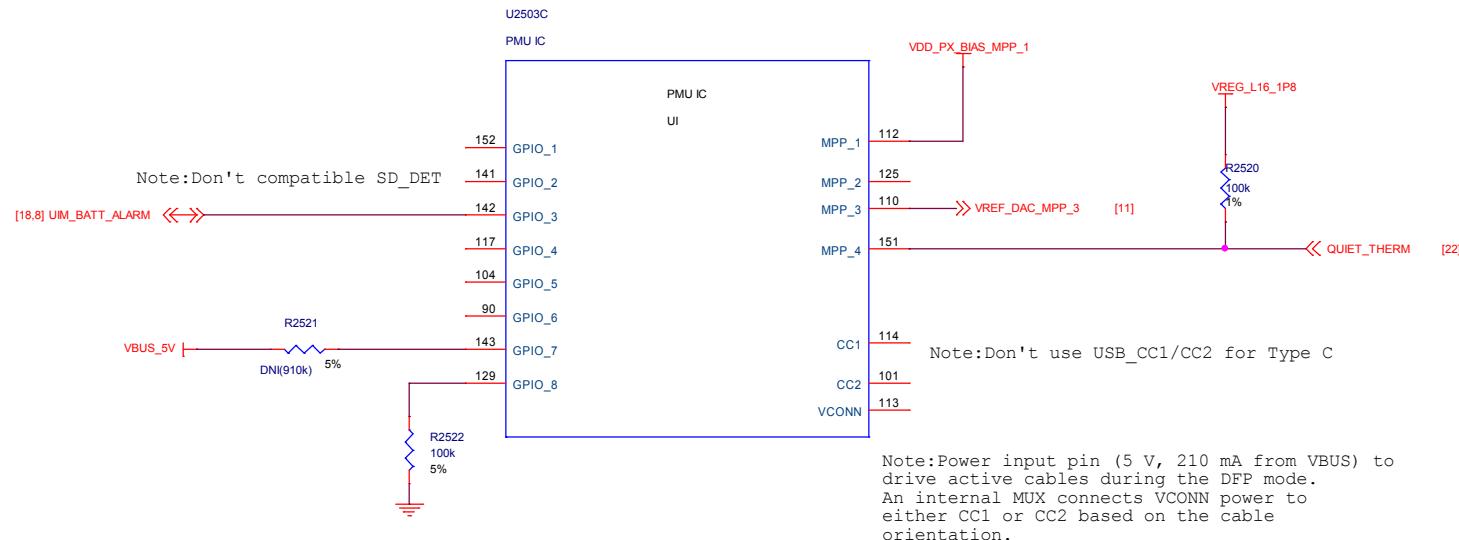
Hytera Communications Co.,Ltd.			
Model Name:	MSM8953 Broadband board	Page:	37 of 50
Part Name:	MSM8953 GND	Date:	Friday, May 10, 2019
File No.:	V1.0	Rev:	A
Prepare:	cxw	Check:	xxx
Approve:	xxx		



MSM8953 GND

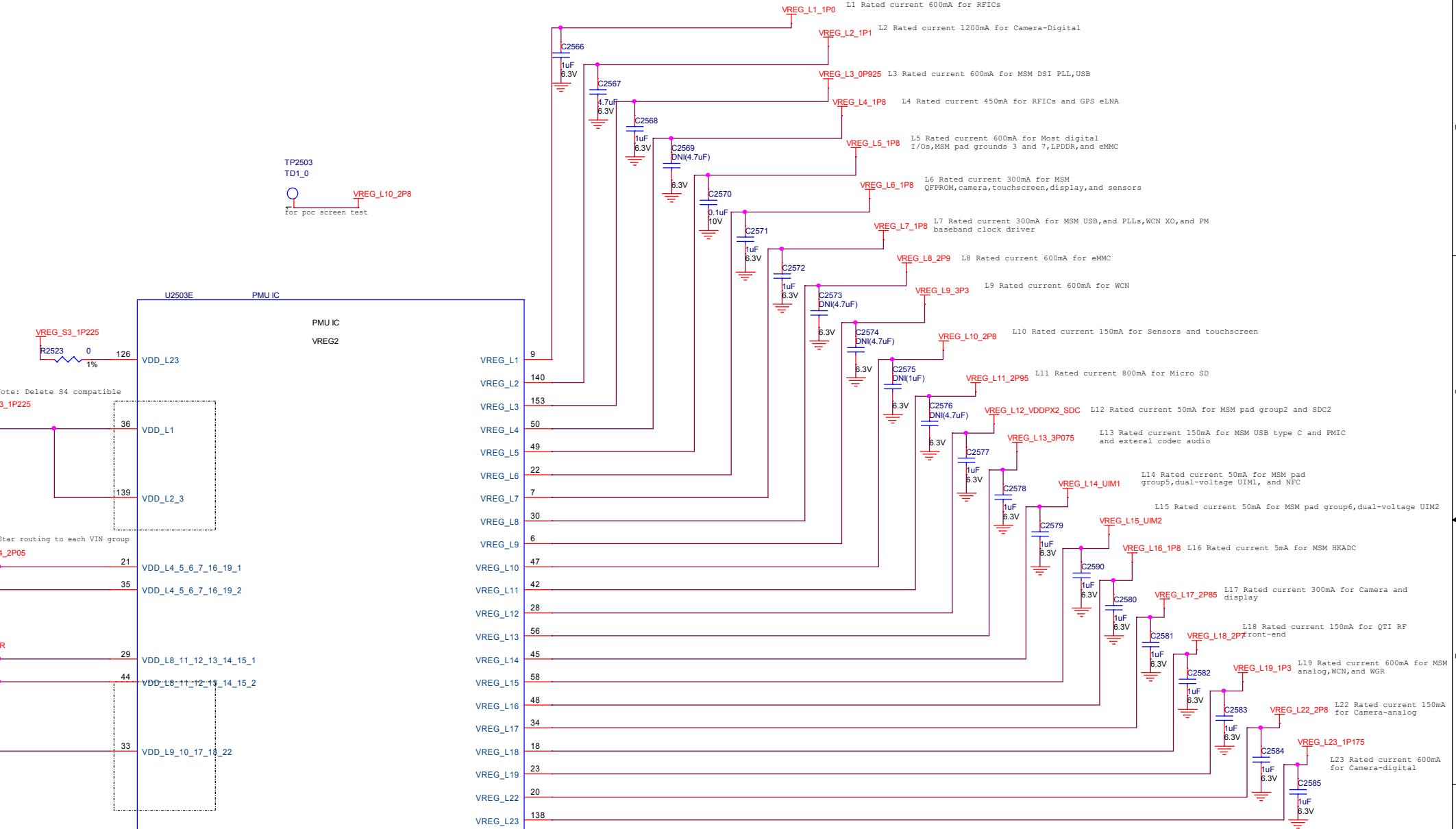
Hytera Communications Co. Ltd.			
Model Name:	MSM8953 Broadband board	Page:	38 of 50
Part Name:	MSM8953 GND	Date:	Friday, May 10, 2019
File No.:	V1.0	Rev:	A
Prepare:	GXW	Check:	xxx
Approve:	xxx		





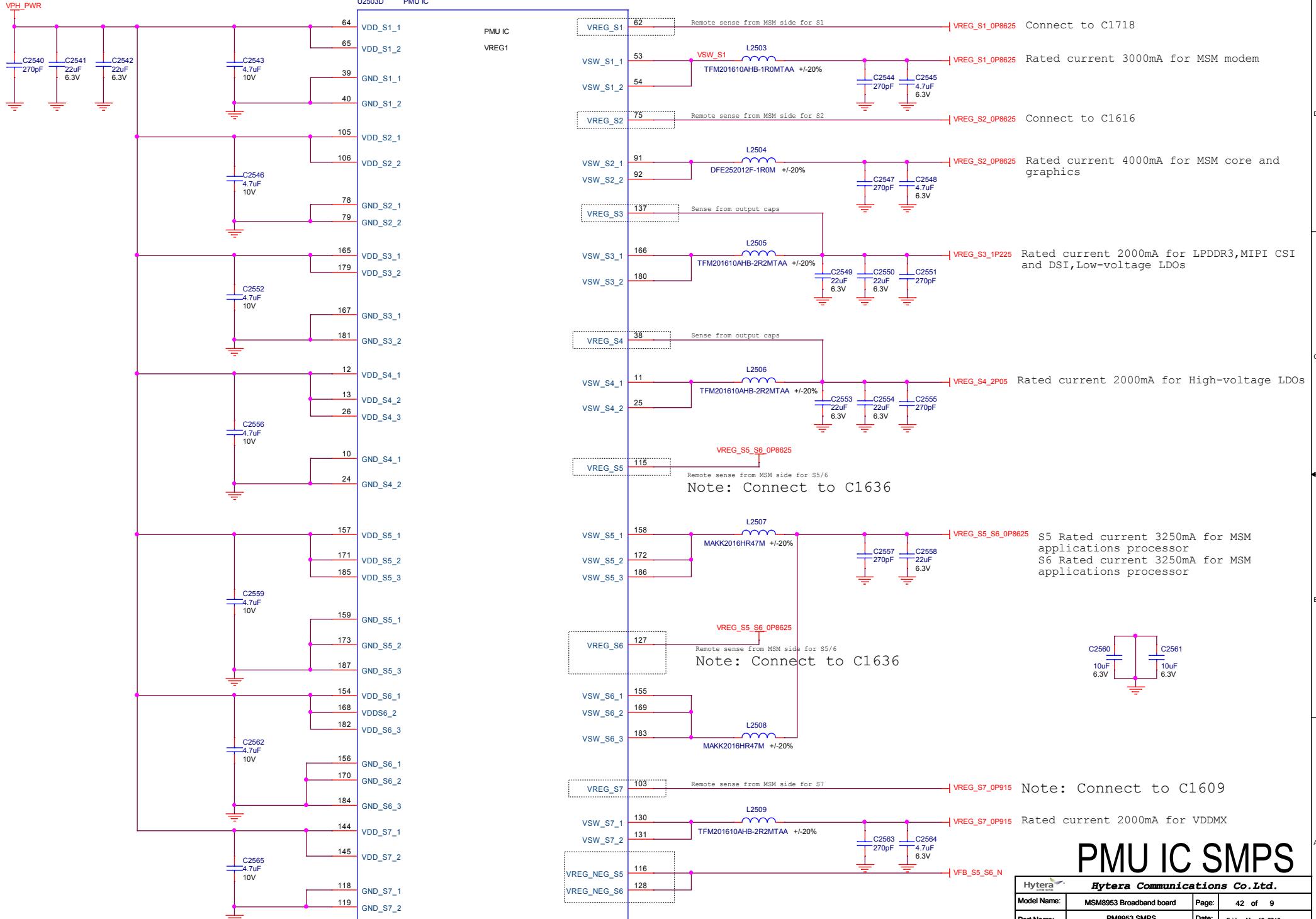
## PMU IC Control/Interface

Hytera Communications Co.,Ltd.			
Model Name:	MSM8953 Broadband board	Page:	40 of 9
Part Name:	PM8953 Control/Interface	Date:	Friday, May 10, 2019
File No.:	V1.0	Rev:	A
Prepare:	GXR	Check:	XXX
Approve:	XXX	Rev:	A



## PMU IC LDO

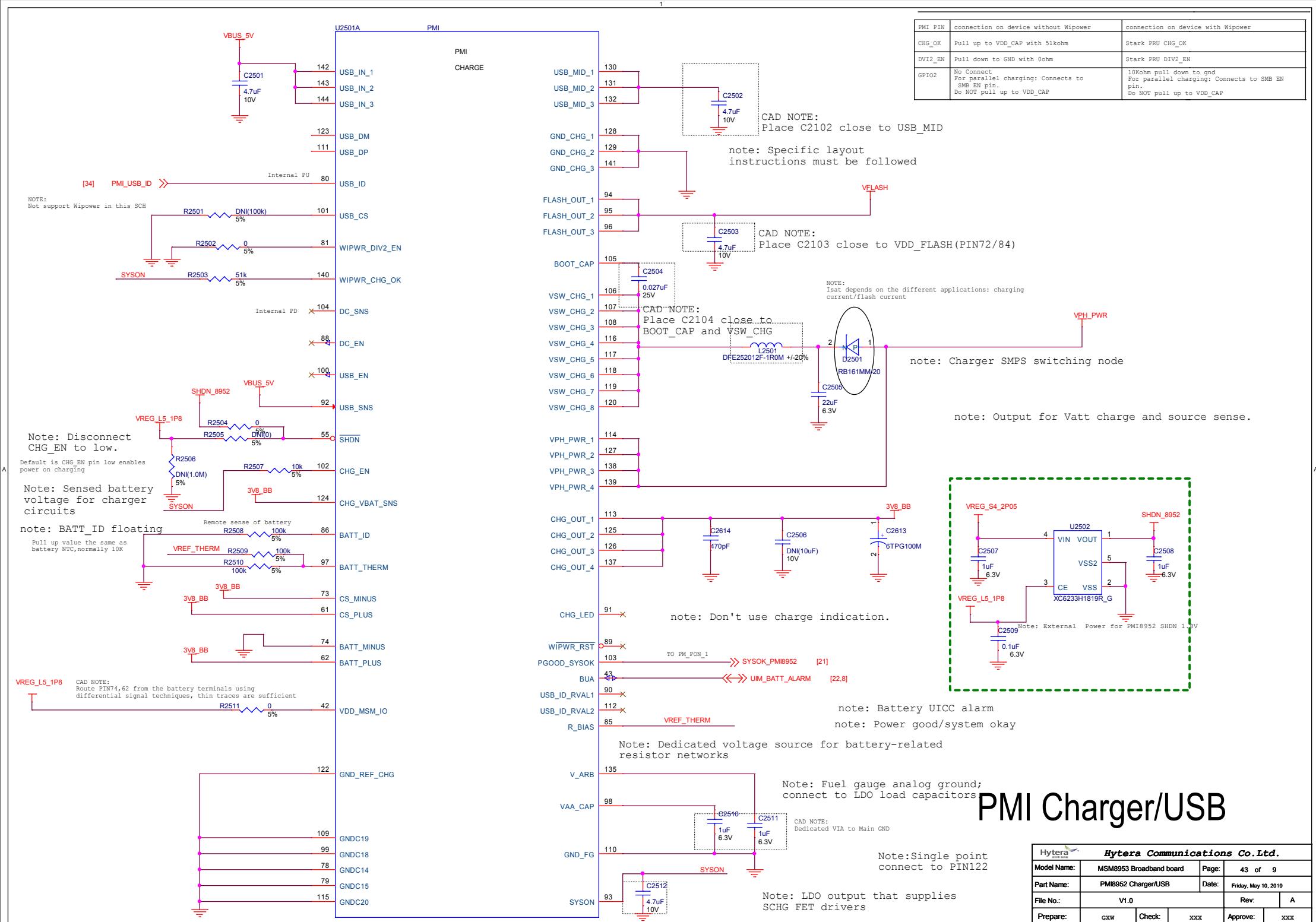
Hytera Communications Co. Ltd.			
Model Name:	MSM8953 Broadband board	Page:	41 of 9
Part Name:	PMU IC LDO	Date:	Friday, May 10, 2019
File No.:	V1.0	Rev:	A
Prepare:	GXW	Check:	xxx
Approve:	xxx		



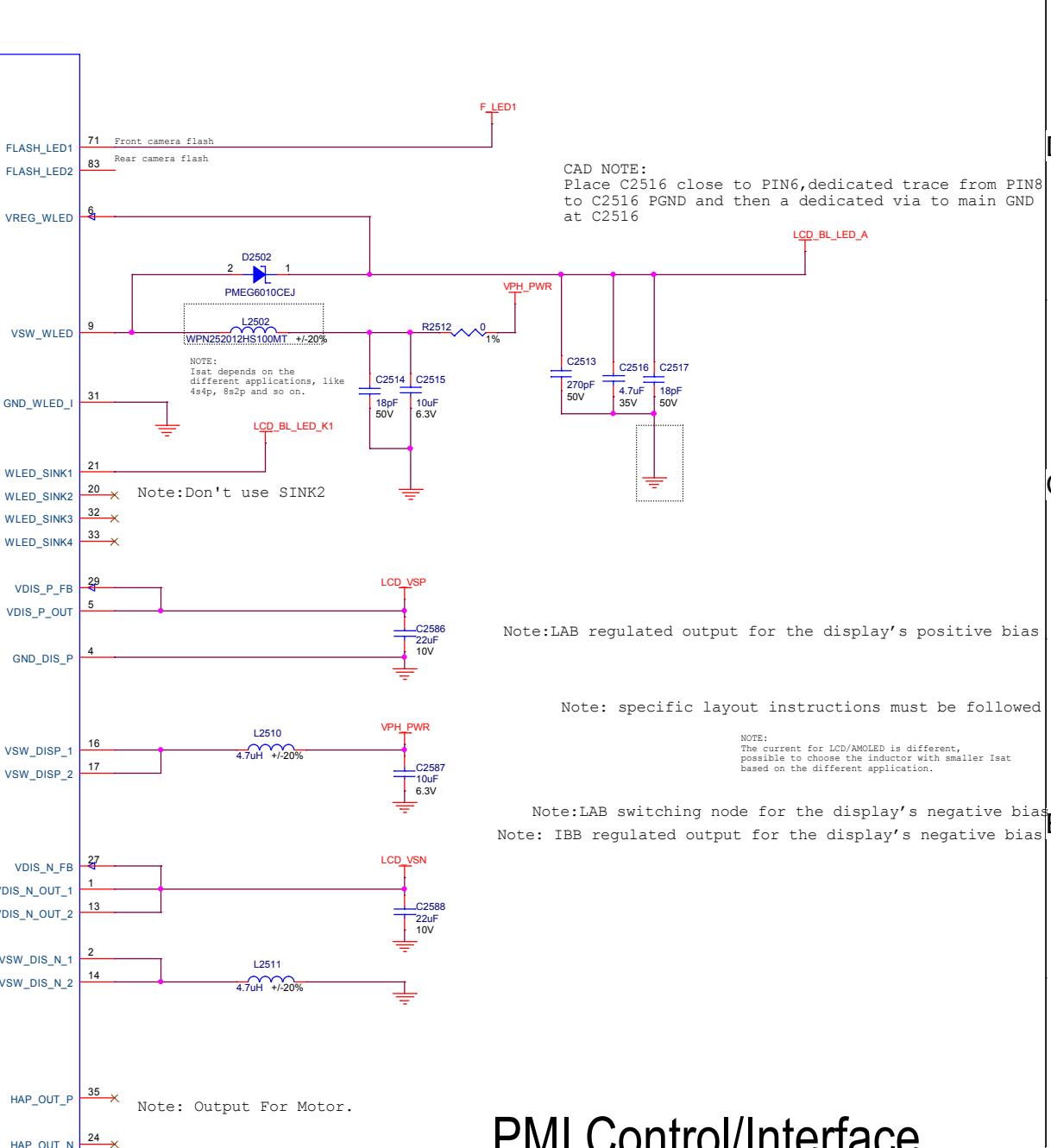
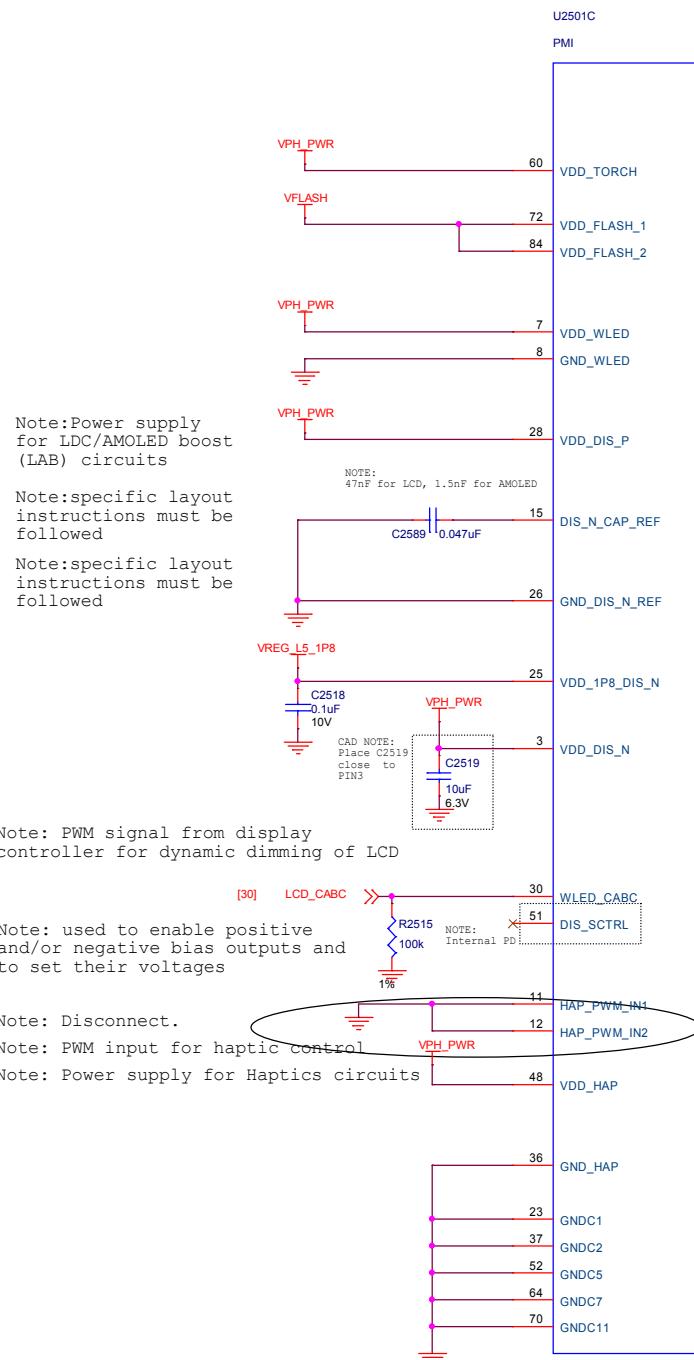
## PMU IC SMPS

Hytera Communications Co. Ltd.			
Model Name:	MSM8953 Broadband board	Page:	42 of 9
Part Name:	PM8953 SMPS	Date:	Friday, May 10, 2019
File No.:	V1.0	Rev:	A
Prepare:	GXW	Check:	xxx
Approve:	xxx		

1

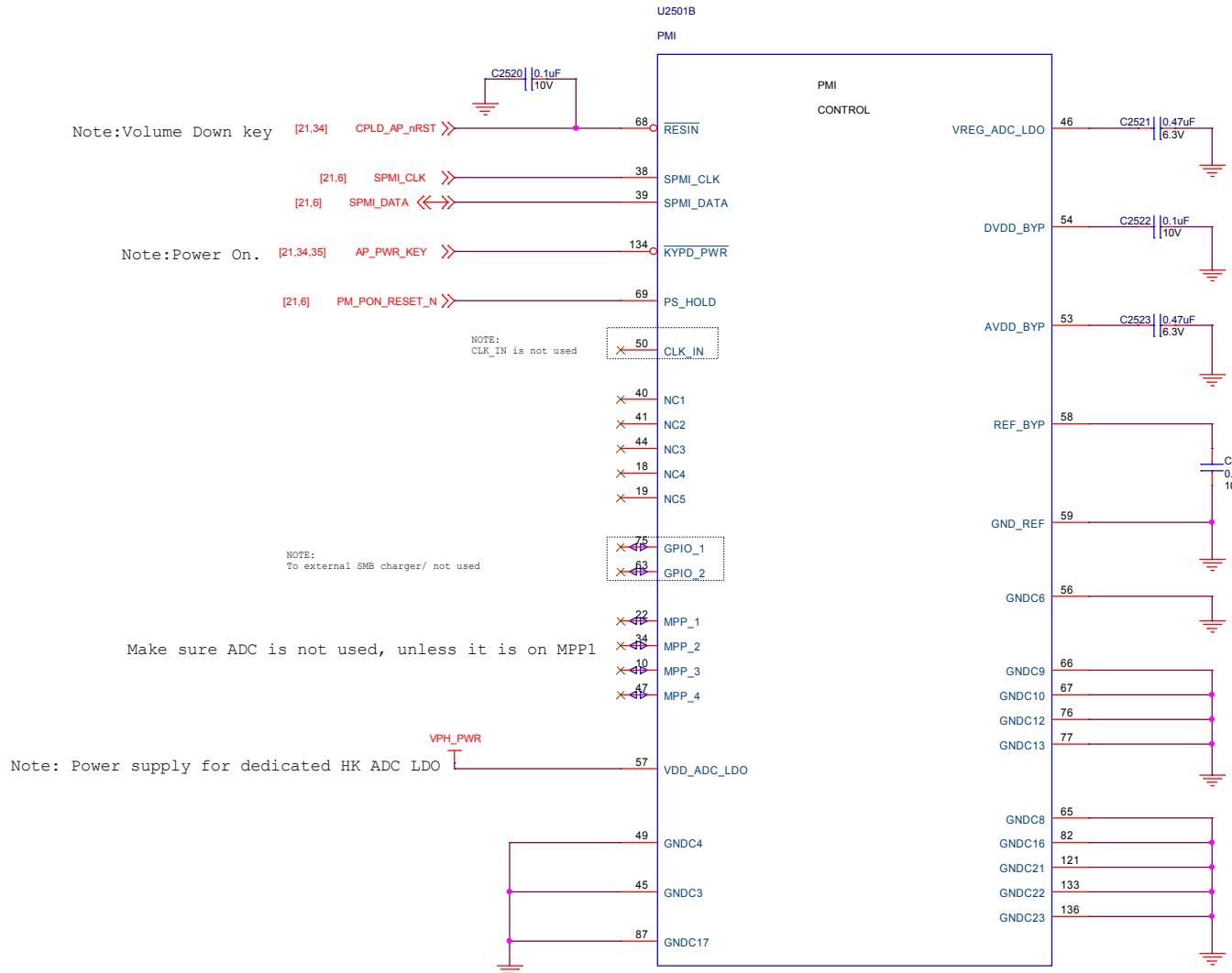


# PMI Charger/USB



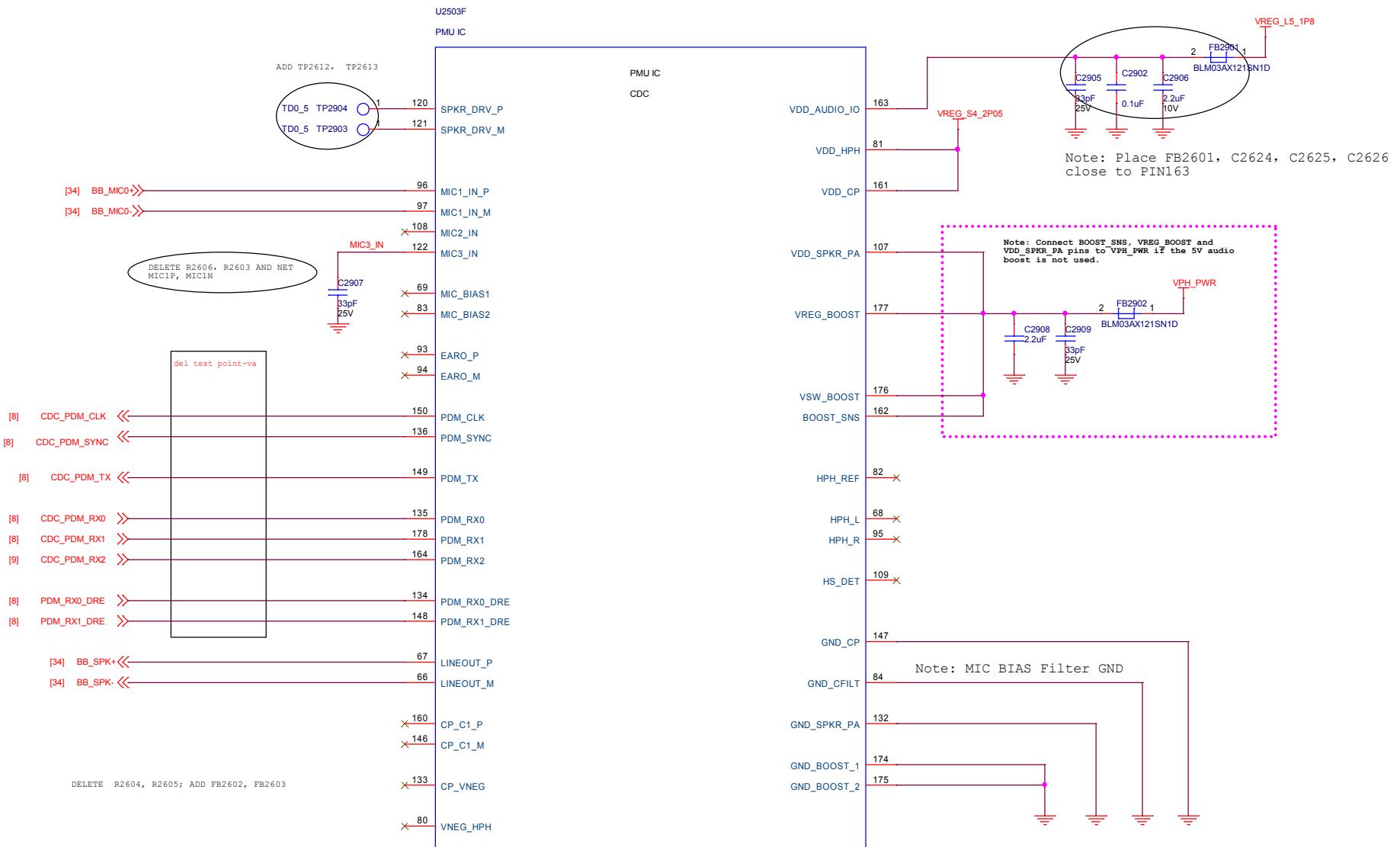
## PMI Control/Interface

Hytera Communications Co.,Ltd.			
Model Name:	MSM8953 Broadband board	Page:	44 of 9
Part Name:	PMI8952 Control/Interface	Date:	Friday, May 10, 2019
File No.:	V1.0	Rev.:	A
Prepare:	GXW	Check:	xxx
Approve:	xxx		



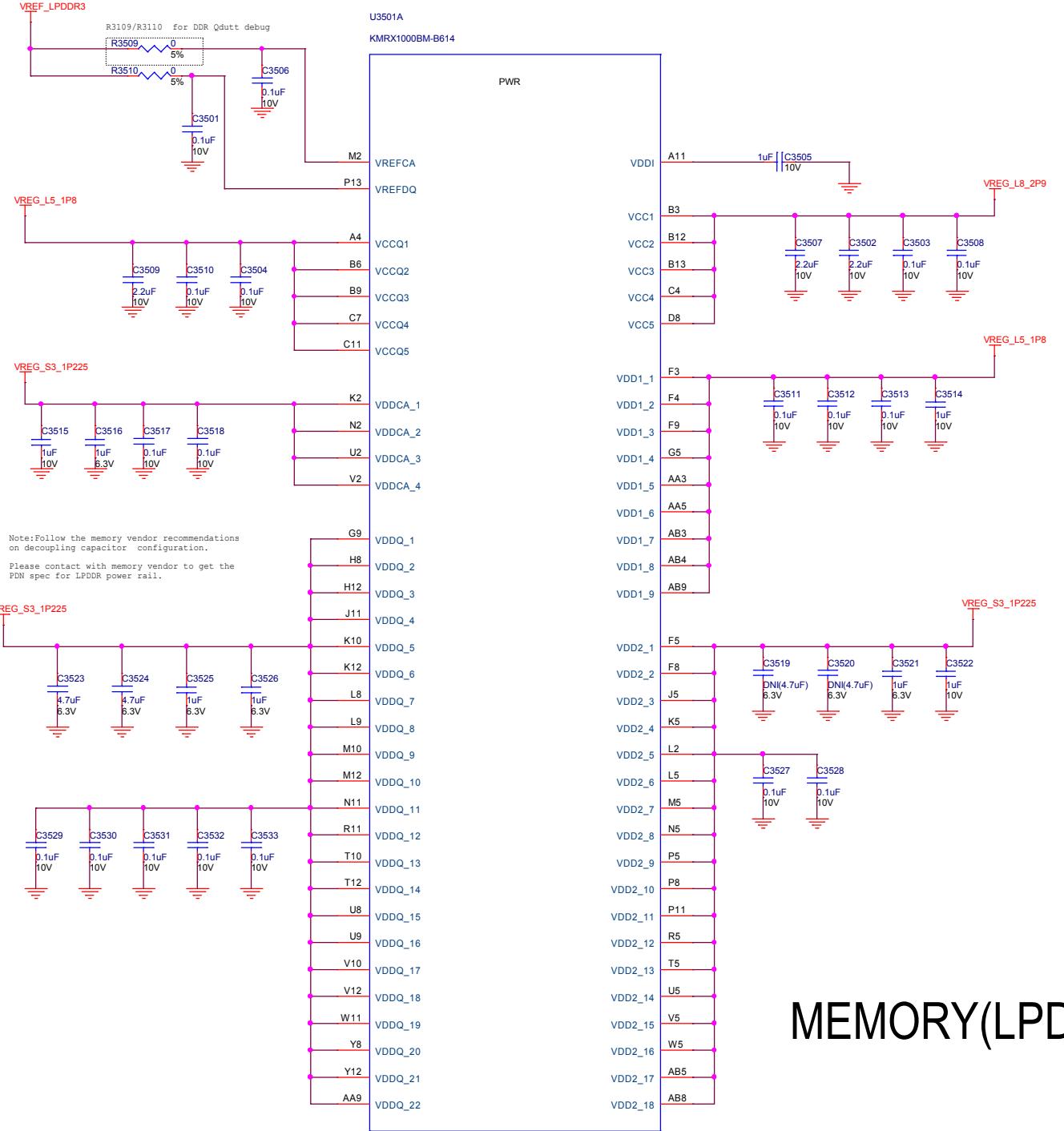
## PMI Control/Interface

Hytera Communications Co.Ltd.			
Model Name:	MSM8953 Broadband board	Page:	45 of 9
Part Name:	PMI8952 Control/Interface	Date:	Friday, May 10, 2019
File No.:	V1.0	Rev:	A
Prepare:	GXW	Check:	XXX
Approve:	XXX		



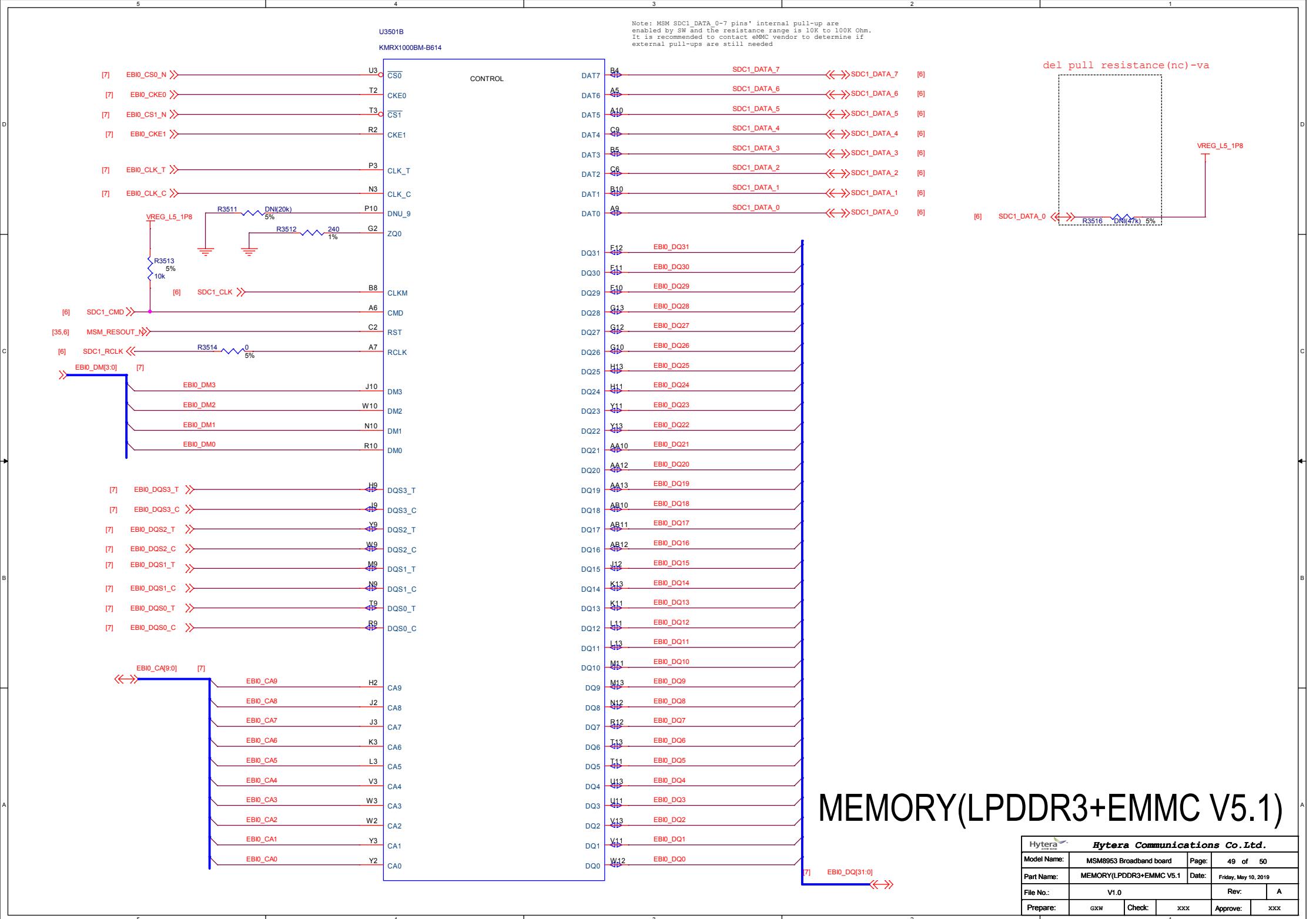
## PMU IC AUDIO CODEC

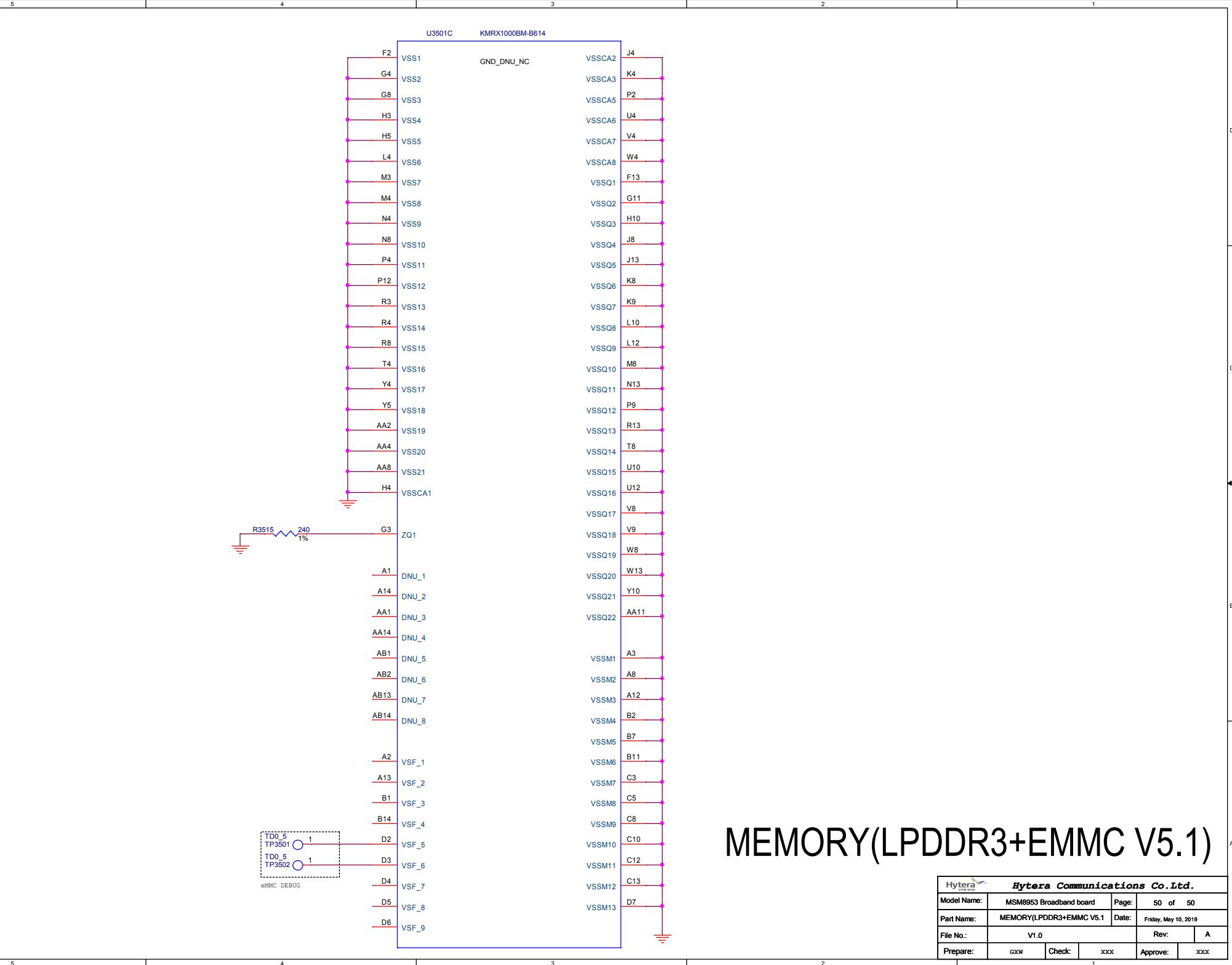
Hytera Communications Co.Ltd.			
Model Name:	MSM8953 Broadband board	Page:	47 of 9
Part Name:	PM8953 AUDIO CEDEC	Date:	Friday, May 10, 2019
File No.:	V1.0	Rev:	A
Prepare:	GXW	Check:	xxx
Approve:	xxx		



# MEMORY(LPDDR3+EMMC V5.1)

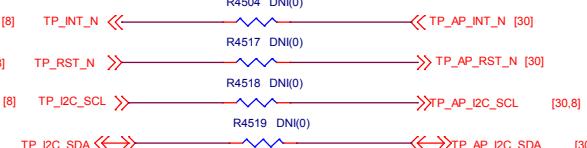
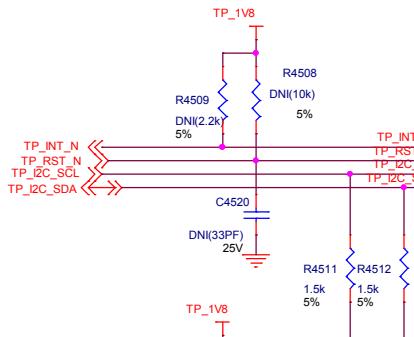
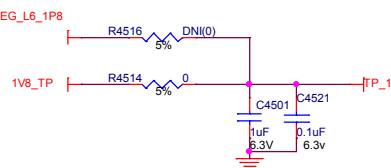
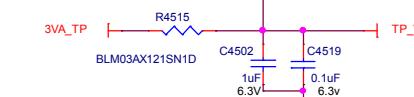
Hytera Communications Co. Ltd.			
Model Name:	MSM8953 Broadband board	Page:	48 of 50
Part Name:	MEMORY(LPDDR3+EMMC V5.1)	Date:	Friday, May 10, 2019
File No.:	V1.0	Rev:	A
Prepare:	GXW	Check:	xxx
Approve:	xxx	Rev:	A



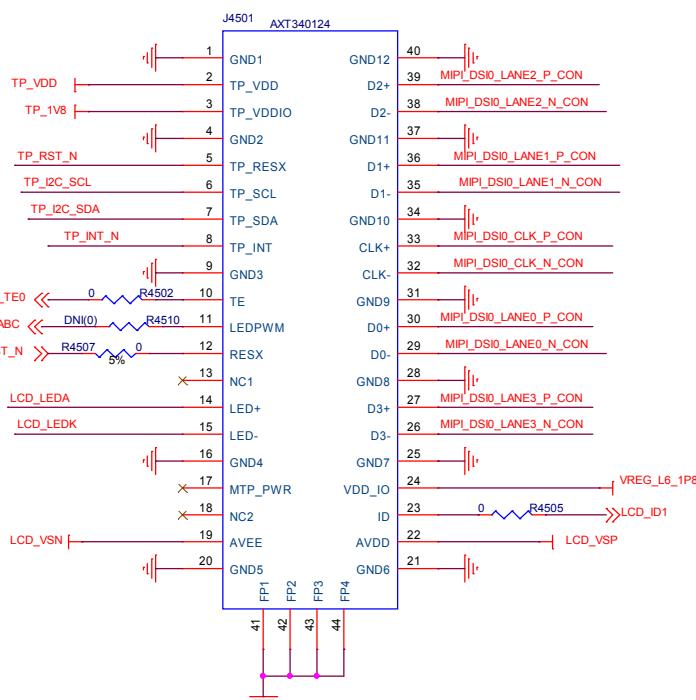


Vendor: [ ]  
 Module: [ ]  
 Driver: [ ]  
 BackLight: 7 LEDs  
 ID: [ ]

CTP

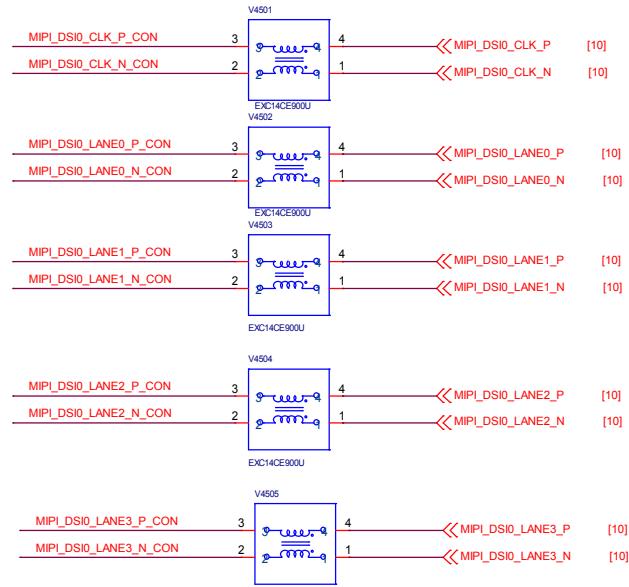
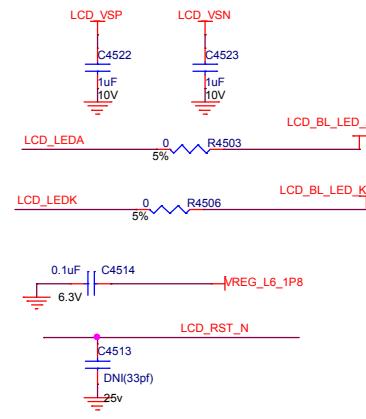


# LCD- Connector



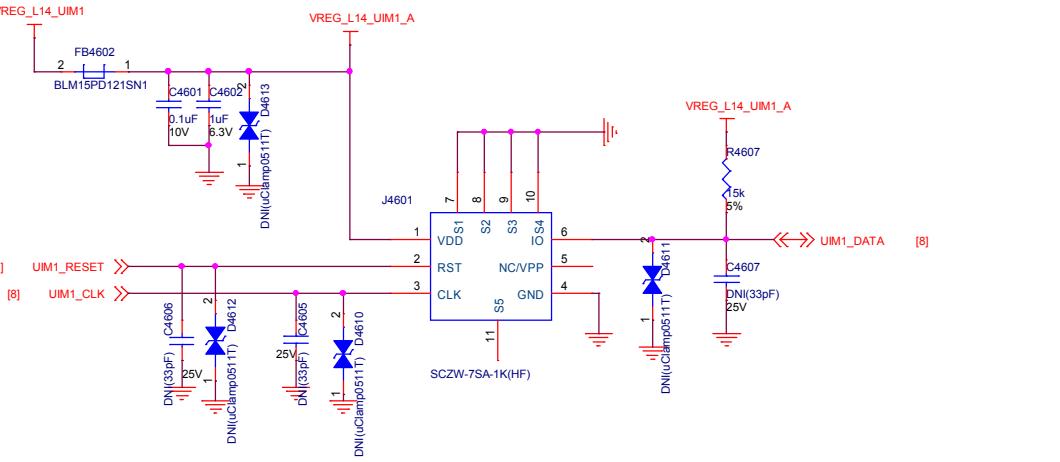
Note: If best EMI practices are followed for MIPI CSI/DSI signals, there is no need for common mode choke filters. You may choose to have placeholders for common mode depending upon your design constraints. Extreme care must be taken that no stubs are created by doing so.

LCD

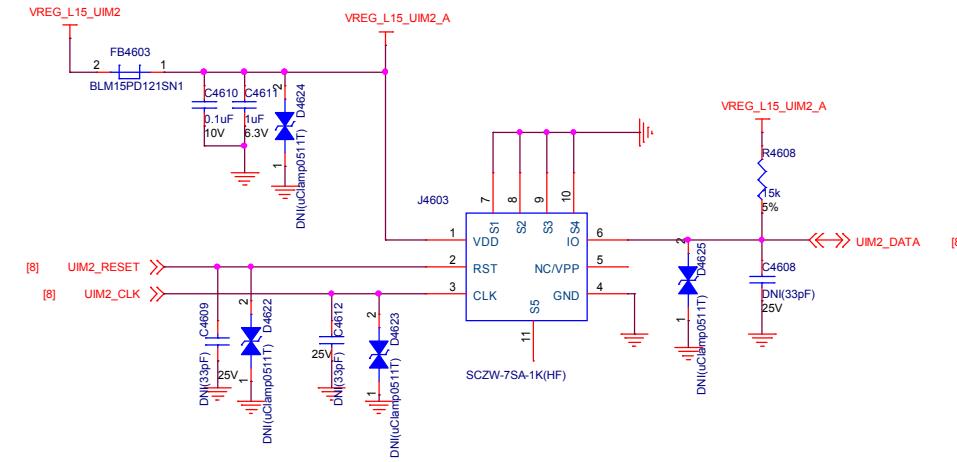


Hytera Communications Co.Ltd.			
Model Name:	MSM8953 Broadband board	Page:	51 of 50
Part Name:	LCD/CTP	Date:	Friday, May 10, 2019
File No.:	V1.0	Rev.:	A
Prepare:	GXW	Check:	xxx
Approve:	xxx		

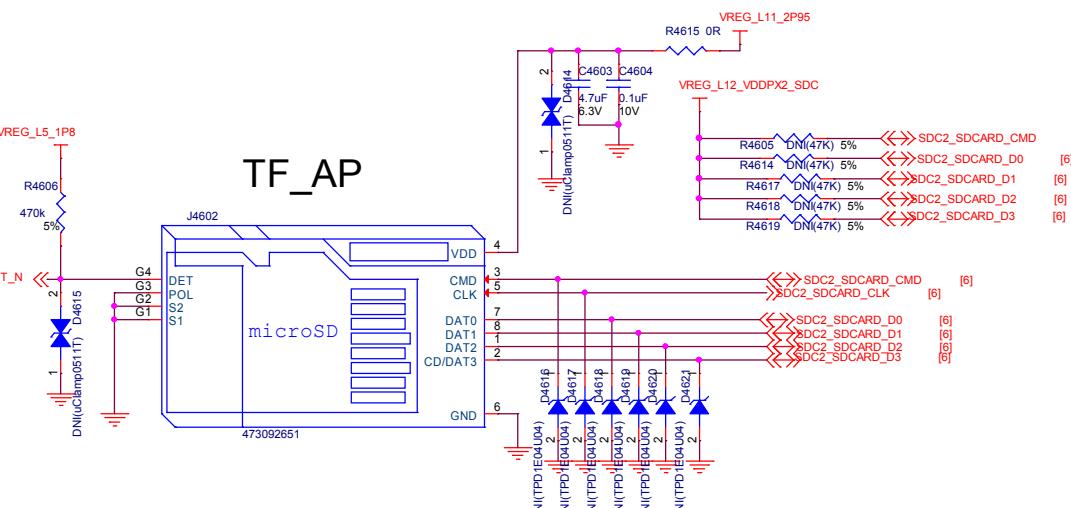
# WB SIM1



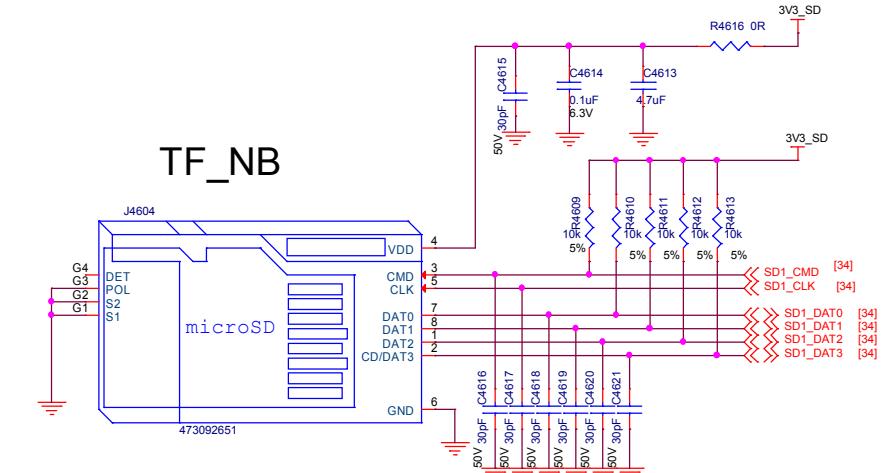
# WB SIM2



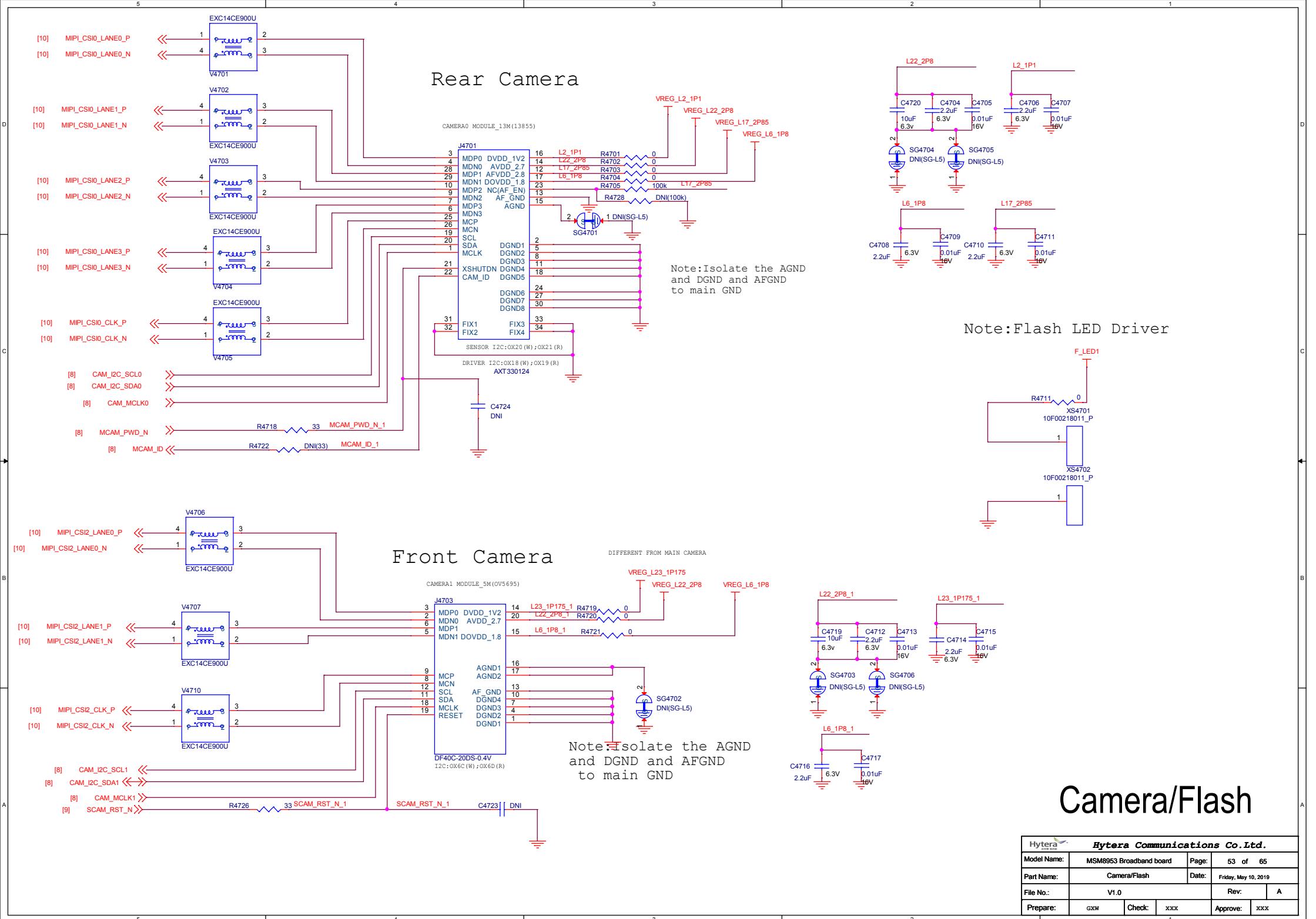
# WB TF card



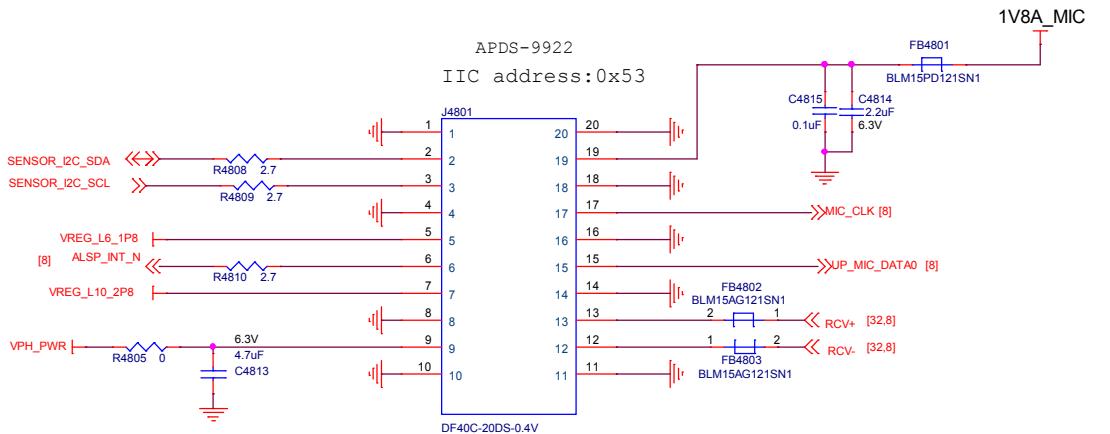
# NB TF card



Hytera Communications Co. Ltd.			
Model Name:	MSM8953 Broadband board	Page:	52 of 50
Part Name:	SIM/TF card	Date:	Friday, May 10, 2019
File No.:	V1.0	Rev:	A
Prepare:	GXW	Check:	xxx
Approve:	xxx		

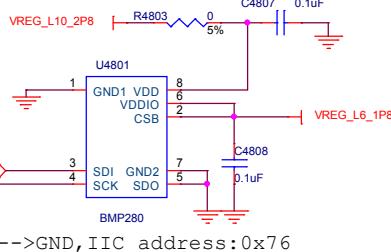


## ALPS\_SENSOR/receiver/MIC

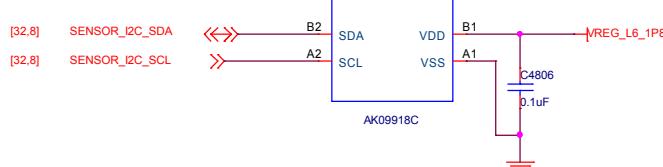


## PRESSURE\_SENSOR

### Barometer



### E-Compass

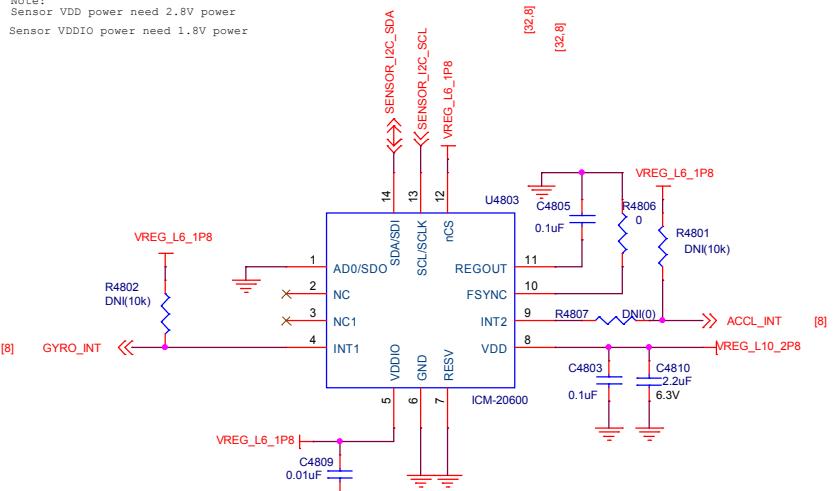


The I2C address of the AK09918C is 0Ch

## MotionTracking (Gyro+Gsensor)

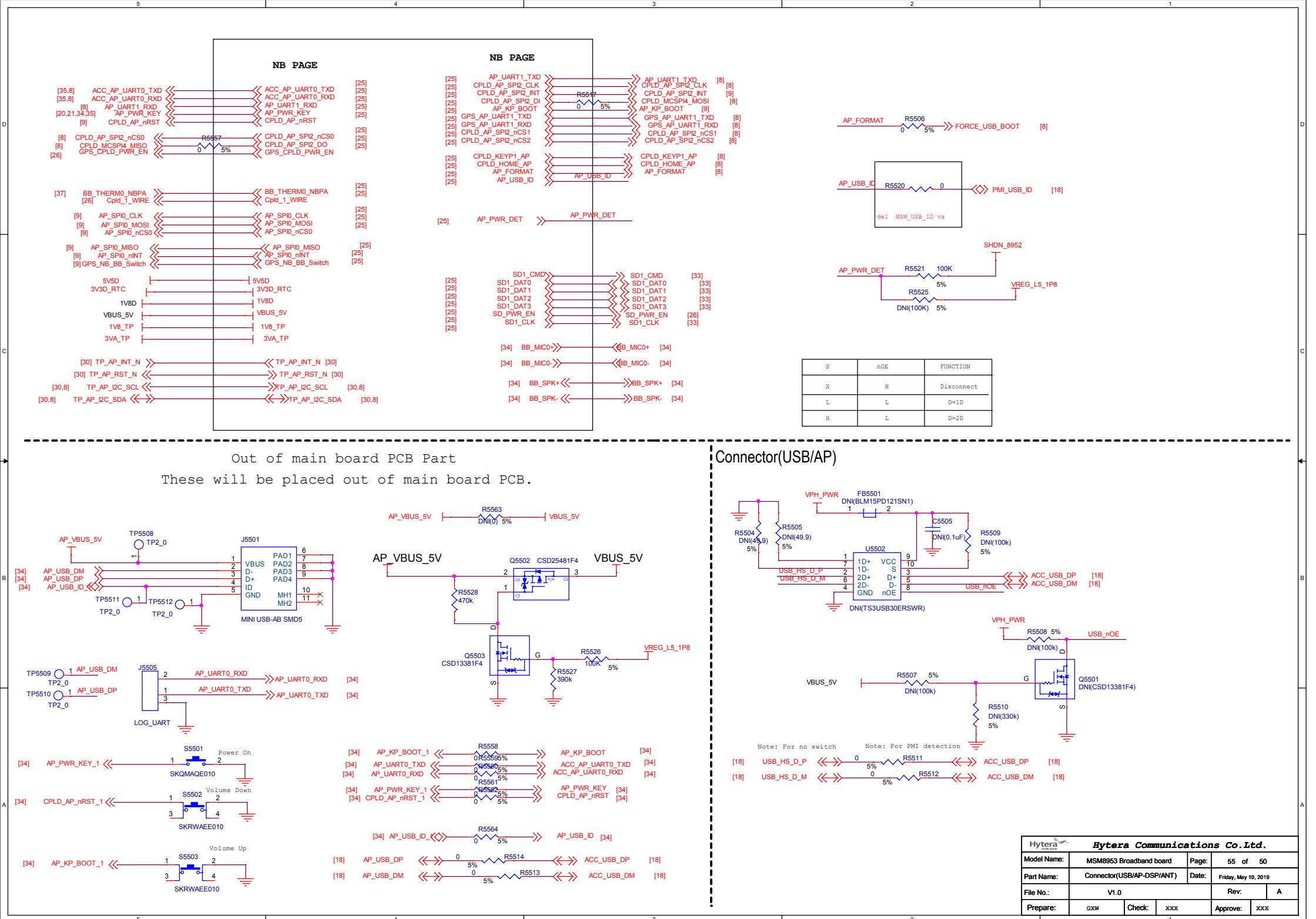
Note:  
Sensor VDD power need 2.8V power  
Sensor VDDIO power need 1.8V power

ICM-20600 is compatible with BMI160  
If BMI160, C4405-NC, R4406-NC



# Sensor

Hytera Communications Co.,Ltd.			
Model Name:	MSM8953 Broadband board	Page:	54 of 50
Part Name:	Sensor	Date:	Friday, May 10, 2019
File No.:	V1.0	Rev:	A
Prepare:	GXW	Check:	xxx
Approve:	xxx		



# NPT Point

PCBA AUTOMATIC TEST point

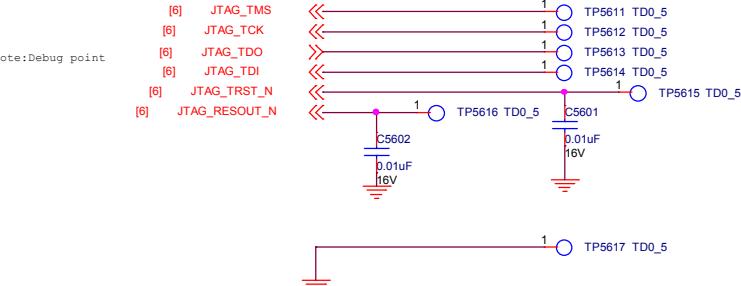
## JTAG TEST POINT



Note:Boot\_config [0], WDOG\_DISABLE delete this test point and replaced by R7008/GRFC6\_SEL

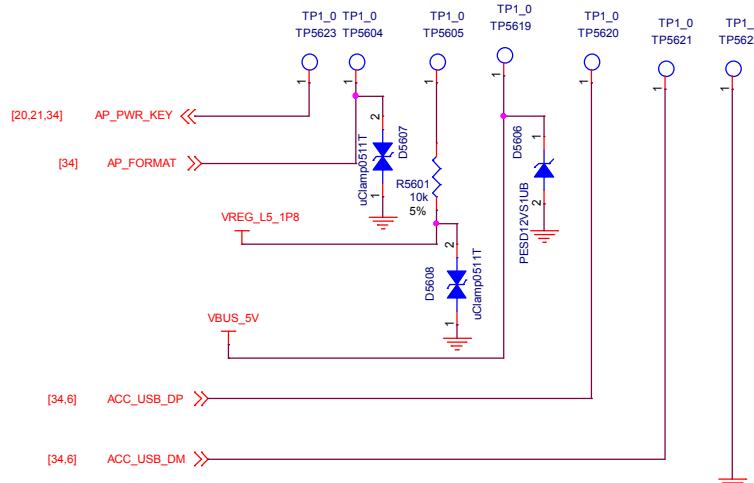


Note:LOG UART



## Test Point

Note:TP5220/21 Place on the line of USB D+/-



Note:Place ESD close to TP5220/21

Signal	Description
KPSNS0	Volume Up
PM_RST_N	Volume Down
KYPD_PWR_N	PWER_ON
PM_RST_N + KYPD_PWR_N	Hardware Reset

## TP/GND/Shielding

Hytera Communications Co.Ltd.			
Model Name:	MSM8953 Broadband board	Page:	56 of 50
Part Name:	TP/GND/Shielding	Date:	Friday, May 10, 2019
File No.:	V1.0	Rev:	A
Prepare:	GXW	Check:	xxx
Approve:	xxx		

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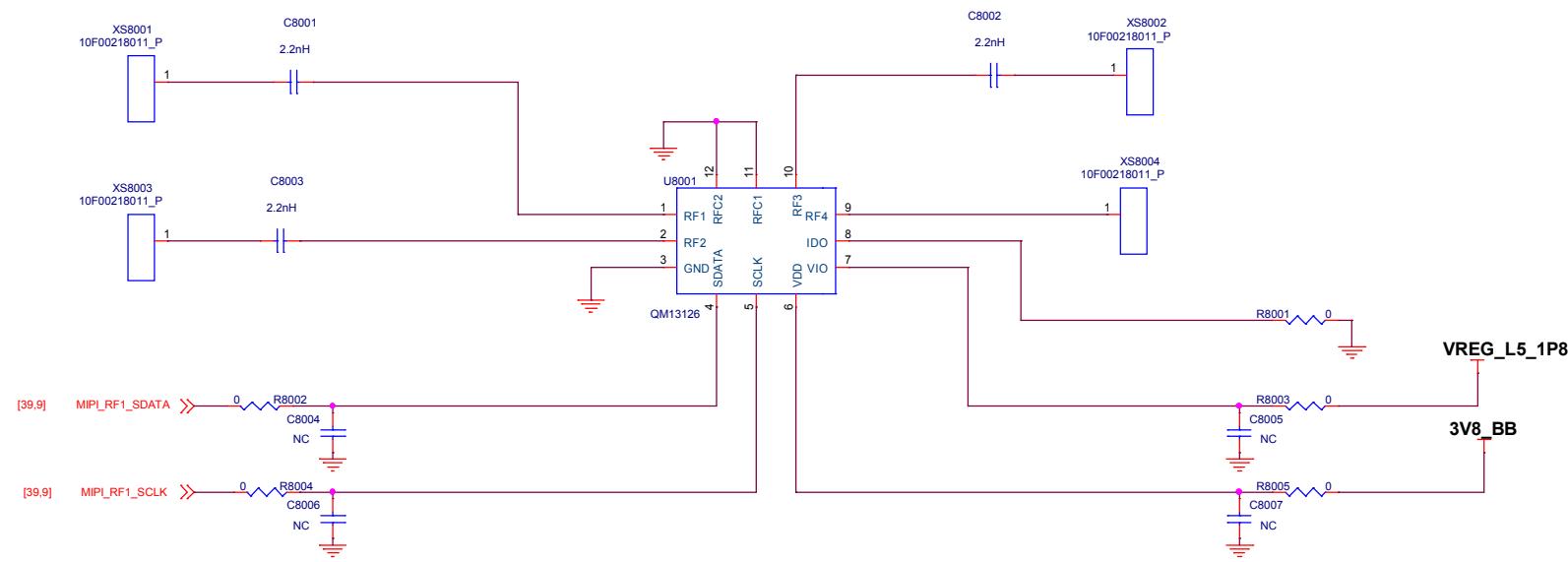
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# ANT Tuner



Hytera Communications Co.,Ltd.			
Model Name:	PD600 Um	Page:	58 of 68
Part Name:	FGU-PLL/DAC	Date:	Friday, May 10, 2019
File No.:	<orgaddr>	Rev:	A01
Prepare:	LENG PENG	Check:	xxx
Approve:	xxx	Approve:	xxx

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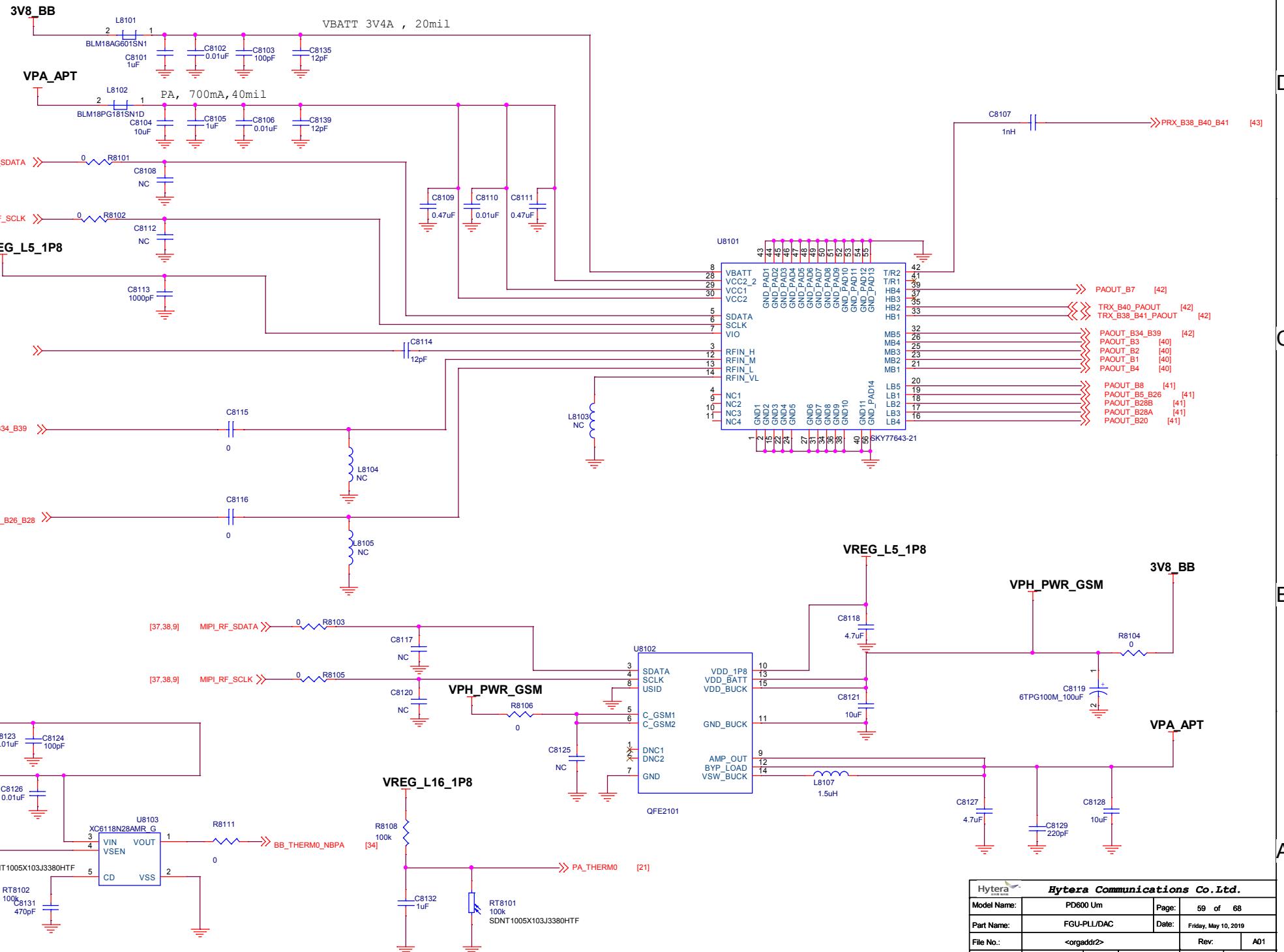
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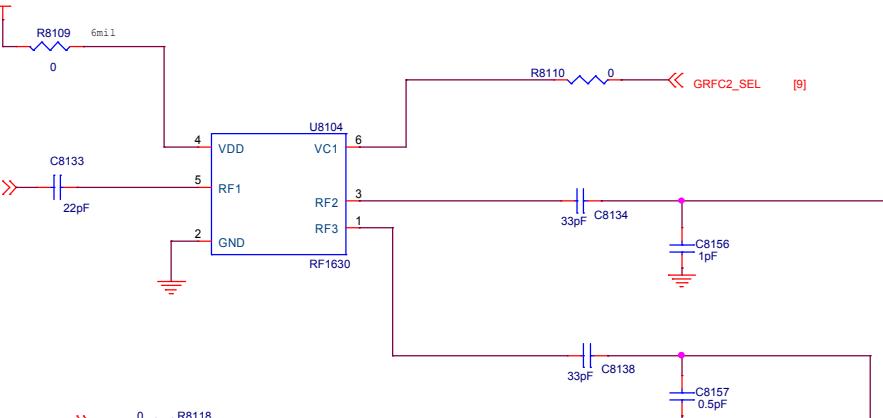
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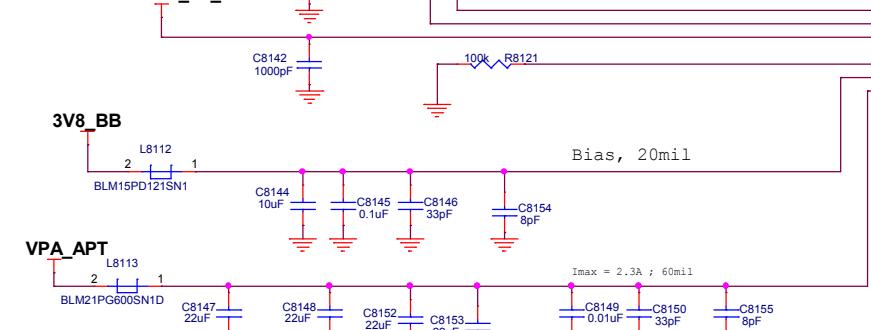


# ANT Switch

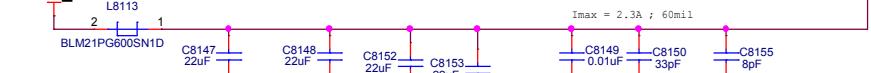
VREG\_L18\_2P7



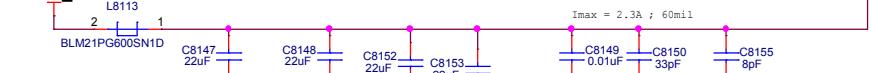
VREG\_L5\_1P8



3V8\_BB



VPA\_APT



VREG\_L18\_2P7



SKY77916-21

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**B1\_B2\_B3\_B4**

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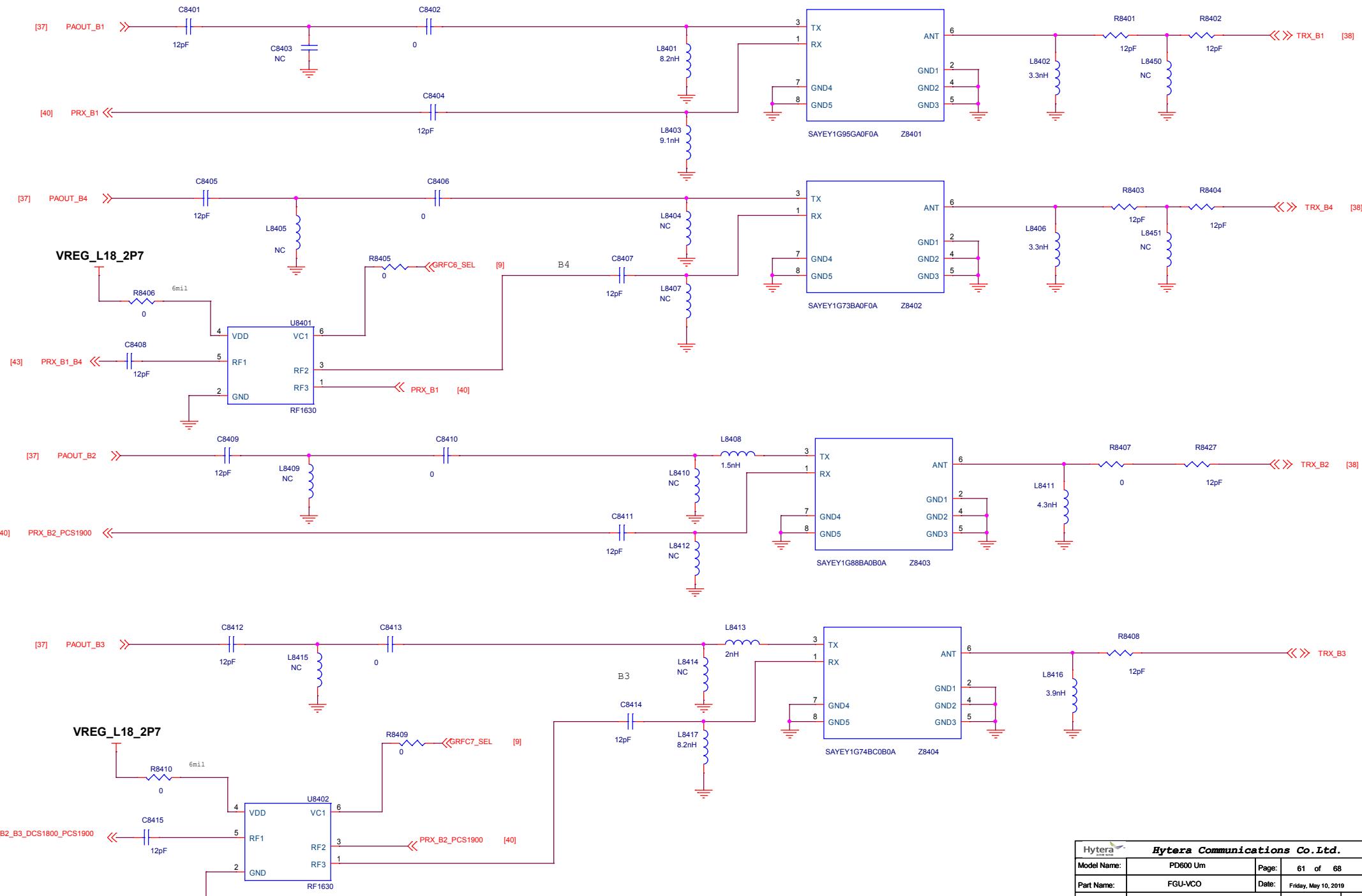
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Note: only the GSM850/1900, CDMA BC0, LTE Band 2/4/5/7/26/38/40/41 can be used in USA market.

Hytera Communications Co.,Ltd.			
Model Name:	PD600 Um	Page:	61 of 68
Part Name:	FGU-VCO	Date:	Friday, May 10, 2019
File No.:	<orgaddr>	Rev:	A01
Prepare:	LENG PENG	Check:	xxx
Approve:	xxx		

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**B5\_B8\_B20\_B26\_B28**

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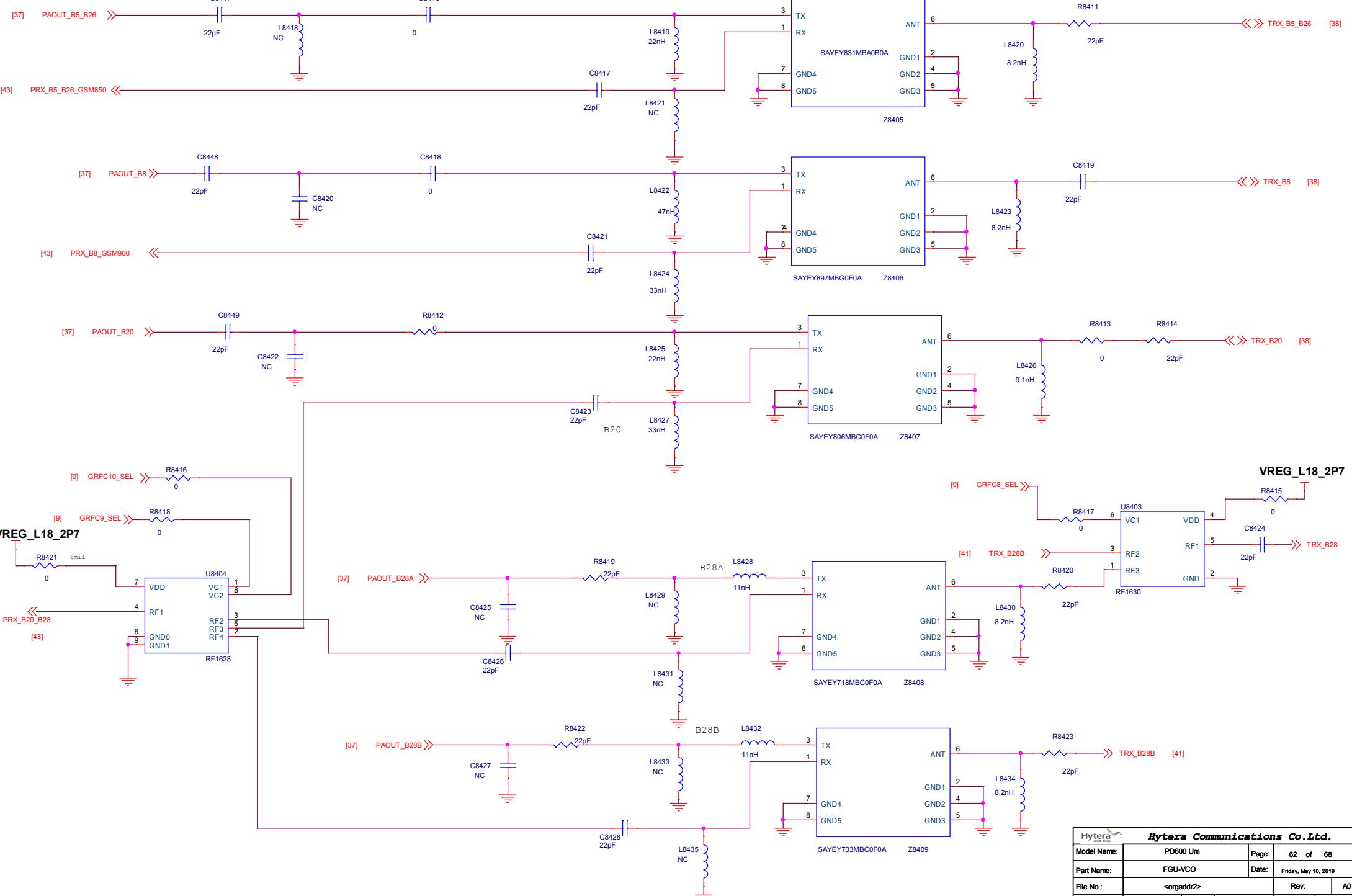
C

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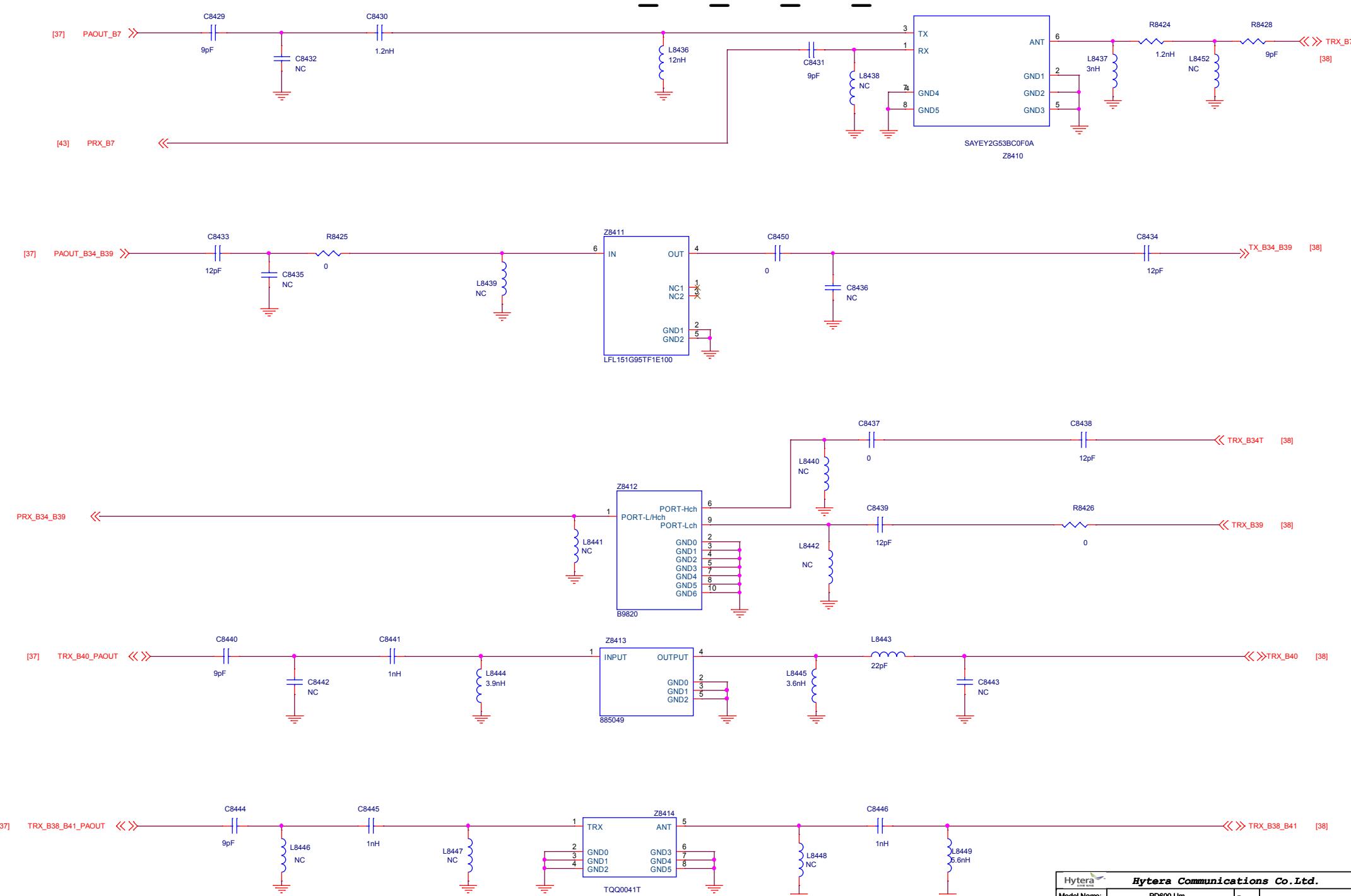
A

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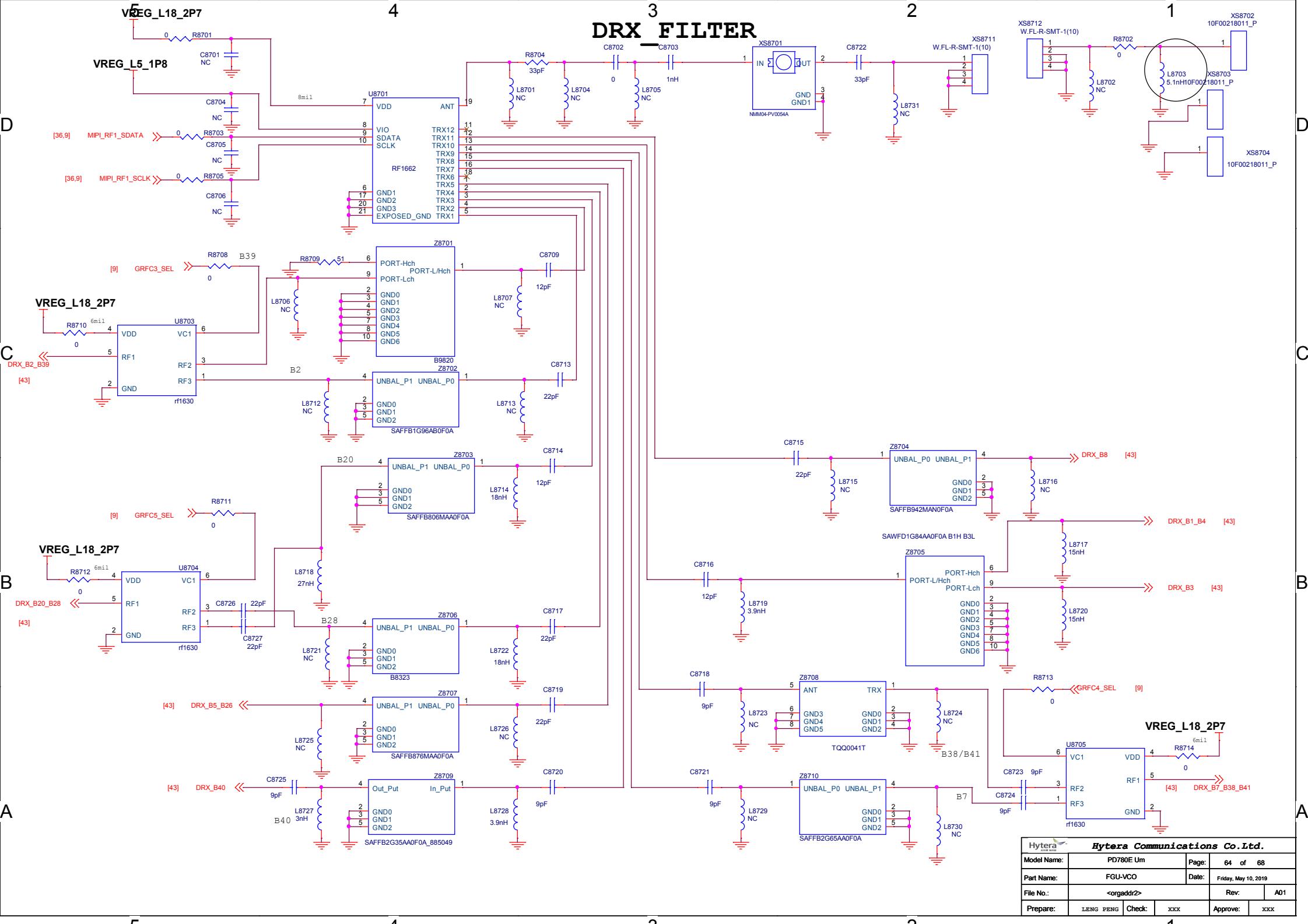
Hytera Communications Co., Ltd.			
Model Name:	PD600 Um	Page:	62 of 68
Part Name:	FGU-VCO	Date:	Friday, May 10, 2019
File No.:	<orgaddr>	Rev:	A01
Prepare:	LENG PENG	Check:	xxx
Approve:	xxx		

# B7\_B38\_B39\_B40\_B41

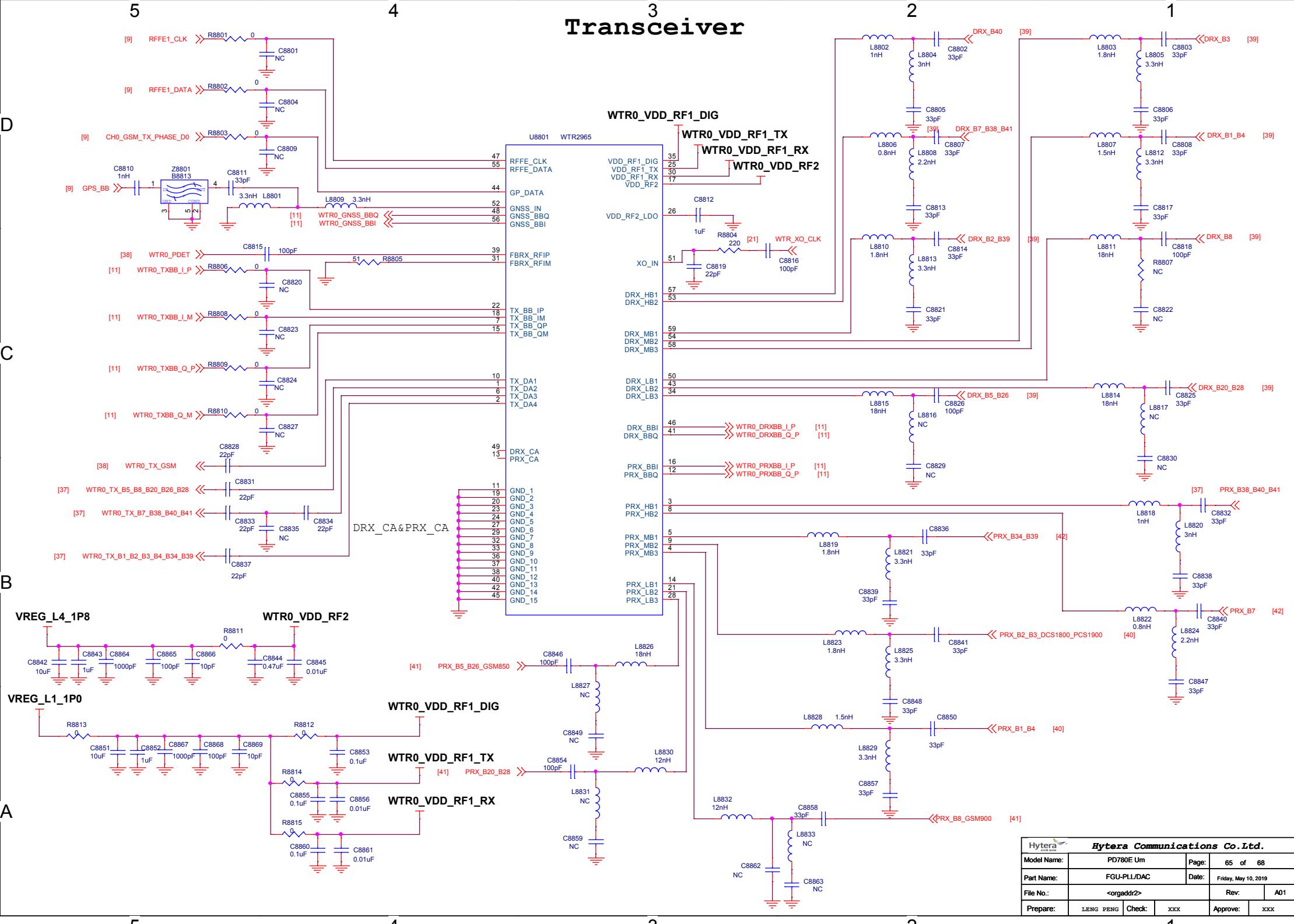


Hytera Communications Co.,Ltd.			
Model Name:	PD600 Um	Page:	63 of 68
Part Name:	FGU-VCO	Date:	Friday, May 10, 2019
File No.:	<orgaddr>	Rev:	A01
Prepare:	LENG PENG	Check:	xxx
Approve:	xxx		

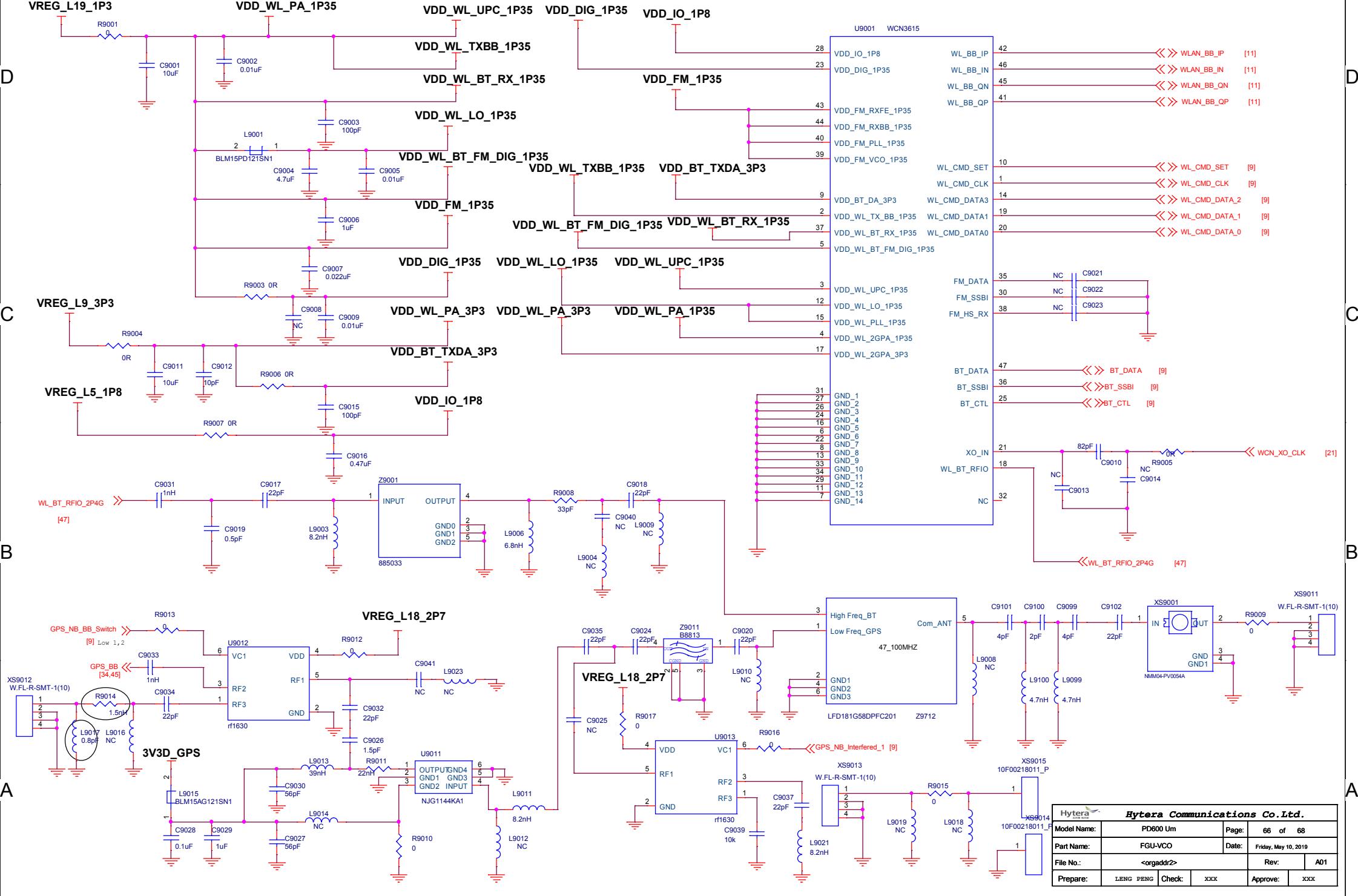
# DRX FILTER



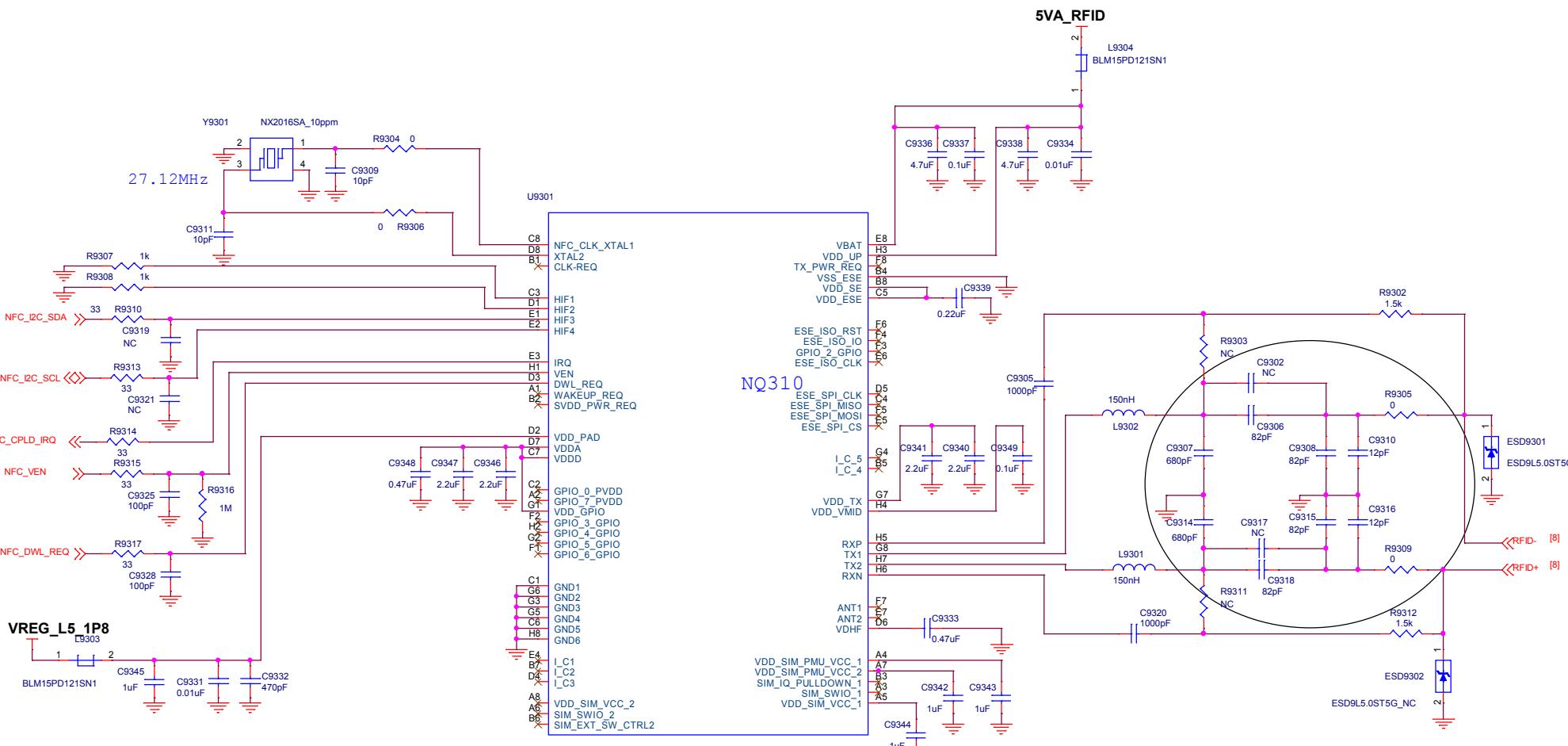
# Transceiver



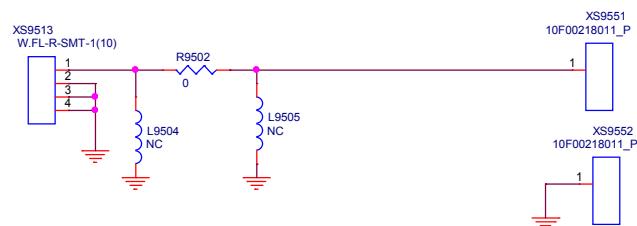
# WIFI&BT



Hytera Communications Co.Ltd.			
Model Name:	PD600 Um	Page:	66 of 68
Part Name:	FGU-VCO	Date:	Friday, May 10, 2019
File No.:	<orgaddr2>	Rev:	A01
Prepare:	LENG PENG	Check:	xxx
Approve:	xxx		



Hytera Communications Co.,Ltd.			
Model Name:	PD600 Um	Page:	67 of 68
Part Name:	FGU-VCO	Date:	Friday, May 10, 2019
File No.:	<orgaddr>	Rev:	A01
Prepare:	LENG PENG	Check:	xxx
Approve:	xxx		



Hytera Communications Co., Ltd.			
Model Name:	PDC680 NB	Page:	68 of 68
Part Name:	BT_ANT_Pogopin	Date:	Friday, May 10, 2019
File No.:		Rev.:	A
Prepare:		Check:	Approve: