

A101P

Rev: A1.0

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CPU+NB:

INTEL PINEVIEW M

Core Power

IMVP6; Controller: ISL6261ACRZ 1 Phase

Chipset:

Intel Tiger Point

Display:

VGA, LVDS

Memory:

DDR2(667) - Up to 1GB

External Storage:

1 SATA(3Gb/s)

Others Chipset:

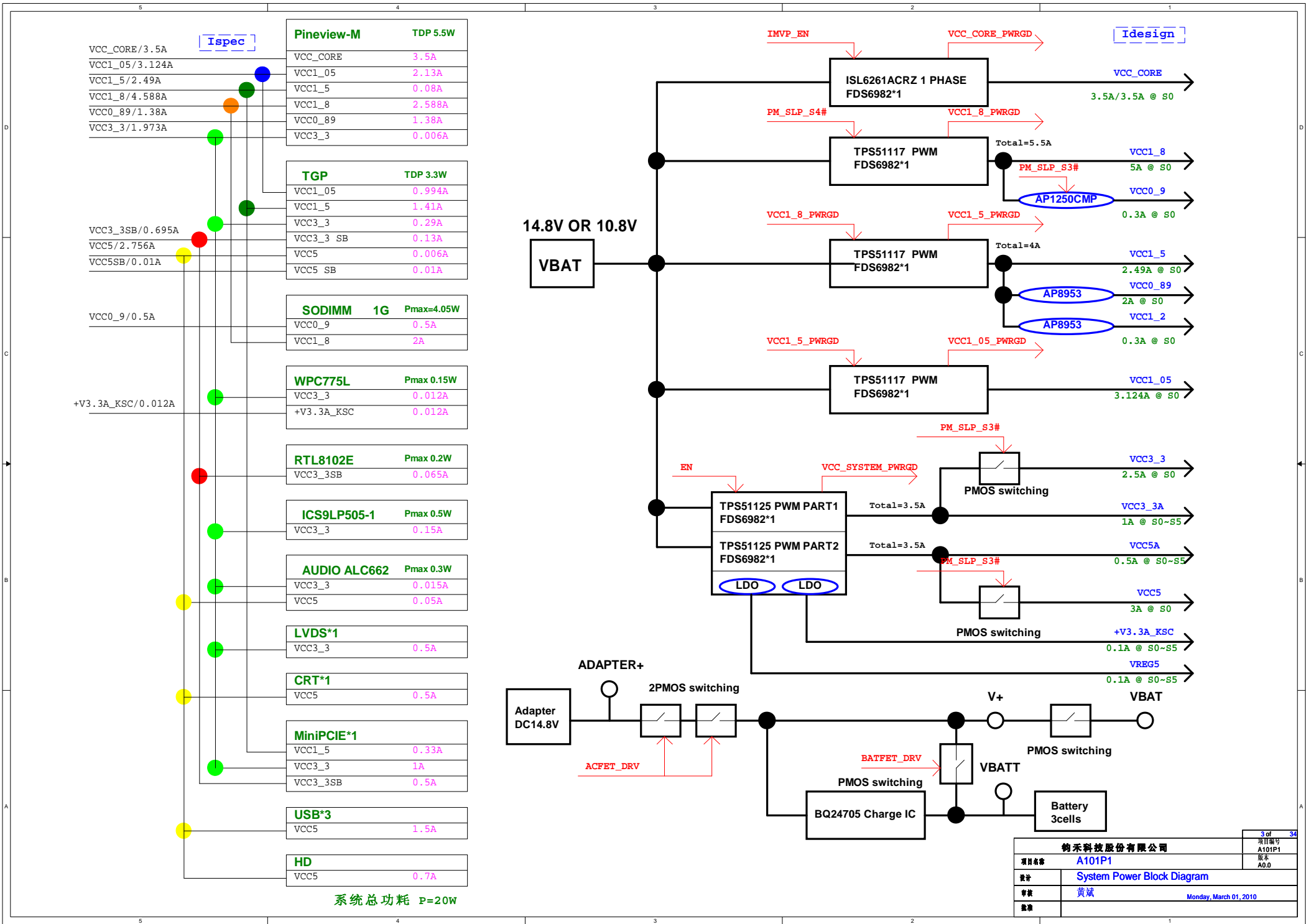
EC - WPC775L

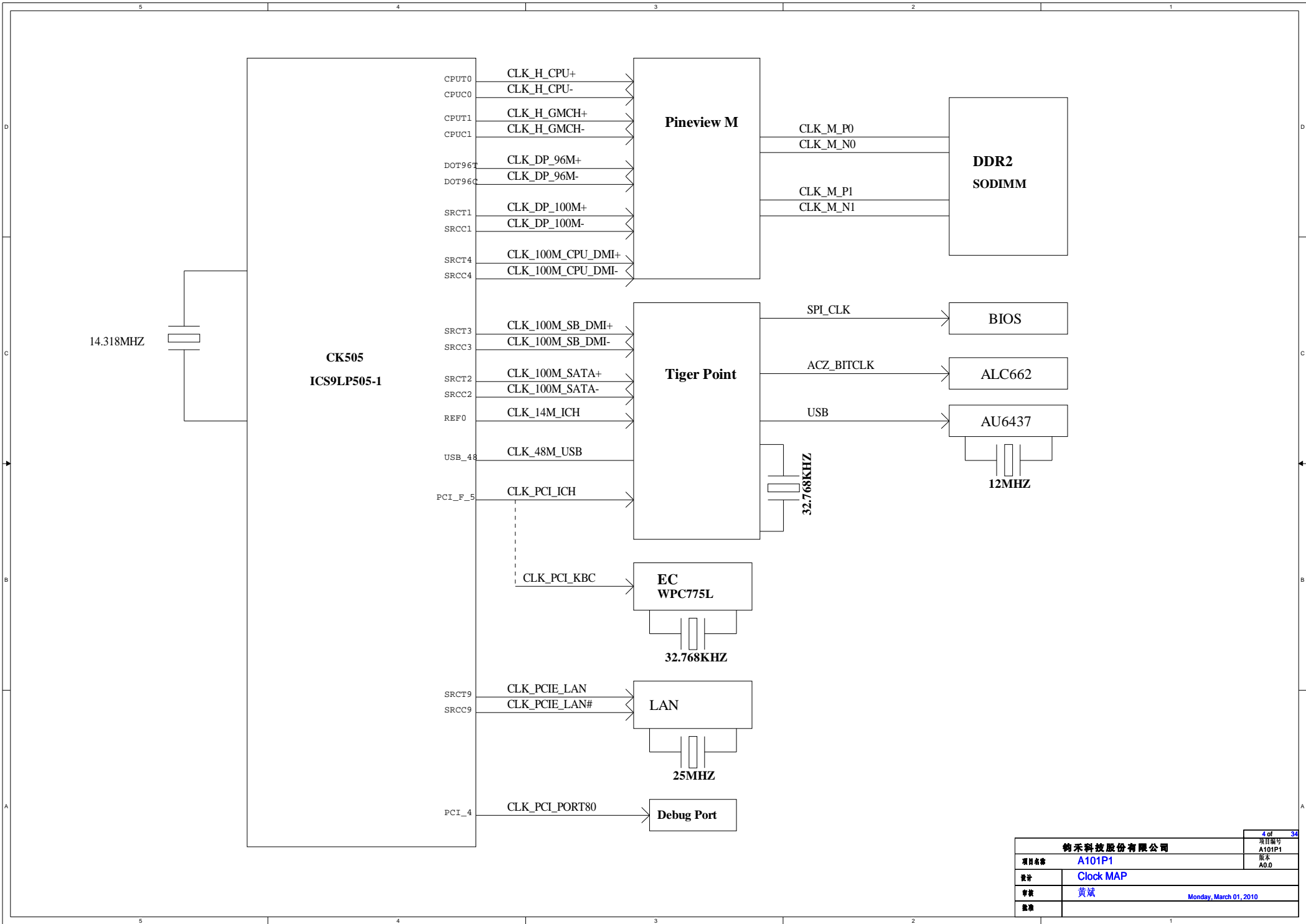
LAN - RTL8102E

BIOS -SPI FLASH

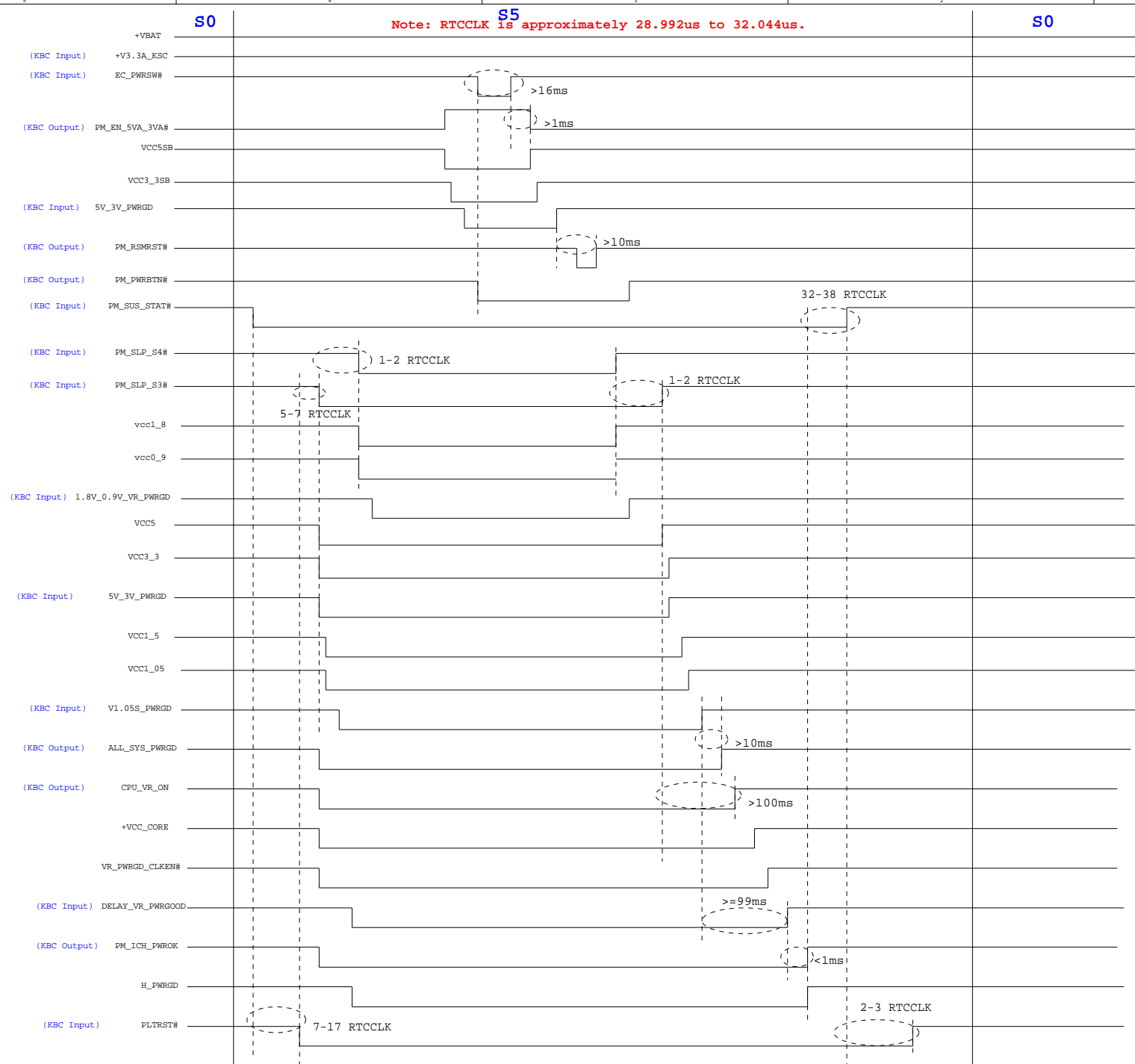
Clock Generator - ICS9LP505-1HGLF

钩禾科技股份有限公司		1 of 34
项目名称	A101P1	项目编号 A101P1
设计	Cover Sheet	版本 A0.0
审核	黄斌	Monday, March 01, 2010
批准		



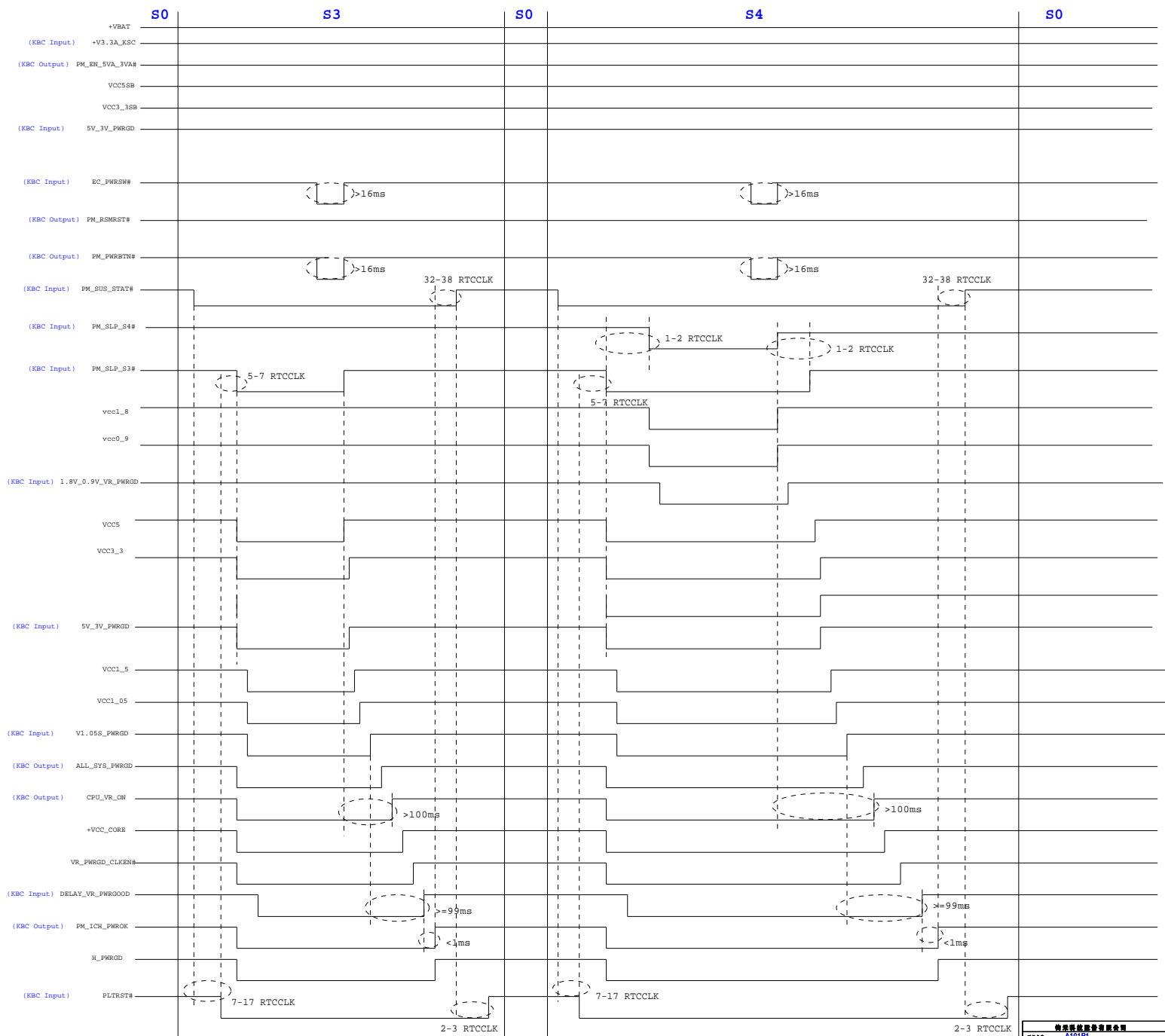


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项目名称 钧禾科技股份有限公司	
设计	A101P1
审核	黄斌
批准	Monday, March 01, 2010

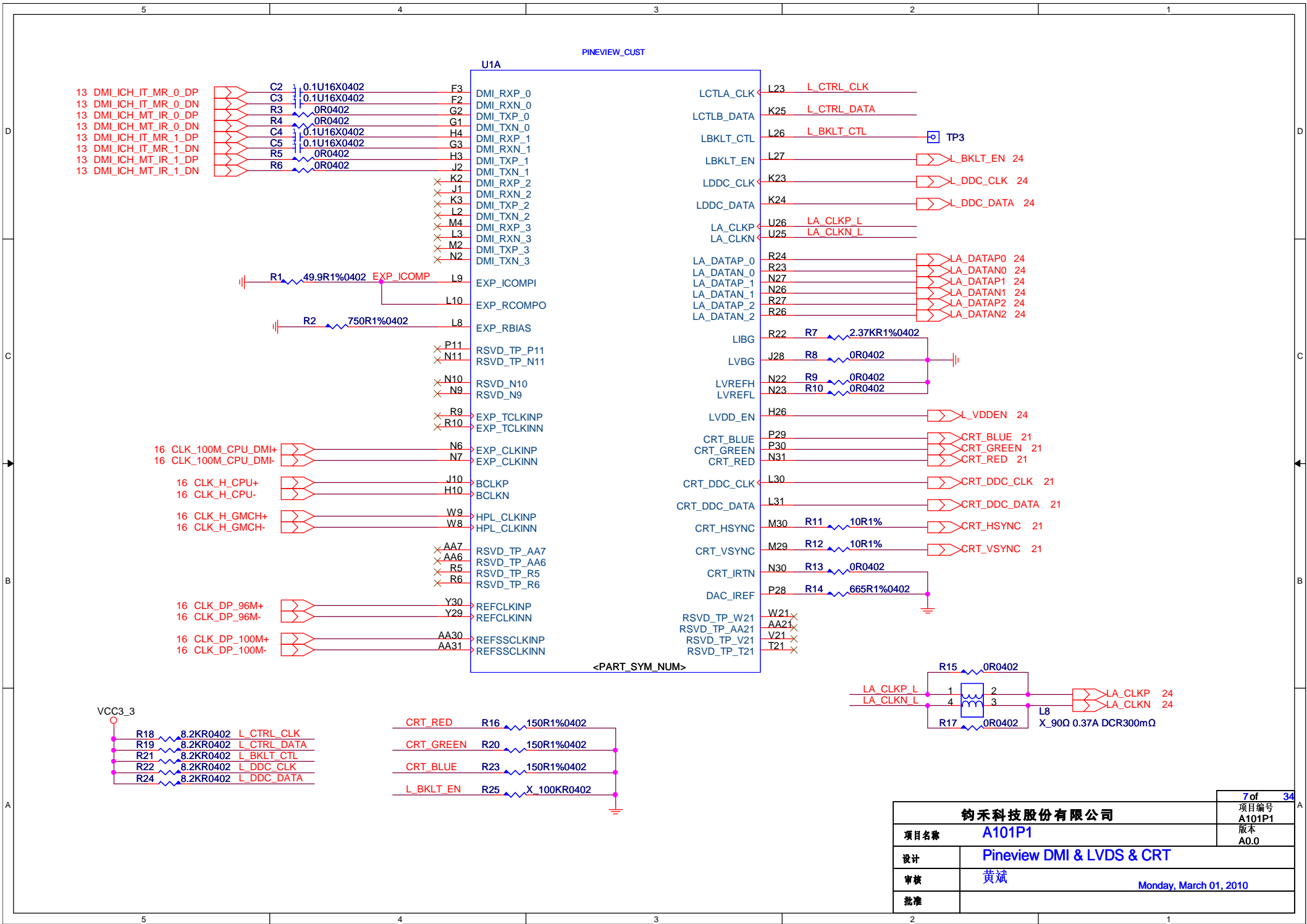


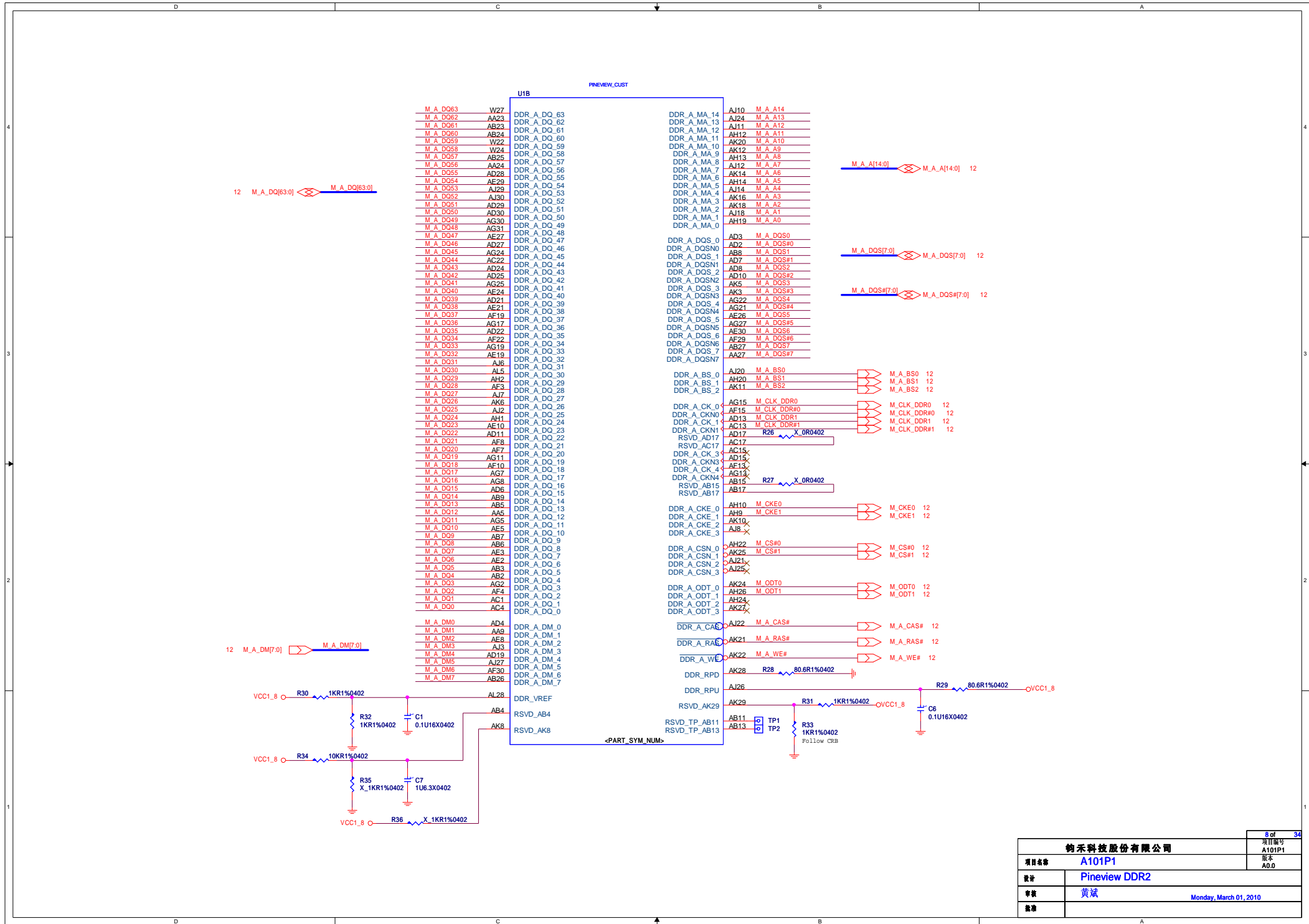
Power Sequence-1		Rev. 1.0
Project Name	A101P1	Project No.
Version	1.0	Rev. 1.0
Author	黄斌	Modified
Check		Modified

Note: RTCCLK is approximately 28.992us to 32.044us.

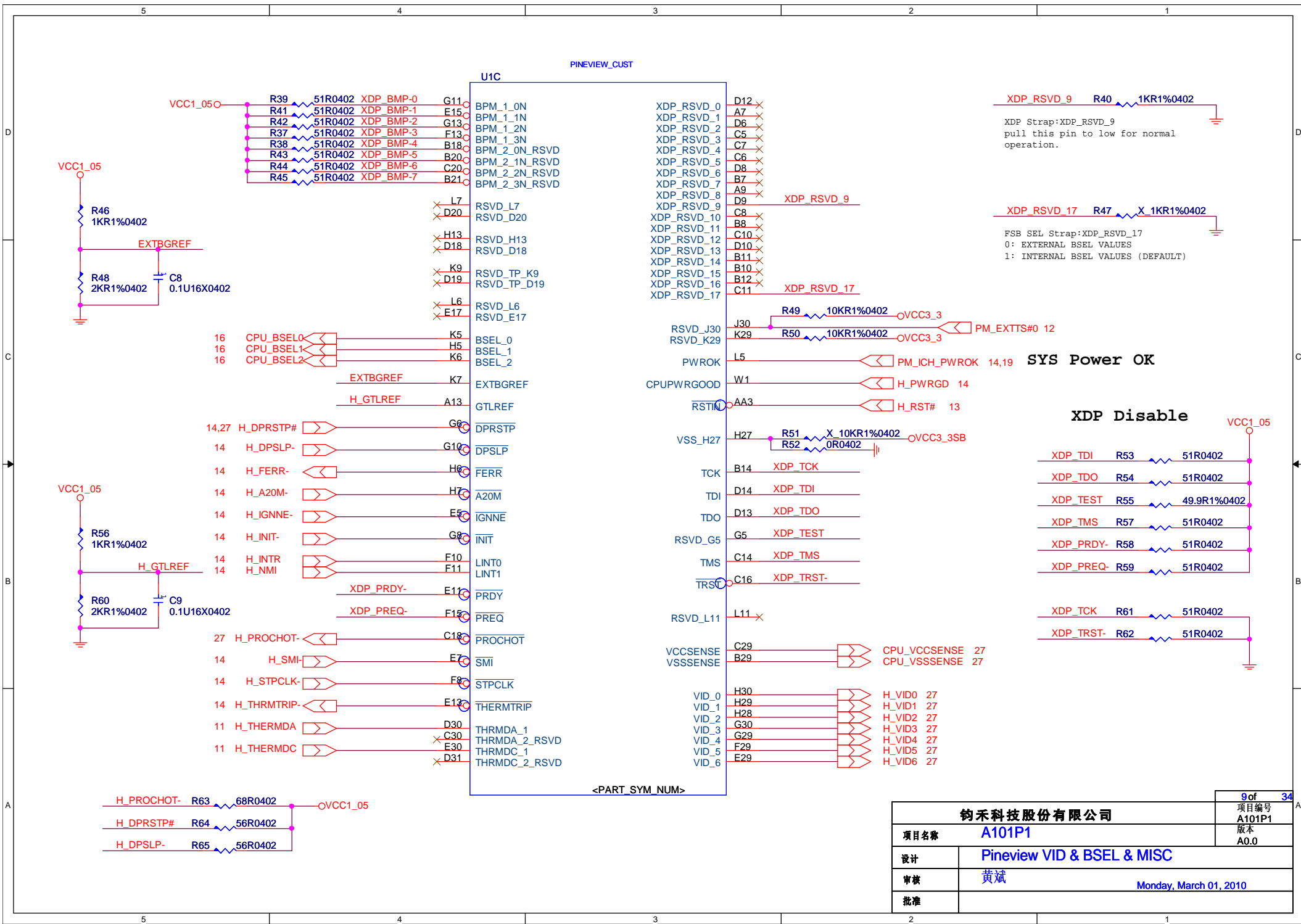


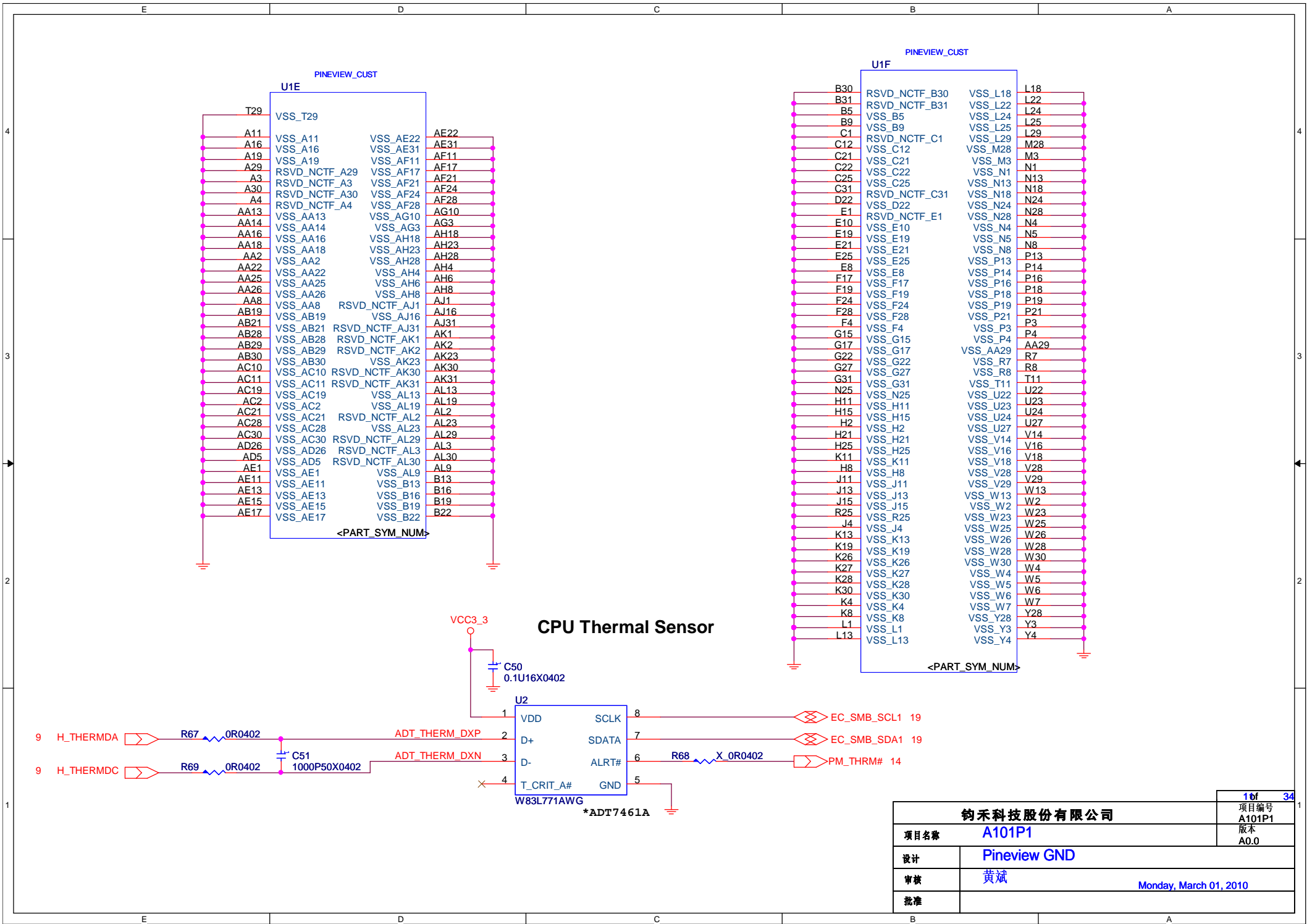
		第 68 / 74
特許技術設備有限公司		
項目番号	A10P1	項目備考 A10P1 版数 A0.0
00	Power Sequence-2	
99	黄斌	Munday, March 01, 2020
00		



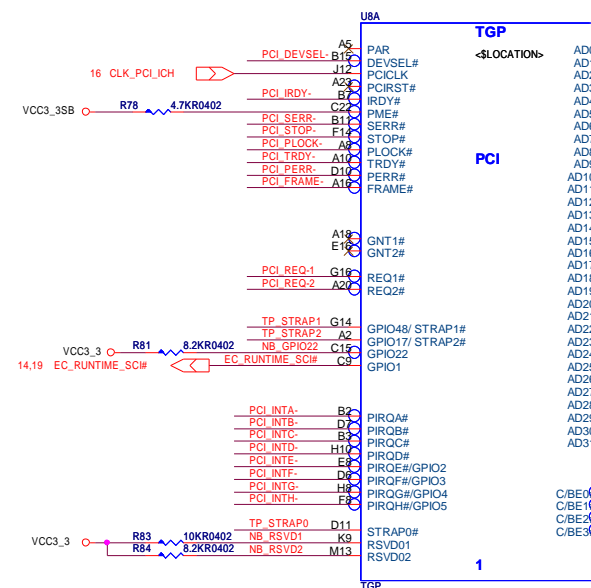


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项目名称		禾禾科技股份有限公司
设计		A101P1
审核		黄斌
日期		Monday, March 01, 2010

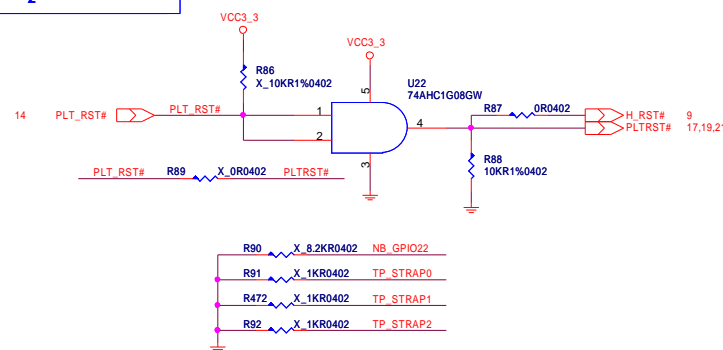




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项目名称	A101P1	项目编号 A101P1
设计	Pineview GND	版本 A0.0
审核	黄斌	Monday, March 01, 2010
批准		



PCI Pull ups



BIOS	STRAP1#/ GPIO48	STRAP2#/ GPIO17
SPI	1	0
PCI	0	1
LPC *	1	1

SATA1

14 SATA_TXP0 SATA_TXP0 C82 3300P50X0402 S1 GND1 GND9 1

14 SATA_TXN0 SATA_TXN0 C83 3300P50X0402 S2 A1+

14 SATA_RXN0 SATA_RXN0 C84 3300P50X0402 S3 S3

14 SATA_RXP0 SATA_RXP0 C85 3300P50X0402 S4 A1-

S5 GND2

S6 B1-

S7 B1+

S8 GND3

VCC3_3 P1 V33_1

C86 X 10U10X0805 P2 V33_2

C87 0.1U16X0402 P3 V33L

P4 GND4

P5 GND5

P6 GND6

VCC5 P7 V5L

C88 22U6.3X0805 P8 V5_1

C89 0.1U16X0402 P9 V5L2

C90 0.1U16X0402 P10 V5L7

P11 RSV

P12 GND8

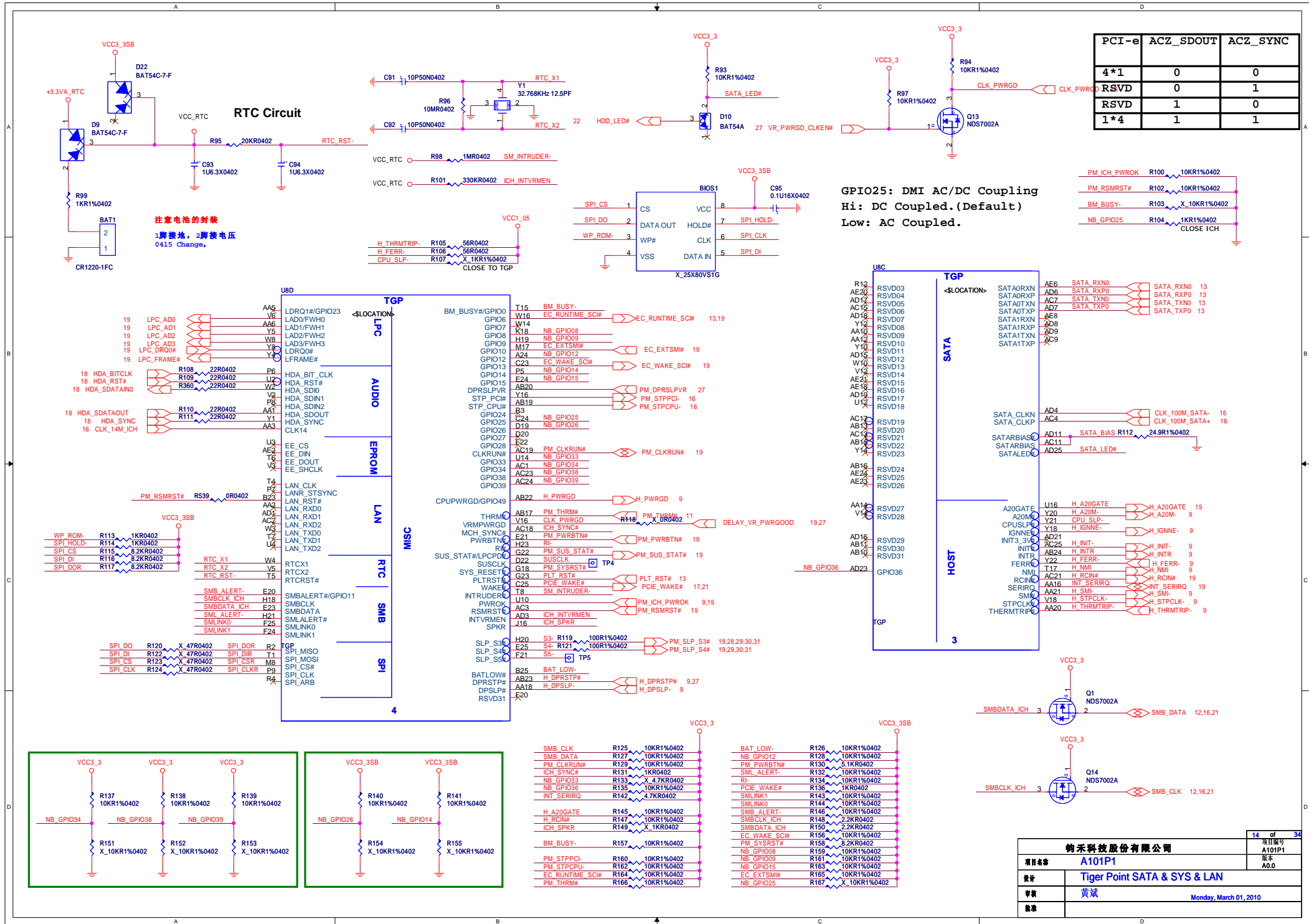
P13 V12L

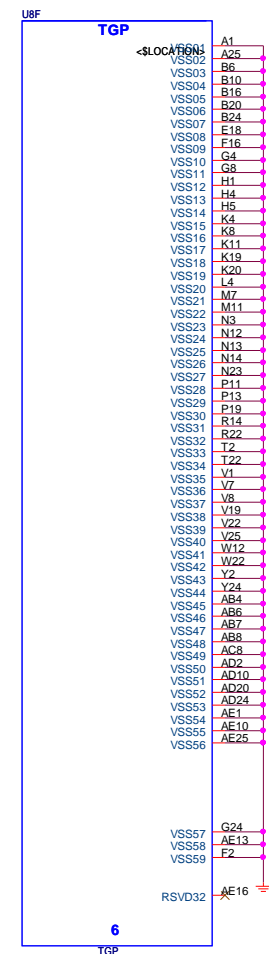
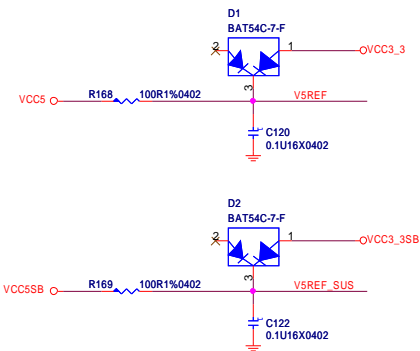
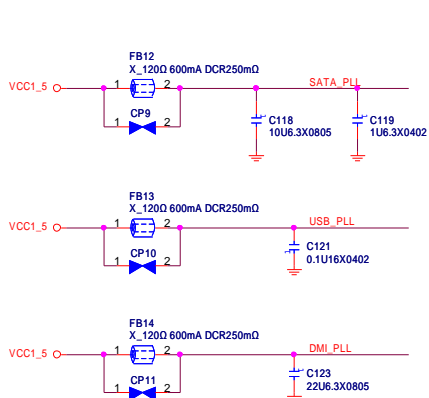
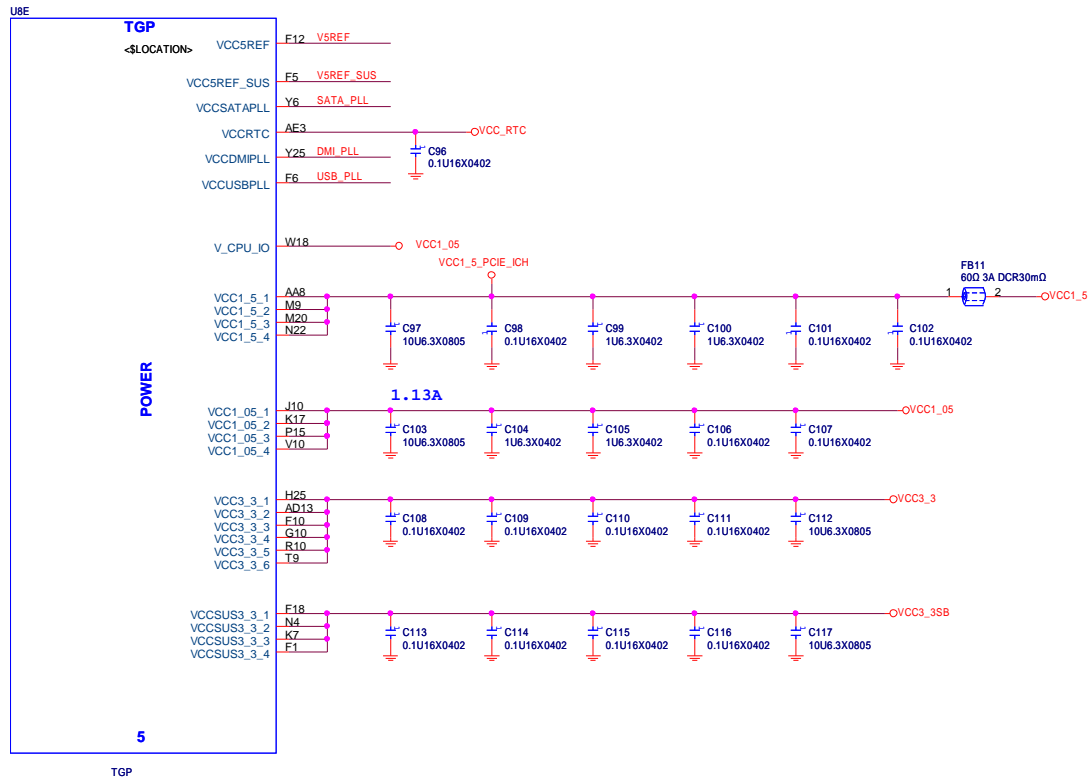
P14 V12_1

P15 V12_2 GND10 2

SATA HDD 22P SMT

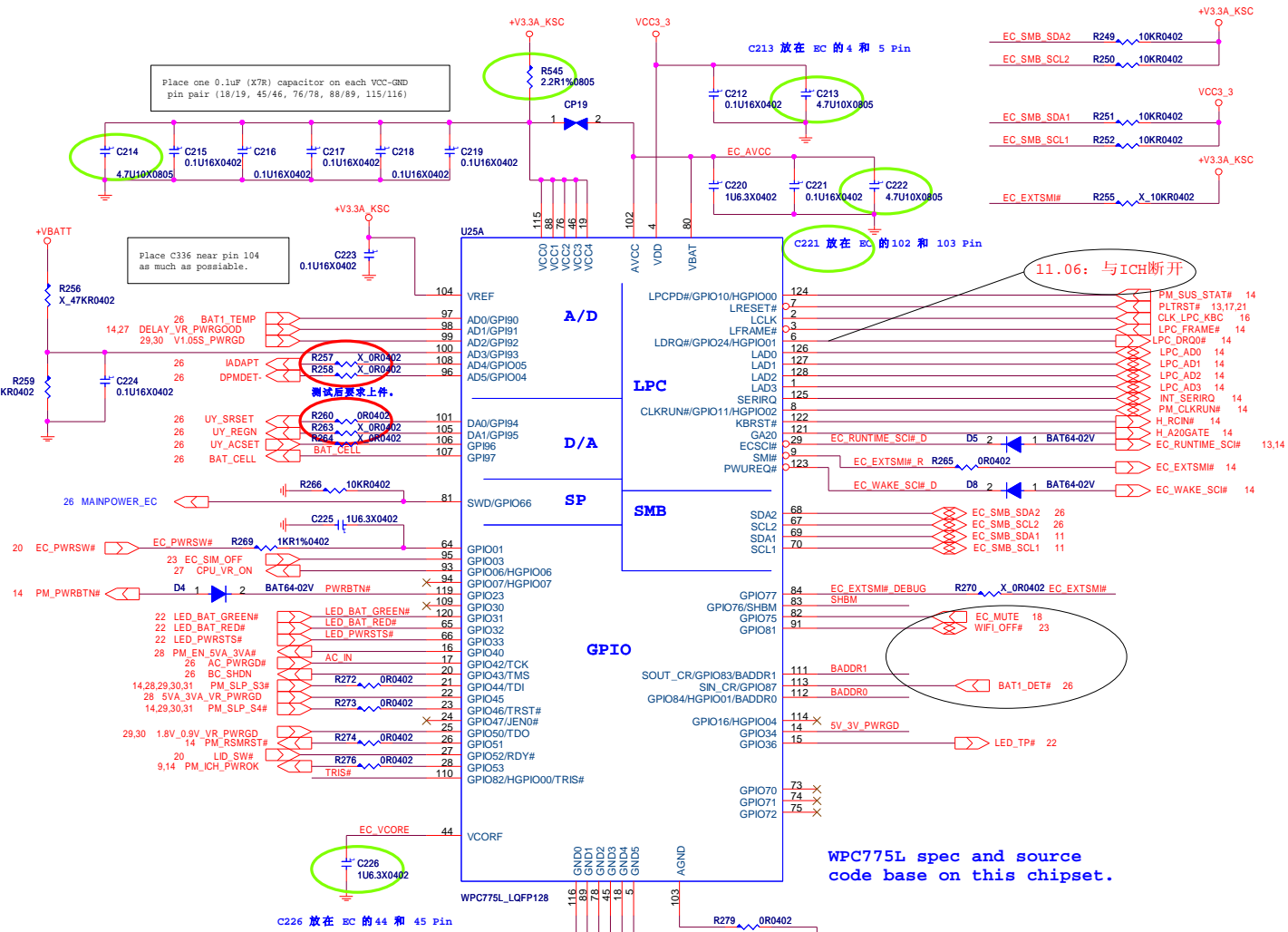
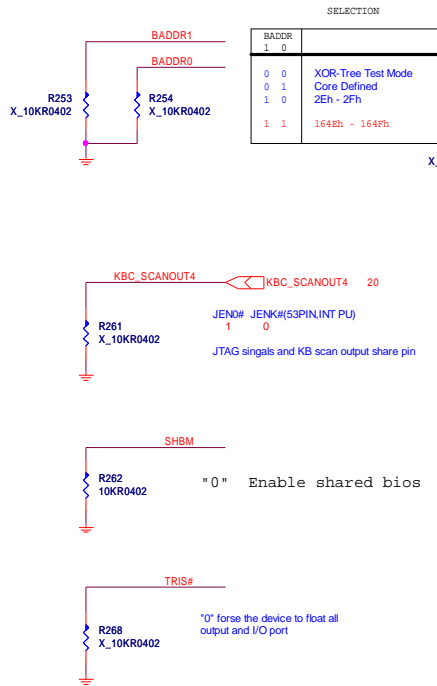
		13 of 3
<p align="center">钧禾科技股份有限公司</p>		项目编号 A101P1
项目名称	A101P1	版本 A.0.0
设计	Tiger Point PCI-E & USB & SATA	
审核	黄斌	Monday, March 01, 2010
批准		



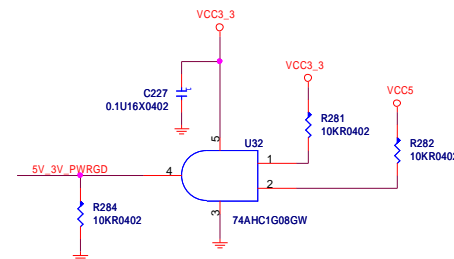


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项目名称		钩禾科技股份有限公司
设计		A101P1
审核		黄斌
日期		Monday, March 01, 2010

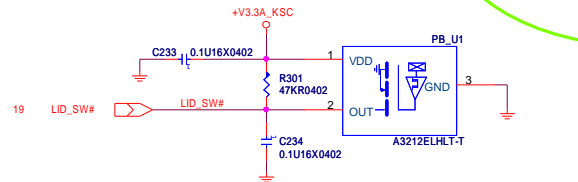
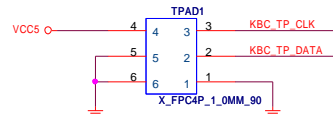
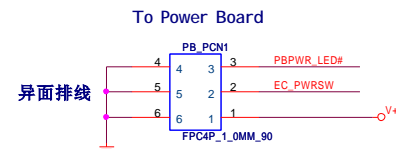
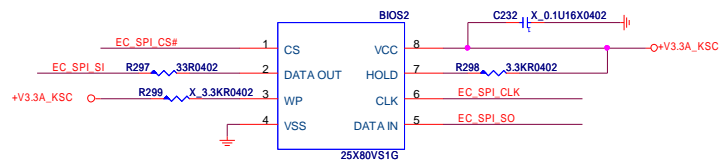
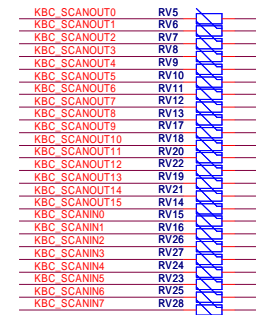
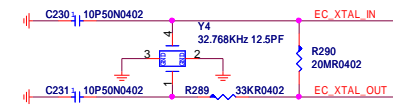
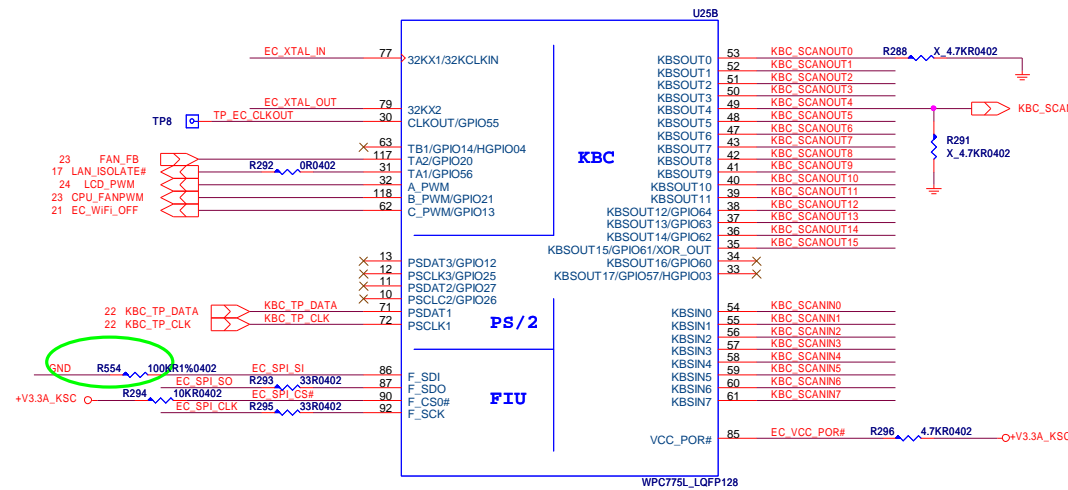
EC hardware strap



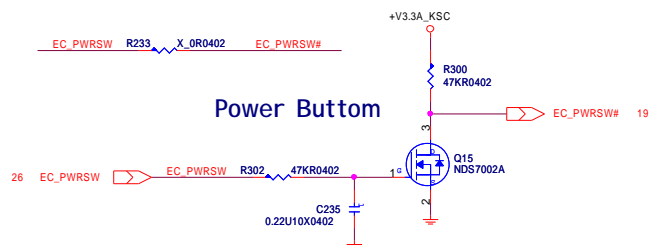
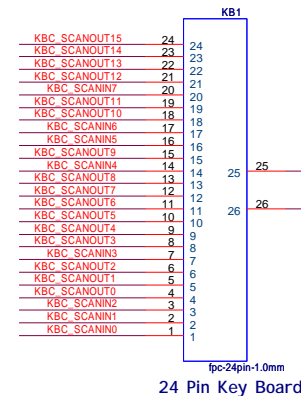
WPC775L spec and source
code base on this chipset.



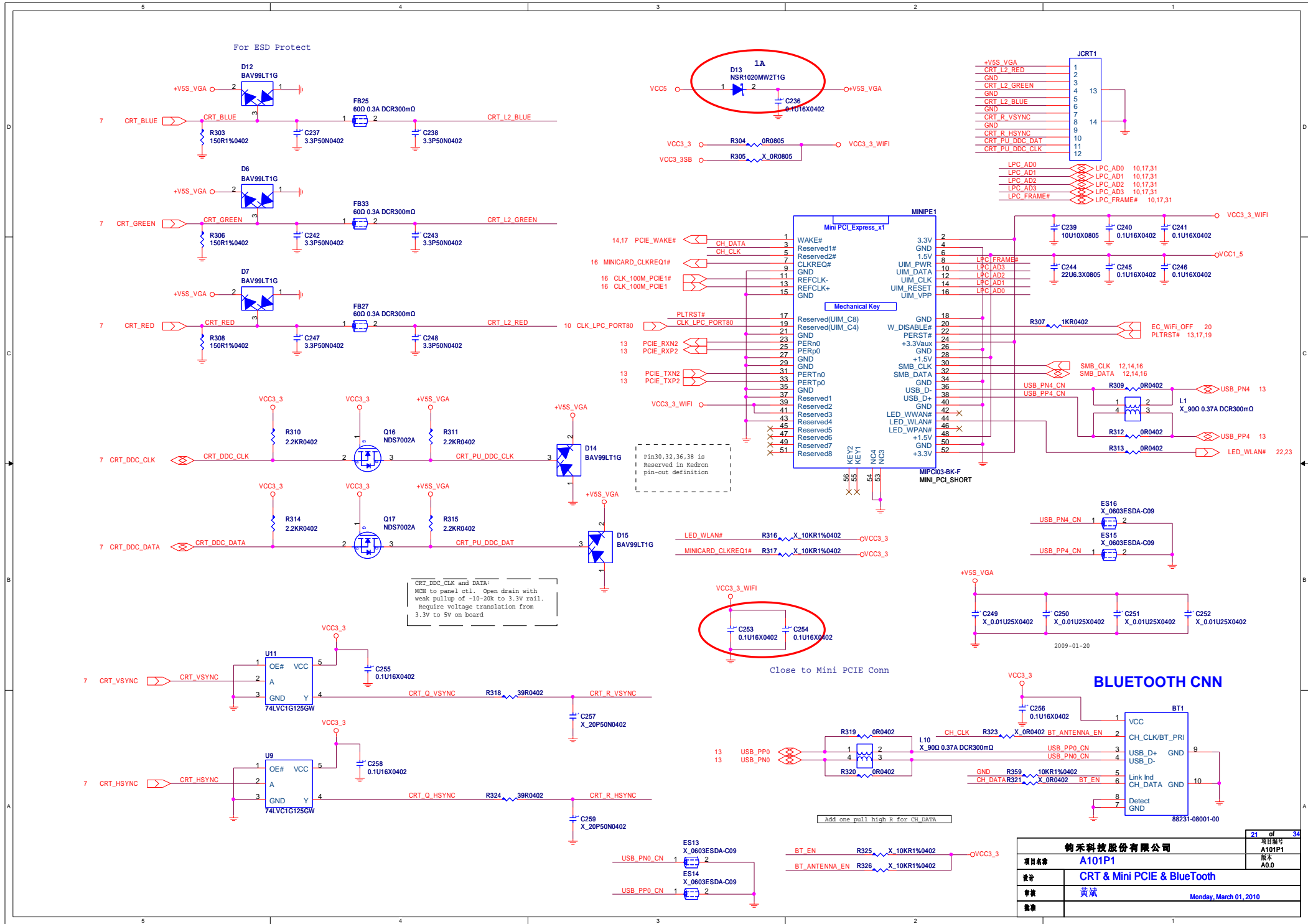
		1st	3rd
禾铄科技股份有限公司		项目编号 A101P1	
项目名称	A101P1	版本 A0.0	
设计	EC(1/2)		
审核	黄斌	Monday, March 01, 2010	
批准			



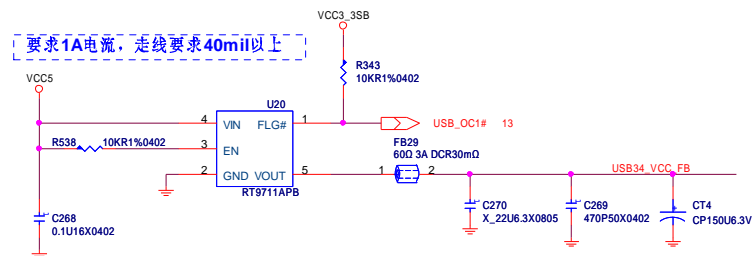
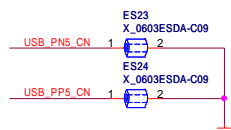
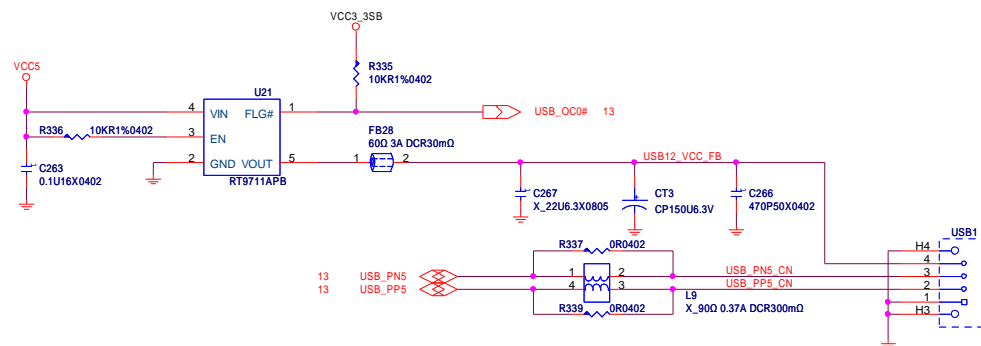
送到 EC 屏幕在盖住时，会有一个磁铁靠近，霍尔开关发出一个 Low Pulse，让系统进入待机状态。



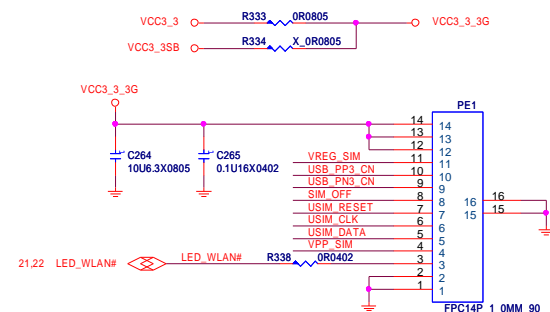
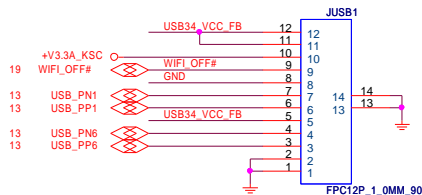
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<p>钧禾科技股份有限公司</p>		<p>项目编号 A101P1</p>
项目名称	<p>A101P1</p> <p>版本 A0.0</p>	
设计	<p>EC(2/2) & BIOS</p>	
审核	<p>黄斌</p> <p>Monday, March 01, 2010</p>	
批准		



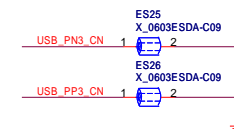
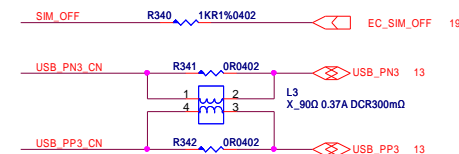
1.USB1换pin lionxiang



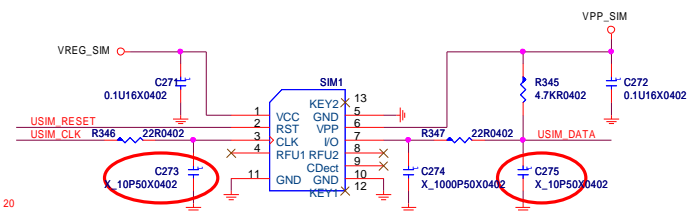
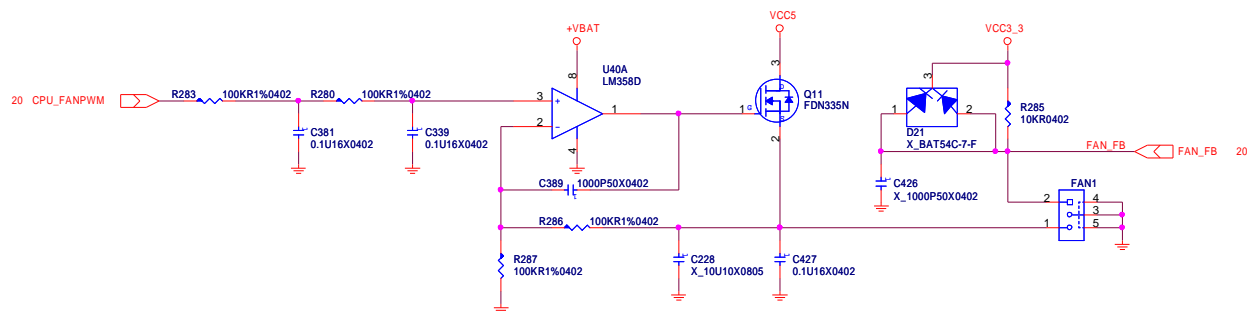
要求1A电流，走线要求40mil以上



排线：间距 1.0mm，同面接触

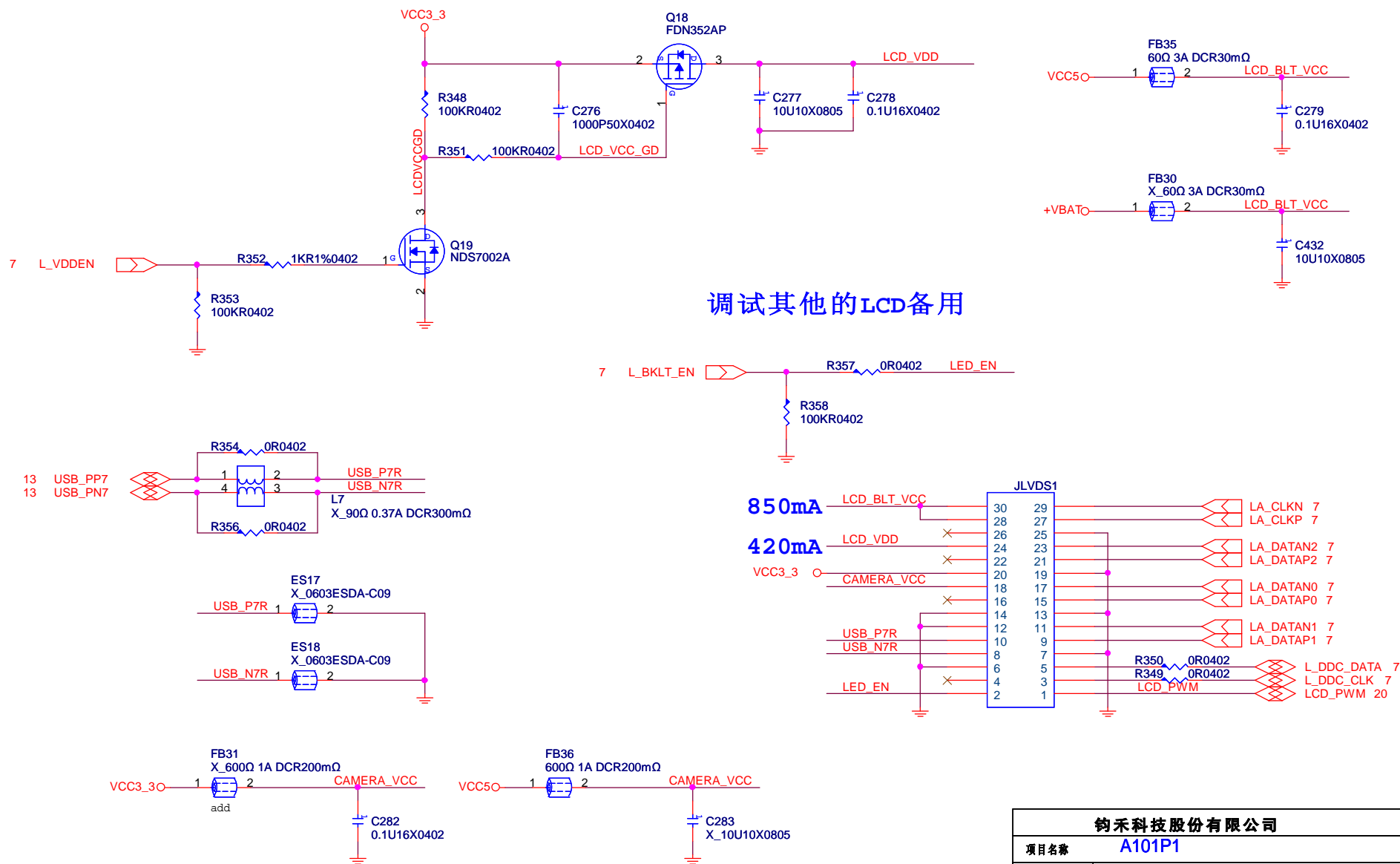


CPU FAN



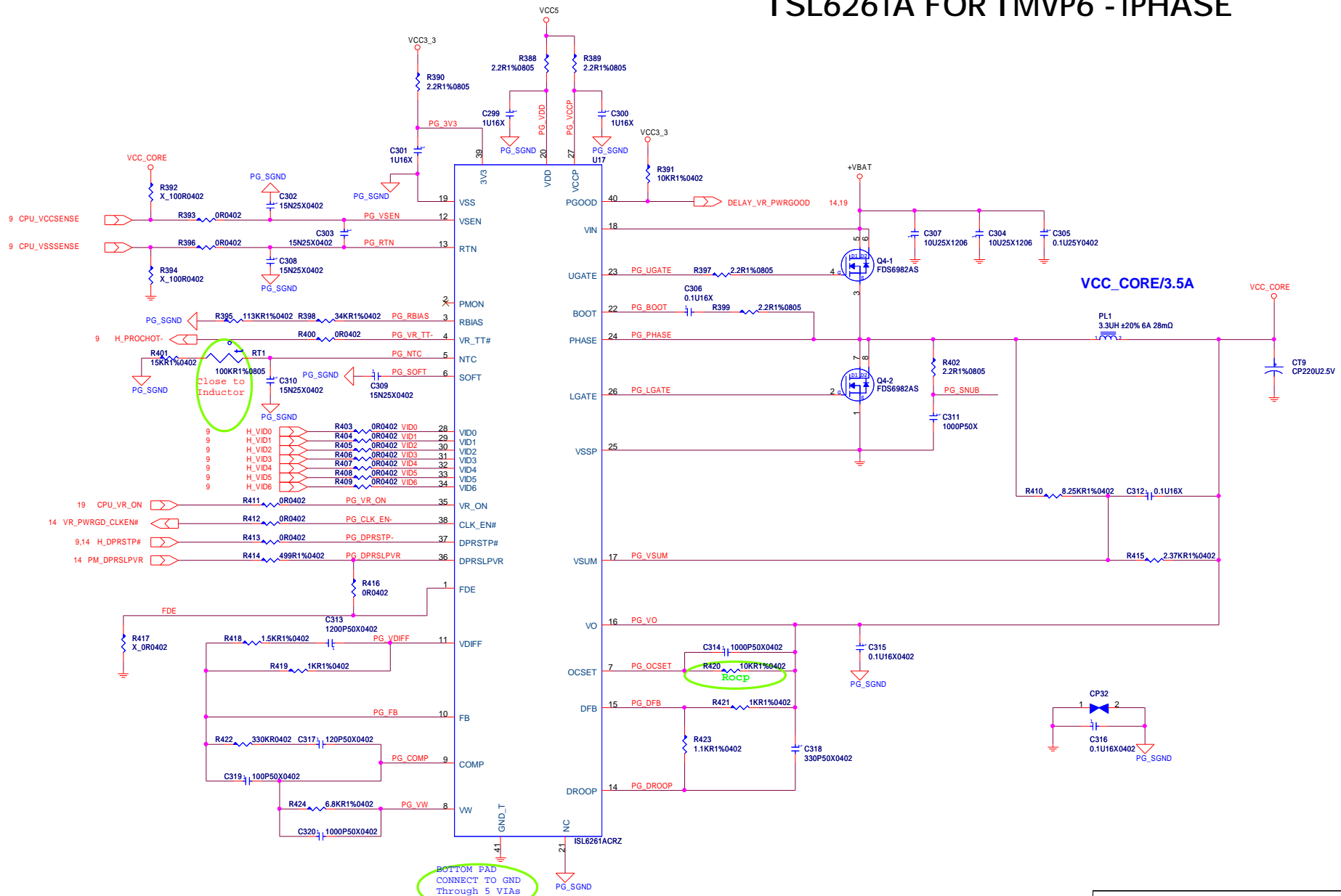
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项目名称	
A101P1	
设计	
USB & FAN & SIM Card	
审核	
黄斌	
日期	
Monday, March 01, 2010	

FootPrint:SOT23
FDN352AP:Continue Current:1.3A
NTR4101PT1G:Continue Current:2.5A
IRLML6402PBF:Continue Current:3.7A

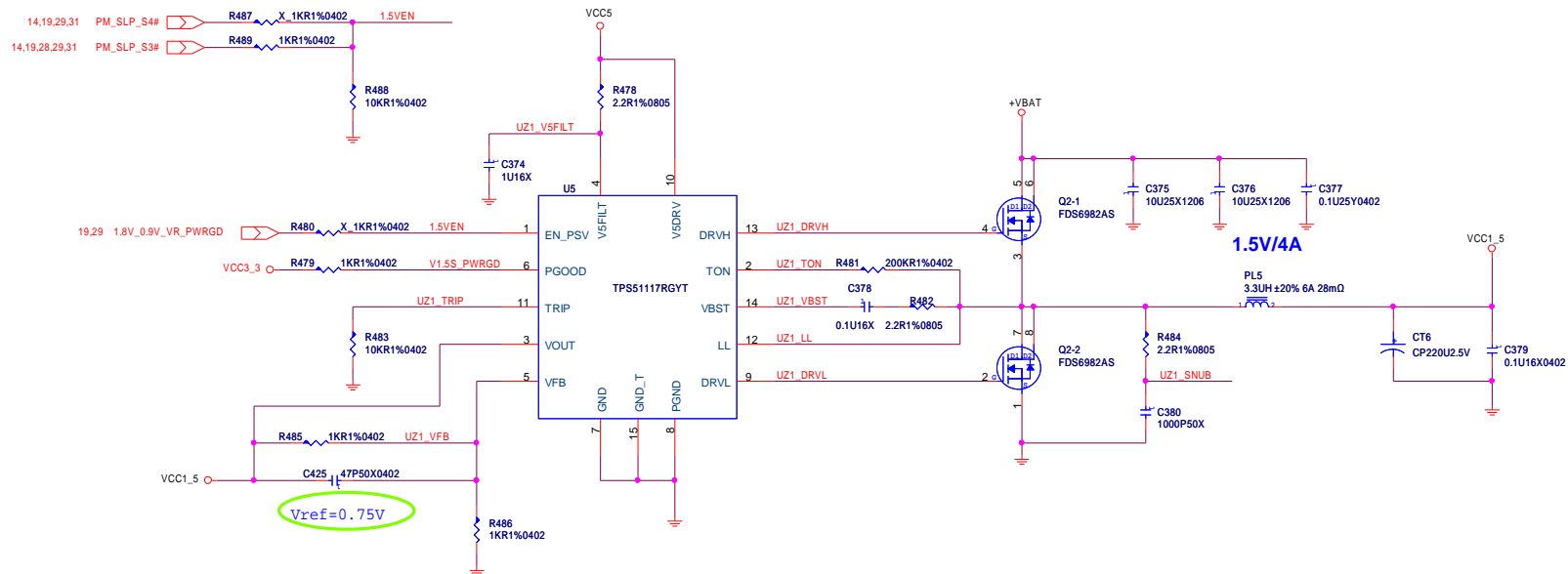


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项目名称	A101P1	项目编号 A101P1 版本 A0.0
设计	LVDS Port & Camera	
审核	黄斌	Monday, March 01, 2010
批准		

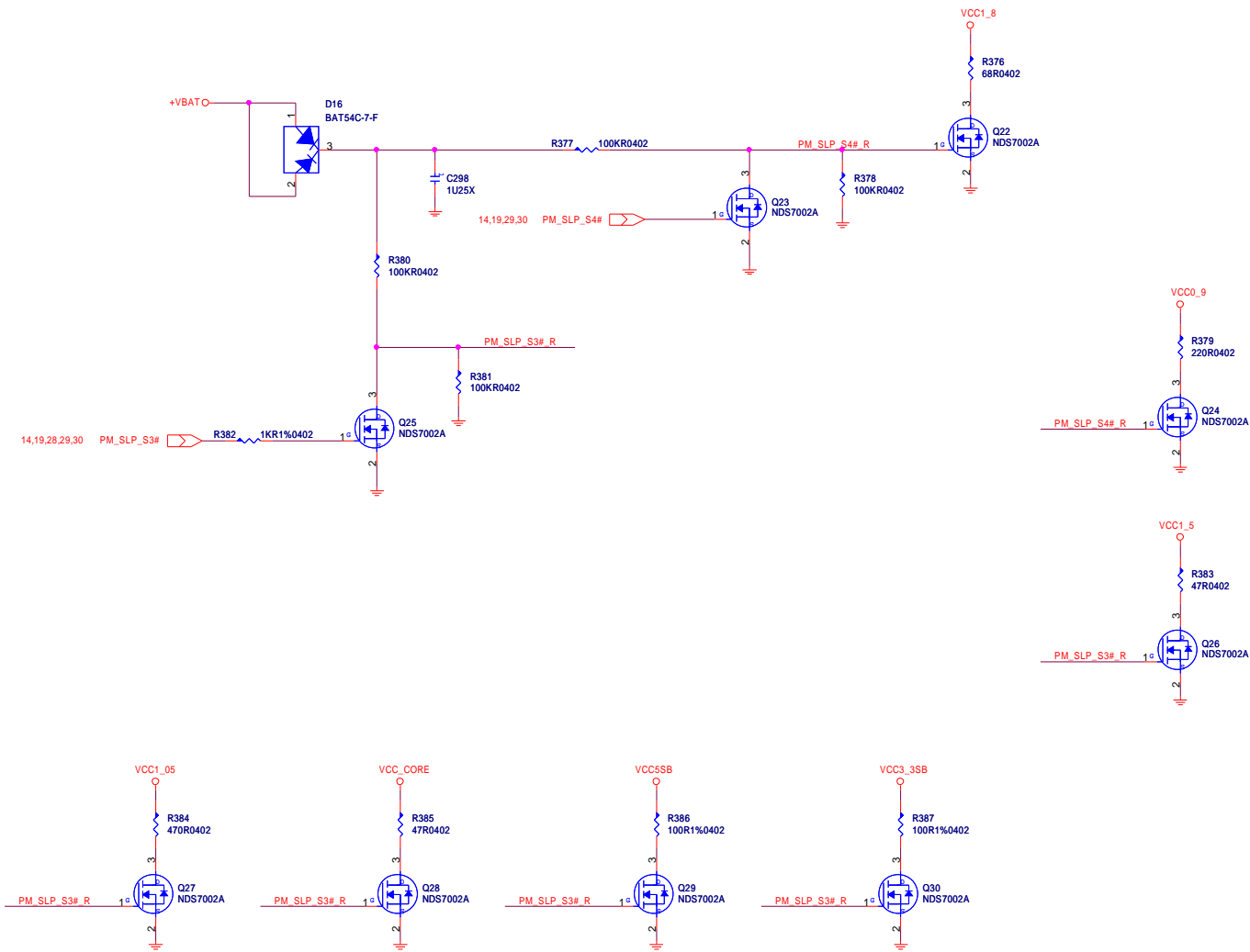
ISL6261A FOR IMVP6 -1PHASE



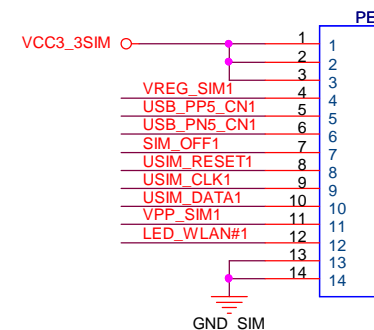
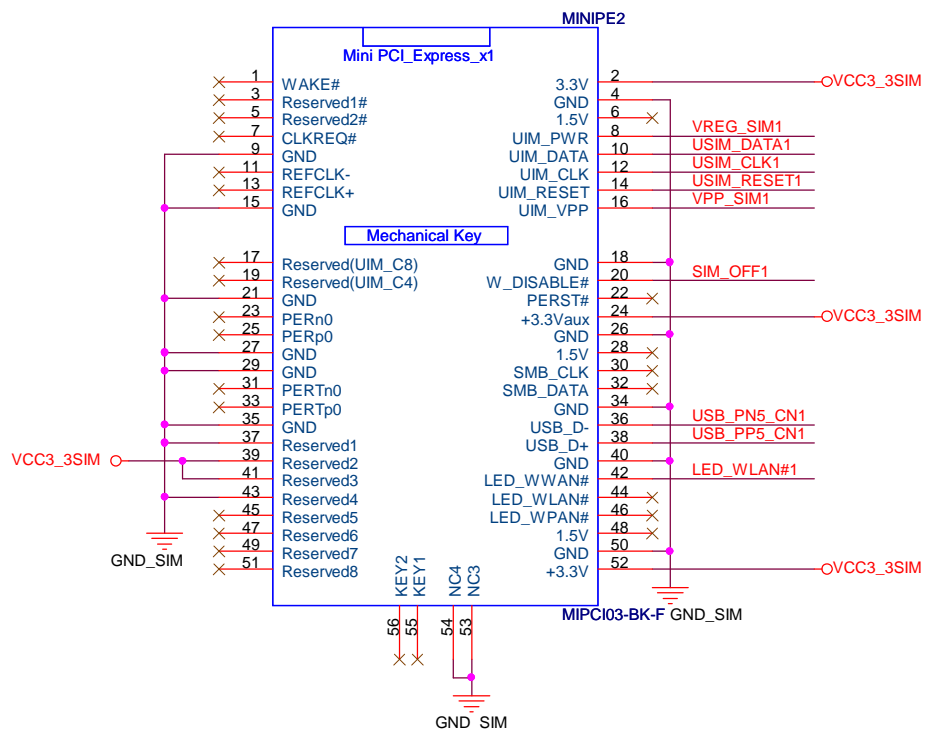
		27	of	34
钧禾科技股份有限公司			项目编号 A101P1	
项目名称	A101P1		版本 A0.0	
设计	ISL6261A For IMVP6 -1 Phase			
审核	黄斌		Monday, March 01, 2010	
批准				



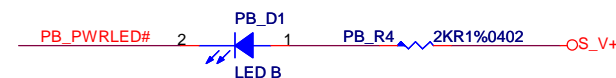
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项目名称	
A101P1	
设计	
VCC1_5 & VCC1_05	
审核	
黄斌	
日期	
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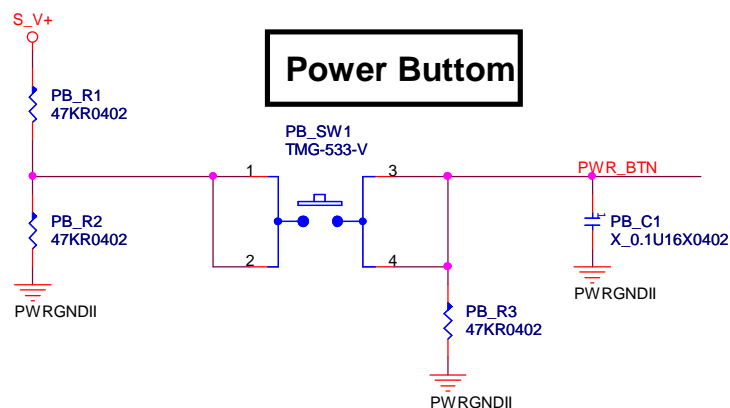
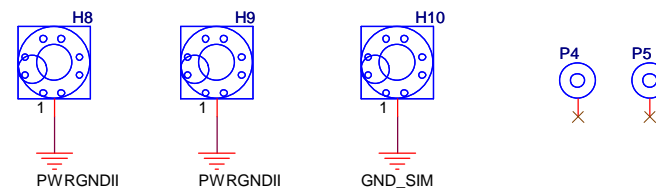
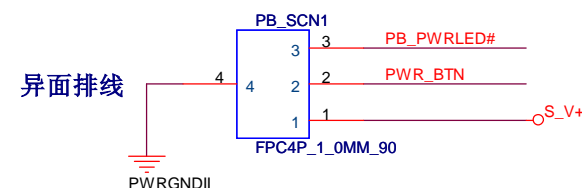
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项目名称	
A101P1	
版本	
A0.0	
设计	DC Rail
审核	黄斌
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排线：间距 1.0mm，同面接触

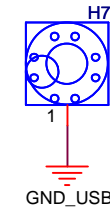
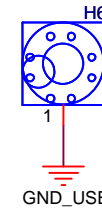
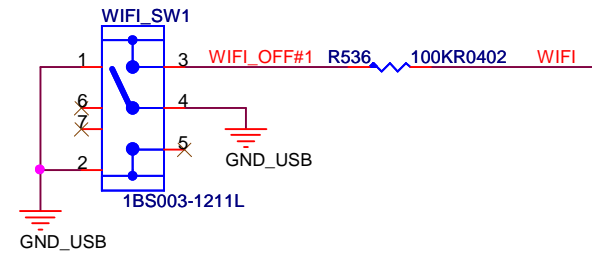
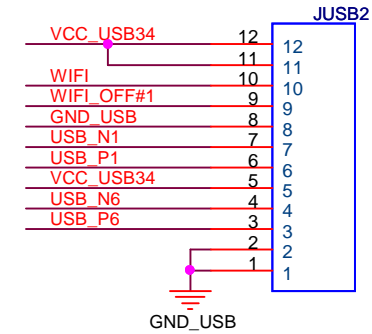
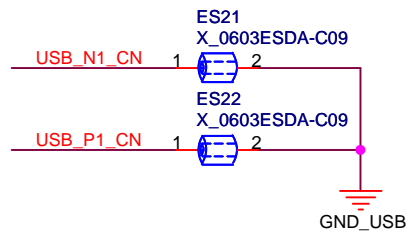
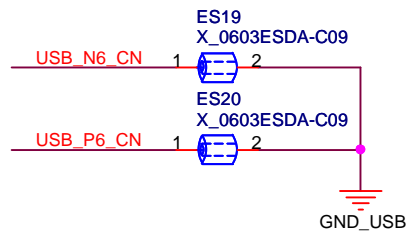
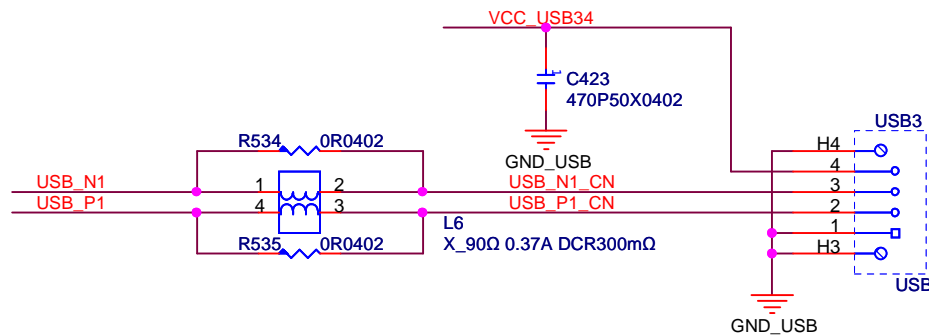
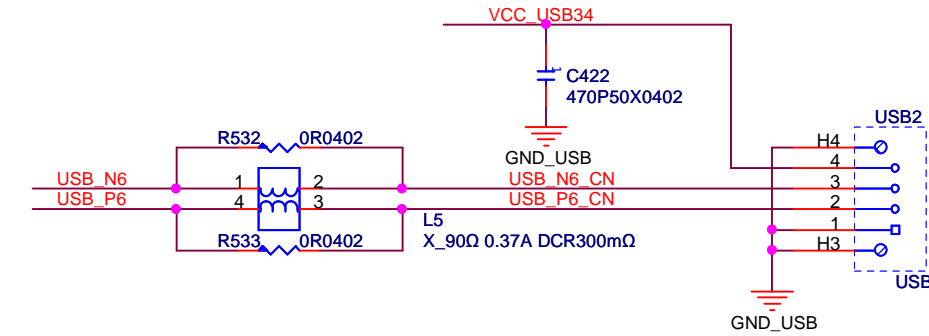


To Power Board PB_PCN1



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设计	Power Button & Mini PCIE2	版本 A0.0
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1.USB2 ,USB3换pin lionxiong



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钩禾科技股份有限公司	
项目名称	A101P1
设计	USB Board
审核	黄斌
批准	Monday, March 01, 2010

版本	更改说明	修改人	日期
A0.0	1.首次发布	1.首次发布	

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项目名称 A101P1		项目编号 A101P1
设计 Revision History		版本 A0.0
审核	黄斌	Monday, March 01, 2010
批准		