


|     |     |   |                             |                         |                                   |                        |                        |
|-----|-----|---|-----------------------------|-------------------------|-----------------------------------|------------------------|------------------------|
|     | OM4 | OM4: 1 & OM3: 0<br>OneNAND/ROM  |                             | 0<br>NAND/iROM/Security |                                   |                        |                        |
|     | OM3 |   |                             | 0<br>NAND<br>Advanced   | 1<br>NAND/iROM/Security<br>Normal |                        |                        |
| OM2 | 0   | 0<br>Muxed OneNAND  | 1<br>ROM/DeMuxed<br>OneNAND | page 4KB                | 1<br>NAND page<br>512B            | 0<br>iROM<br>/Security |                        |
|     | 1   |   |                             | page 2KB                |                                   |                        |                        |
| OM1 | 0   |  | 8 bit                       | ADDR4                   | ADDR3                             | 0<br>iROM              | 1<br>Security<br>eFUSE |
|     | 1   |   |                             | 16 bit                  | ADDR5                             |                        |                        |
| OM0 | 0   | XTiPLL (MPLL/UPLL)  |                             |                         |                                   |                        | 128                    |
|     | 1   | EXTCLK (MPLL/UPLL)  |                             |                         |                                   |                        | 192                    |



