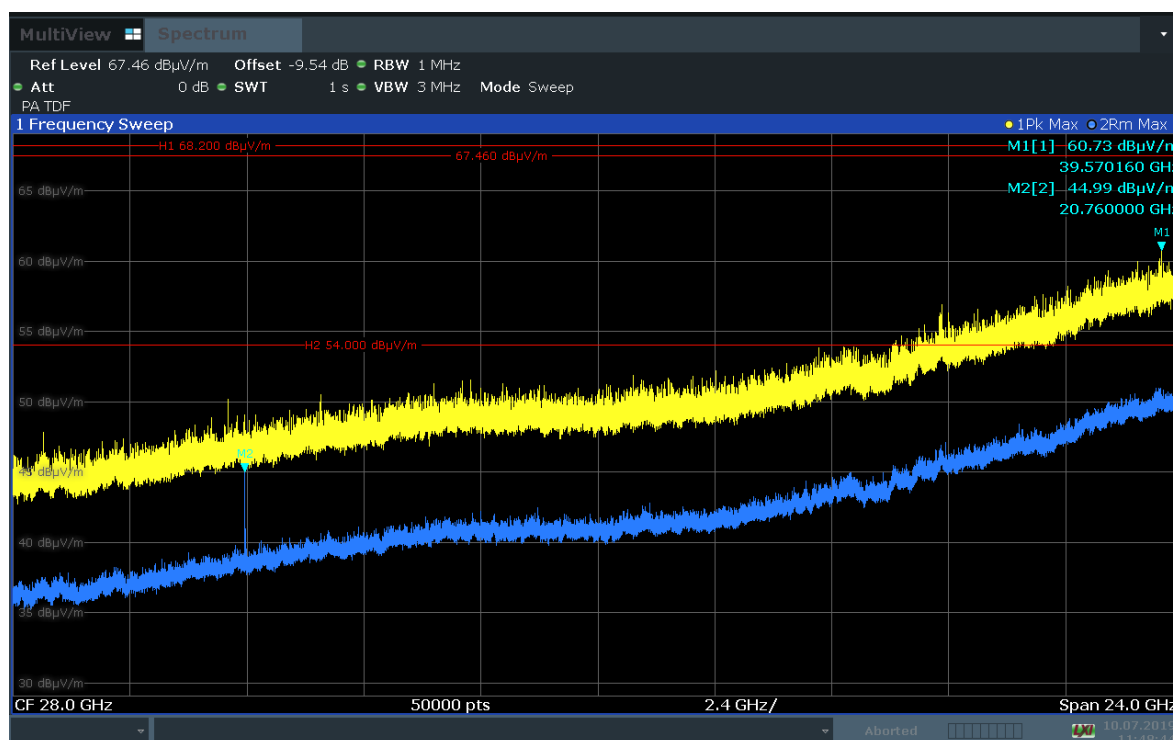
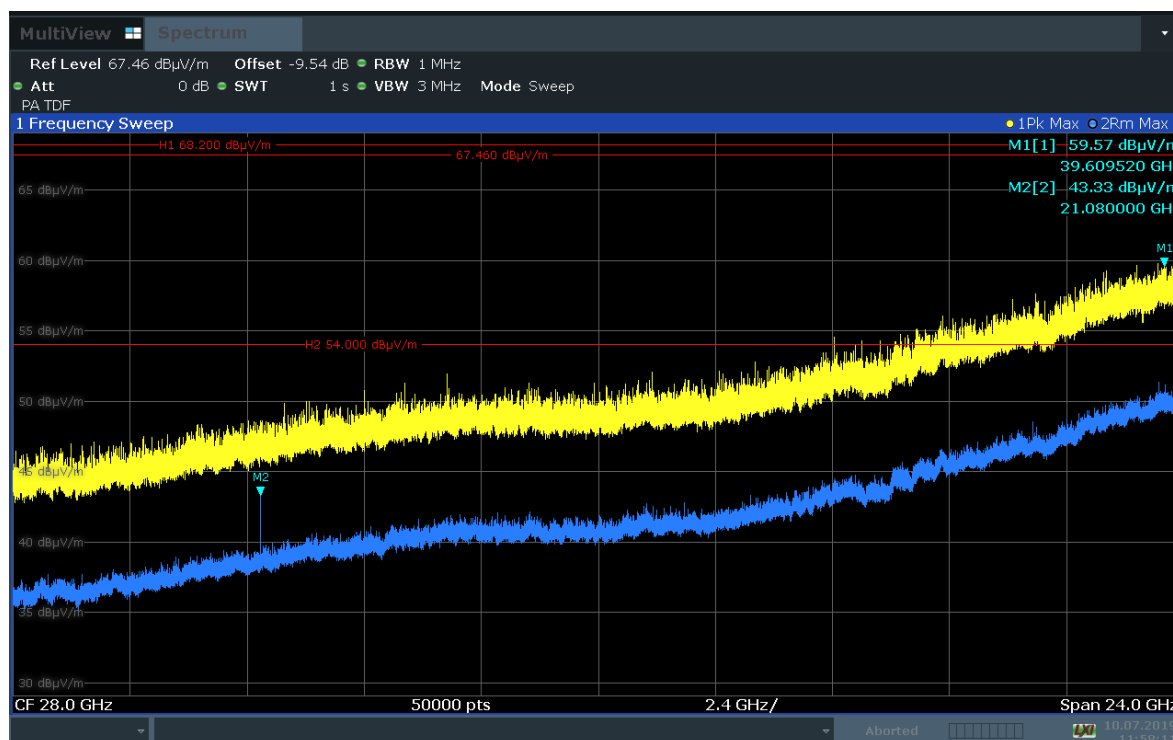


4.13c_n-mode_MCS3_ch038



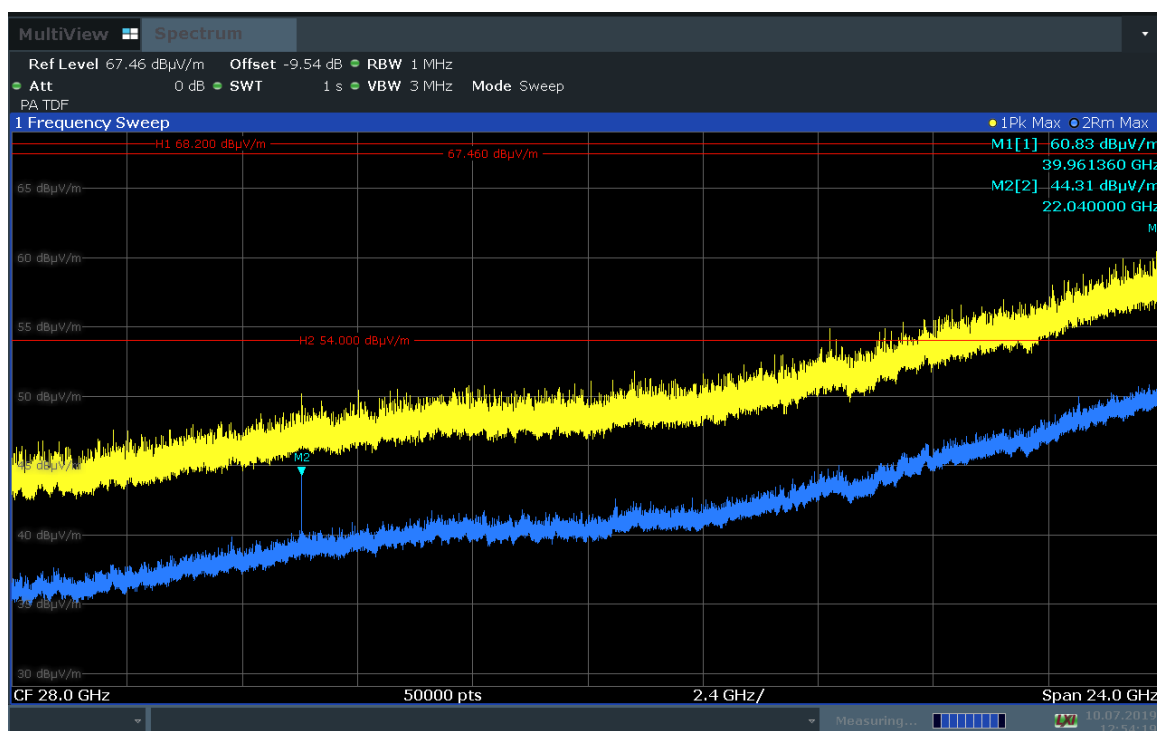
11:48:44 10.07.2019

4.14c_n-mode_MCS3_ch54



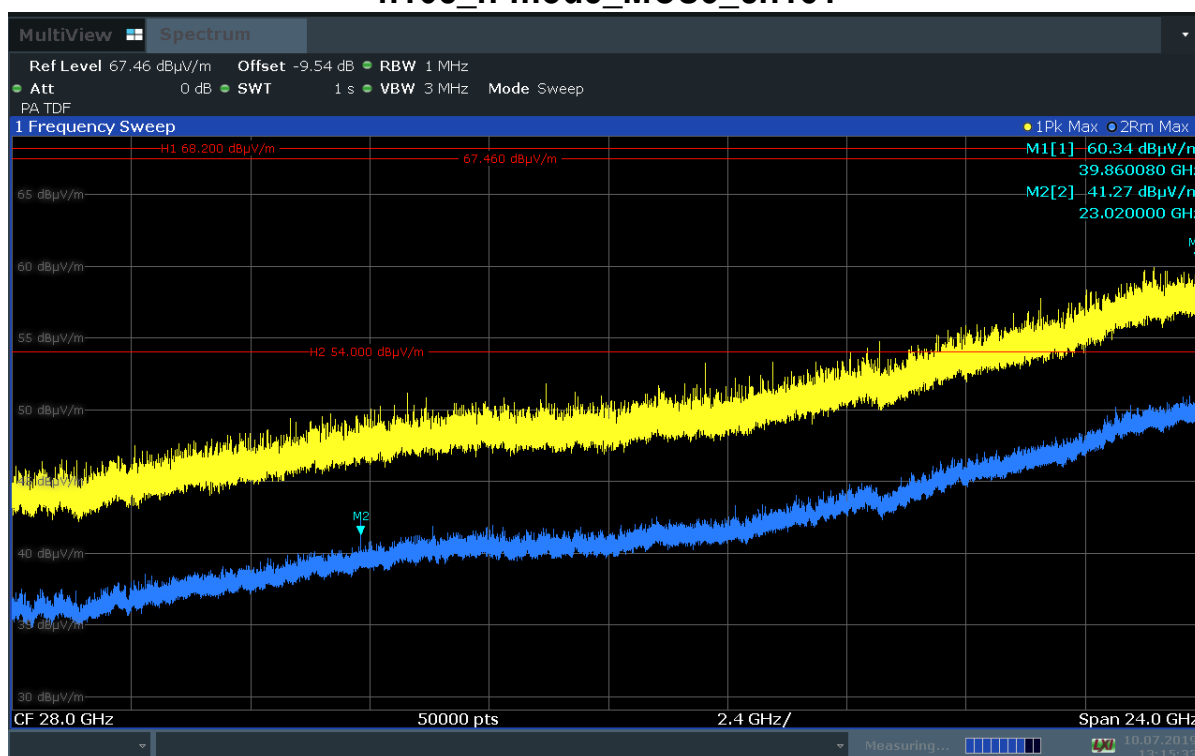
11:58:11 10.07.2019

4.15c_n-mode_MCS3_ch102



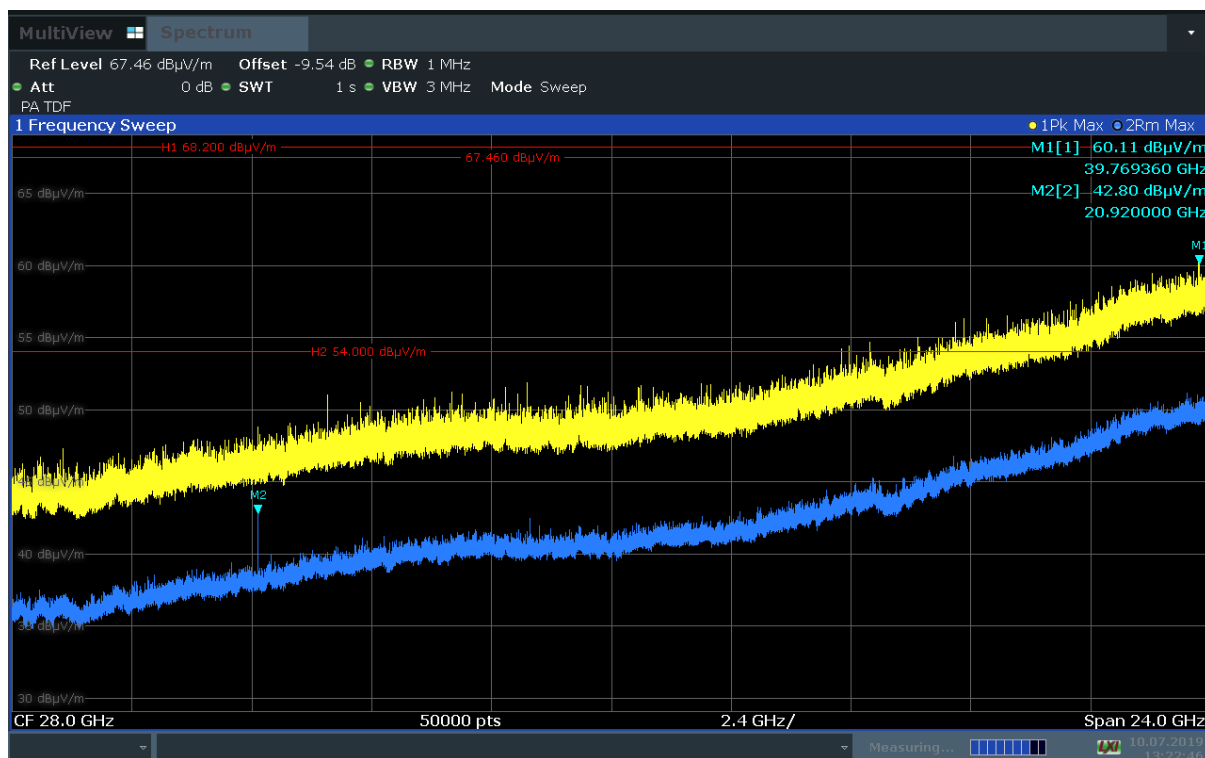
12:54:20 10.07.2019

4.16c_n-mode_MCS3_ch151



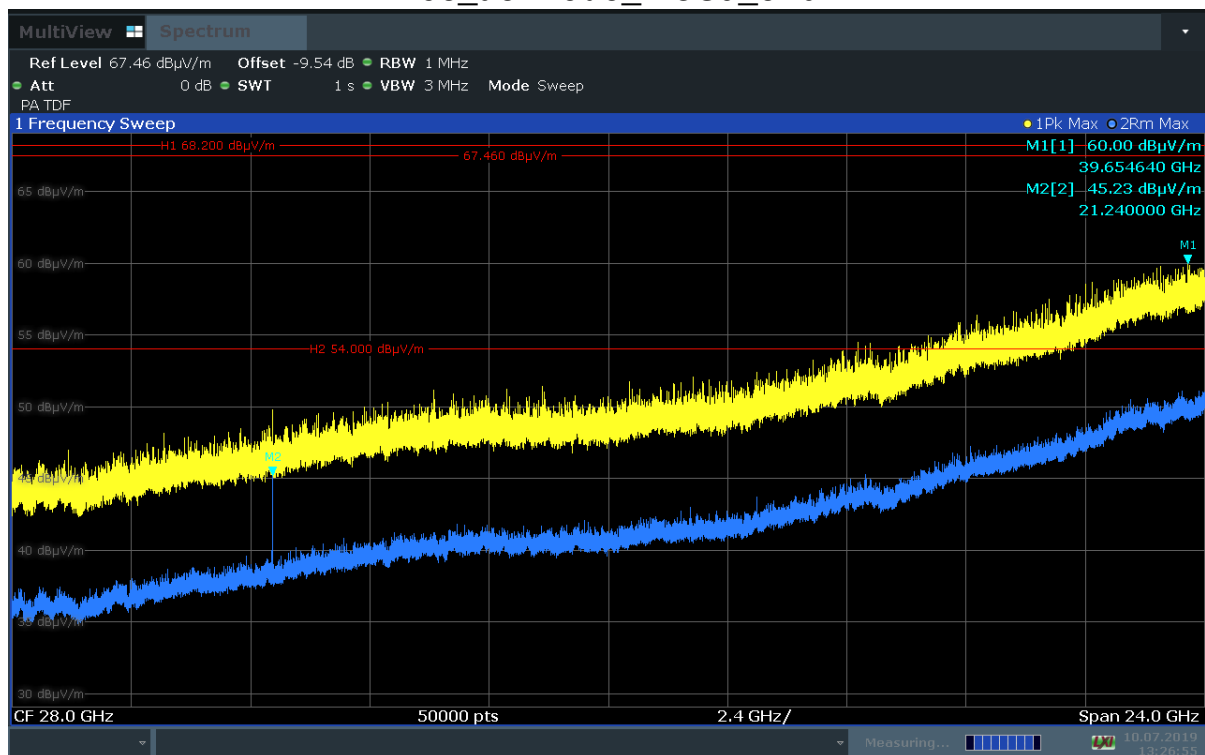
13:15:33 10.07.2019

4.17c_ac-mode_MCS9_ch046



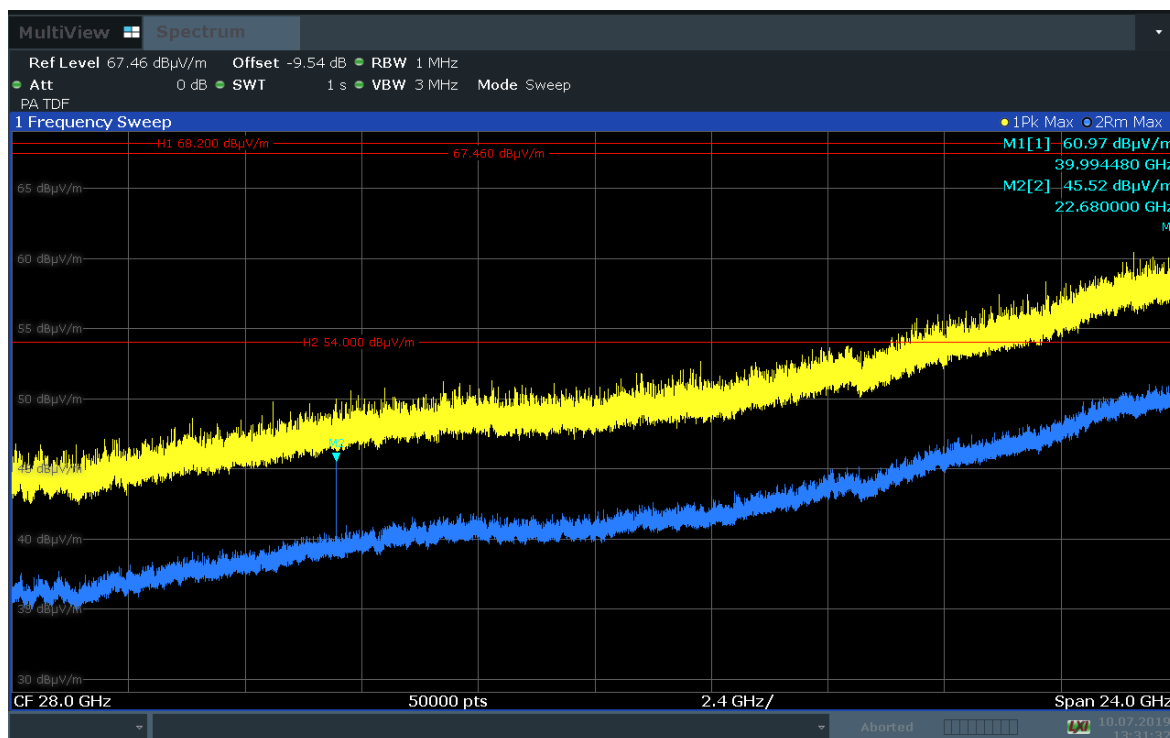
13:22:47 10.07.2019

4.18c_ac-mode_MCS9_ch62



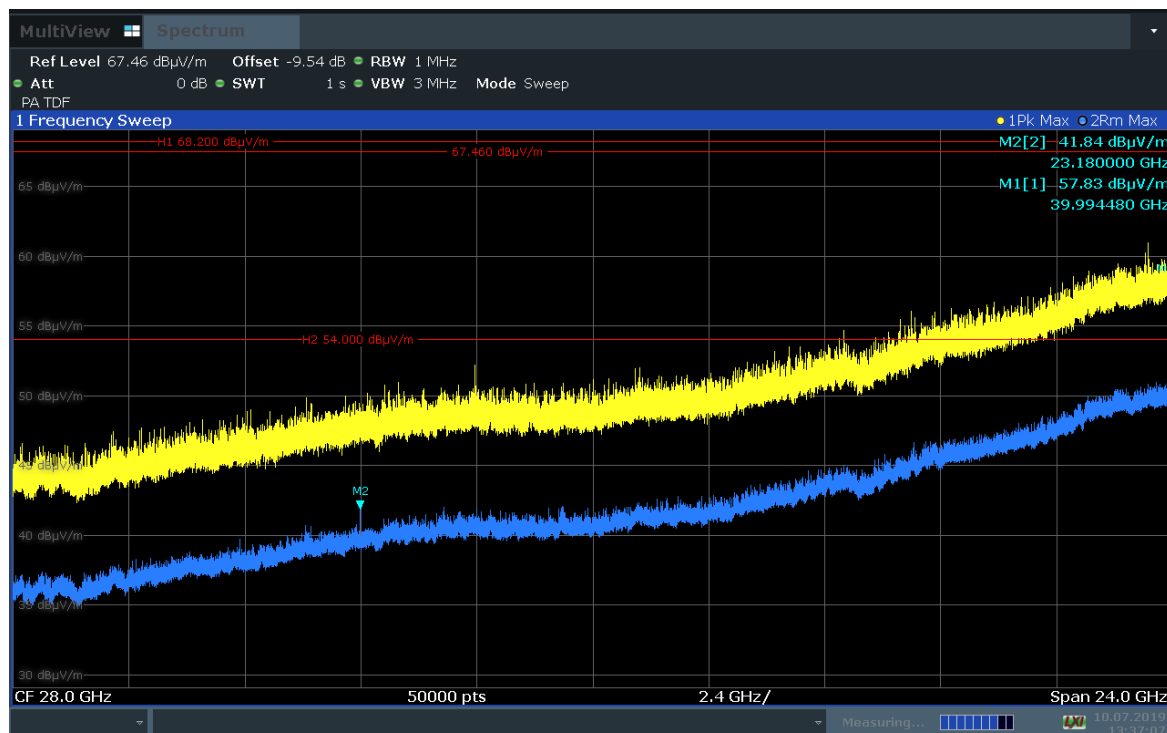
13:26:56 10.07.2019

4.19c_ac-mode_MCS9_ch134



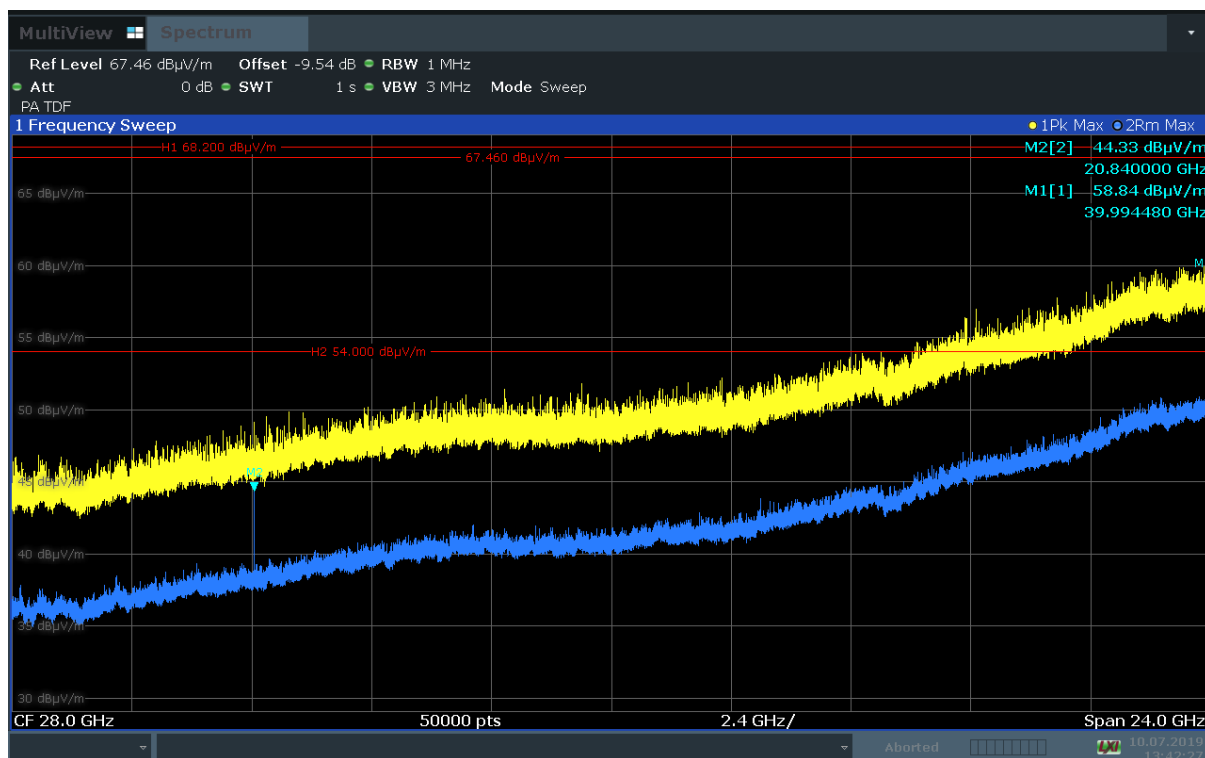
13:31:33 10.07.2019

4.20c_ac-mode_MCS9_ch159



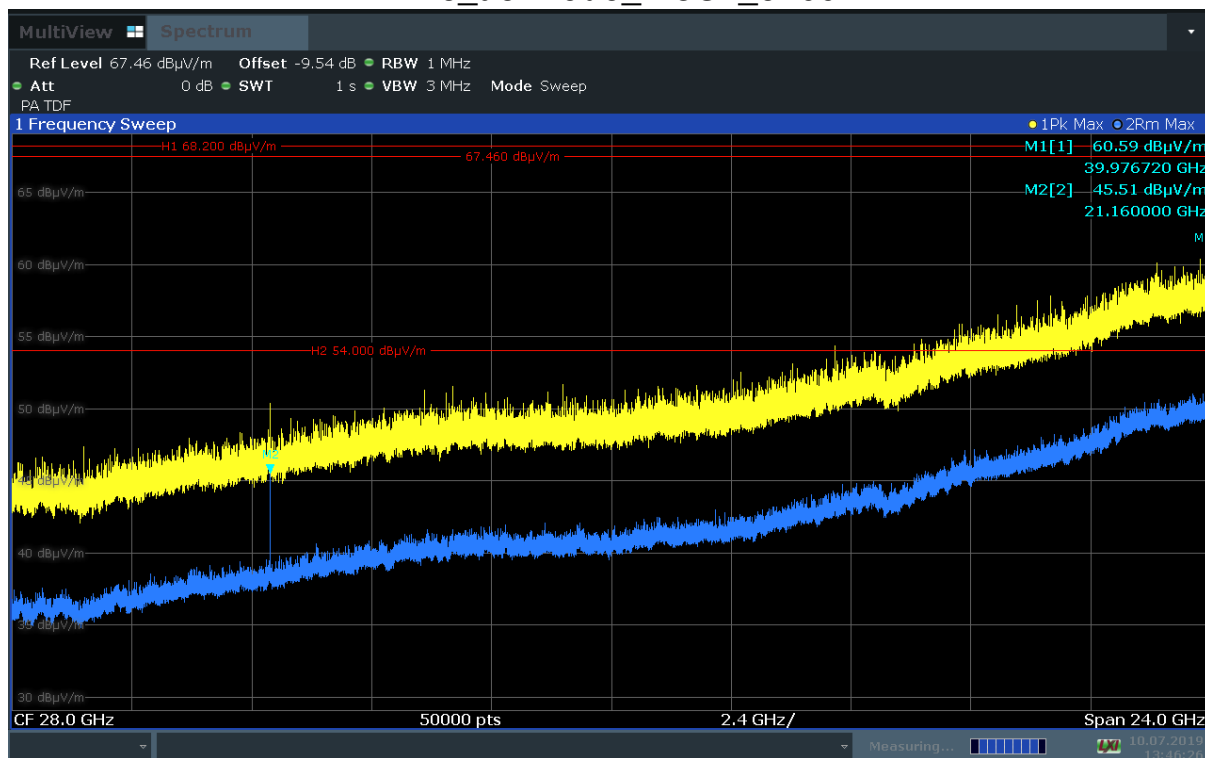
13:37:07 10.07.2019

4.21c_ac-mode_MCS7_ch042



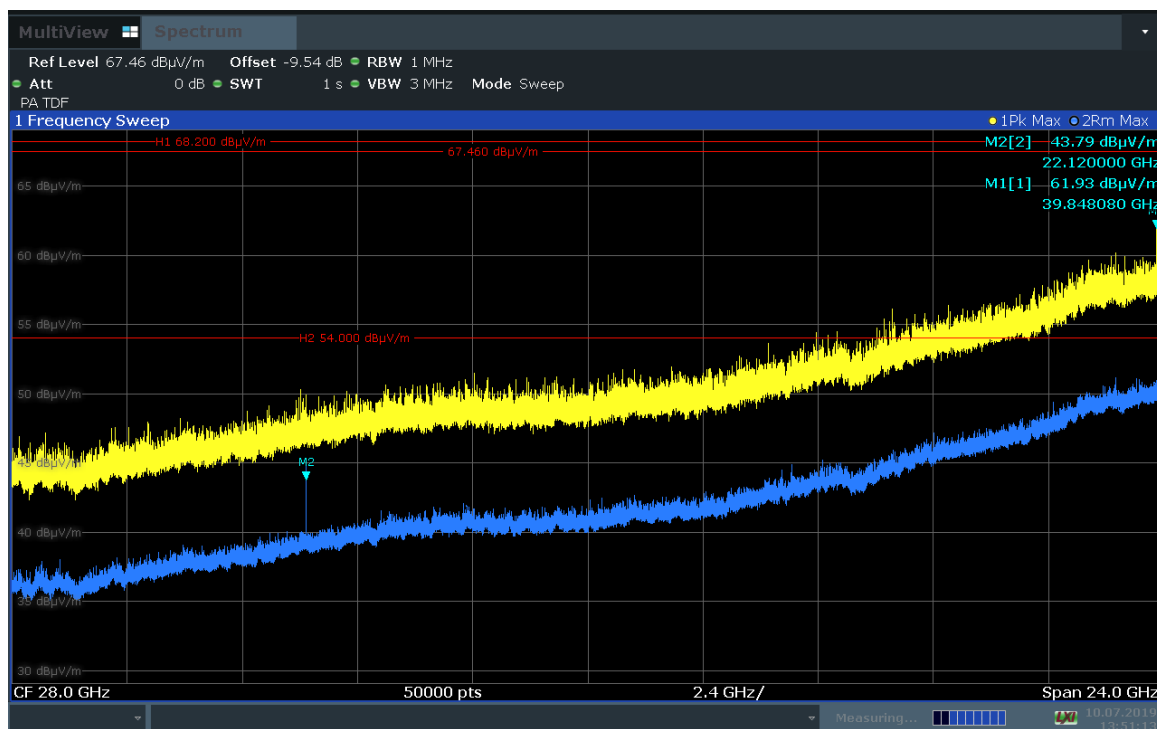
13:42:28 10.07.2019

4.22c_ac-mode_MCS7_ch58



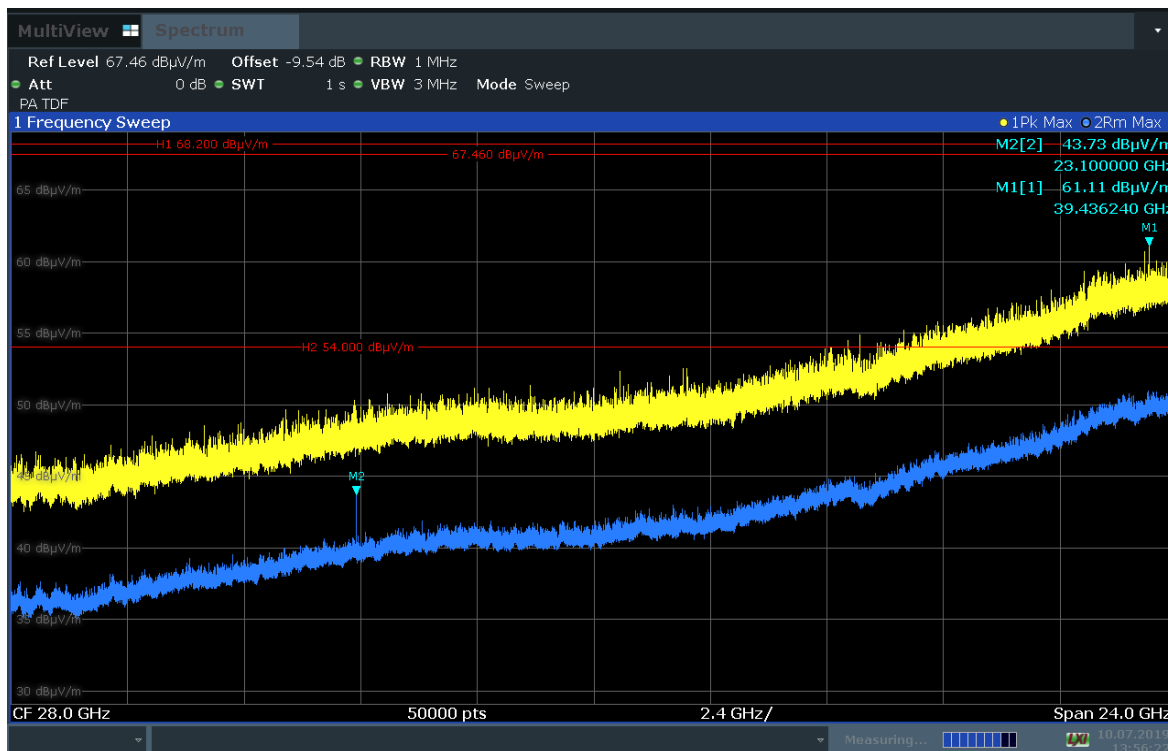
13:46:26 10.07.2019

4.23c_ac-mode_MCS7_ch106



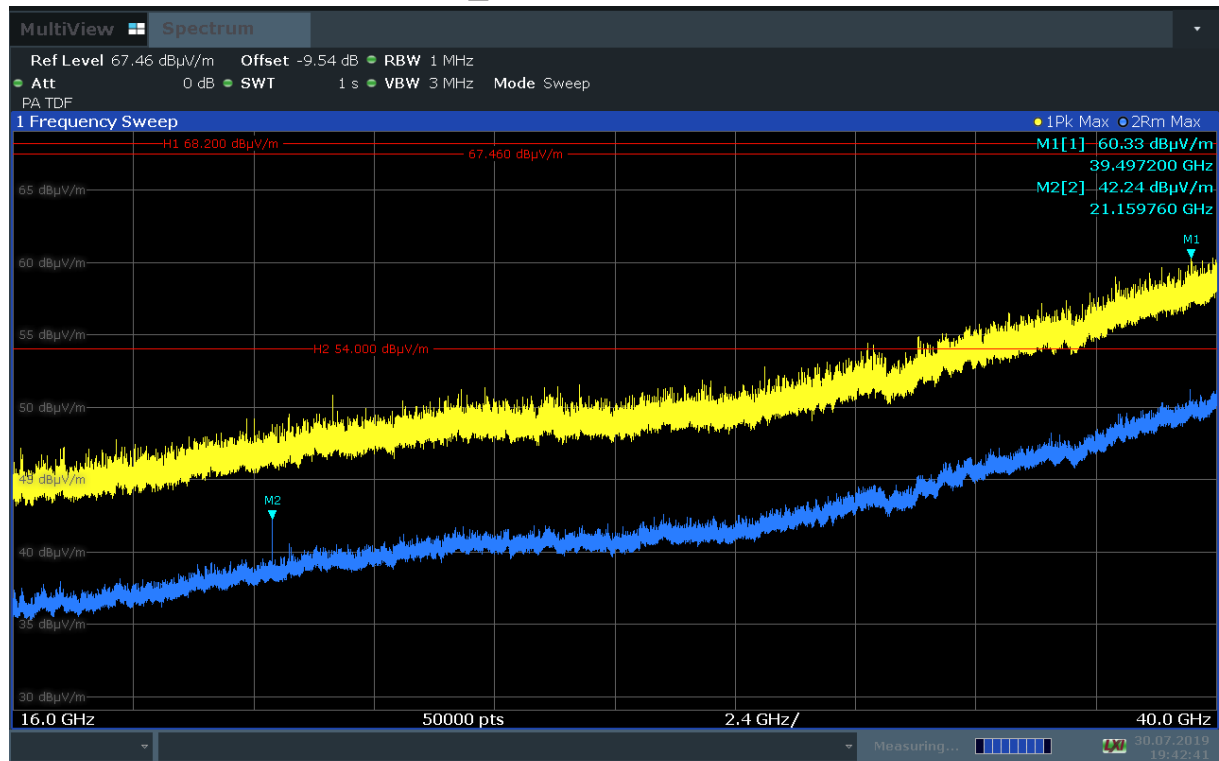
13:51:14 10.07.2019

4.24c_ac-mode_MCS7_ch155



13:56:22 10.07.2019

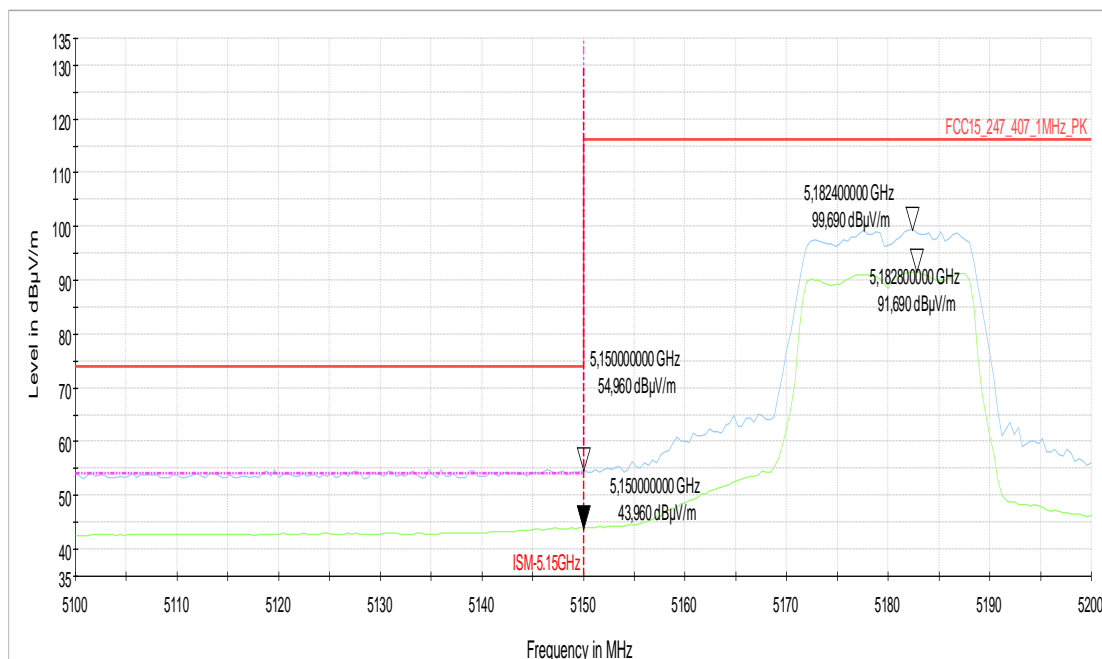
4.30c_ W-LAN 5GHz +BT



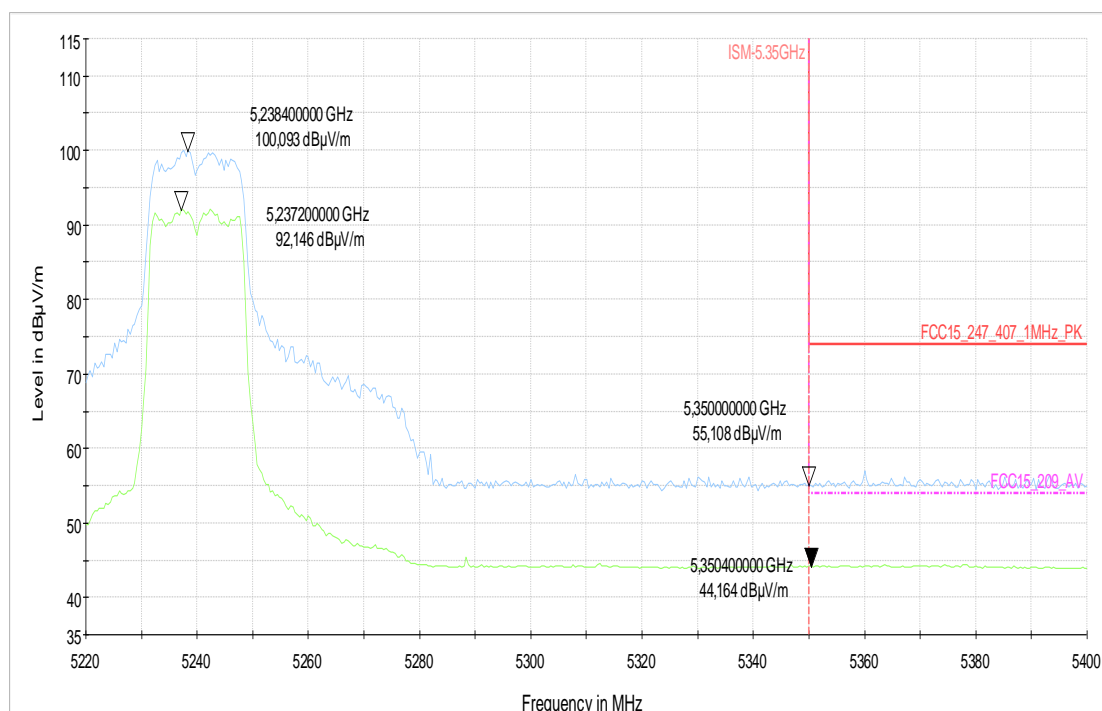
19:42:42 30.07.2019

2.6. Band edge compliance

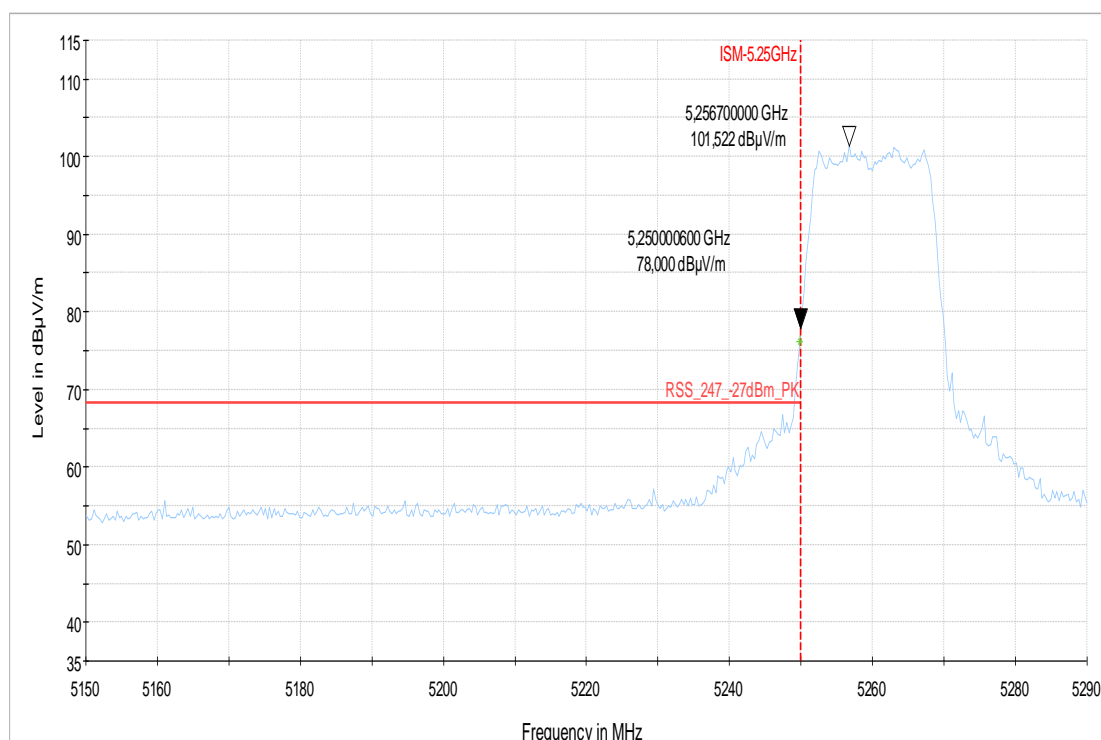
9.01a_a-mode_9MBps_ch036



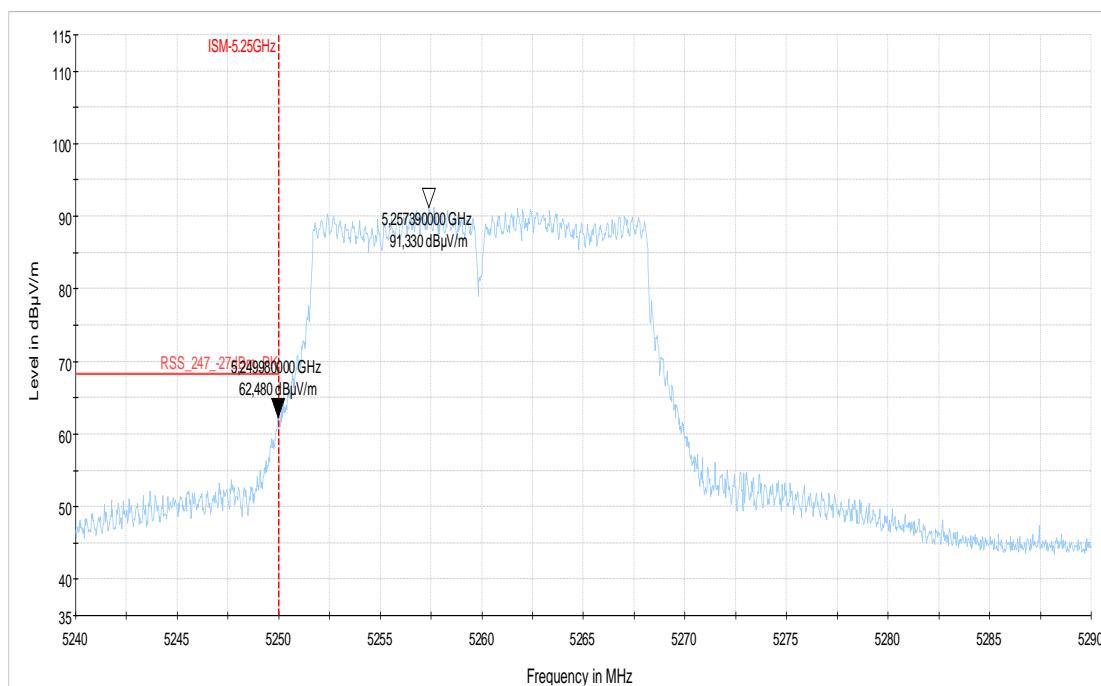
9.01b_a-mode_9MBps_ch048



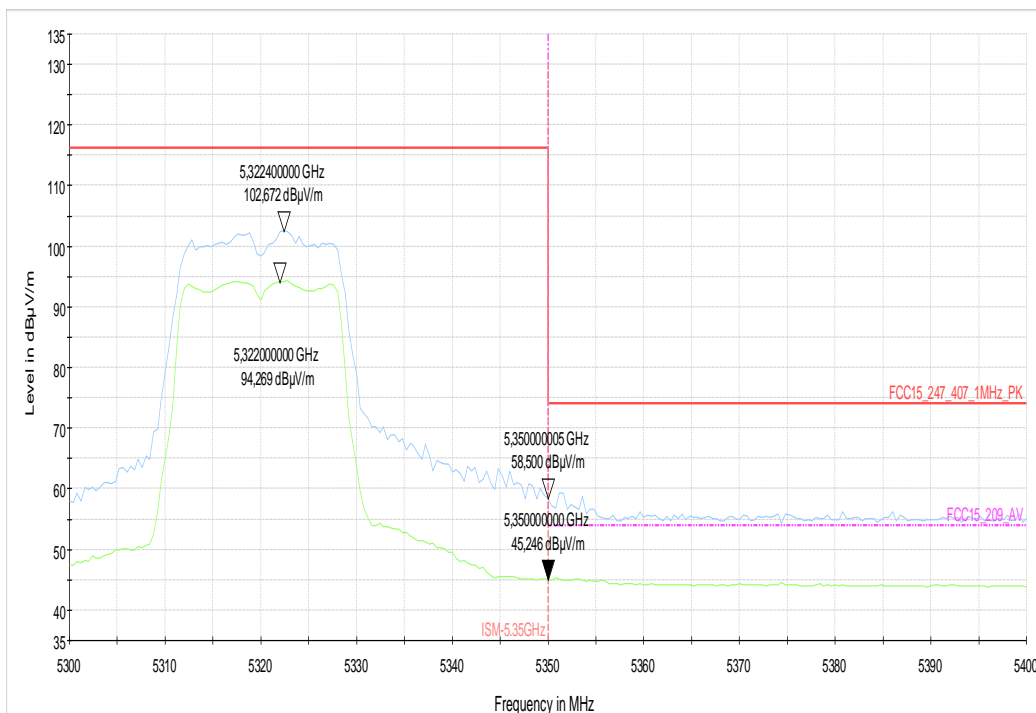
9.02a_a-mode_9MBps_ch052_STep1



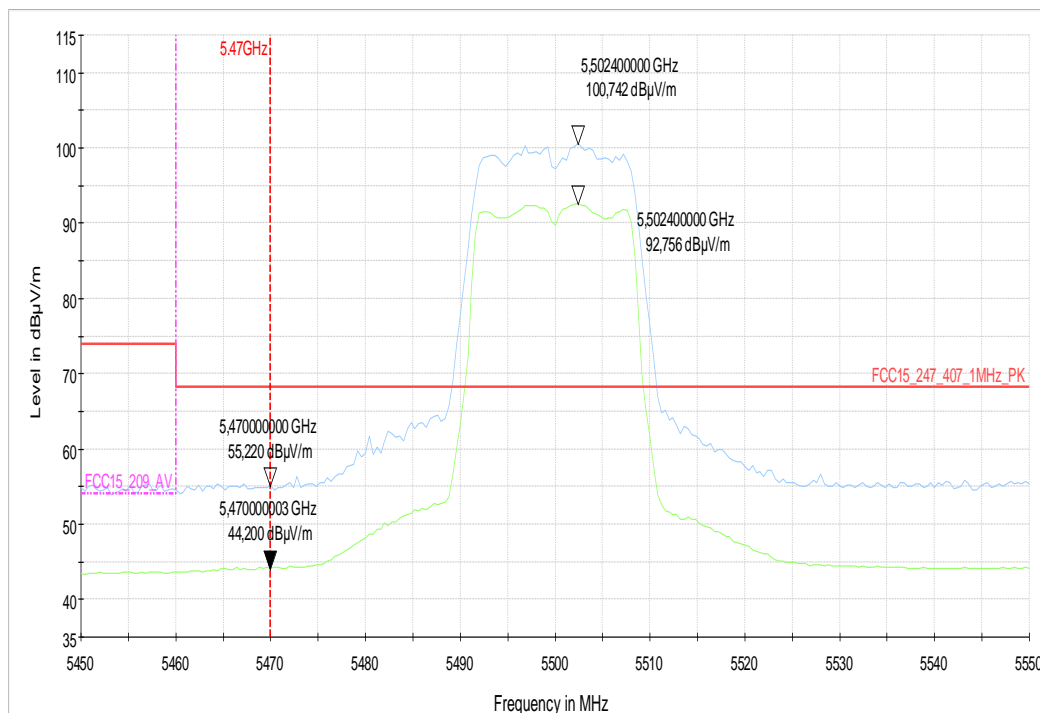
9.02a_a-mode_9MBps_ch052_STep2



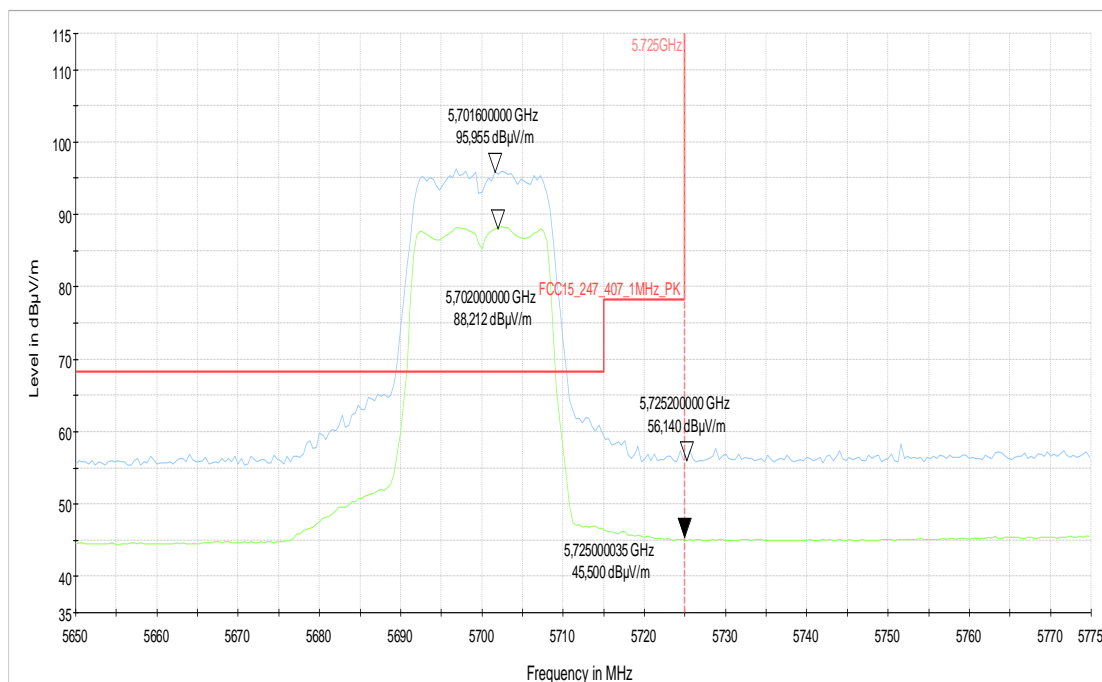
9.02b_a-mode_9Mbps_ch064



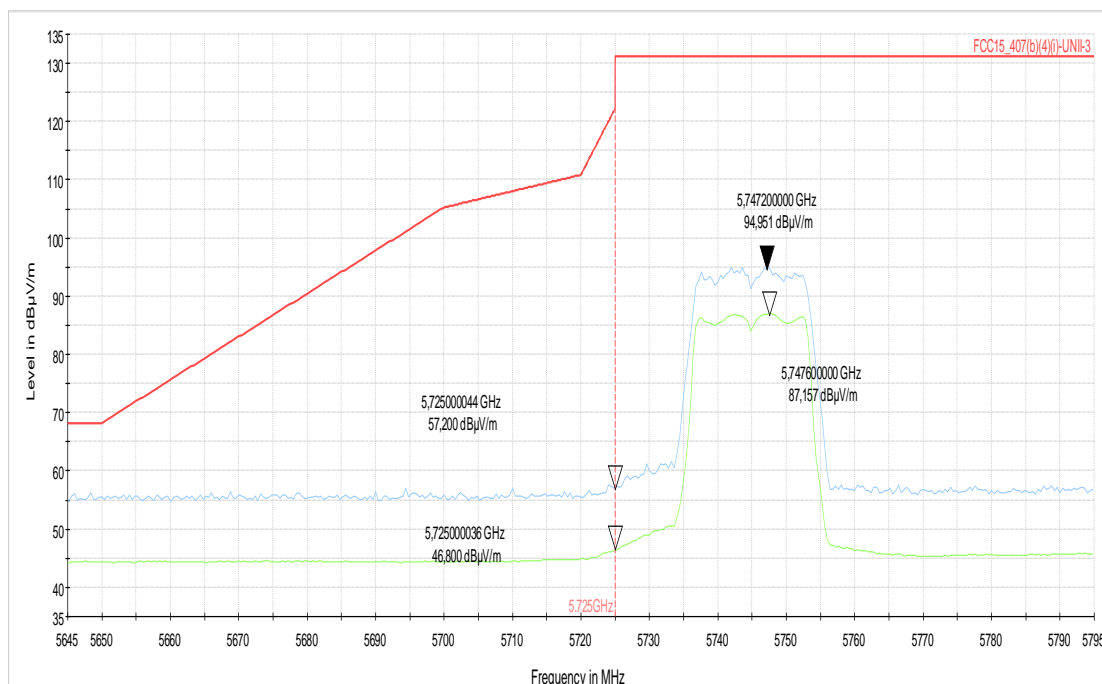
9.03a_a-mode_9Mbps_ch100



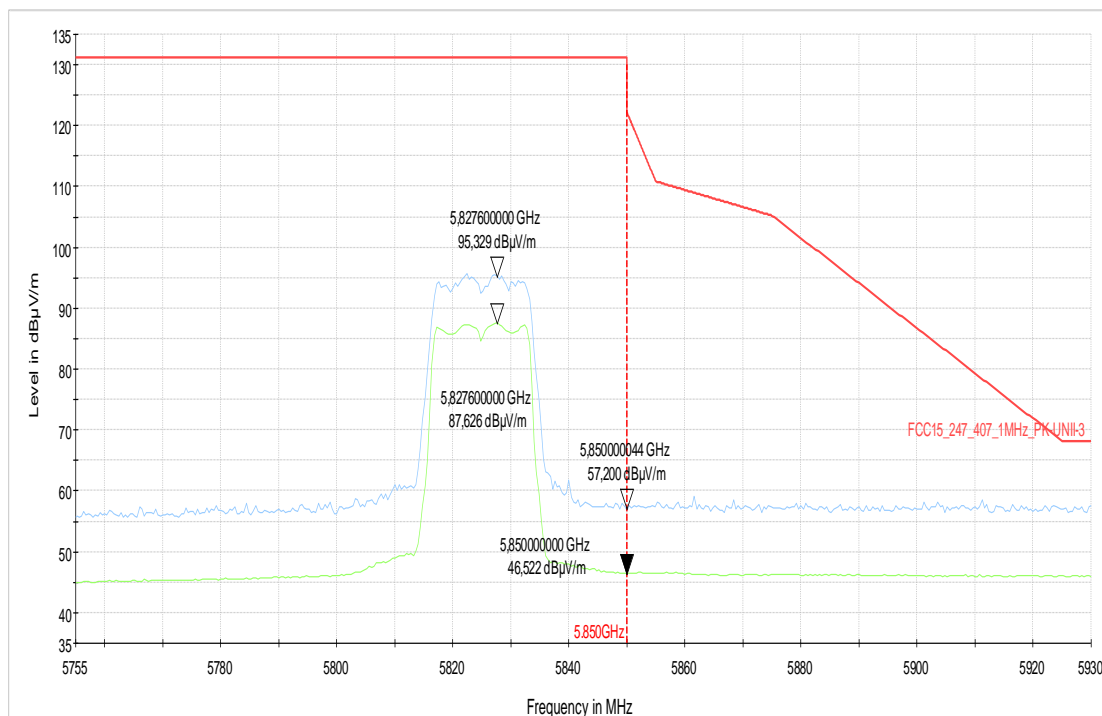
9.03b_a-mode_9MBps_ch140



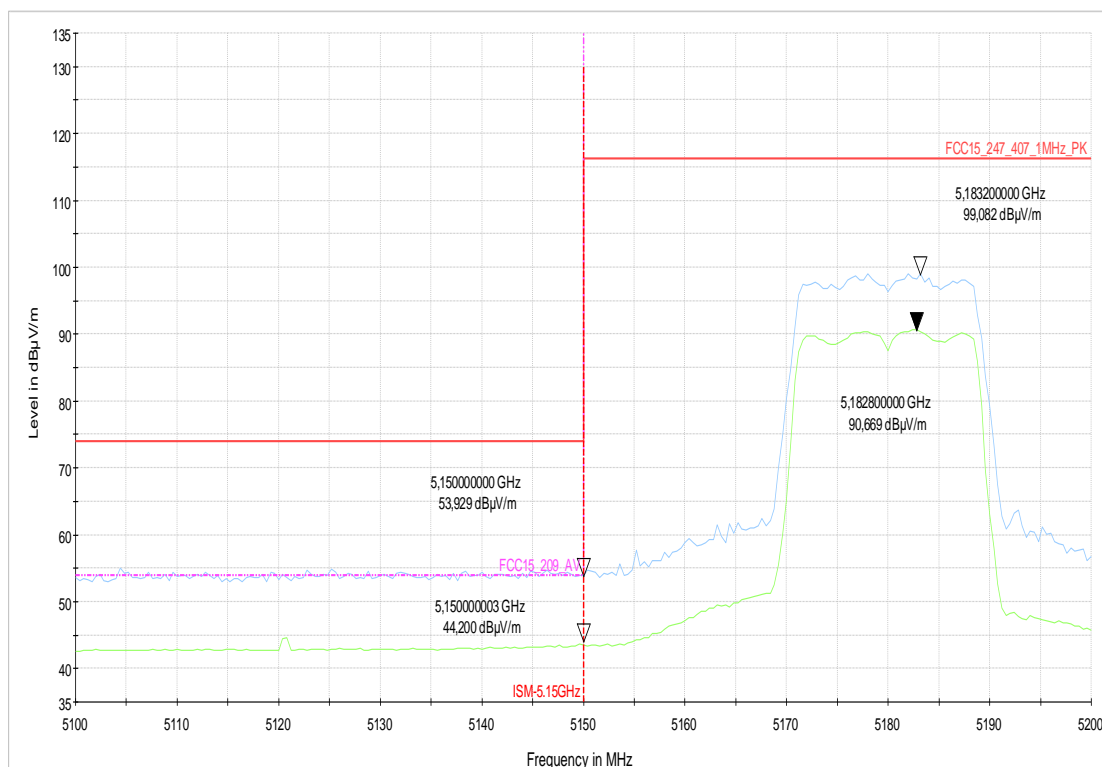
9.04a_a-mode_9MBps_ch149



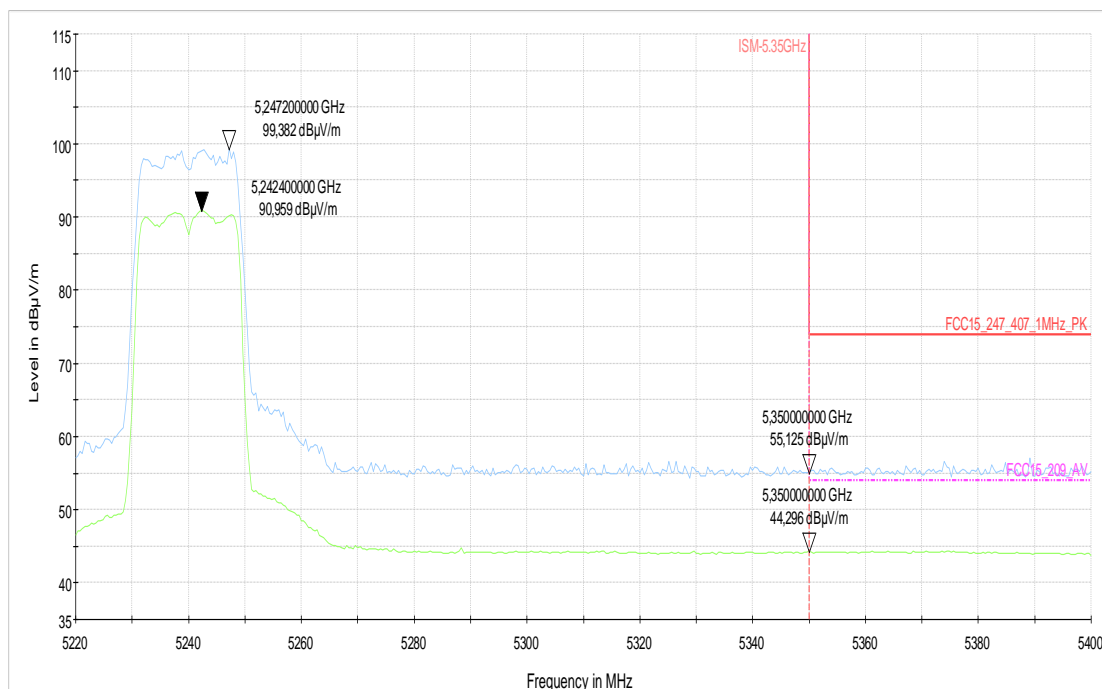
9.04b_a-mode_9Mbps_ch165



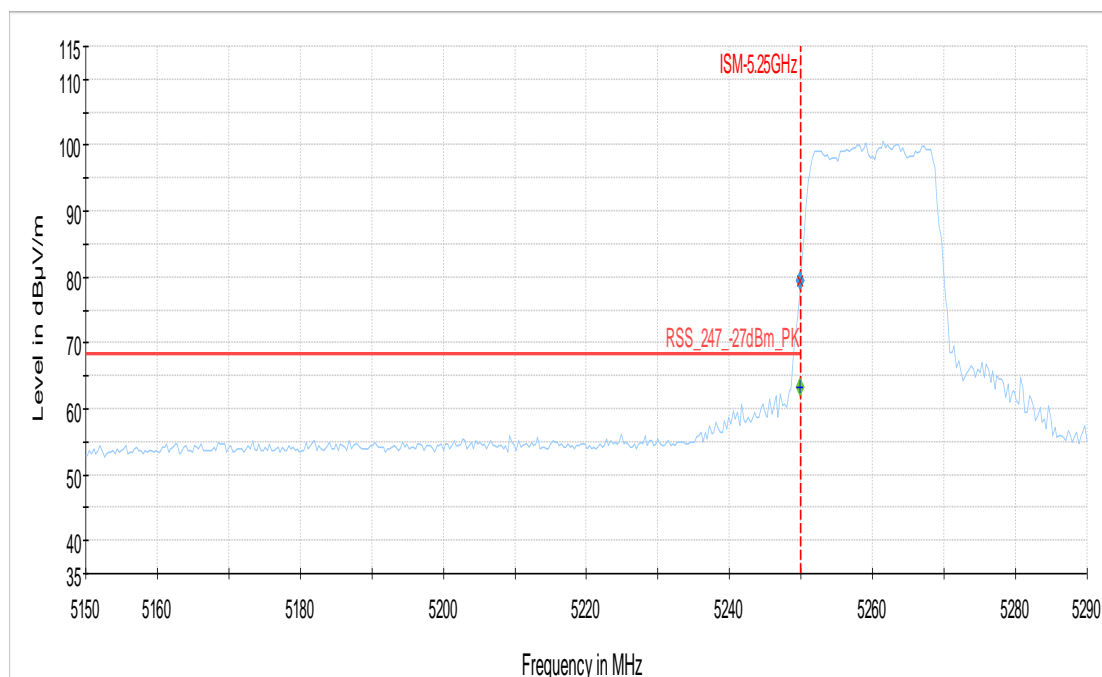
9.05a_n-mode_MCS3_ch036



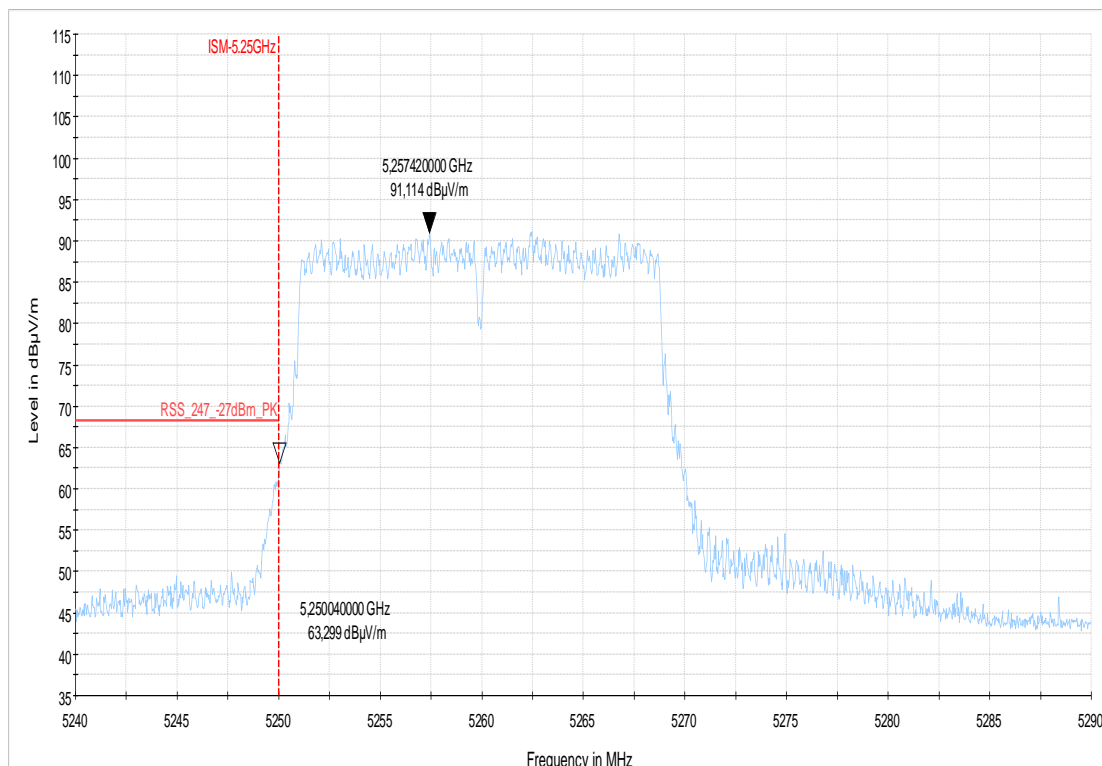
9.05b_n-mode_MCS3_ch048



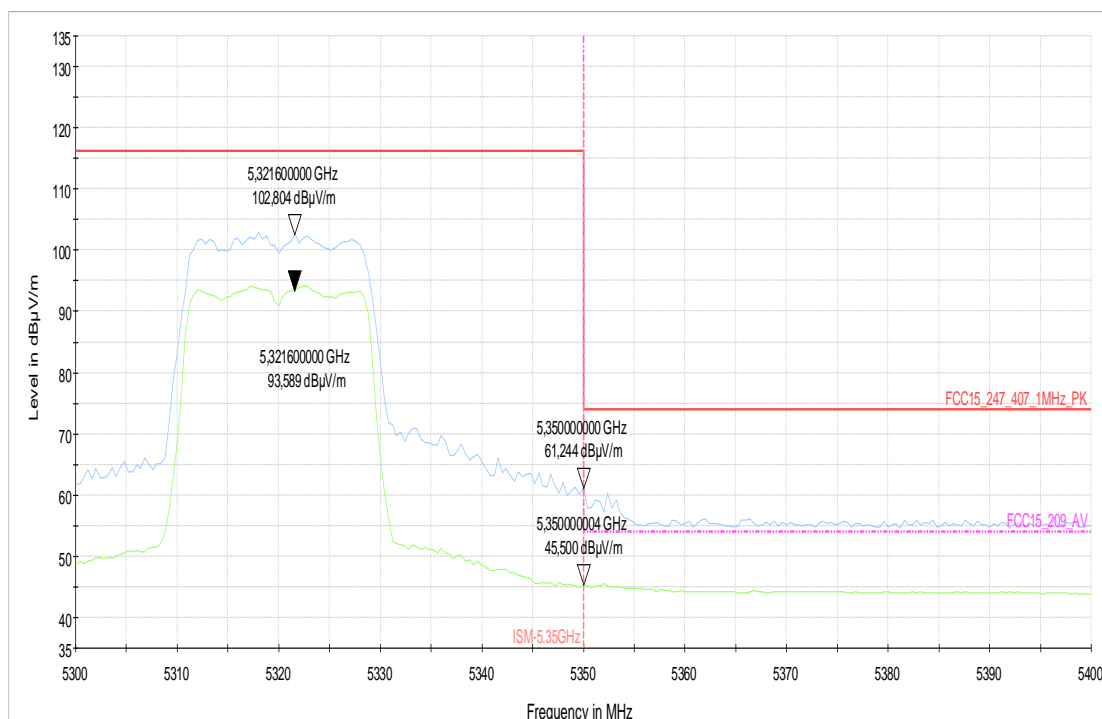
9.06a_n-mode_MCS3_ch52_Step1



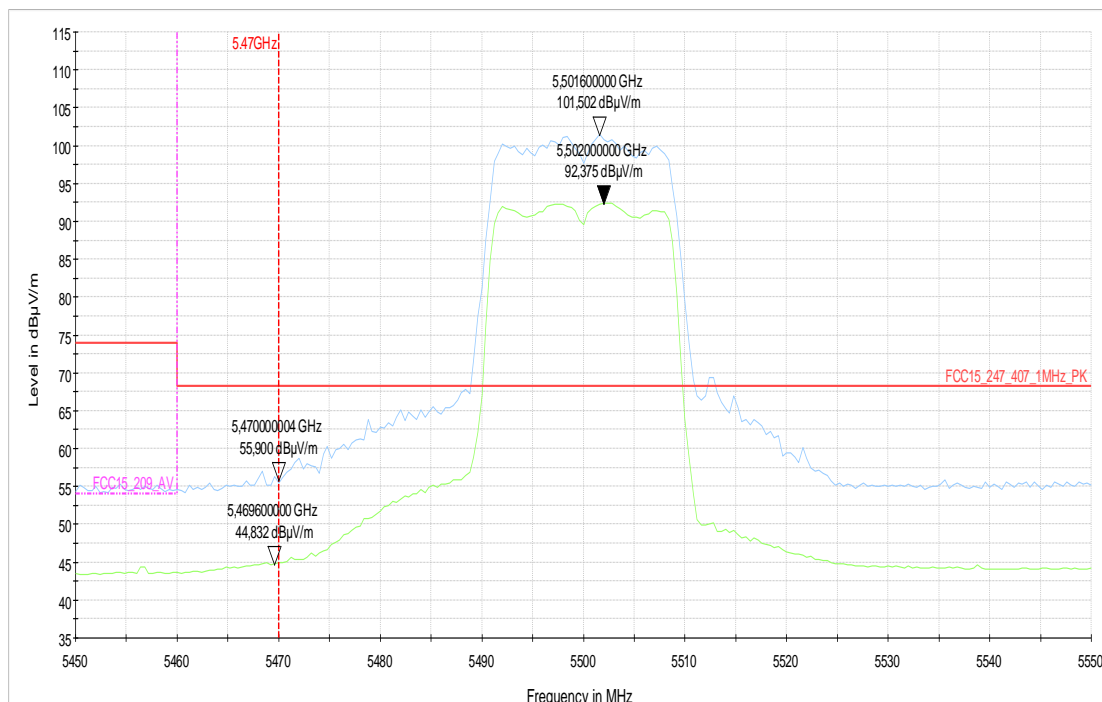
9.06a_n-mode_MCS3_ch52_Step2



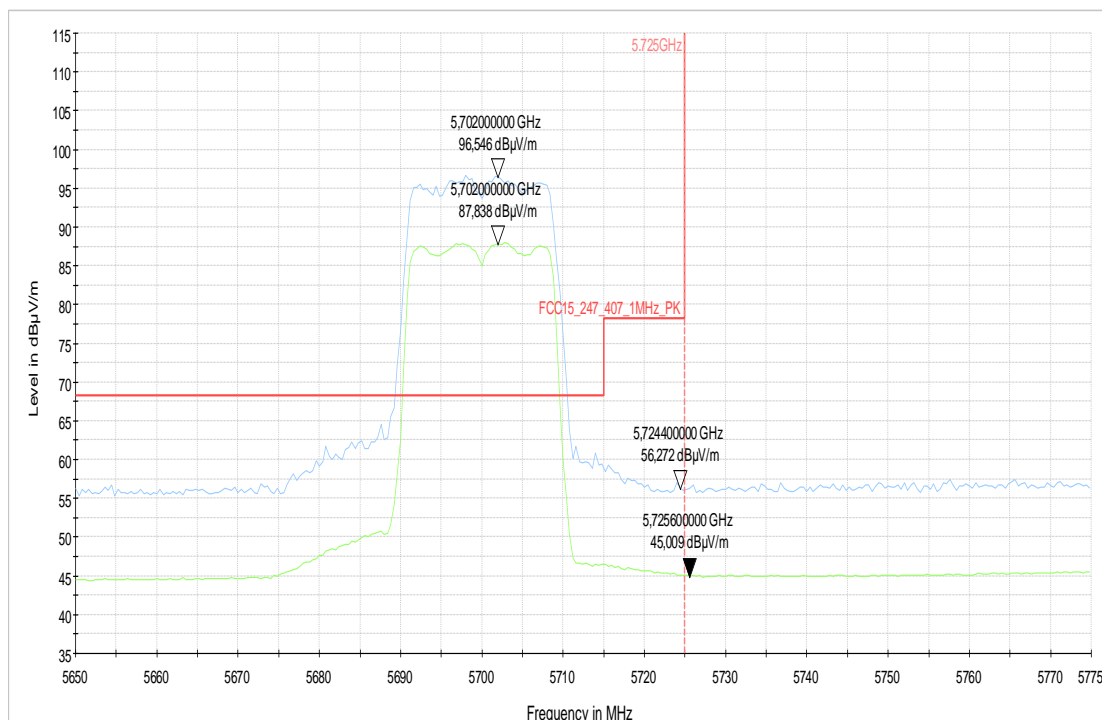
9.06b_n-mode_MCS3_ch064



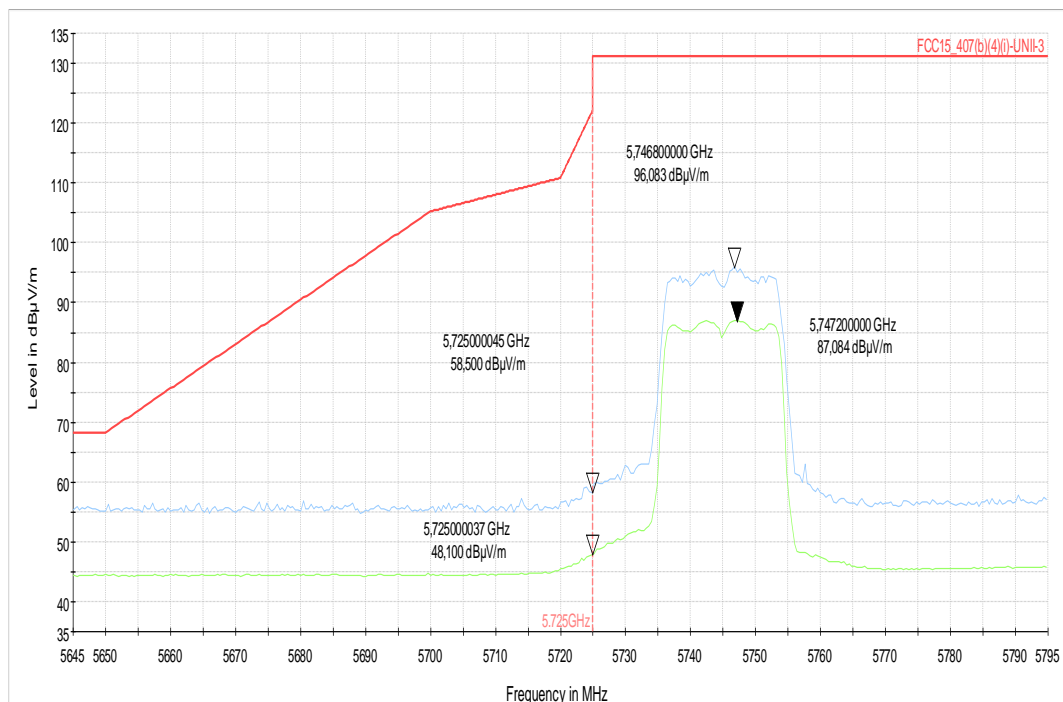
9.07a_n-mode_MCS3_ch100



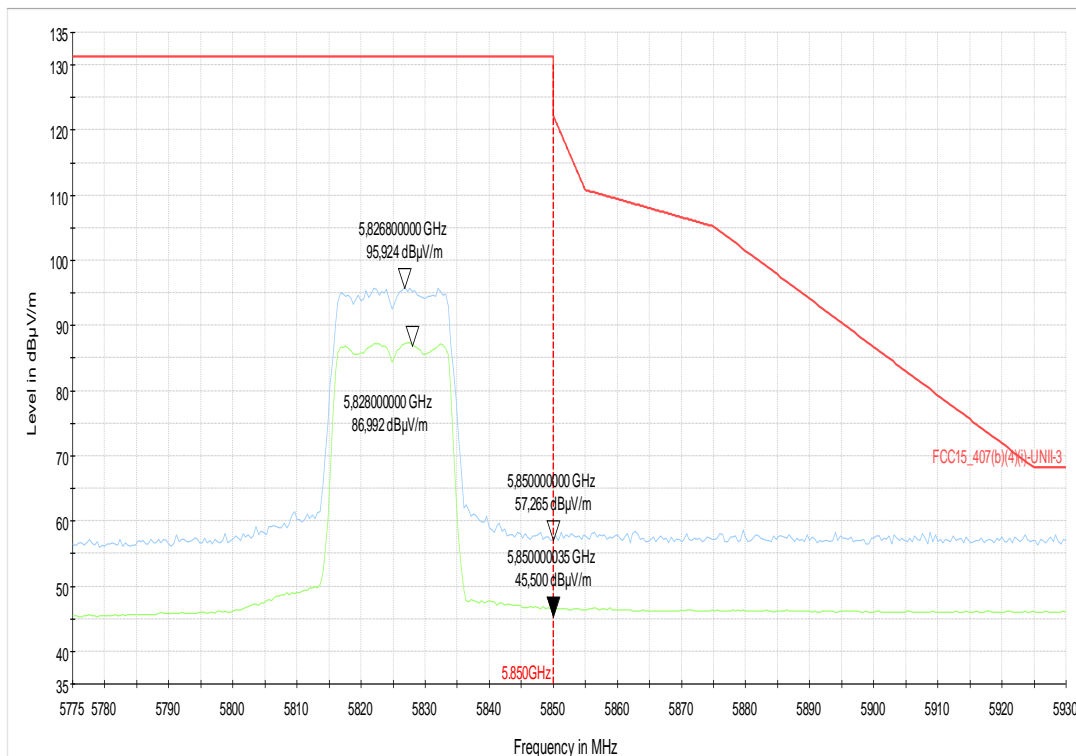
9.07b_n-mode_MCS3_ch140



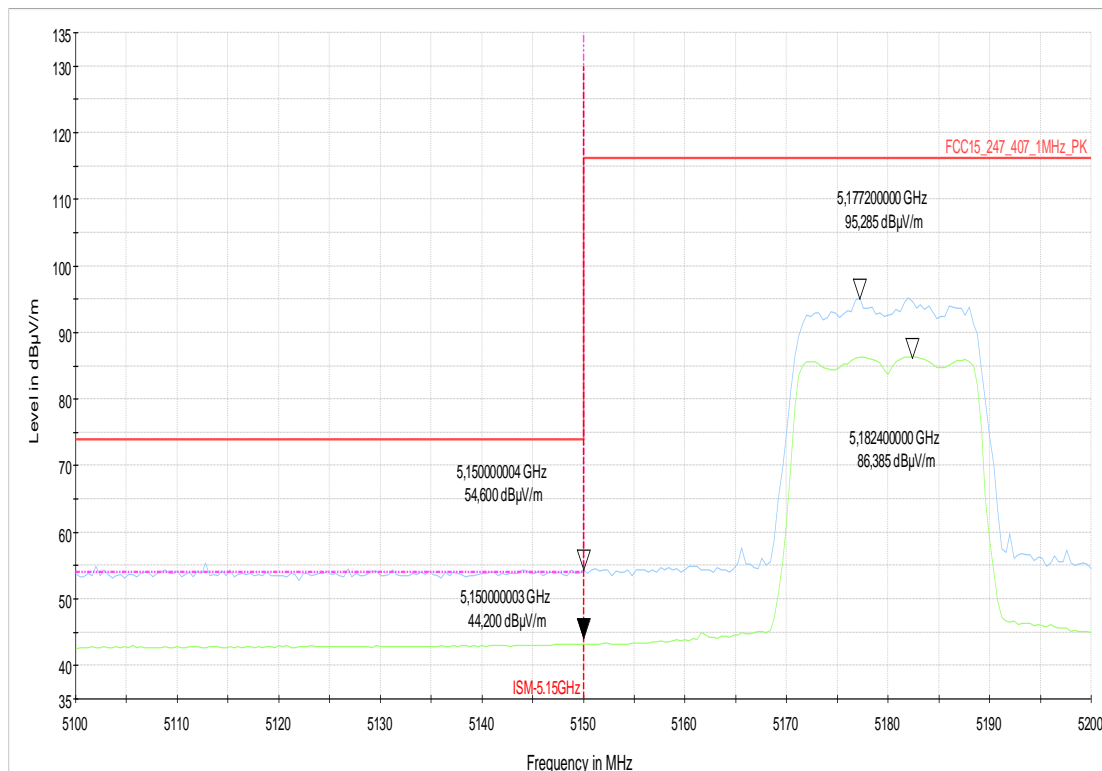
9.08a_n-mode_MCS3_ch149



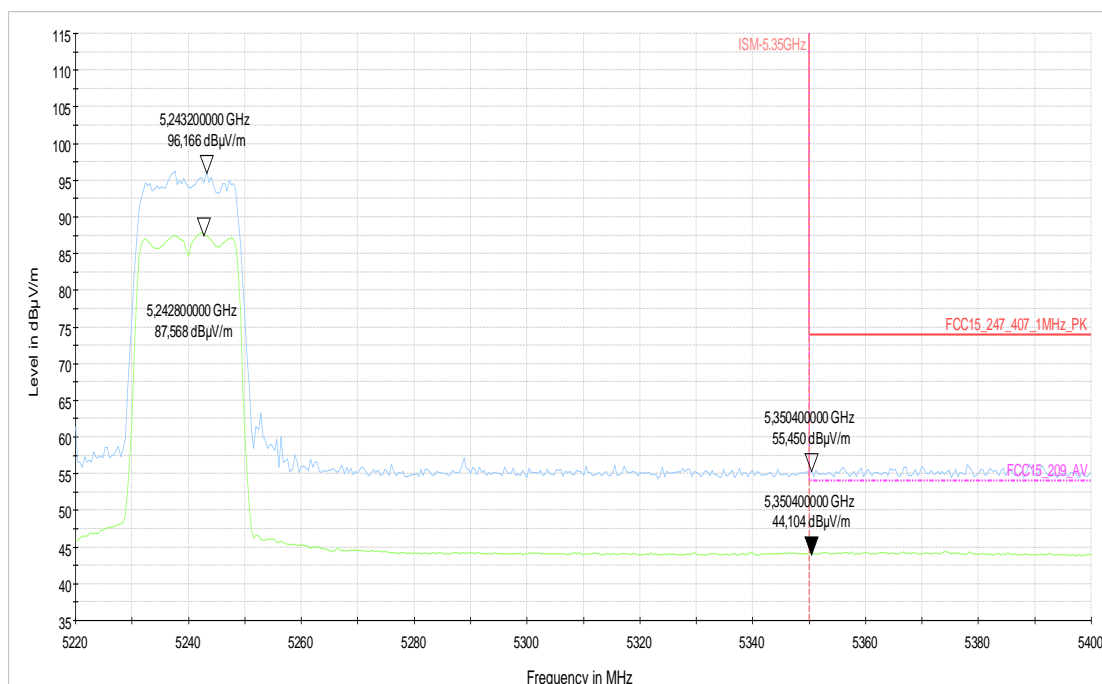
9.08b_n-mode_MCS3_ch165



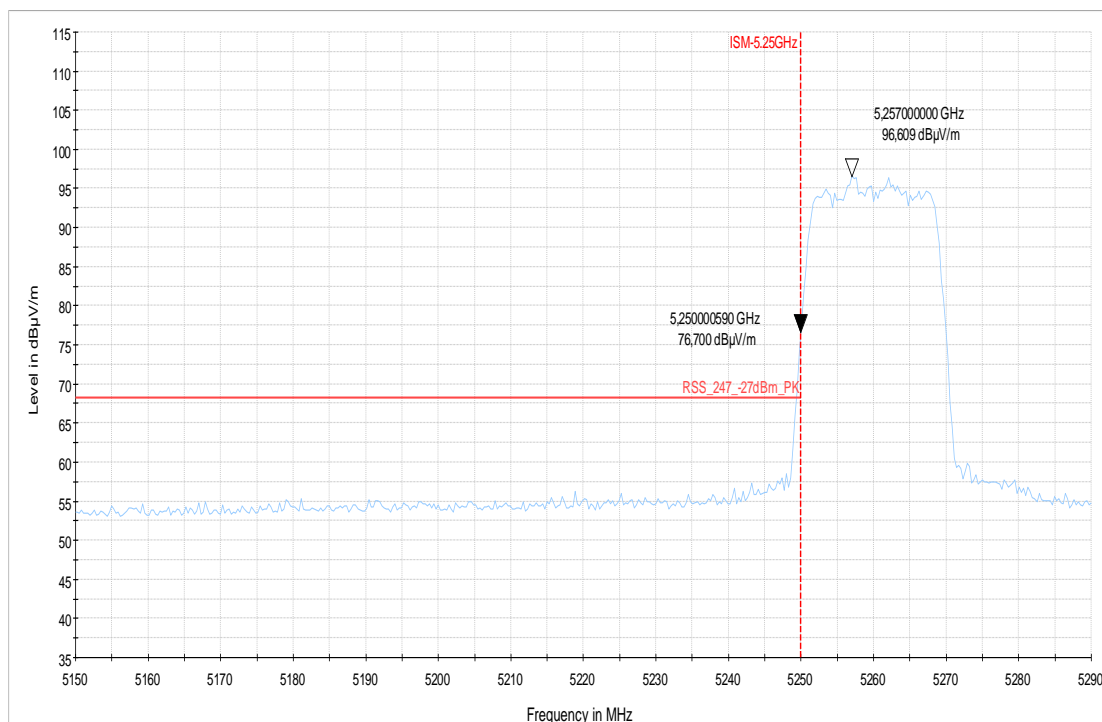
9.09a_ac-mode_MCS3_ch036



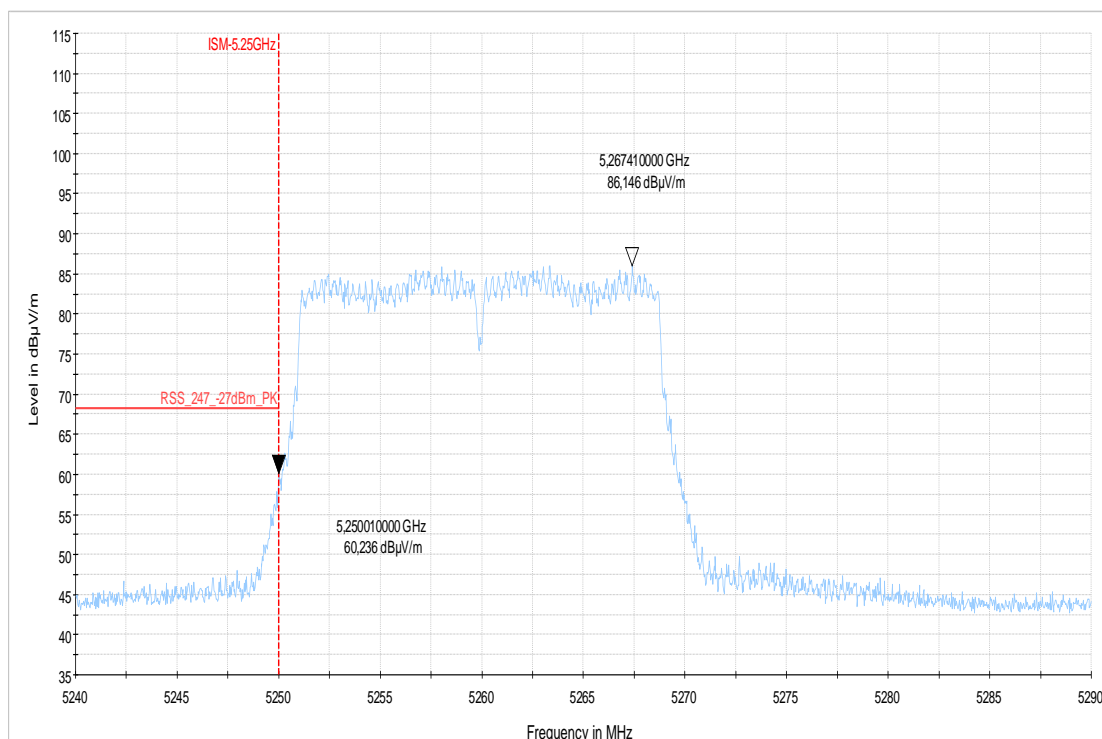
9.09b_ac-mode_MCS3_ch048



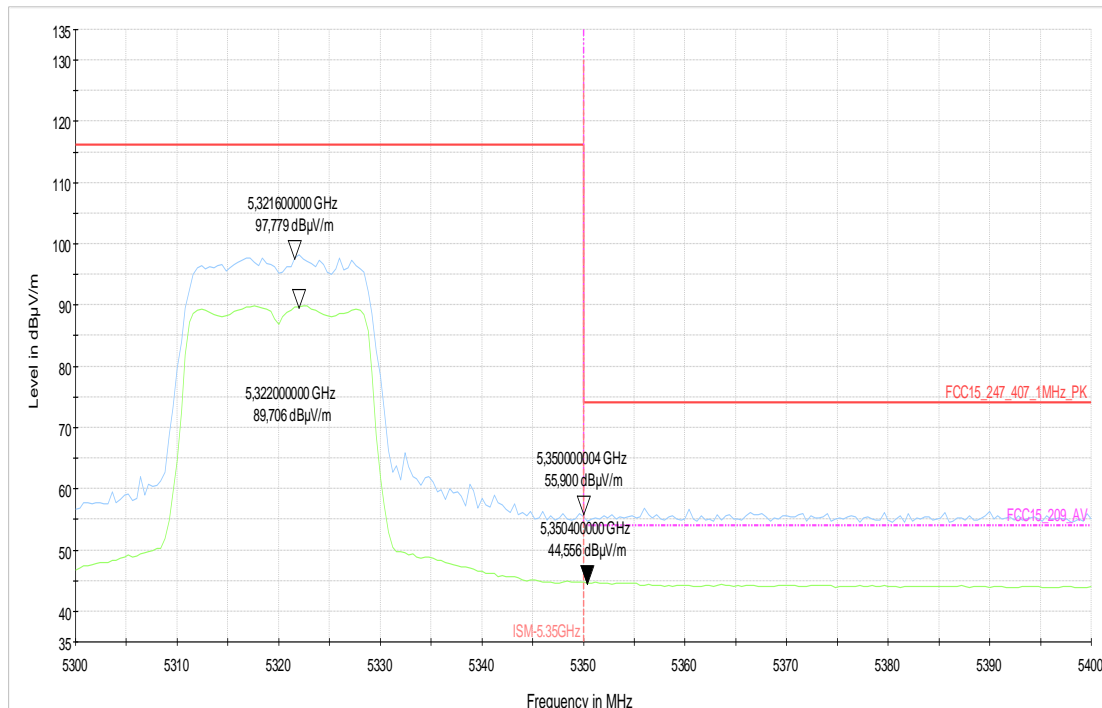
9.10a_ac-mode_MCS3_ch52_Step1



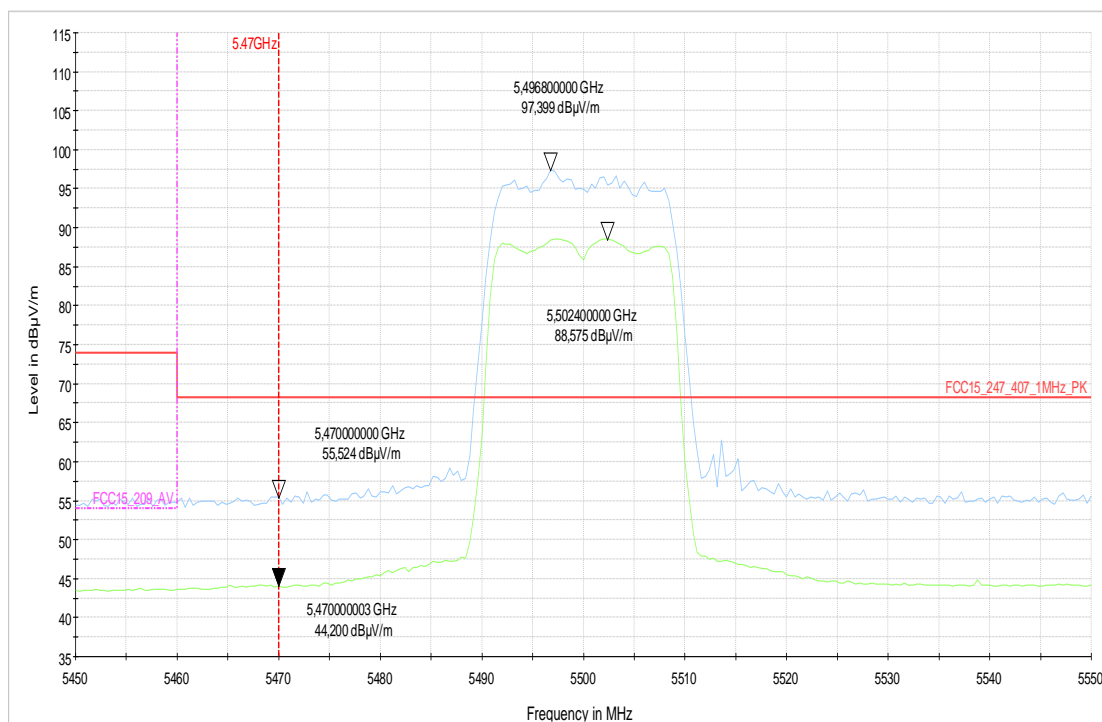
9.10a_ac-mode_MCS3_ch52_Step2



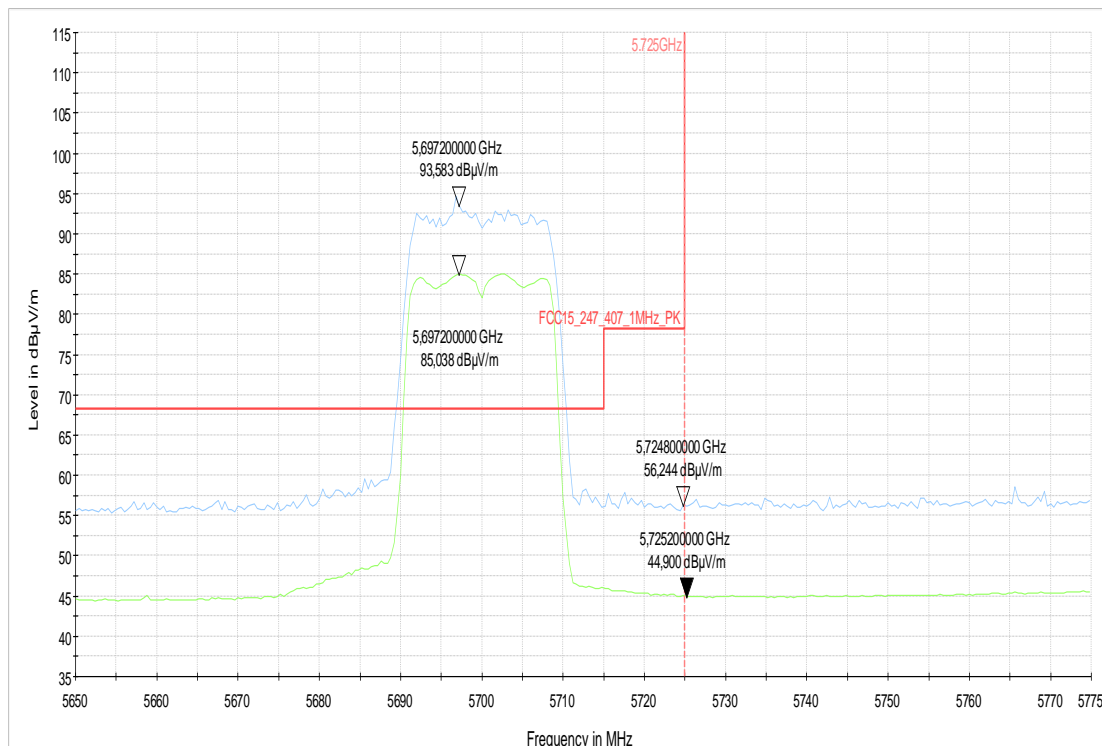
9.10b_ac-mode_MCS3_ch064



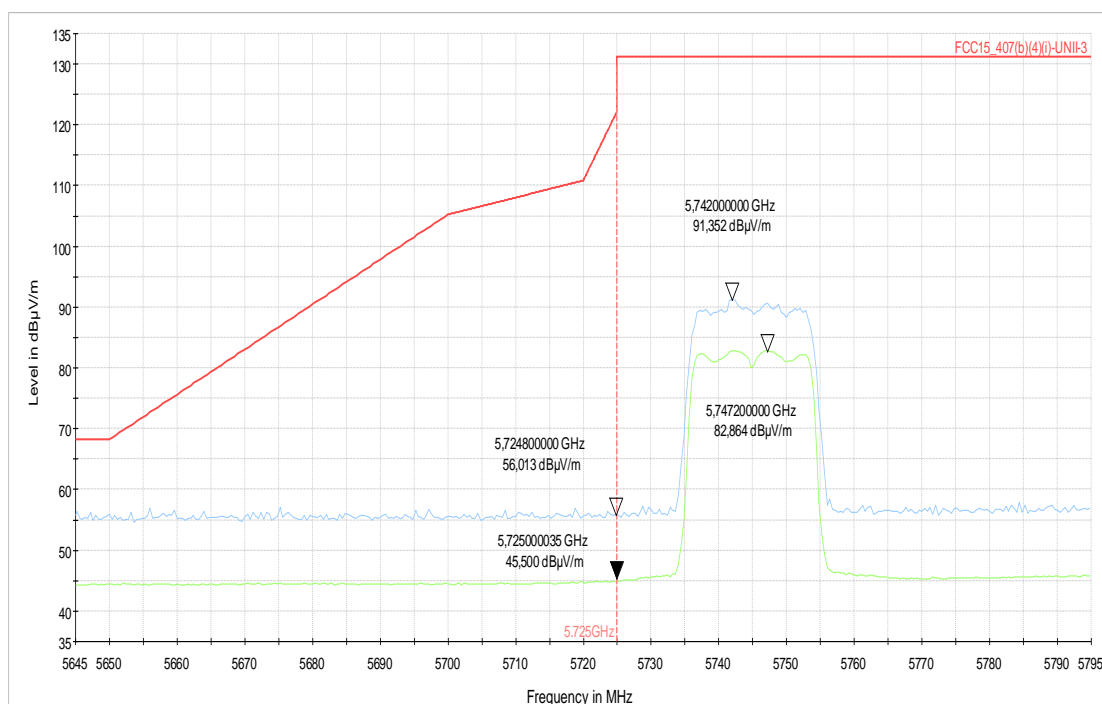
9.11a_ac-mode_MCS3_ch100



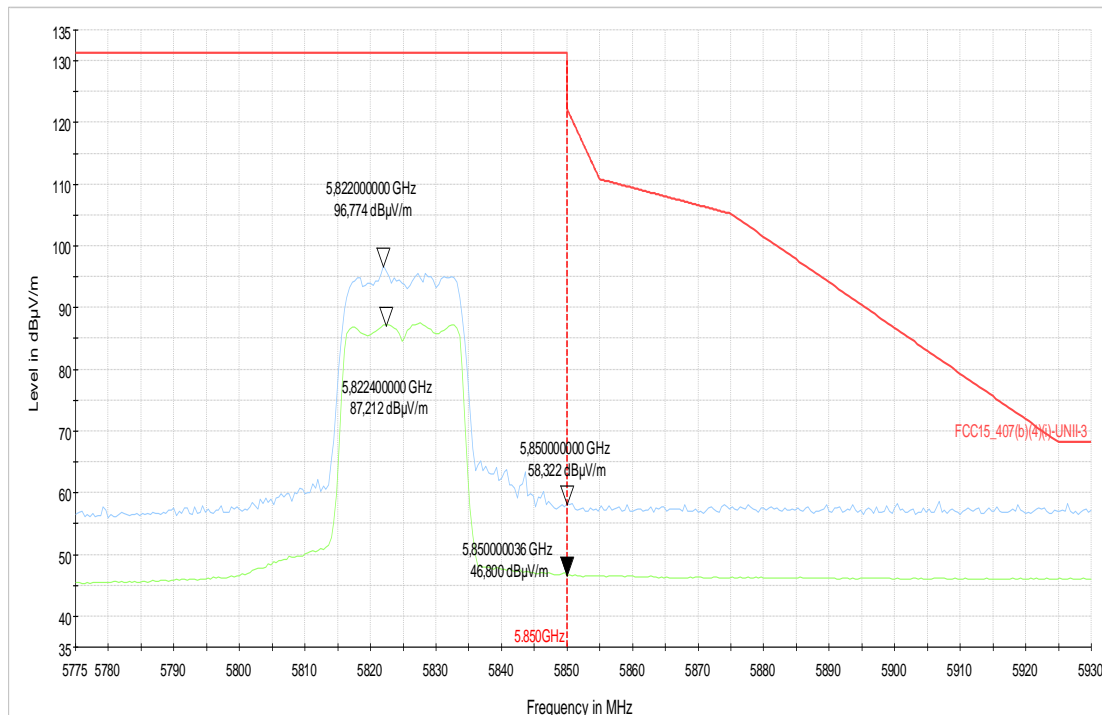
9.11b_ac-mode_MCS3_ch140



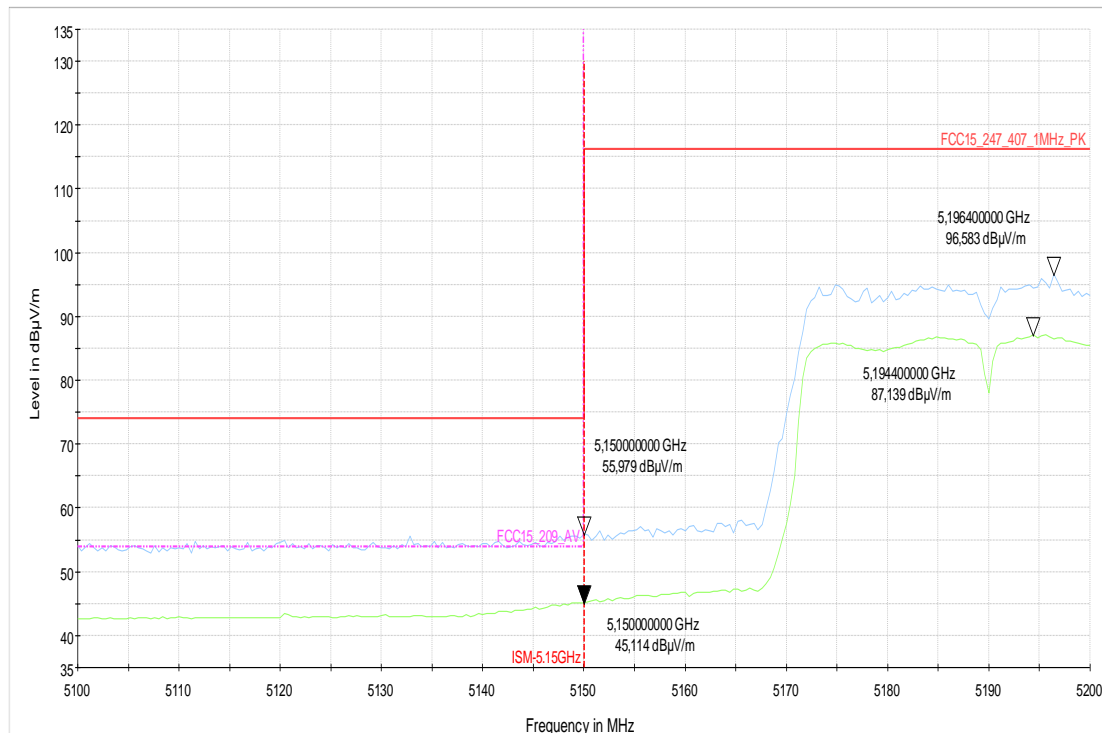
9.12a_ac-mode_MCS3_ch149



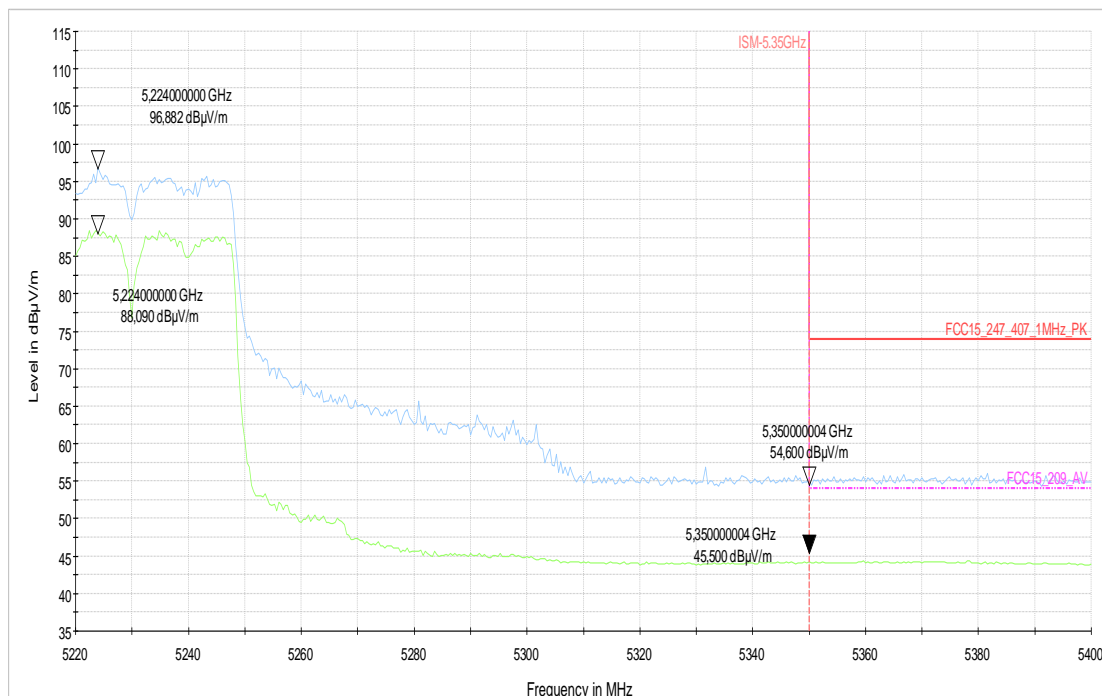
9.12b_ac-mode_MCS3_ch165



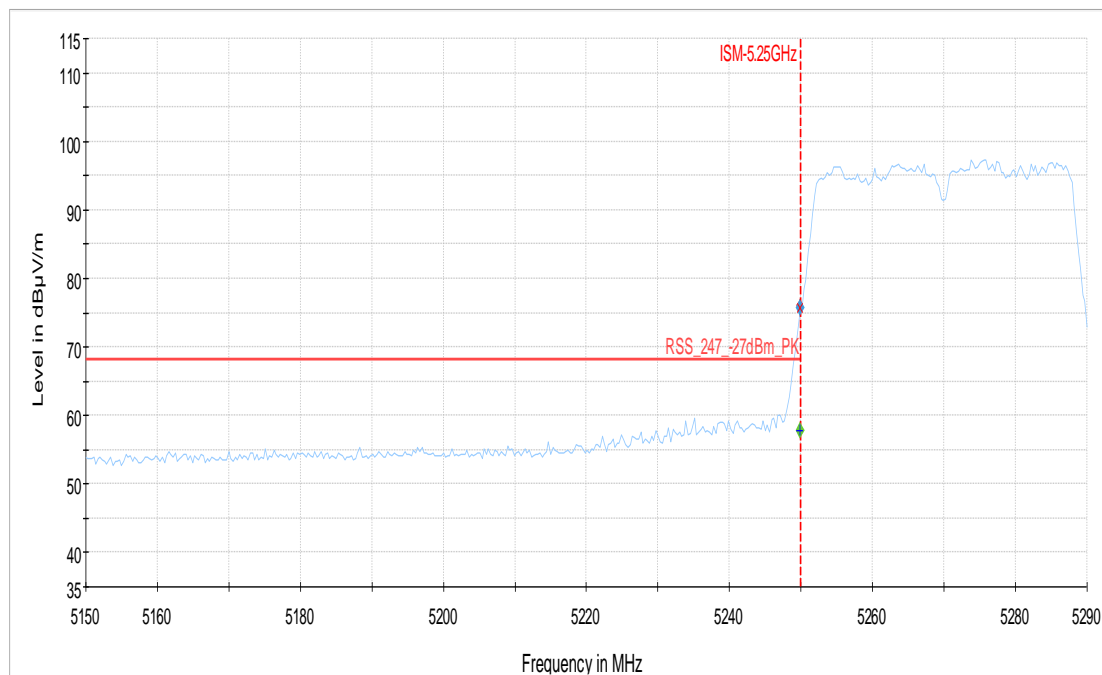
9.13a_n-mode_MCS3_ch038



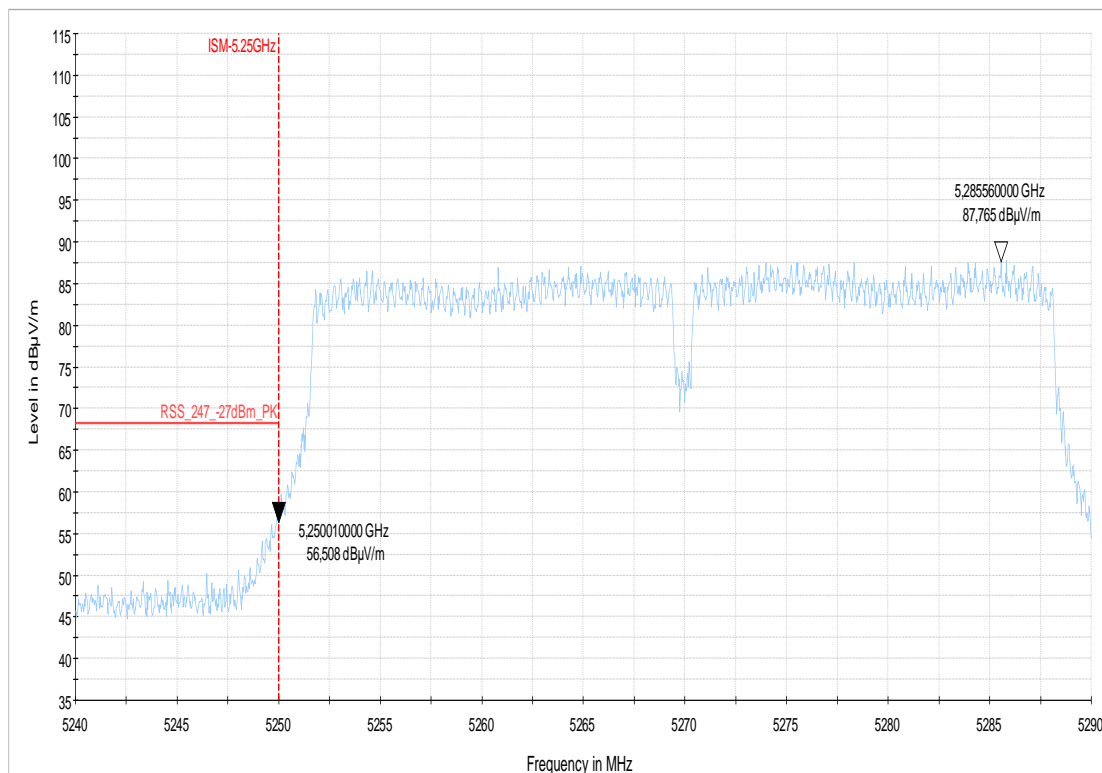
9.13b_n-mode_MCS3_ch046



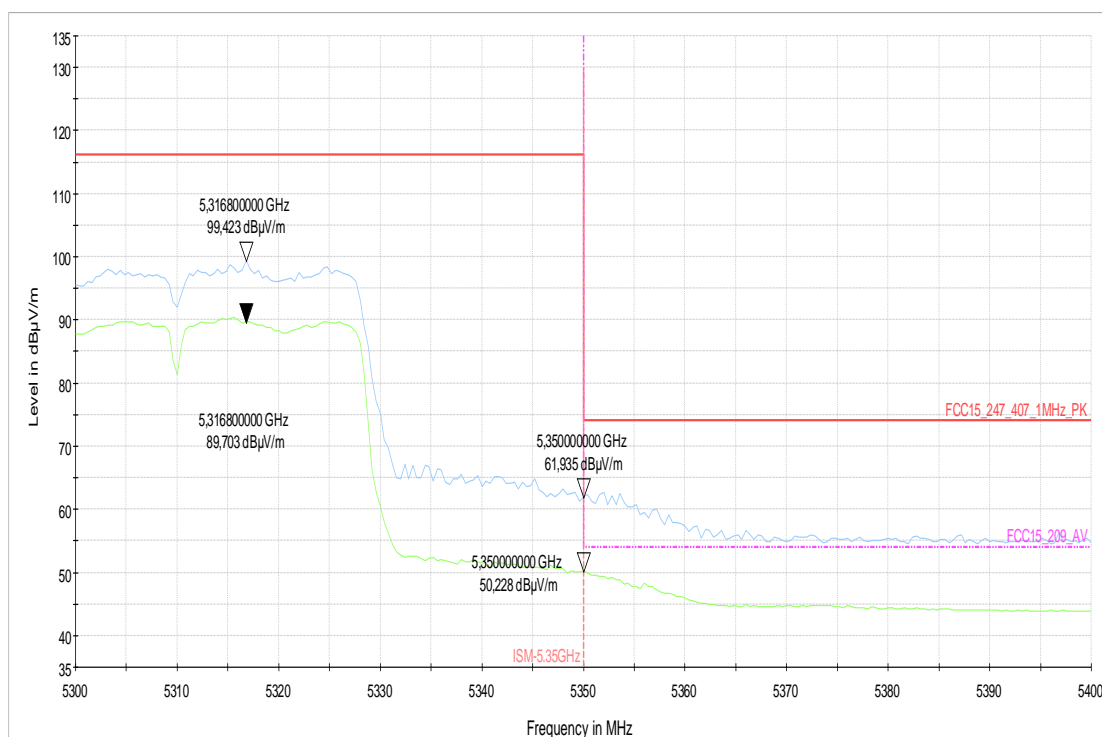
9.14a_n-mode_MCS3_ch54_Step1



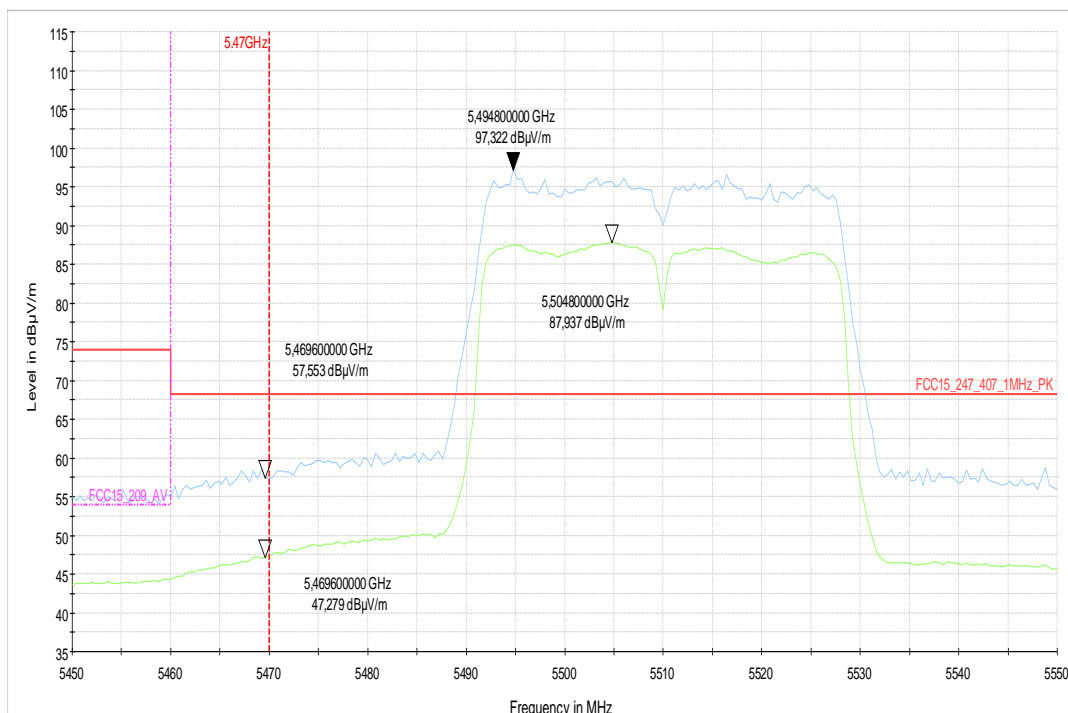
9.14a_n-mode_MCS3_ch54_Step2



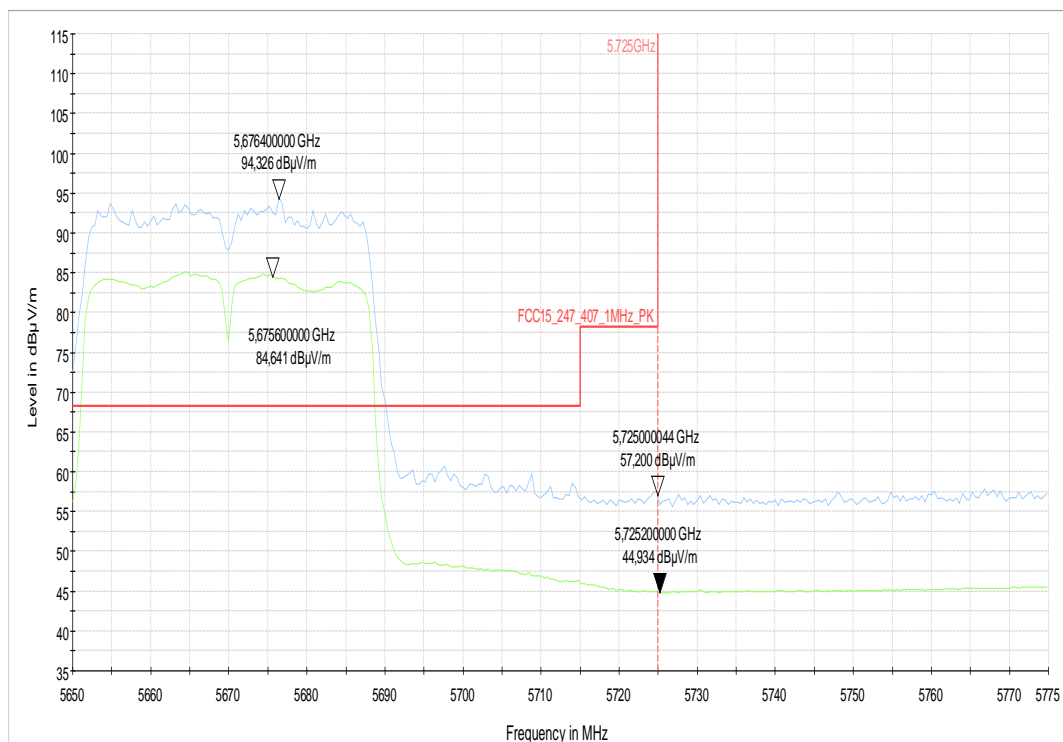
9.14b_n-mode_MCS3_ch062



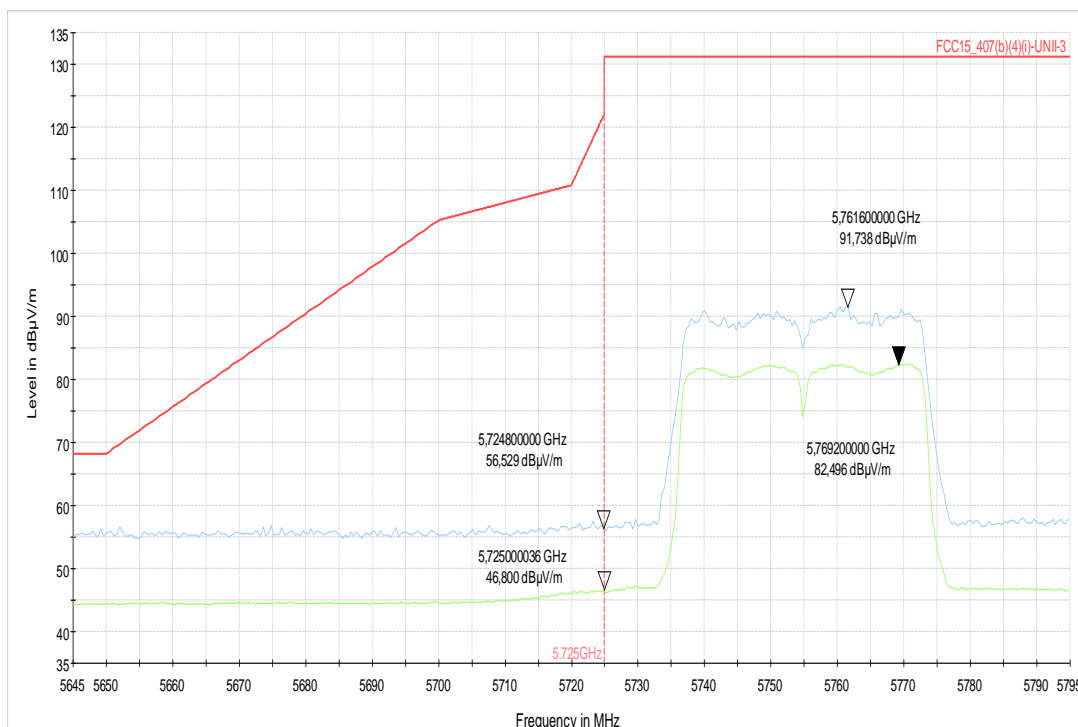
9.15a_n-mode_MCS3_ch102



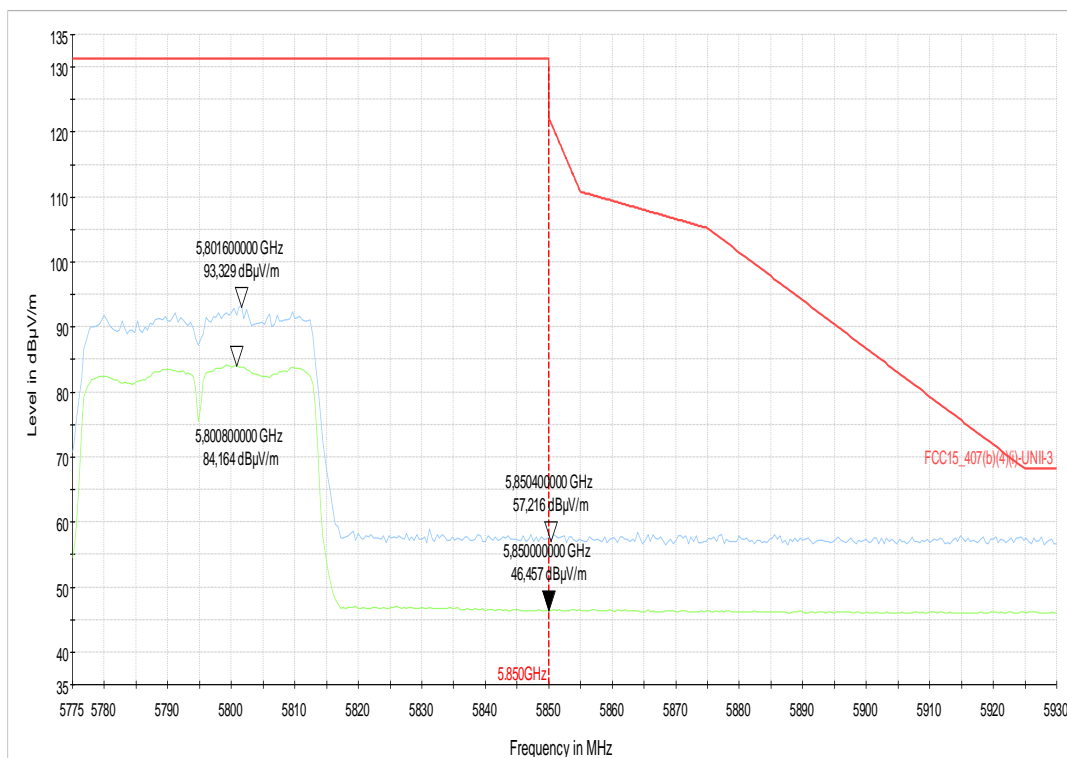
9.15b_n-mode_MCS3_ch134



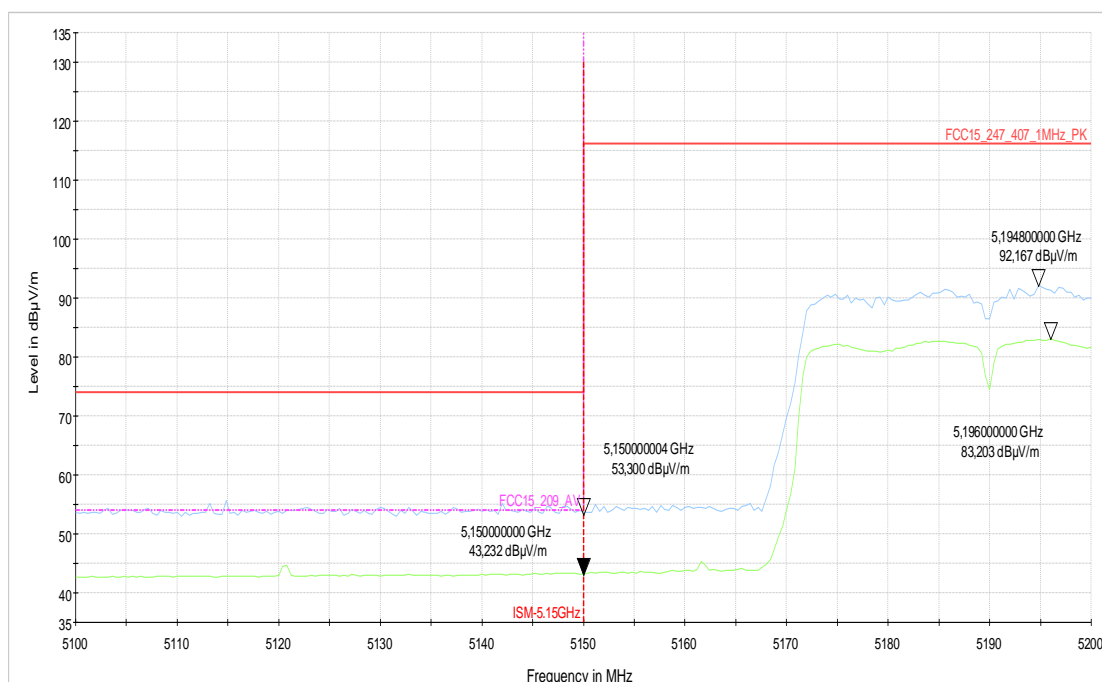
9.16a_n-mode_MCS3_ch151



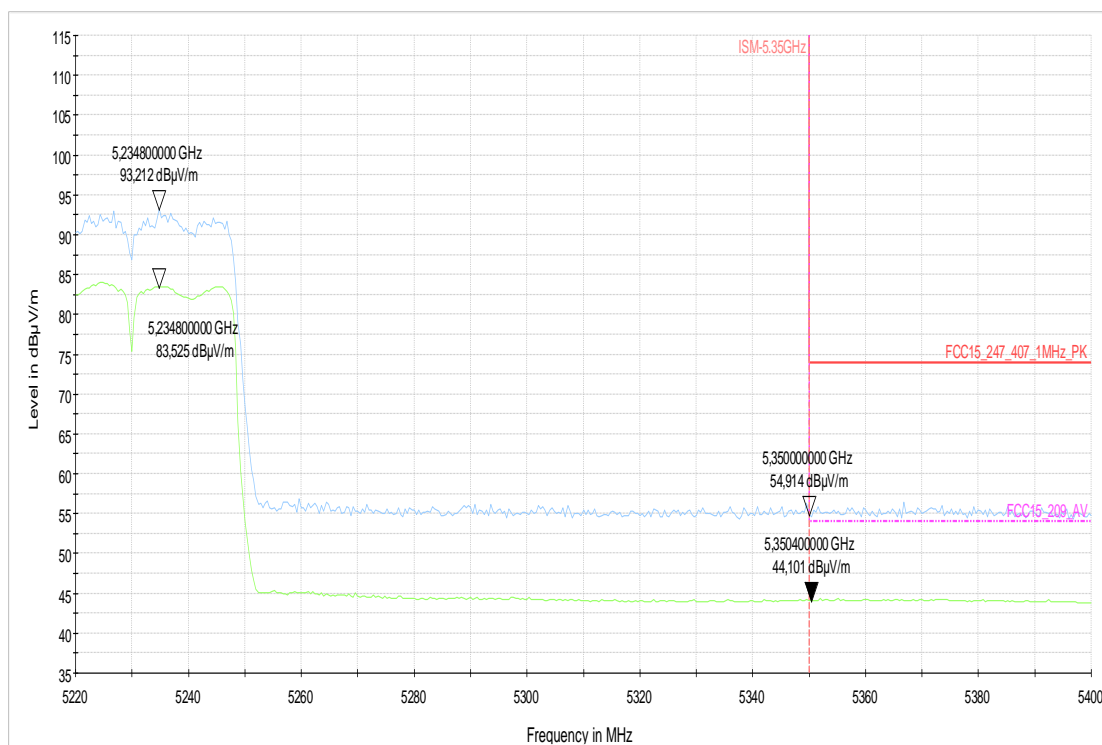
9.16b_n-mode_MCS3_ch159



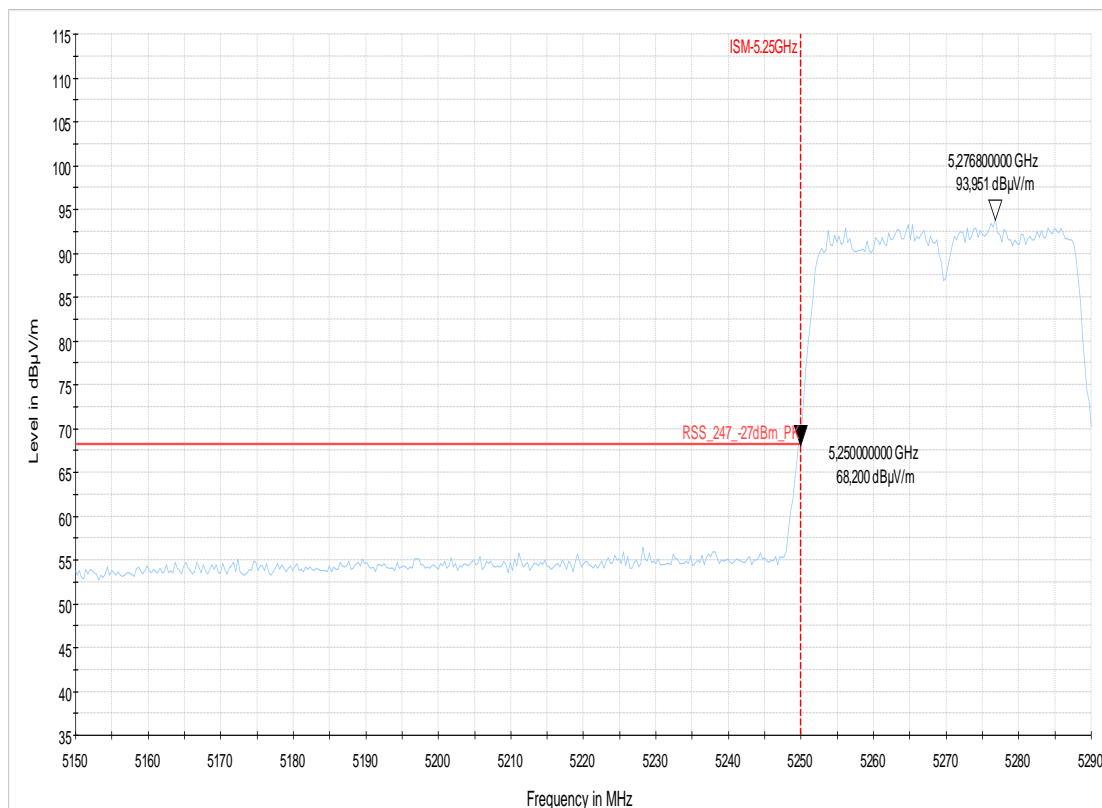
9.17a_ac-mode_MCS9_ch038



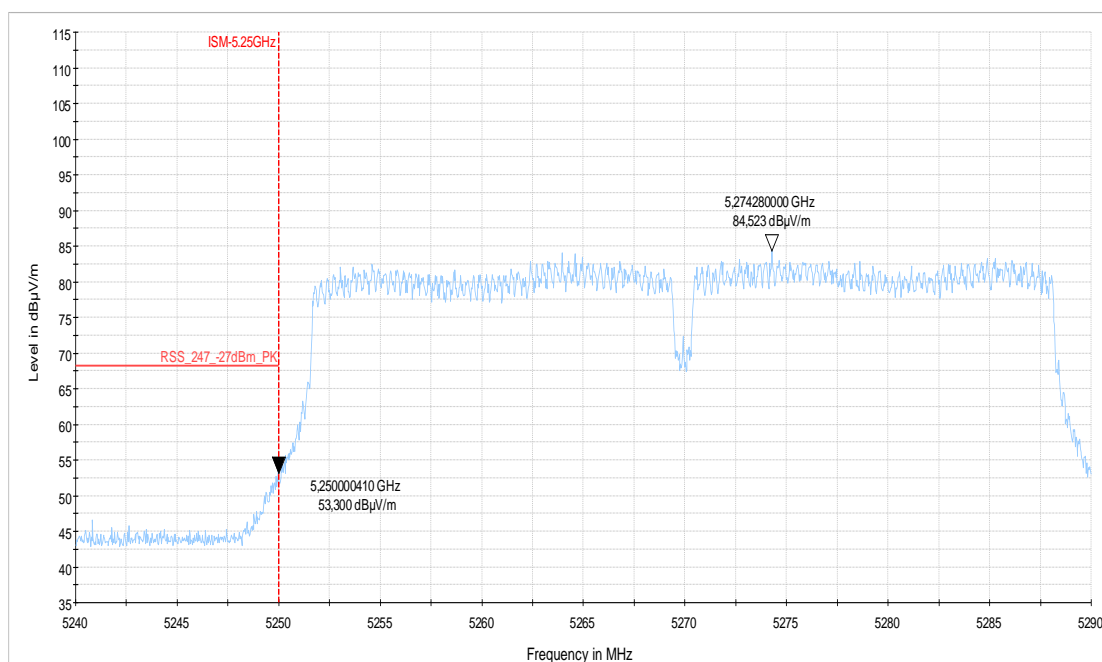
9.17b_ac-mode_MCS9_ch046



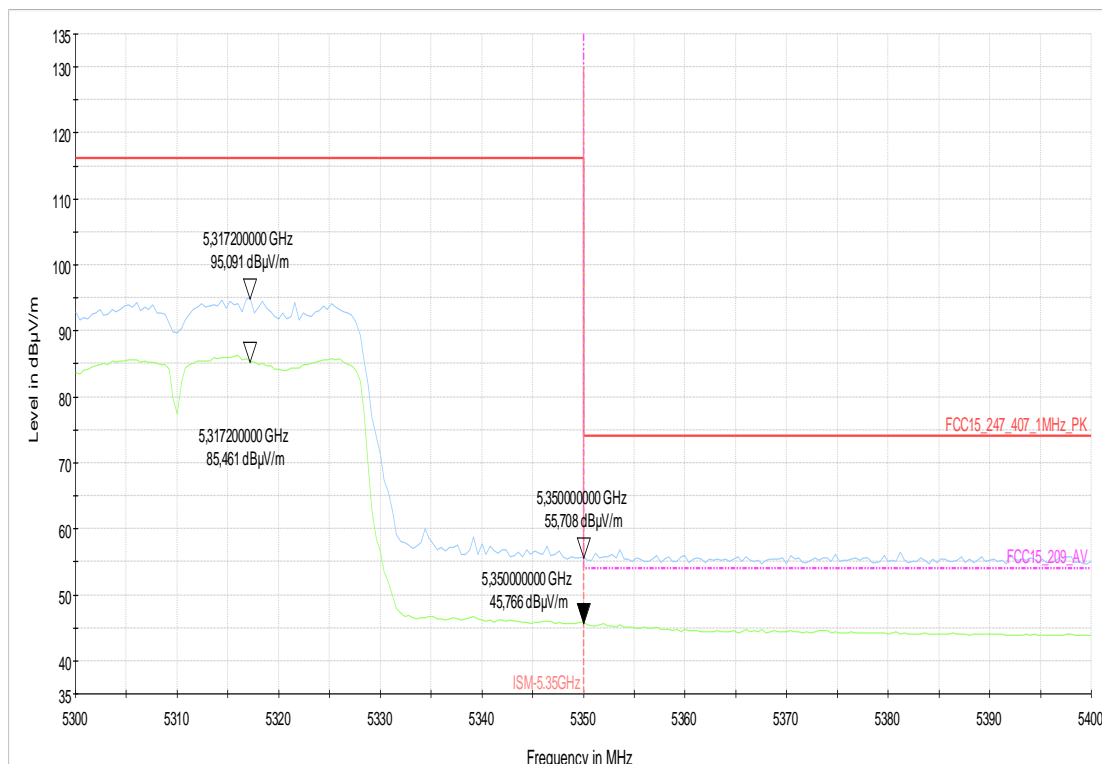
9.18a_ac-mode_MCS9_ch54_Step1



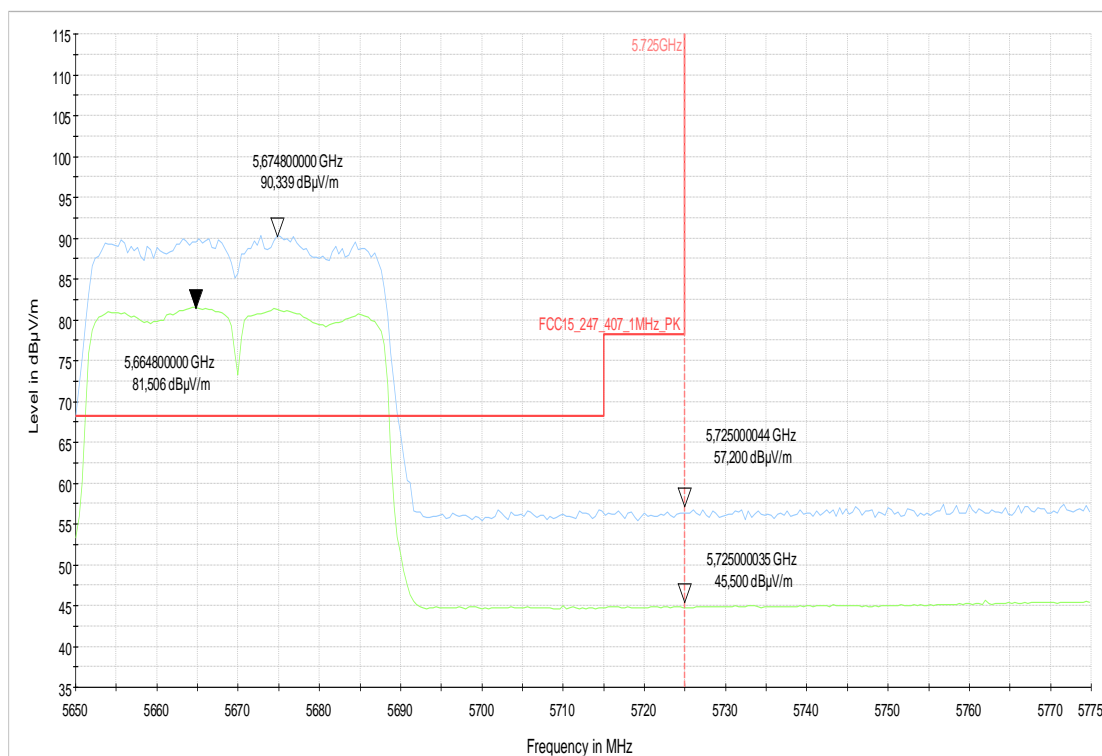
9.18a_ac-mode_MCS9_ch54_Step2



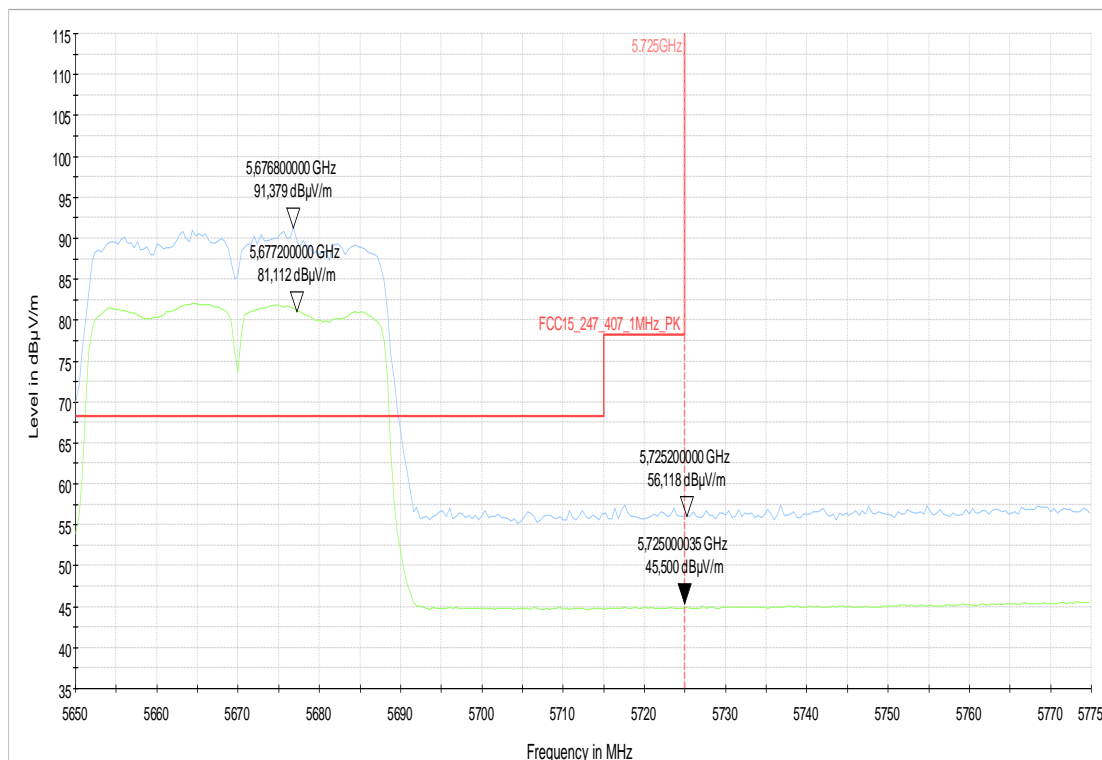
9.18b_ac-mode_MCS9_ch62



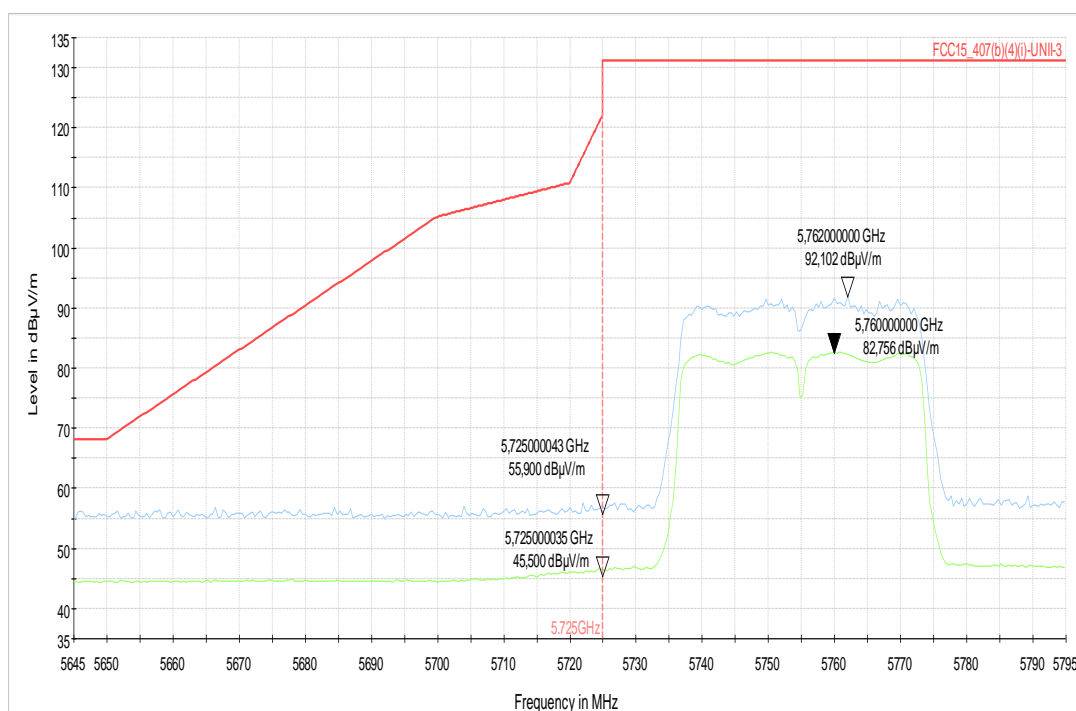
9.19a_ac-mode_MCS9_ch134



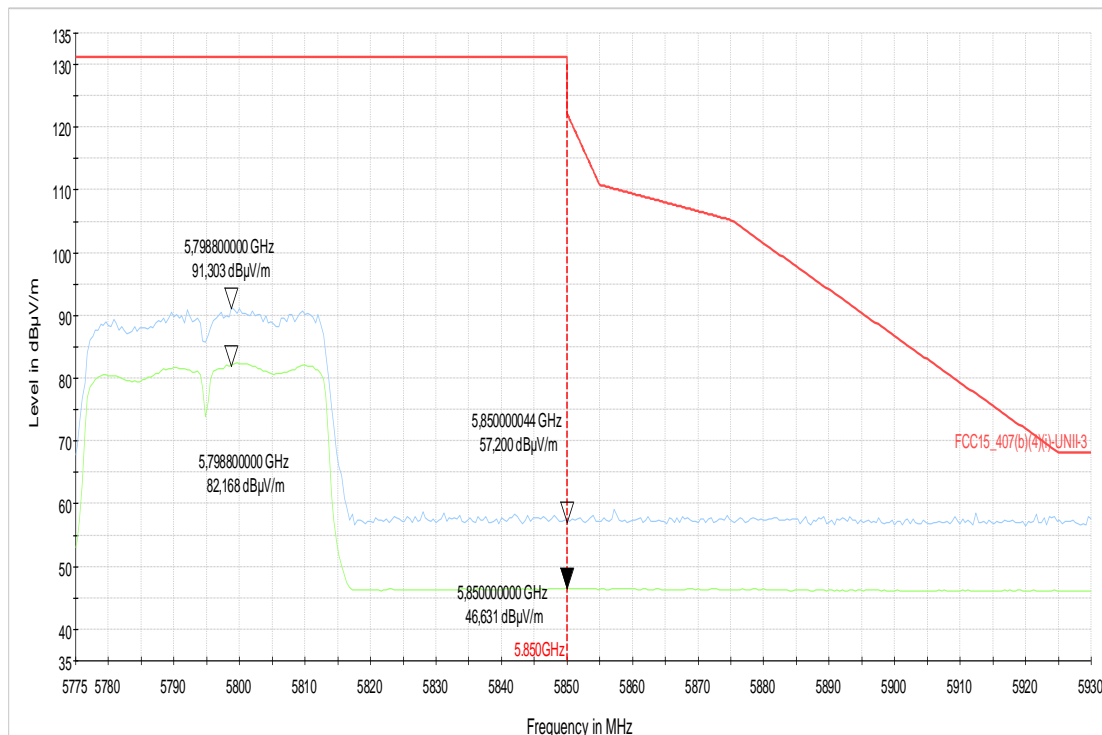
9.19b_ac-mode_MCS9_ch134



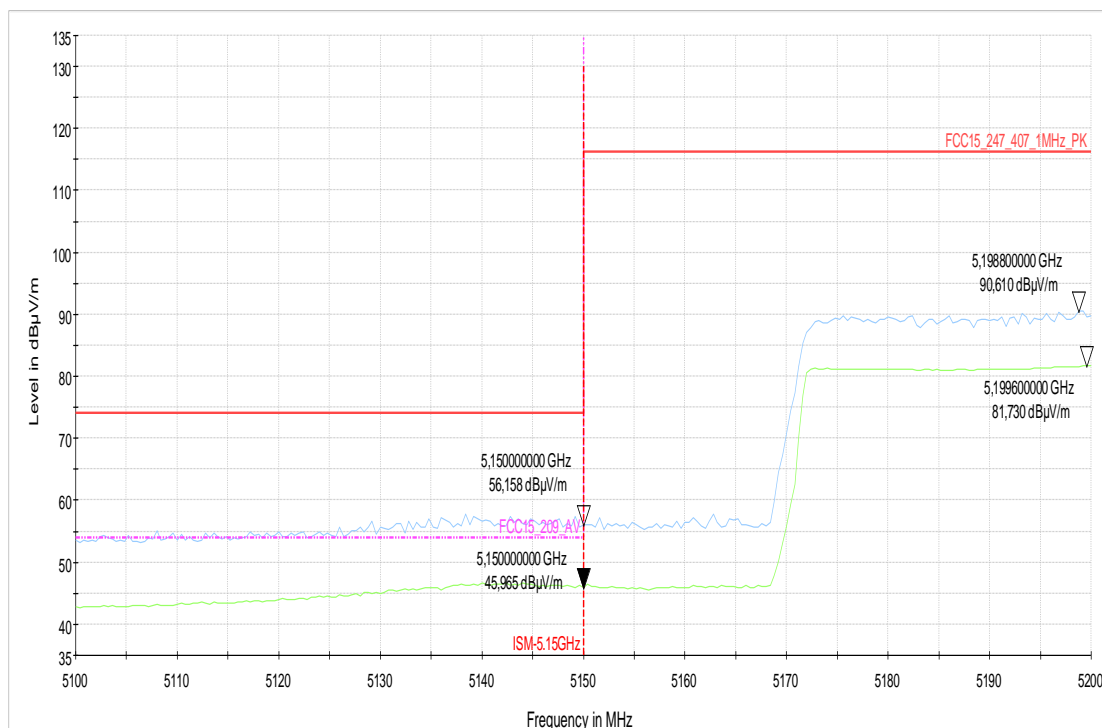
9.20a_ac-mode_MCS9_ch151



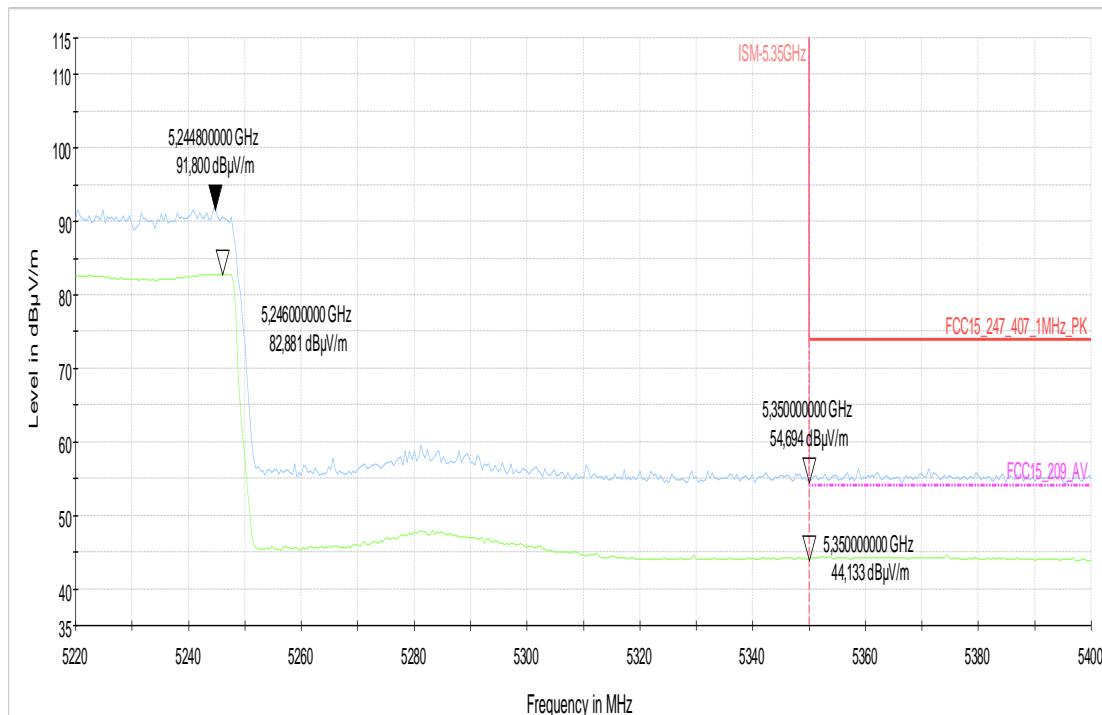
9.20b_ac-mode_MCS9_ch159



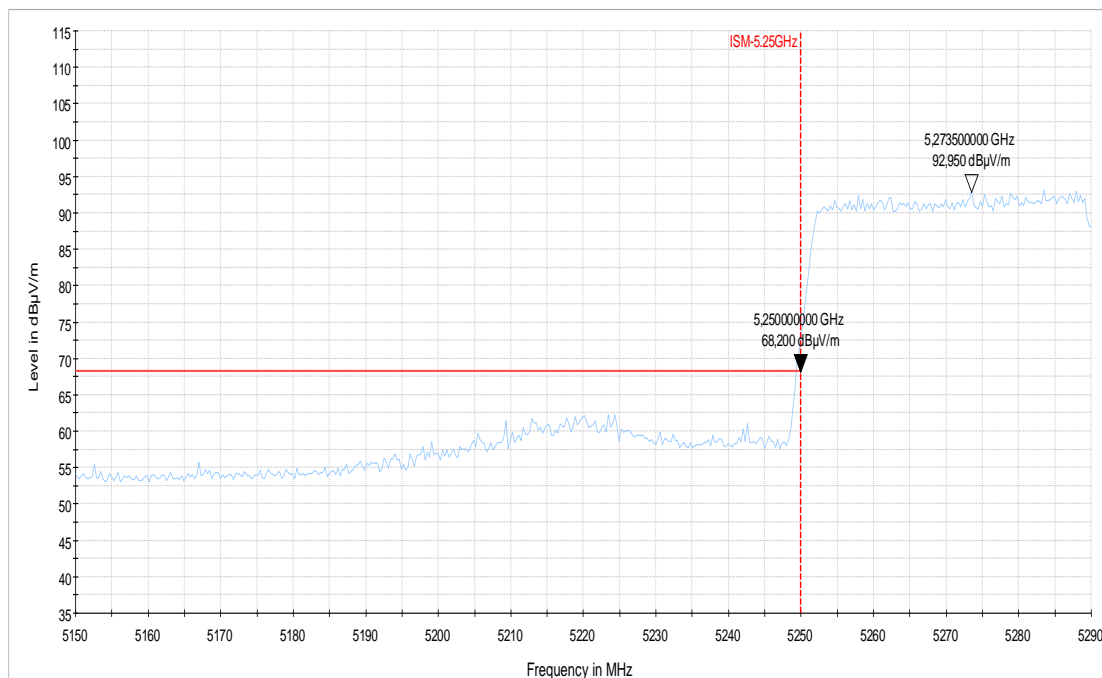
9.21a_ac-mode_MCS7_ch042



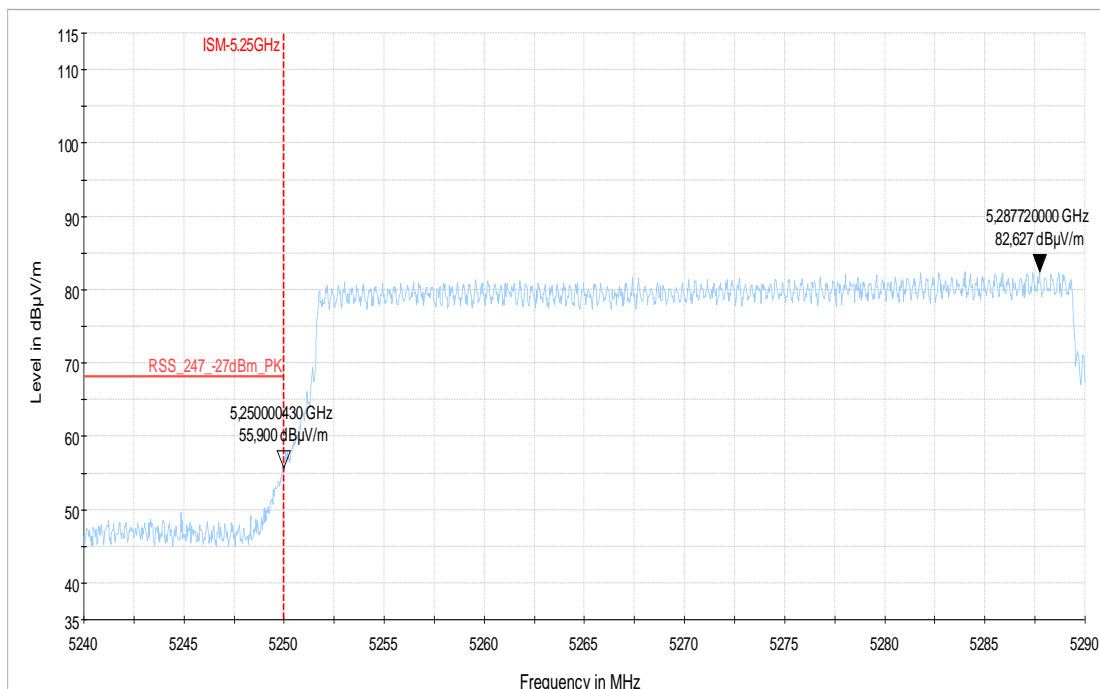
9.21b_ac-mode_MCS7_ch042



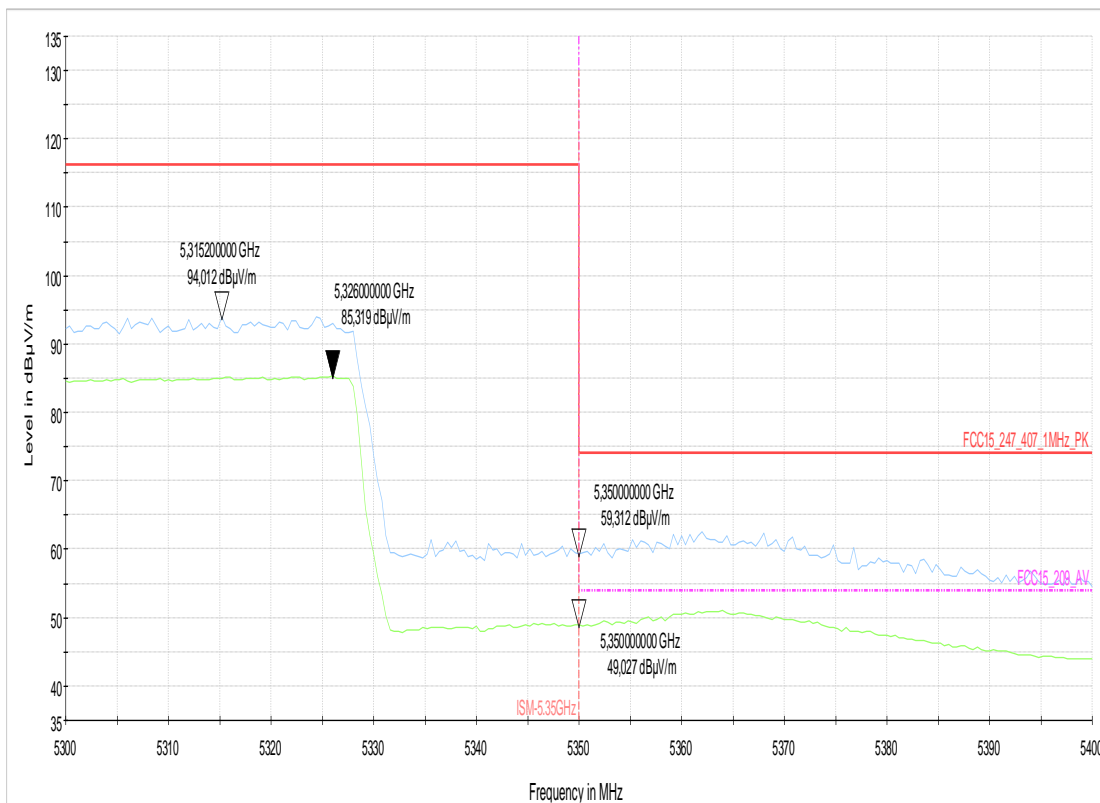
9.22a_ac-mode_MCS7_ch58_Step1



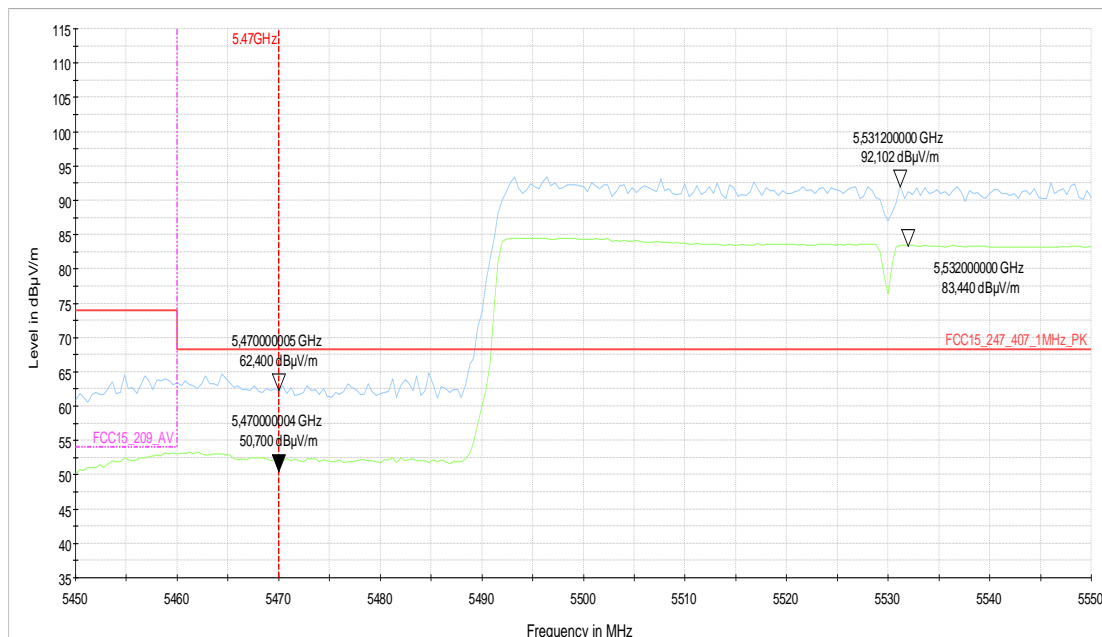
9.22a_ac-mode_MCS7_ch58_STep2



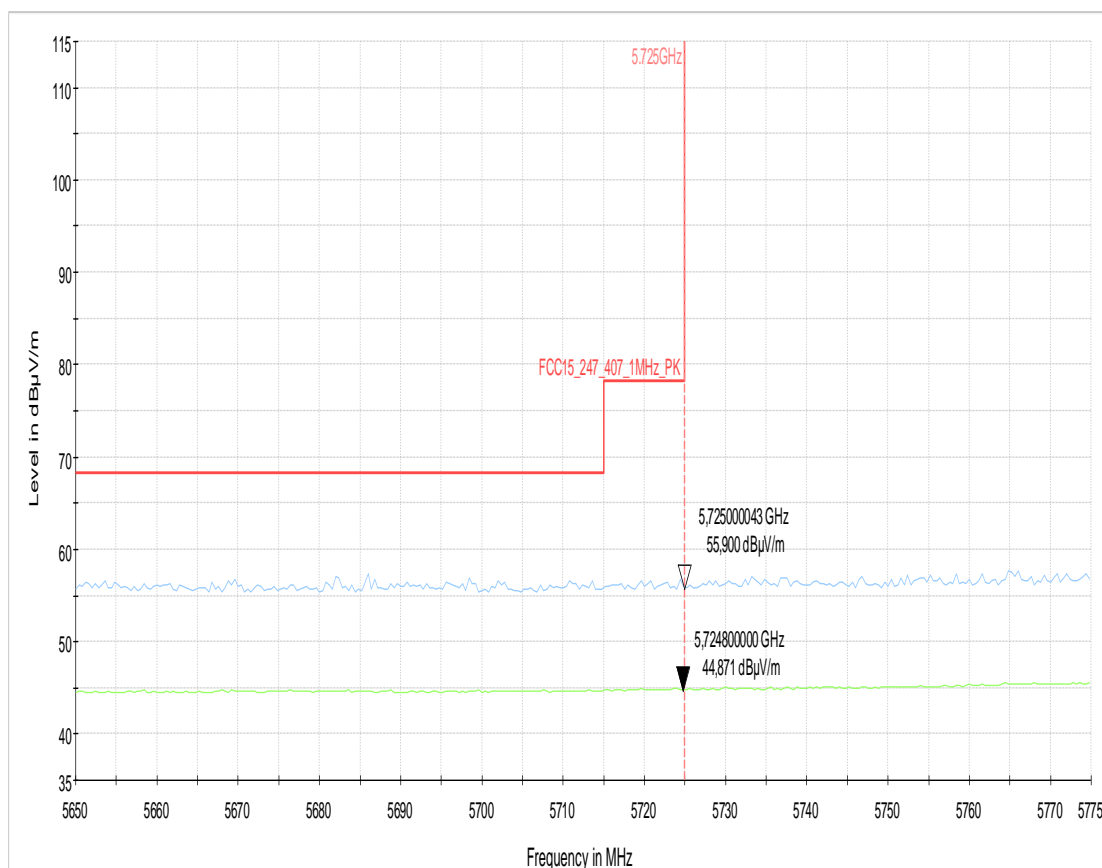
9.22b_ac-mode_MCS7_ch58



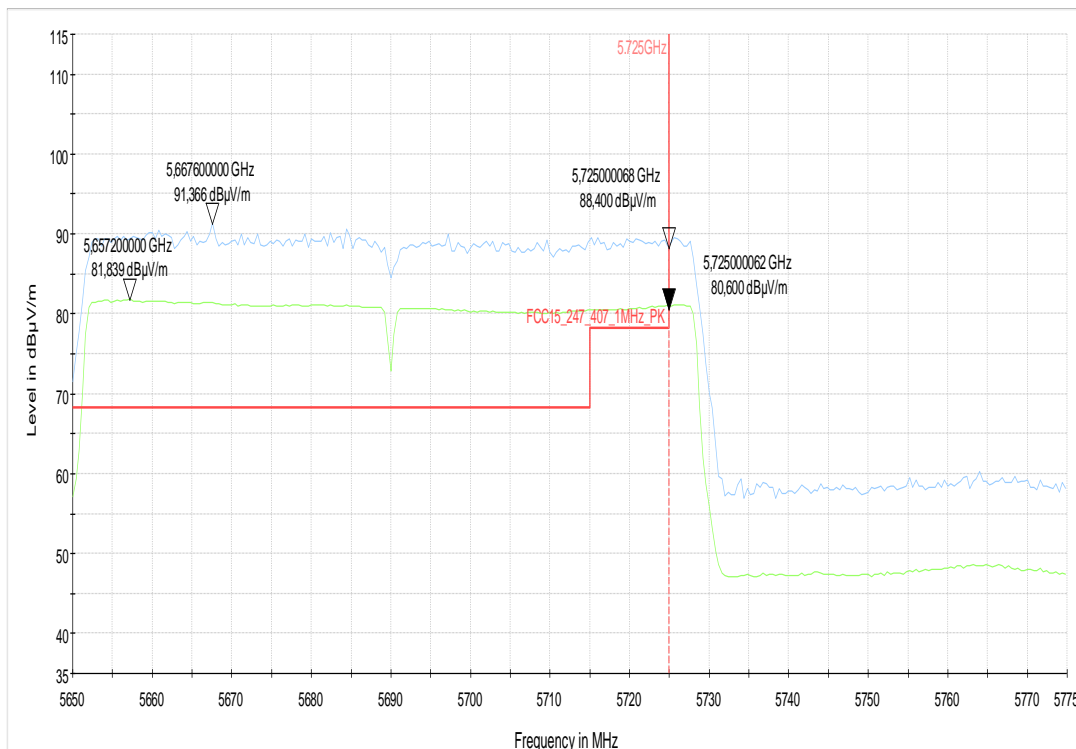
9.23a_ac-mode_MCS7_ch106



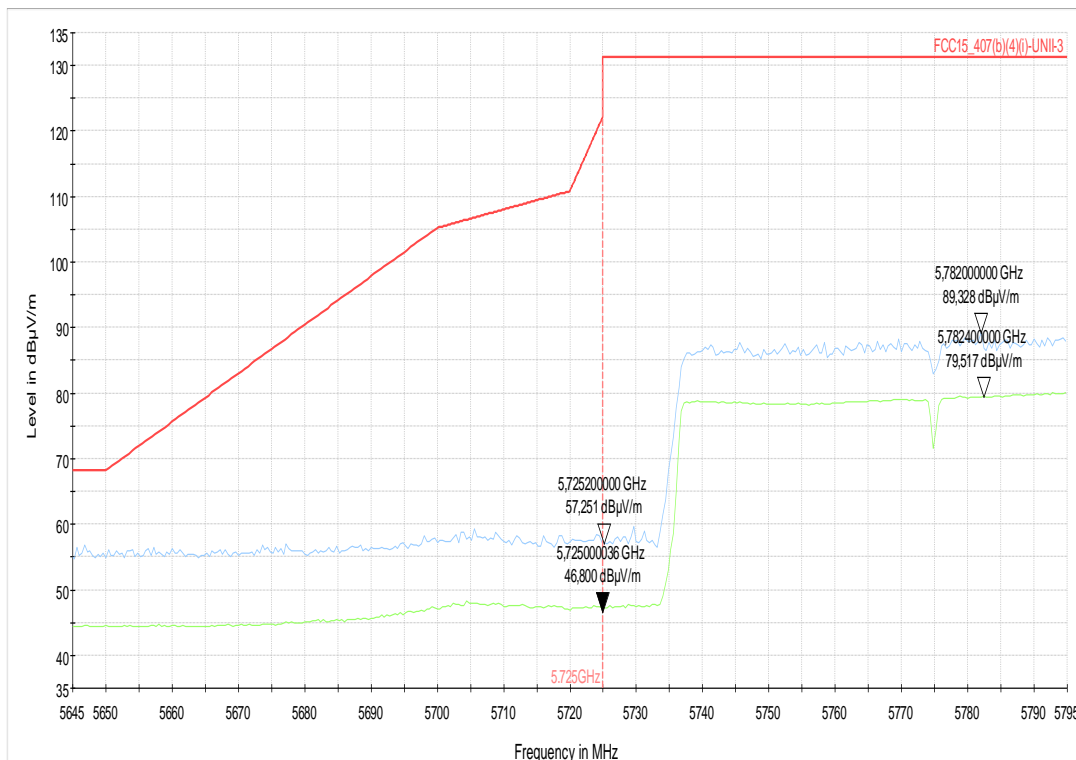
9.23b_ac-mode_MCS7_ch106



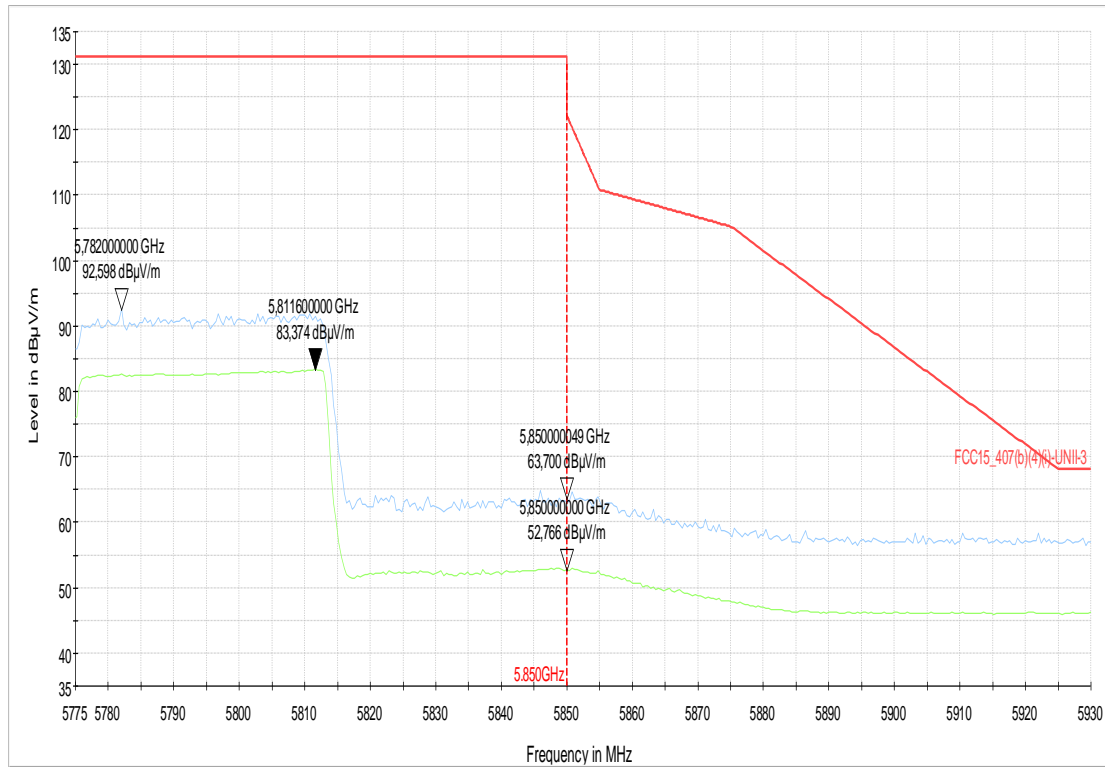
9.23b_ac-mode_MCS7_ch138



9.24a_ac-mode_MCS7_ch155

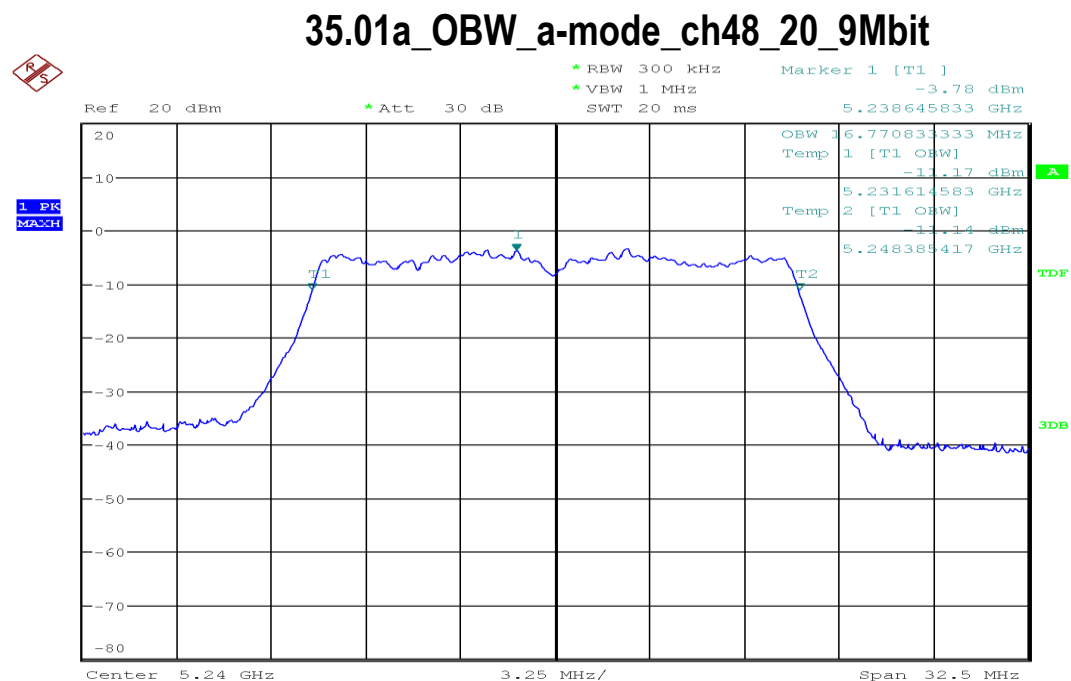


9.24b_ac-mode_MCS7_ch155

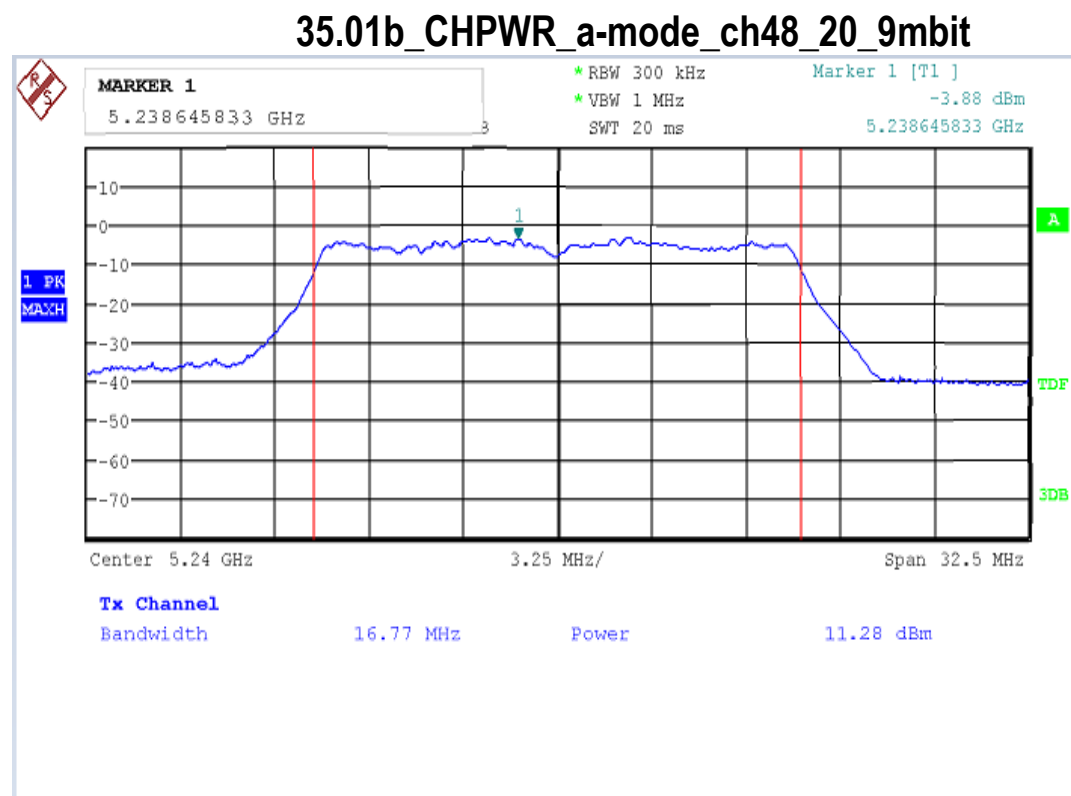


2.6.1. Canada RSS-247 requirement, Issue 2, 6.2.1.2

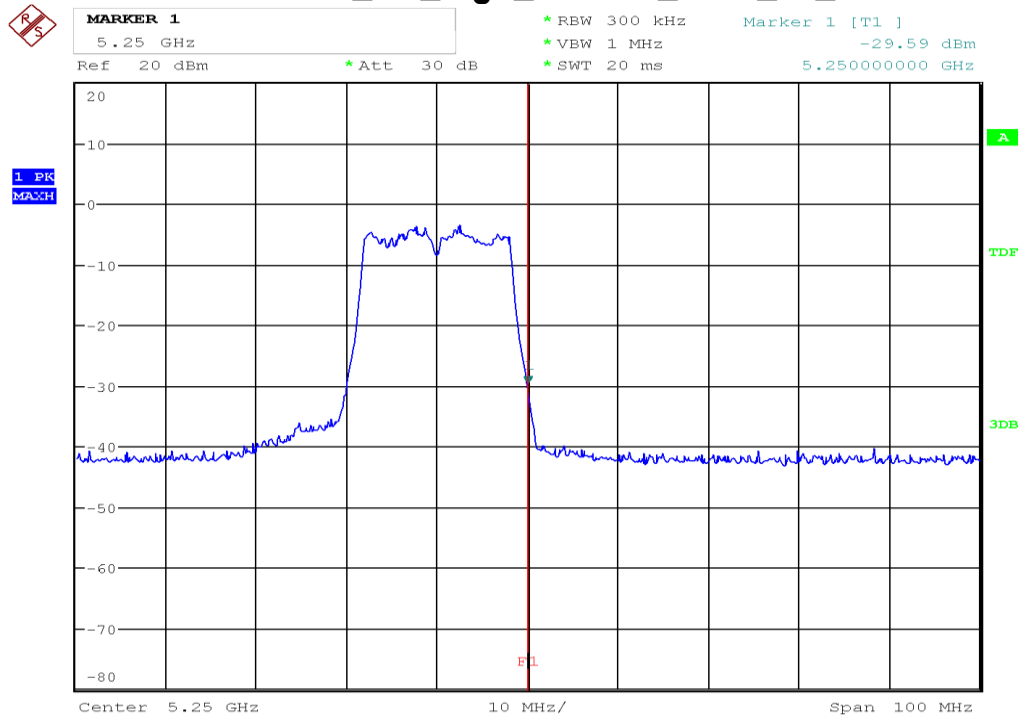
2.6.1.1. 20 MHz



Date: 25.JUL.2019 10:20:04

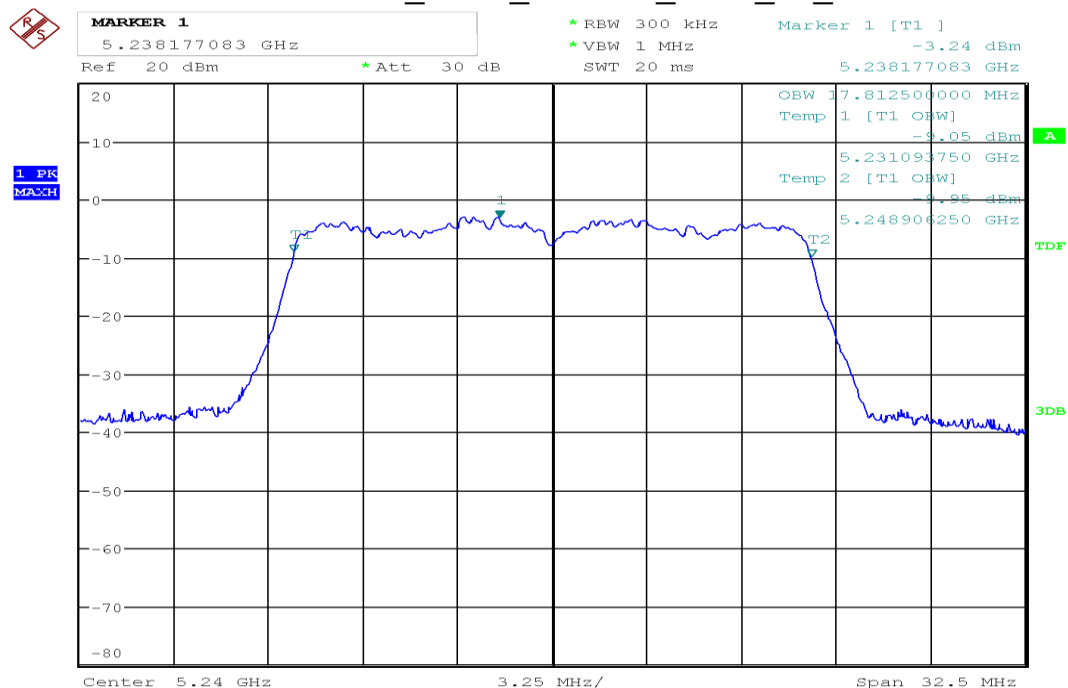


35.01c_BE_high_a-mode_ch48_20_9mbit



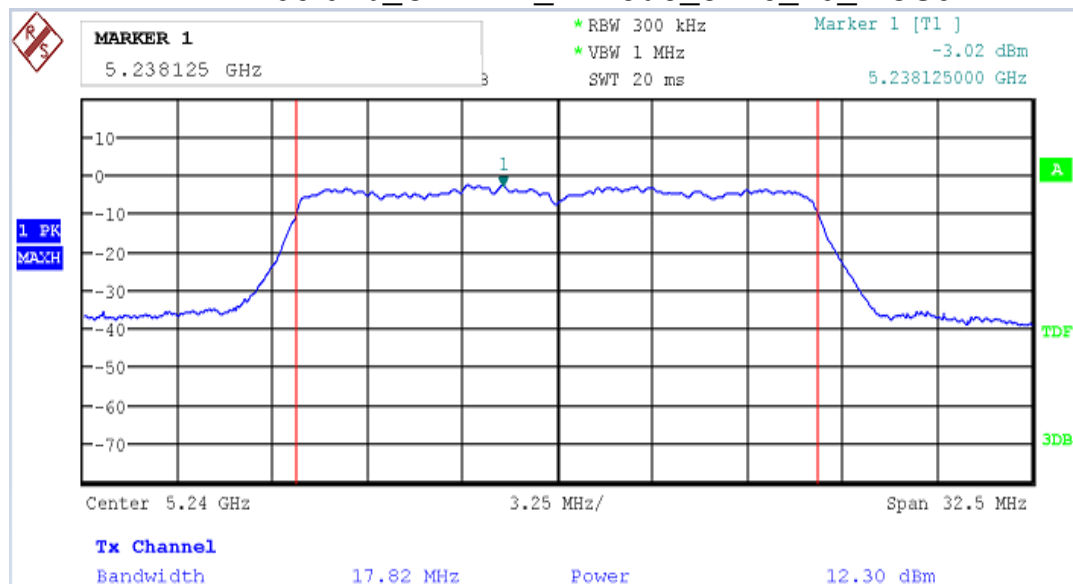
Date: 25.JUL.2019 10:59:40

35.02a_OBW_n-mode_ch48_20_MCS3

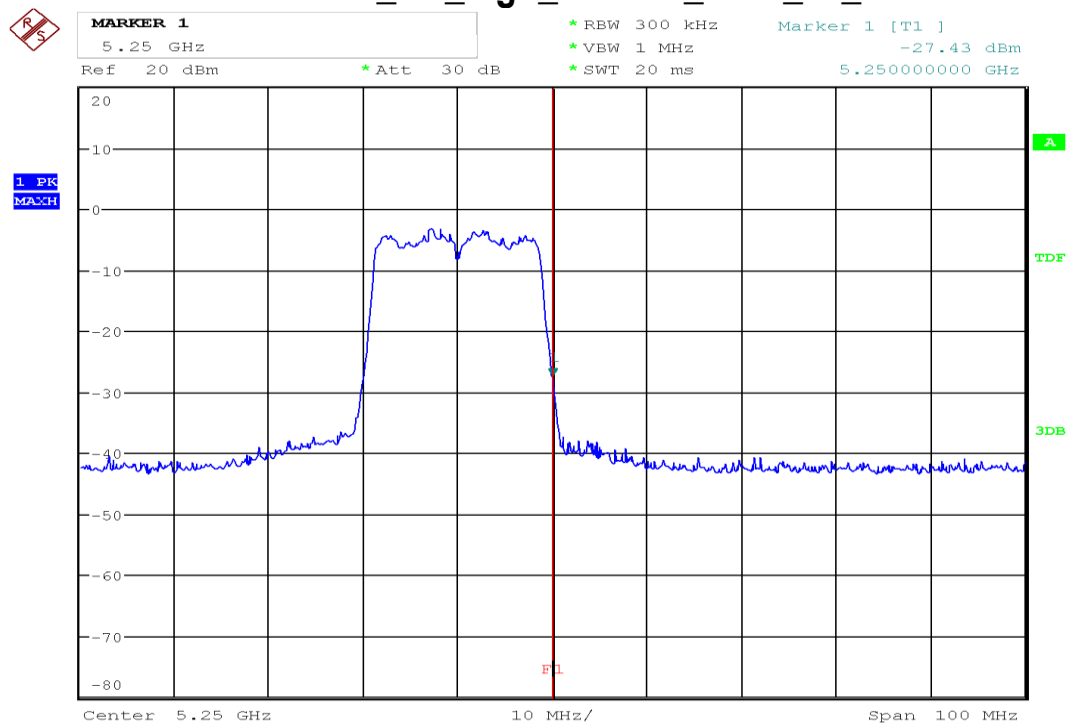


Date: 25.JUL.2019 11:16:06

35.02b_CHPWR_n-mode_ch48_20_MCS3

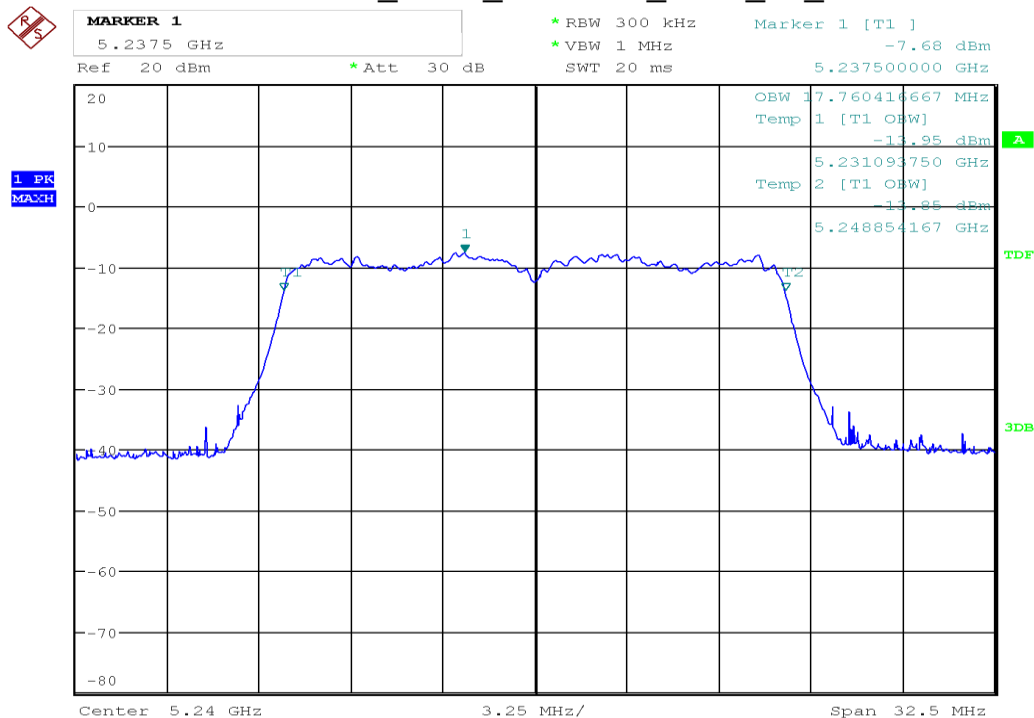


35.02c_BE_high_n-mode_ch48_20_MCS3



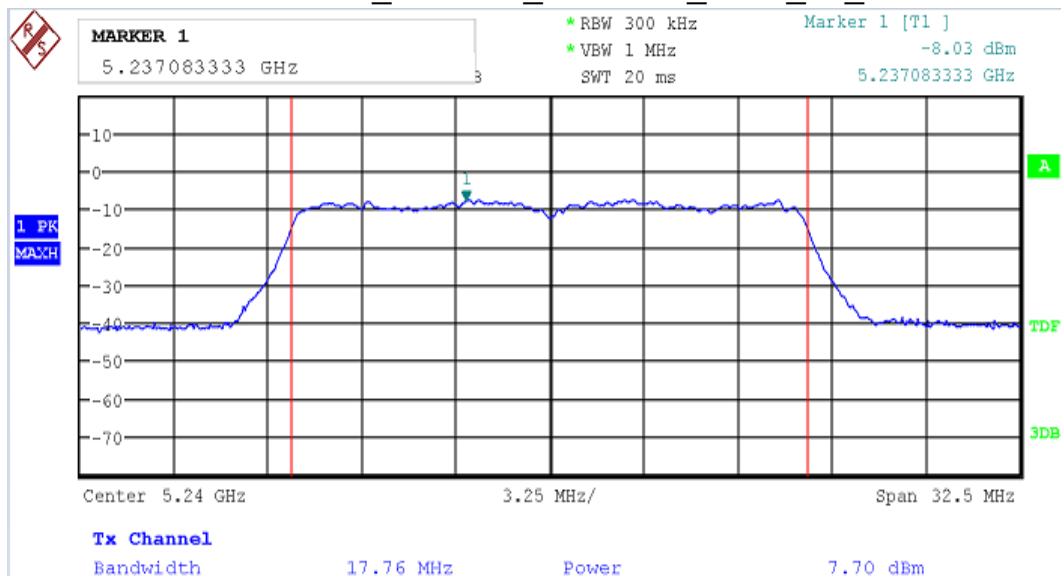
Date: 25.JUL.2019 11:25:26

35.03a_OBW_ac-mode_ch48_20_MCS3

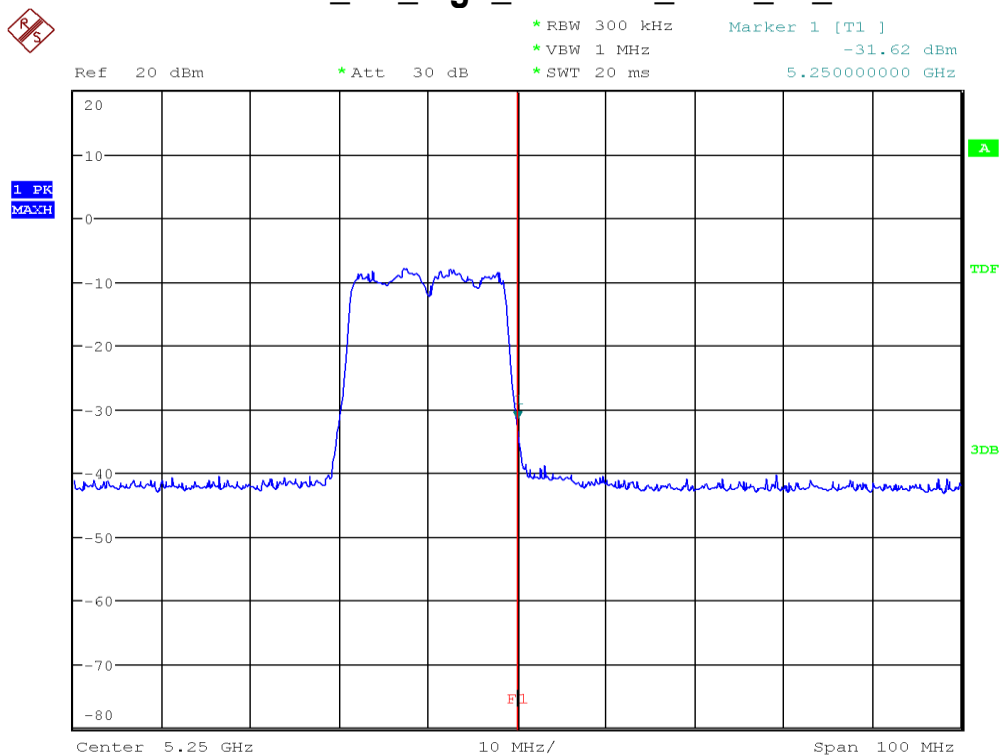


Date: 25.JUL.2019 11:33:38

35.03b_CHPWR_ac-mode_ch48_20_MCS3



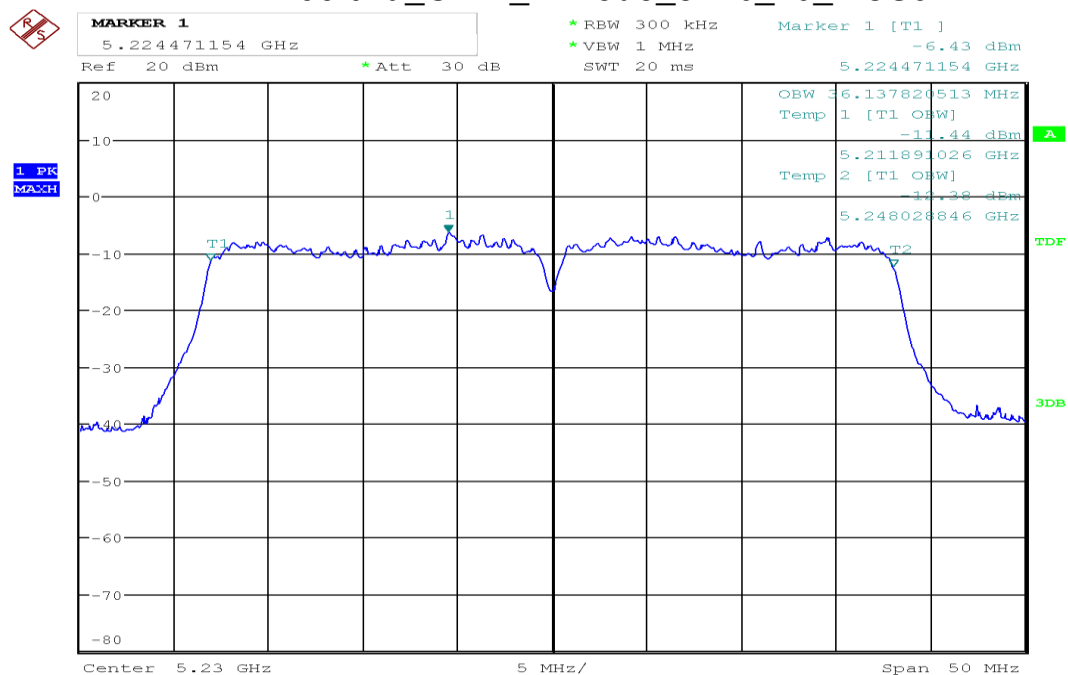
35.03c_BE_high_ac-mode_ch48_20_MCS3



Date: 25.JUL.2019 11:39:42

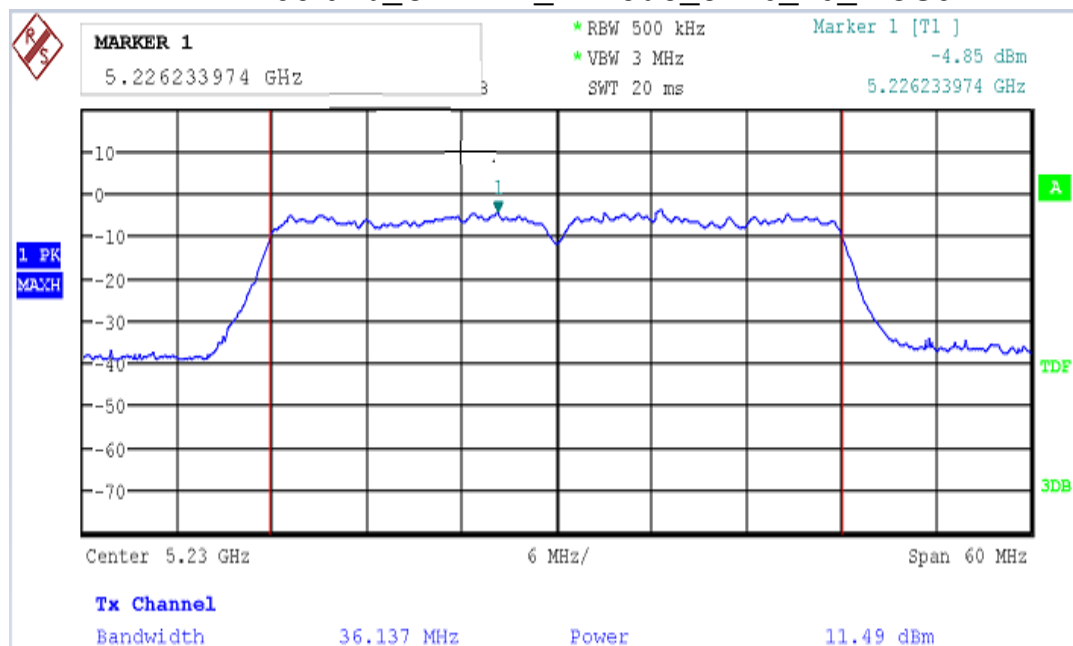
2.6.1.2. 40MHz

35.04a_OBW_n-mode_ch46_40_MCS3

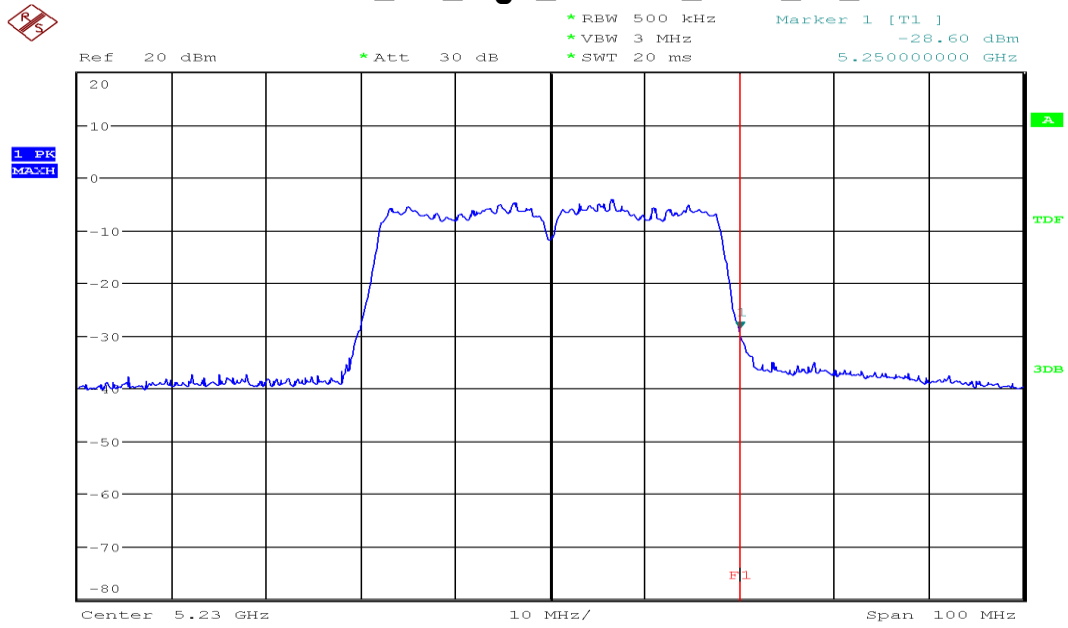


Date: 25.JUL.2019 11:55:27

35.04b_CHPWR_n-mode_ch46_40_MCS3

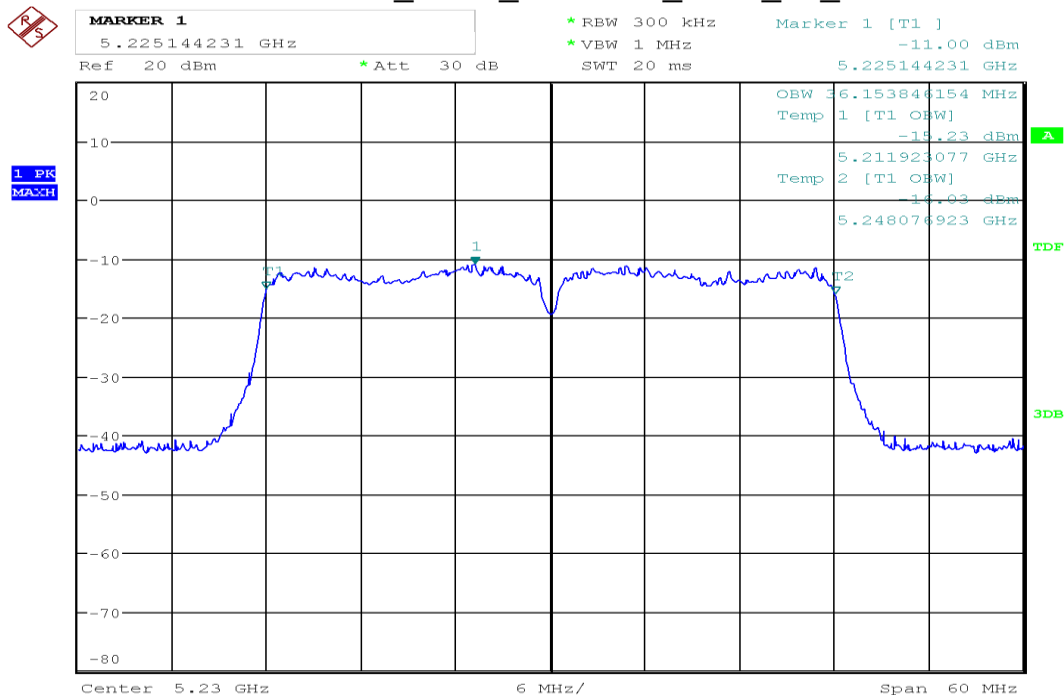


35.04c_BE_high_n-mode_ch46_40_MCS3



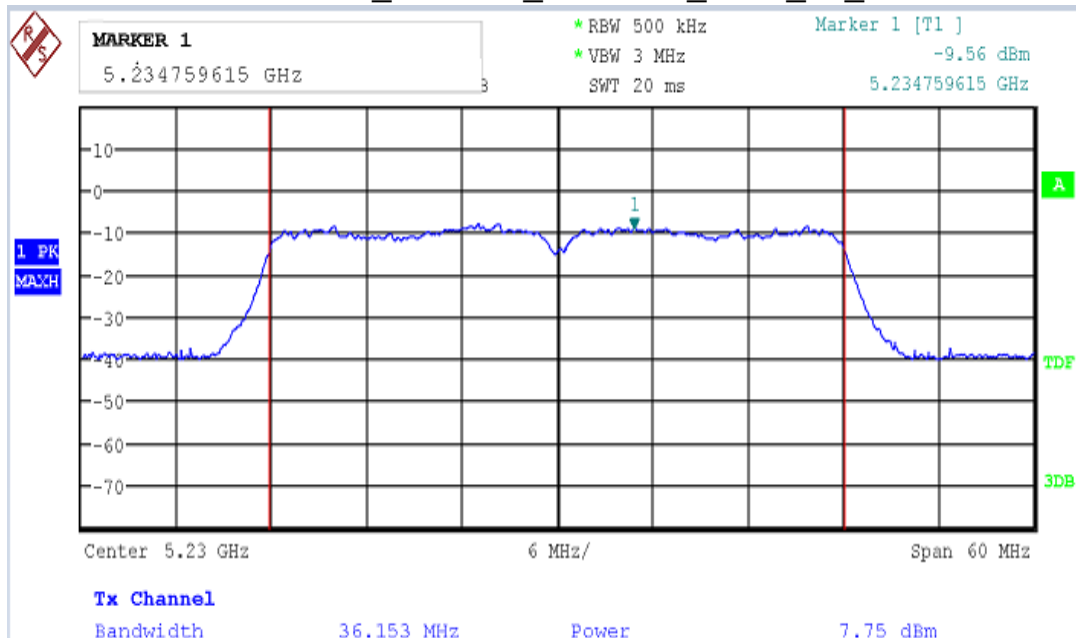
Date: 25.JUL.2019 12:00:43

35.05a_OBW_ac-mode_ch46_40_MCS9

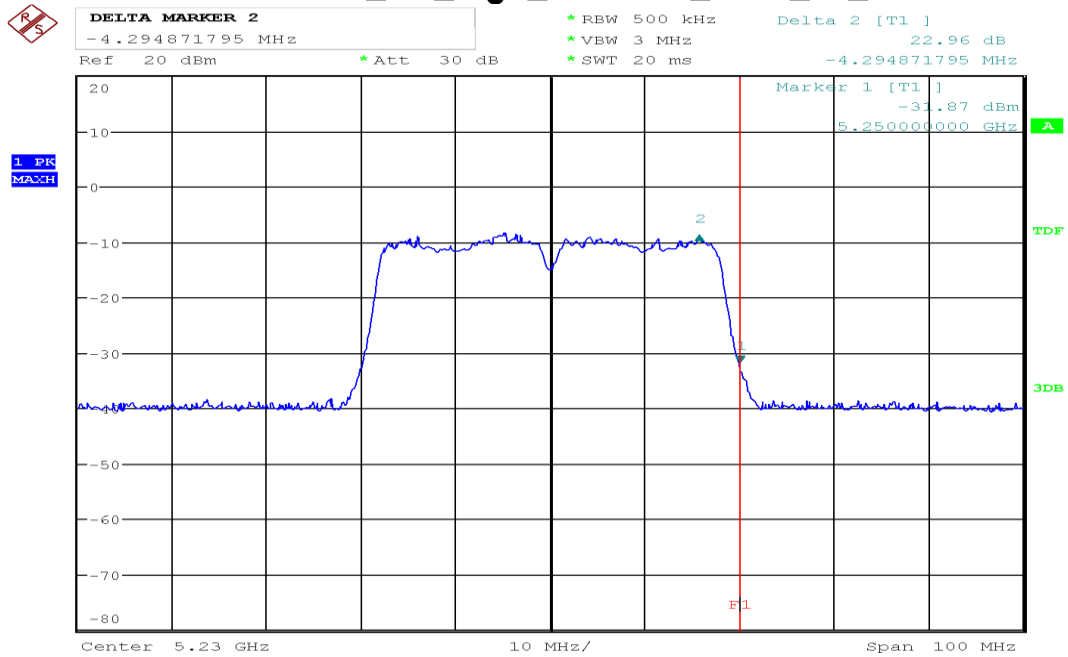


Date: 25.JUL.2019 12:05:22

35.05b_CHPWR_ac-mode_ch46_40_MCS9

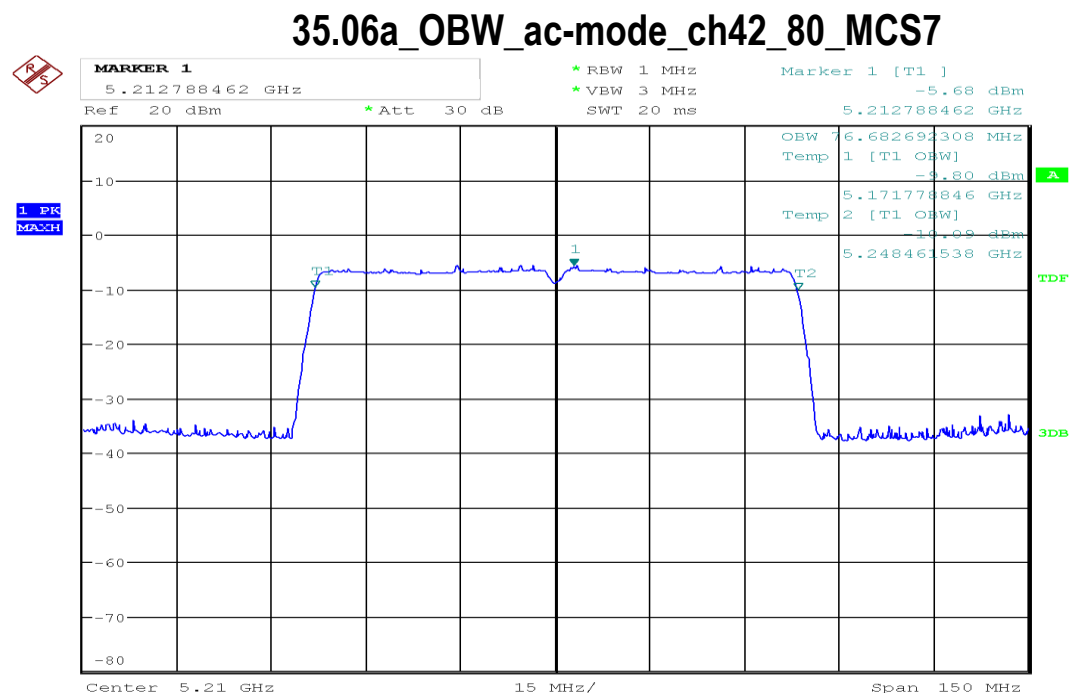


35.05c_BE_high_ac-mode_ch46_40_MCS9

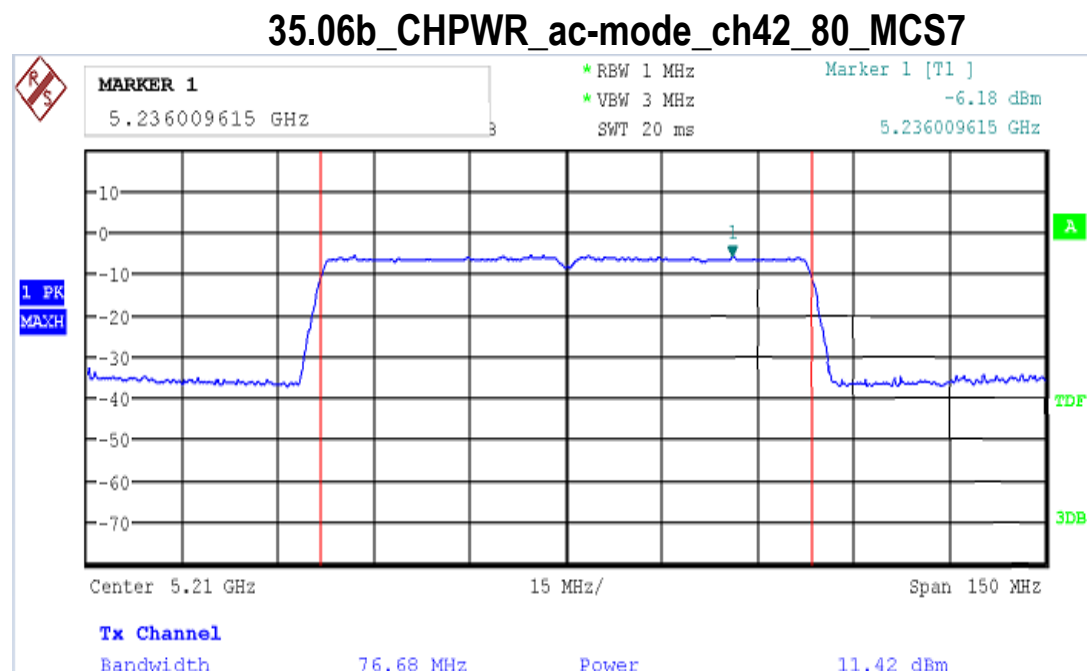


Date: 25.JUL.2019 12:09:30

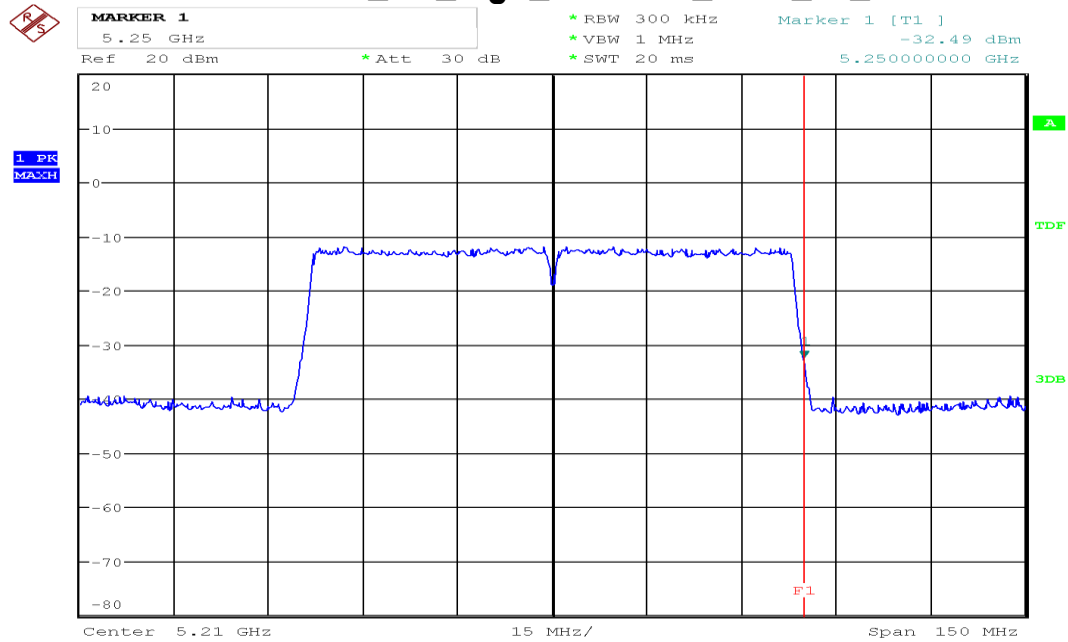
2.6.1.3. 80MHz



Date: 25.JUL.2019 12:16:46



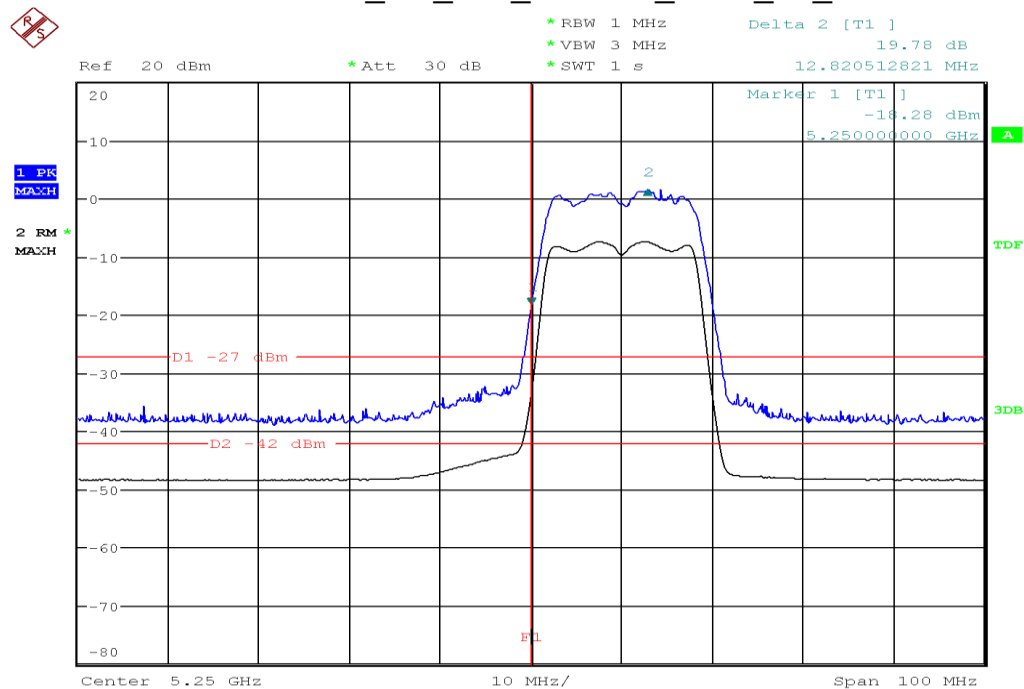
35.06c_BE_high_ac-mode_ch42_80_MCS7



Date: 25.JUL.2019 12:19:51

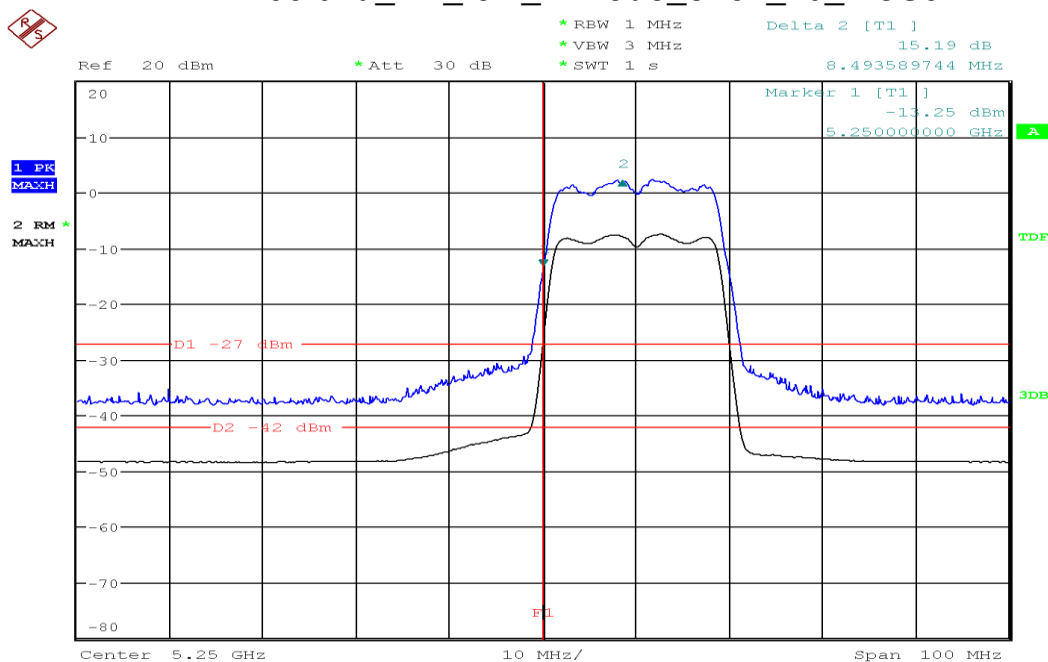
2.6.2. Canada RSS-247 requirement Issue 2, Chapter 6.2.2.2 b

35.01d_BE_low_a-mode_ch52_20_9mbit



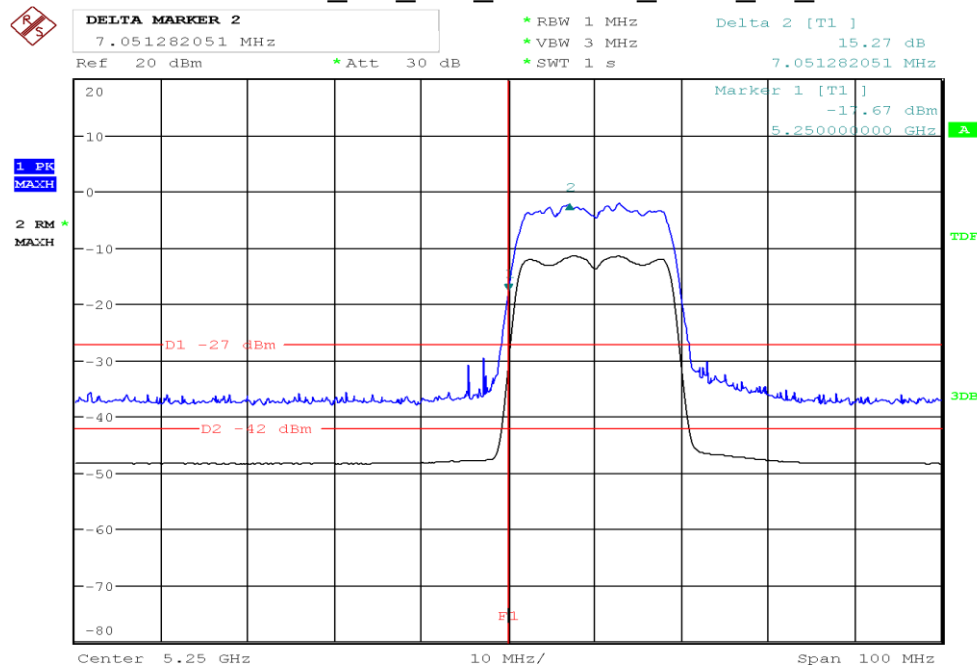
Date: 25.JUL.2019 11:09:31

35.02d_BE_low_n-mode_ch52_20_MCS3



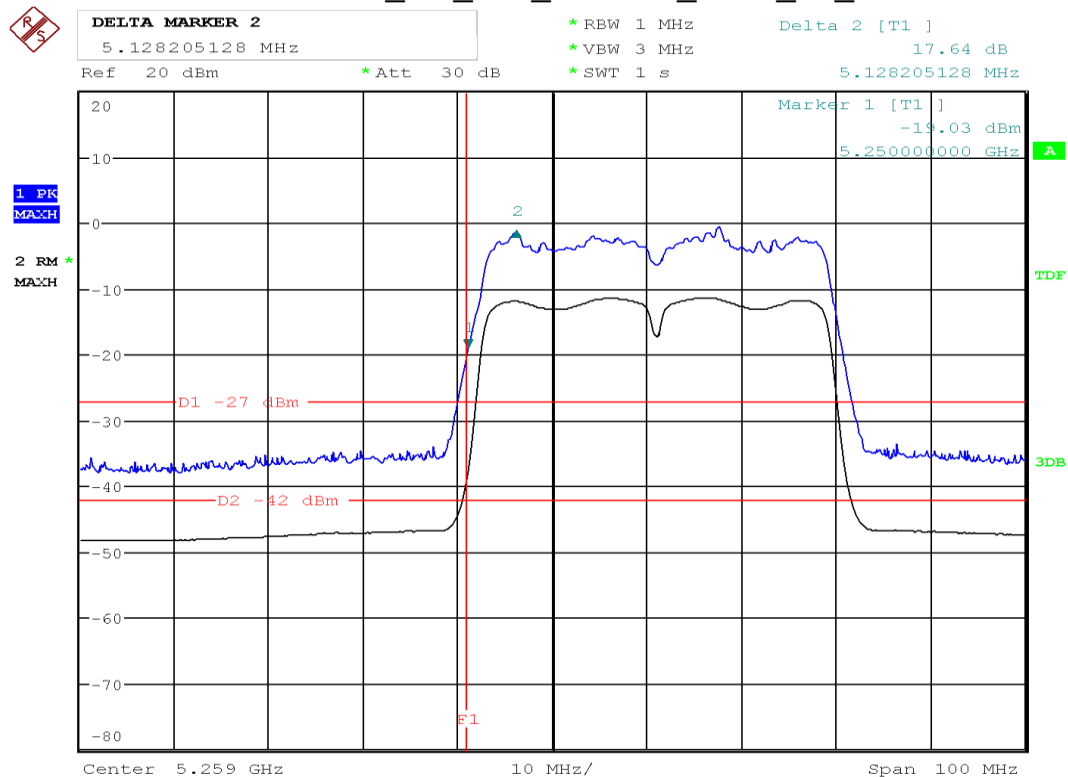
Date: 25.JUL.2019 11:28:39

35.03d_BE_low_ac-mode_ch52_20_MCS3



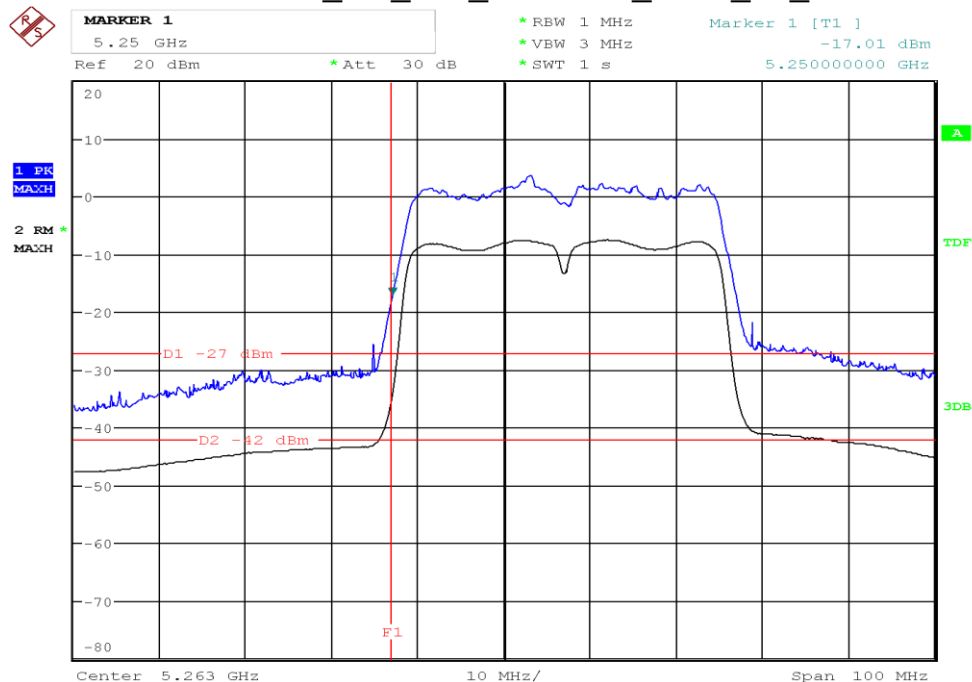
Date: 25.JUL.2019 11:42:25

35.04d_BE_low_n-mode_ch54_40_MCS3



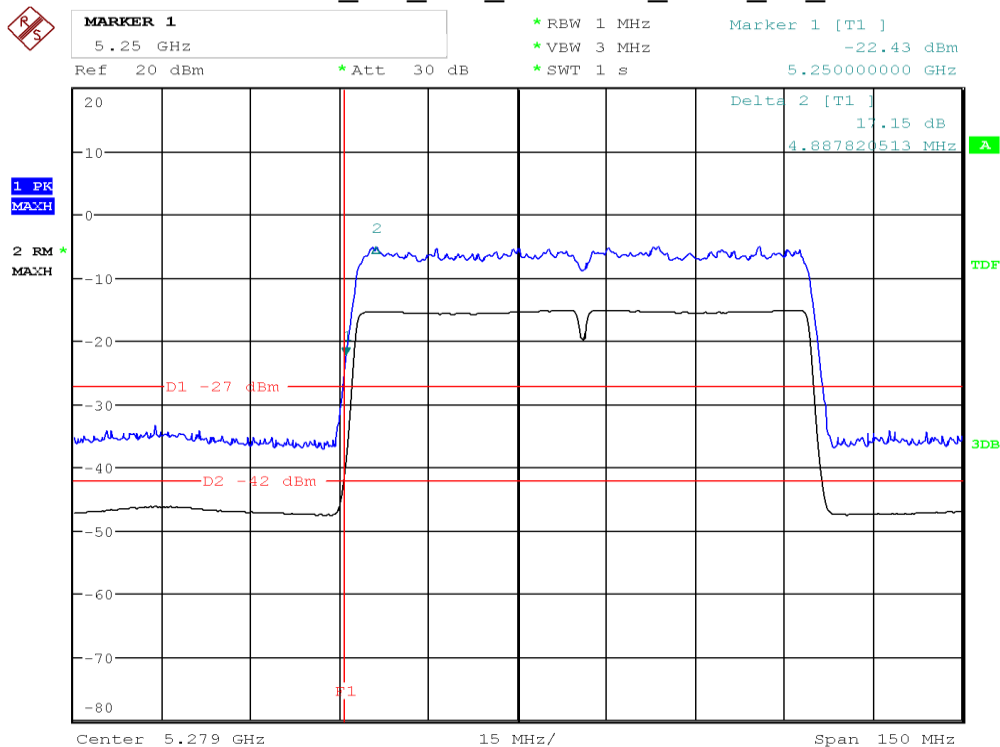
Date: 25.JUL.2019 12:03:16

35.05d_BE_low_ac-mode_ch54_40_MCS9



Date: 25.JUL.2019 12:12:59

35.06d_BE_low_ac-mode_ch58_80_MCS7



Date: 25.JUL.2019 12:25:37

End of Annex 1