GuruPlug Server Hardware Functional block diagram

GLOBALSCALE TECHNOLOGIES, INC.

GTI-2010.04.22

1F-U2,U3,U4,U5

DDR2
1Gb x 4
=512MB

CPU Y'tal = 25.0MHz
CPU RTC=32.768MHz

2F-ANT1

1F-J12 RJ45

1F-U9
Gb PHY

Y'tal = 25.0 MHz

1F-U1 CPU

SDIO

2F-U1
WiFi 802.11b/g

2F-J1
connector

Gb
Ethernet

NAND

NAND FLASH
512 MB

1F-U6

1F-J13

eSATA #1

USB2

USB1

SATA I/O

SPI

SPI FLASH
512KB

1F-U7

2F-U4
USB HUB
controller
1 to 4

USB I/O

Y'tal = 12.0 MHz

2F-U2
USB to SD
Controller

Y'tal = 12.0 MHz

TDM
SPI

1F-J4 U-SNAP connector

External JTAG board
Only for development

2F-J3
Micro
SD Card

2F-J4
Micro
SD Card

1F-U14
DC 3.3V IN
DC 1.2V out

DC 3.3V

UART0

JTAG

JTAG Debug
board

Mini-USB

AC 100~240V IN

AC/DC
converter

DC 5V/3A out

1F-U8
DC 1.8V out
DC 3.3V out

1F-U12
DC 1.0V out
DC 1.1V out

Note
1F-Uxx means the components on the 1st floor Main board
2F-Uxx means the components on the 2nd floor board