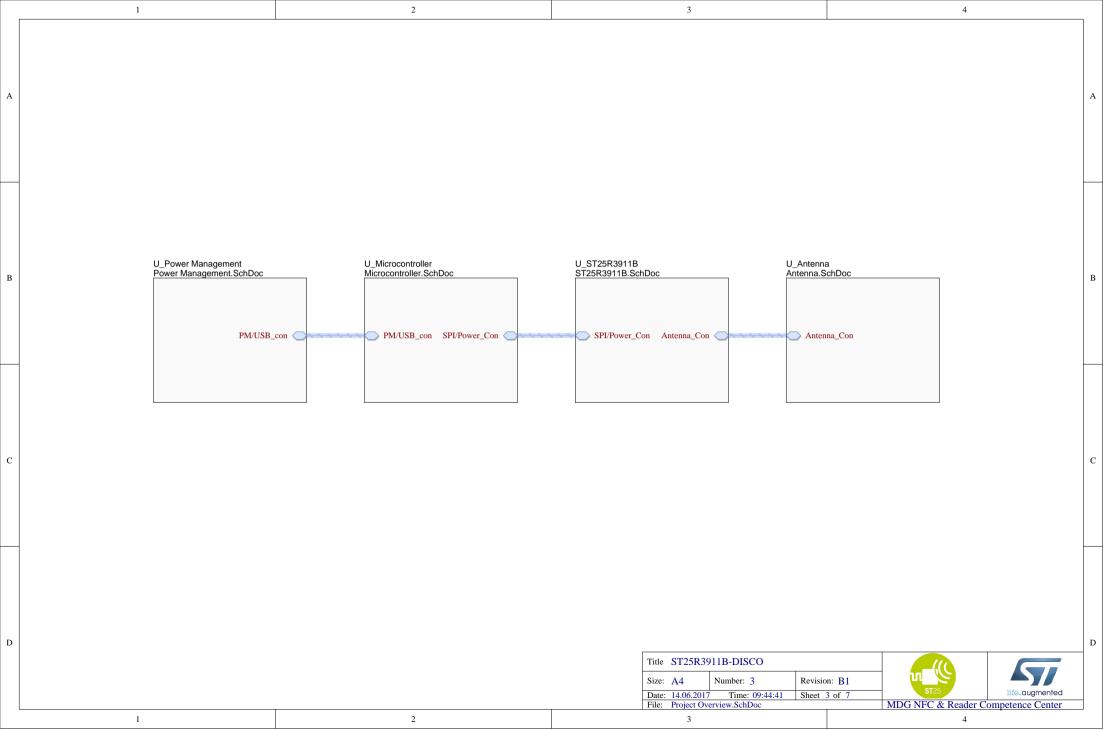
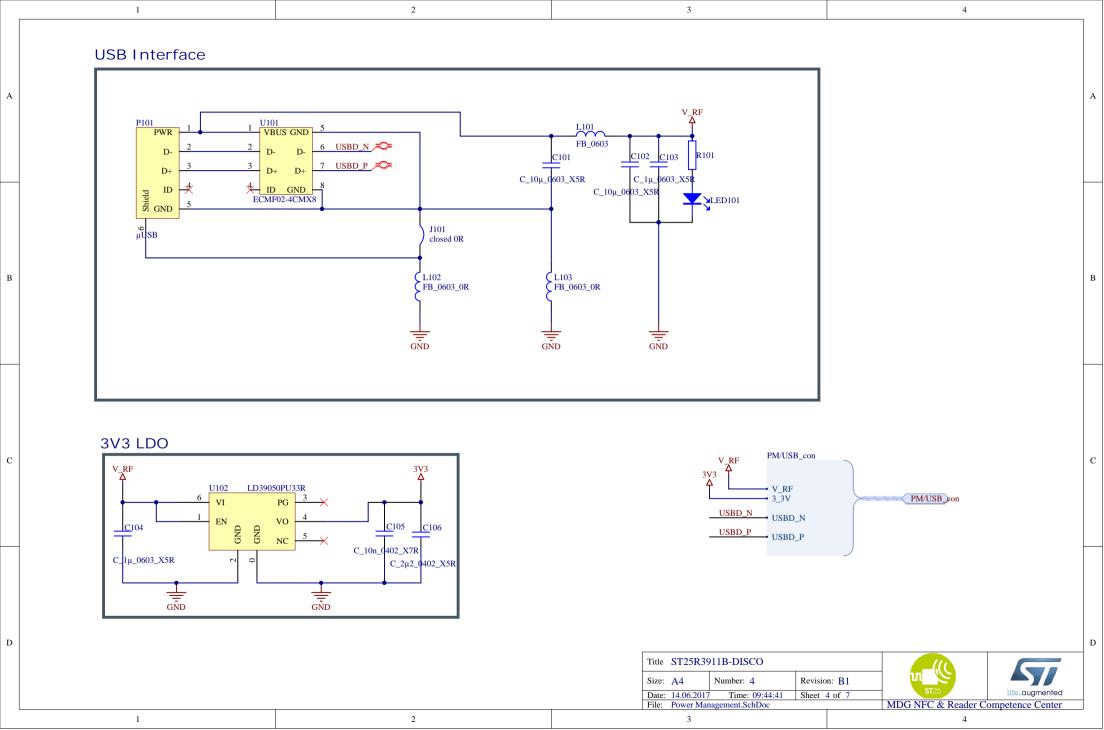
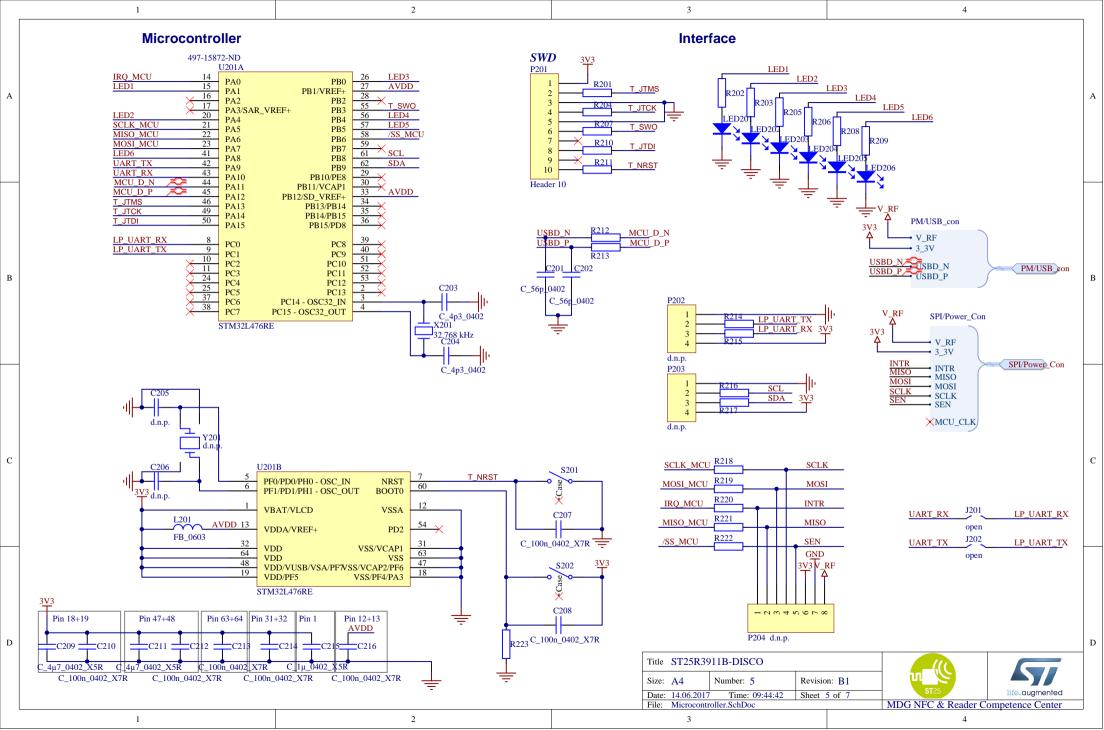
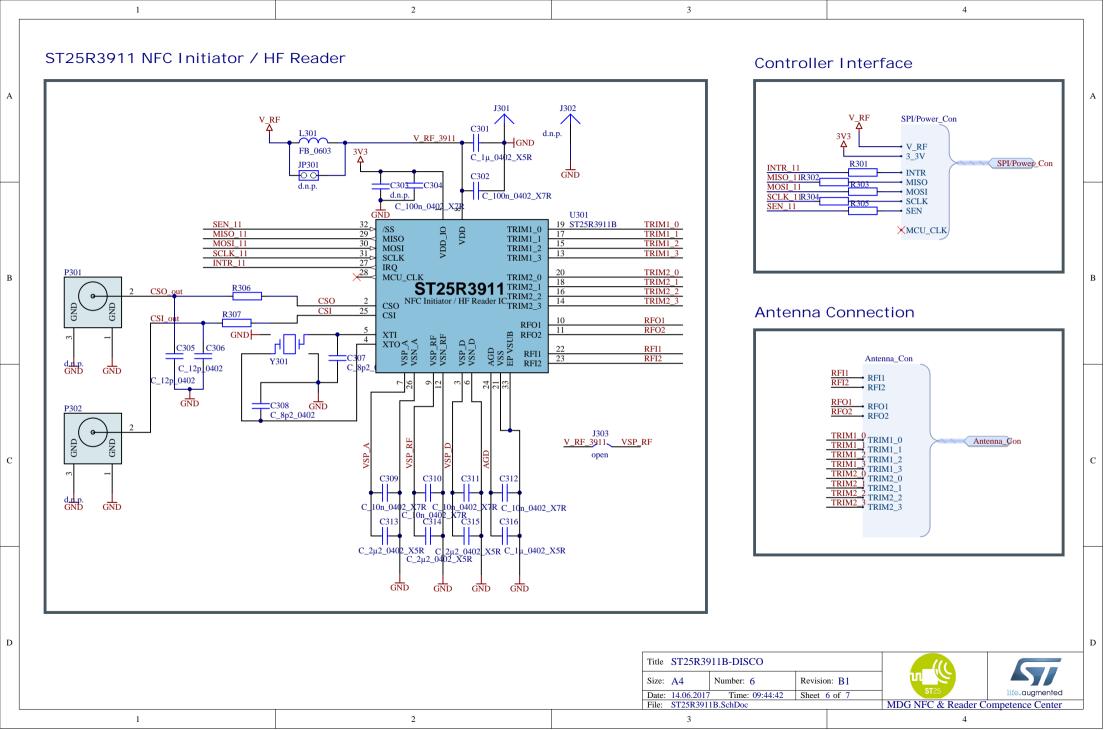


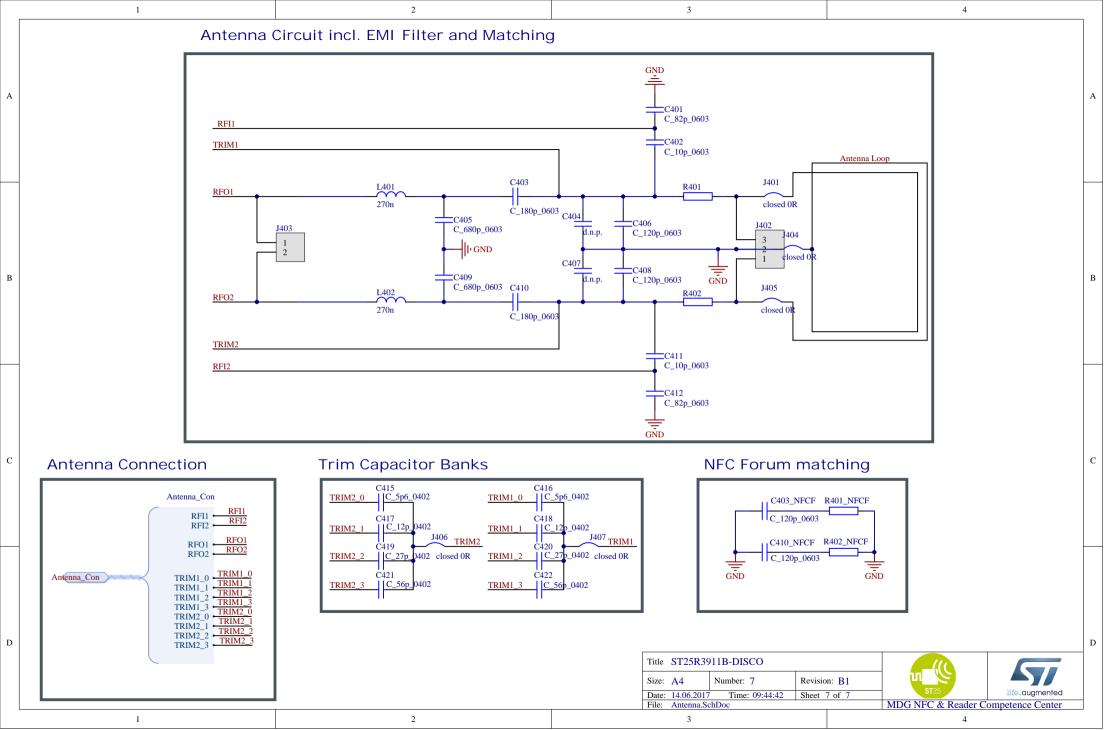
ST25R3911B-DISCO **Table of Contents Revision History** Revision Release Date Design Changes Sheet 1: Title Page Α1 31/03/2017 Internal Release Sheet 2: Project Page (This page) B1 06/06/2017 Official Release Sheet 3: Project Overview Sheet 4: USB & PMU Schematic Sheet 5: STM32L4 Schematic Sheet 6: ST25R3911B Schematic Sheet 7: Matching and Antenna Circuit Schematic PCB Stackup Board Layer Stack Top Overlay 0,000mm Top Solder 0,010mm Top Layer 0,620mm Dielectric1 GND 0,035mm Dielectric 3 0,150mm **VBUS** 0,035mm 0,620mm Dielectric 2 0,052mm Bottom Layer Bottom Solder 0,010mm Bottom Overlay 0,000mm Title ST25R3911B-DISCO Size: A4 Revision: B1 Date: 14.06.2017 Time: 09:44:41 Sheet 2 of 7 MDG NFC & Reader Competence Center File: Project Page.SchDoc 3





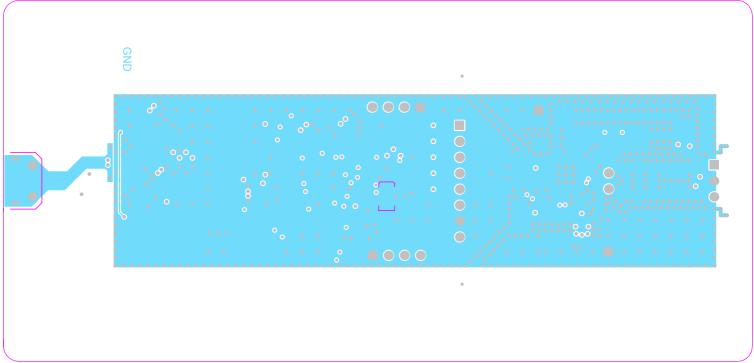


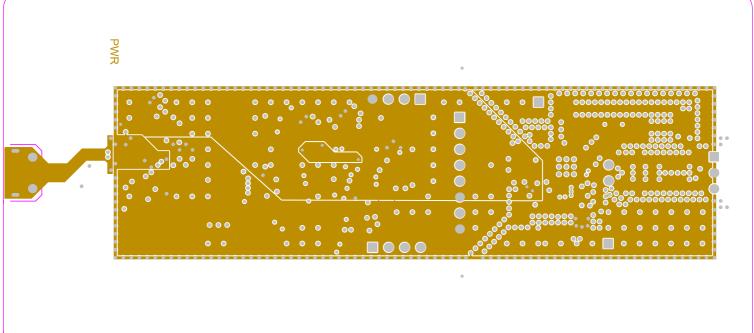


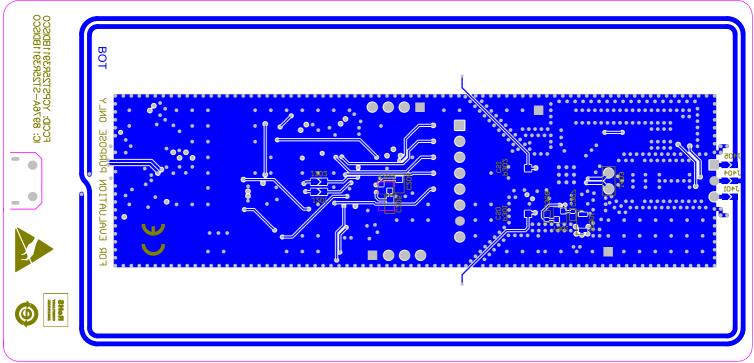


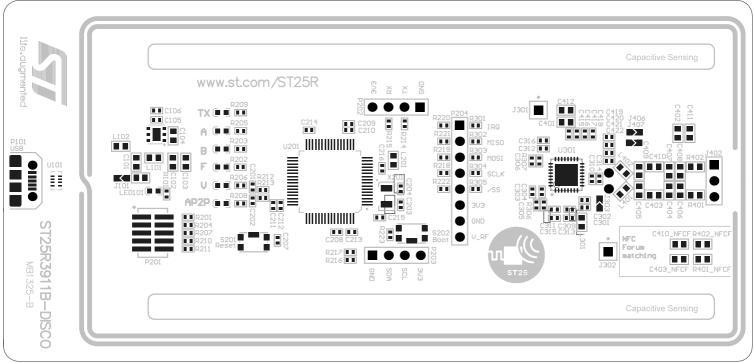




















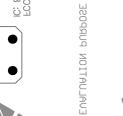


















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