May 31, 2016



Federal Communications Commission 7435 Oakland Mills Road Columbia, MD 21046

Subject: DFS Expedited Review for FCC ID: YETD32-21266NU and YETD32-21266CU

To whom it may concern:

Please apply the DFS Expedited Review Process for these devices based on the following criteria:

Expedited Review Checklist	Previous	New Application
FCC ID	YETD32-21366NU and	YETD32-21266NU and
	YETD32-21366CU	YETD32-21266CU
Technology:	5GHz UNII/SISO	5GHz UNII/SISO
Test labs used	TUV SUD	TUV SUD
Differences between the	Supports 1 TX/1RX on each	Supports 1 TX/1RX on each
products such as TX power,	device, 21 dBm maximum	device, 21 dBm maximum
modulation, receivers,	output power	output power
processing circuitry etc.		
Differences in DFS functioning	Uses Nextivity proprietary	Uses Nextivity proprietary
circuitry, software, etc.	ARES SoC chipset and DFS	ARES SoC chipset and DFS
	waveform detection	waveform detection
	mechanism	mechanism
Antenna information and	OdBi/PCB Monopole	0dBi/PCB Monopole
differences for the minimum		
gain antennas		
Bandwidth information and	Supports 30 and 40 MHz	Supports 30 and 40 MHz
differences	bandwidths	bandwidths

Sincerely,

Company Officer: C K Li

Title: Sr. Principle Engineer, Regulatory