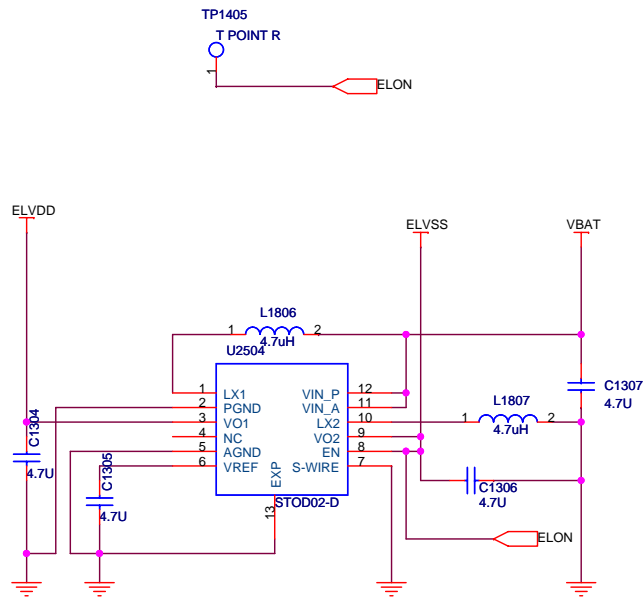
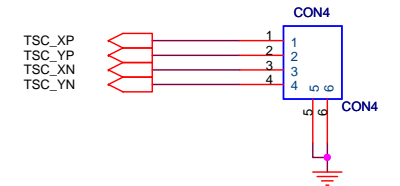
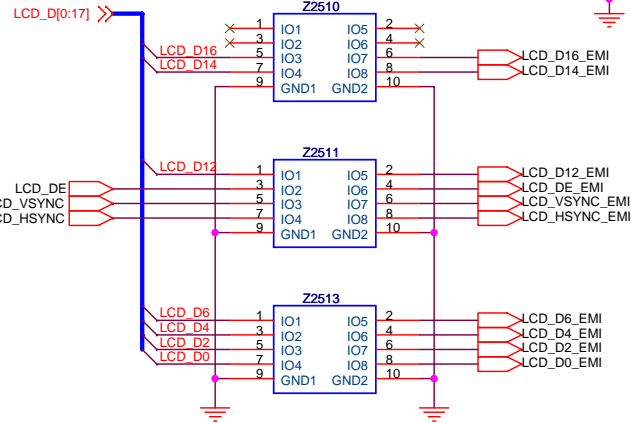
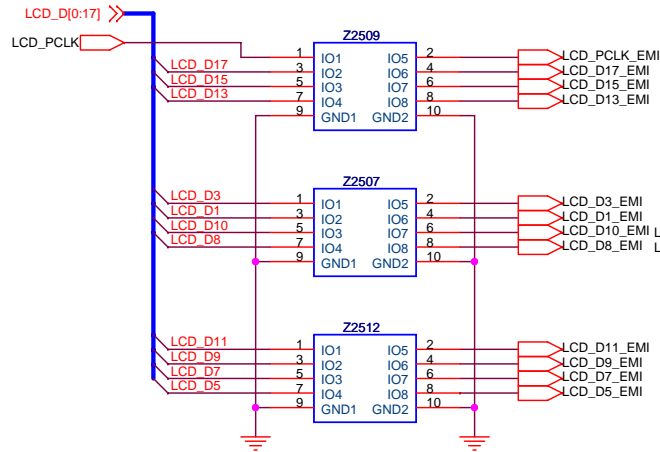
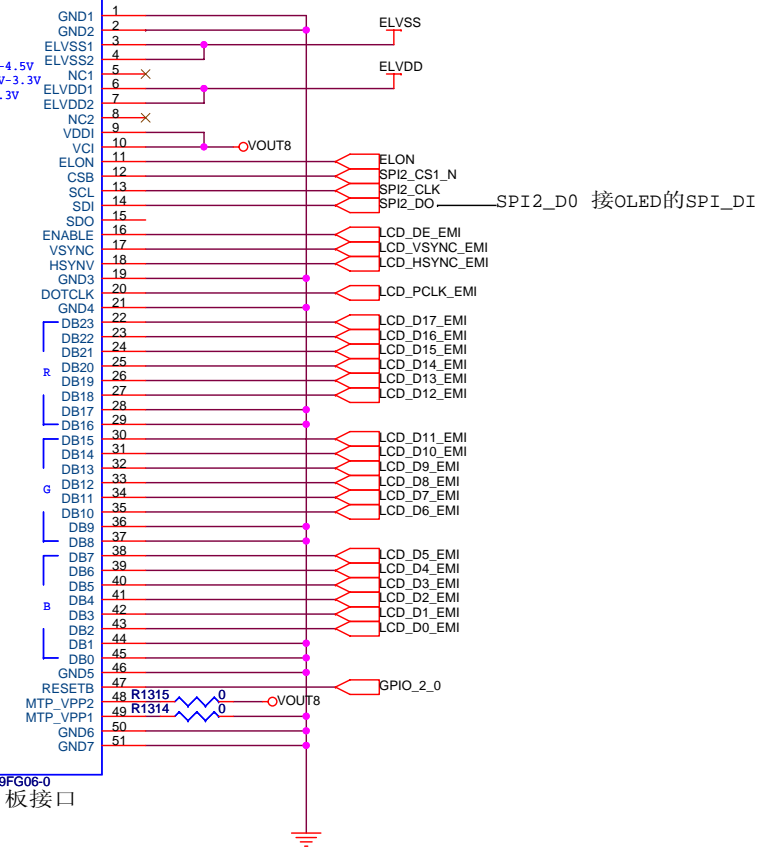


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LH-K3-081100		
Size	Document Number	Rev
A	55555555	66666666
Date: Tuesday, March 30, 2010		
Sheet 1 of 1		



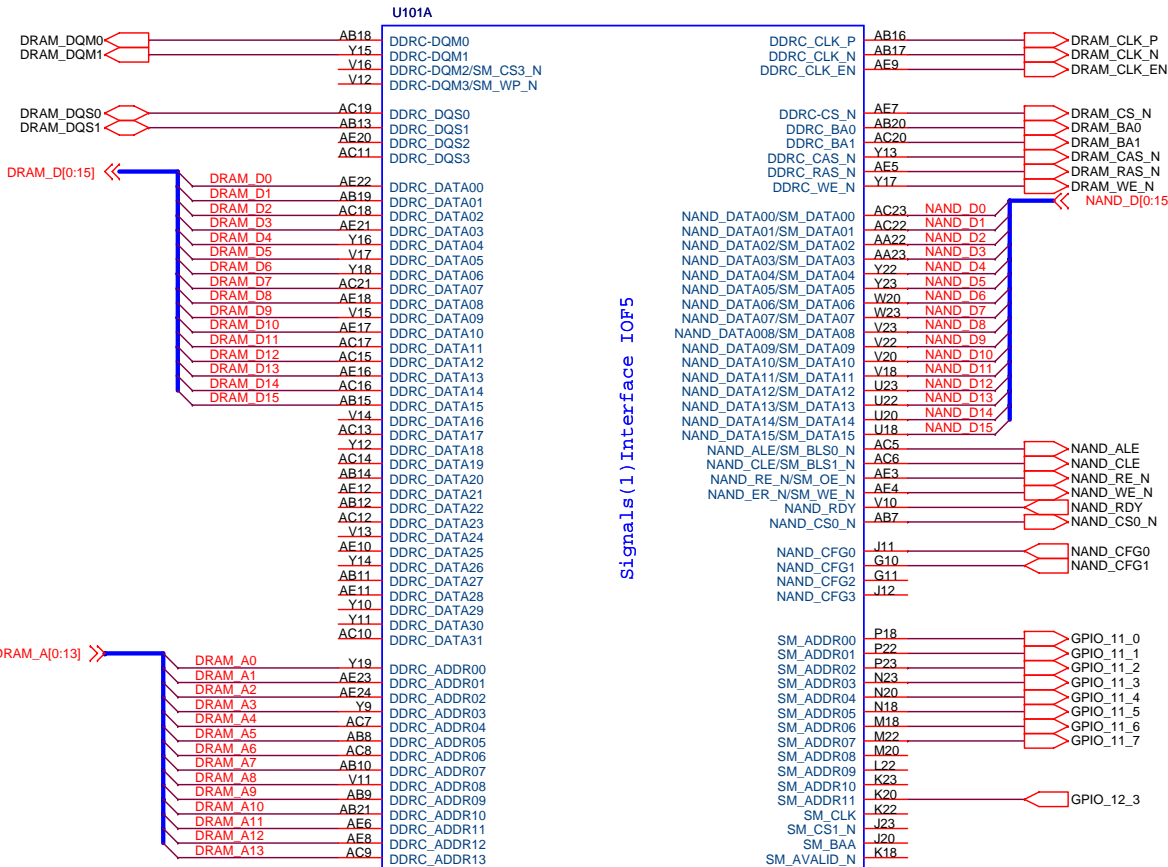
CON1



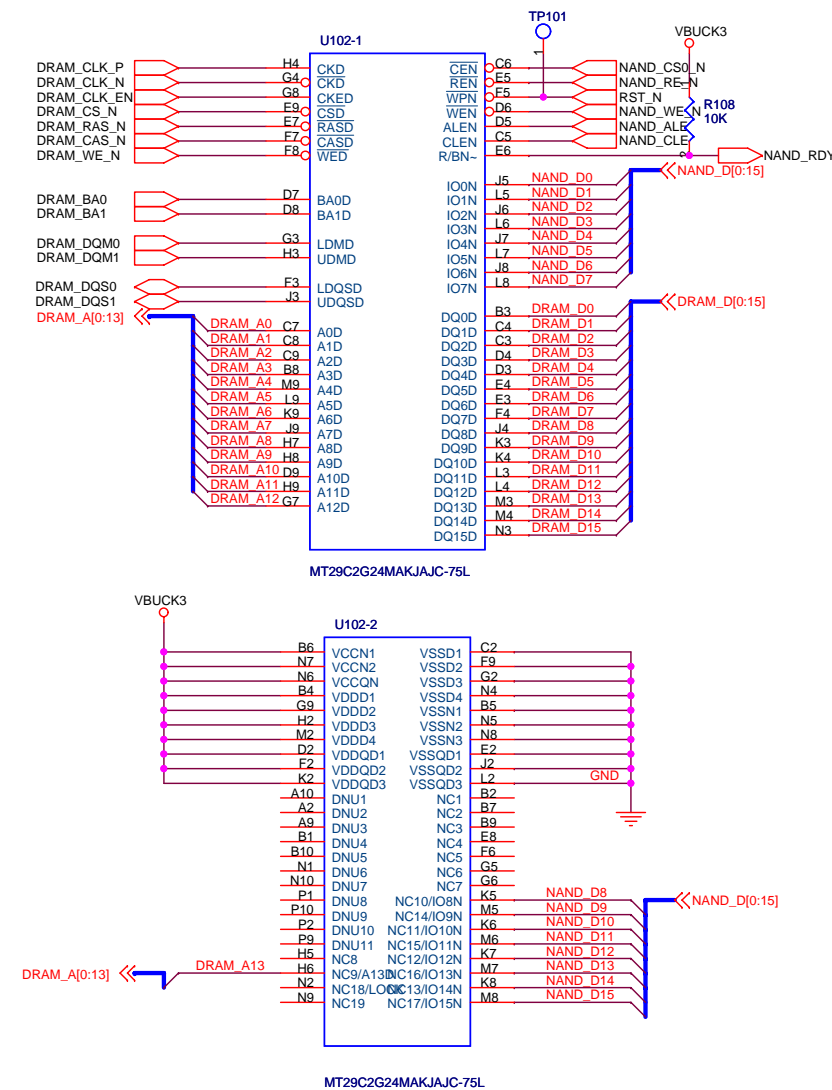
LCM Connector

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Size	Document Number	Rev	<RevCode>
B	<Doc>		
Date:	Wednesday, May 05, 2010	Sheet	1 of 1

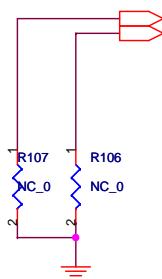
Hi3611_Memory IF



Memory(2Gbit NAND+1Gbit Mobile DDR)

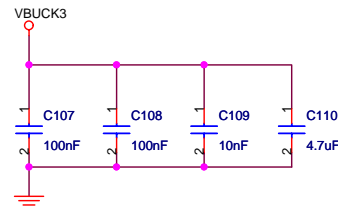


NAND Configuration



NAND_CFG[3:0]	DATAWIDTH	PAGESIZE	ADDRESS	CYCLE
1000	8	512	3	
1001	8	512	4	
1010	16	512	3	
1011	16	512	4	
1100	8	2K	4	
1101	8	2K	5	
1110	16	2K	4	
1111	16	2K	5	

Decoupling Cap of Memory



Hi3611_Memory

Title: HI3611_C2		
P/N: <>		
Design by: Qinyx	Checked by: tanghui	Approved by: zhangjiog
Date: 2009-06-30	Date: 2009-06-30	Date: 2009-06-30
Scale: <xxx>	Size: B	Sheet: 3 of 20

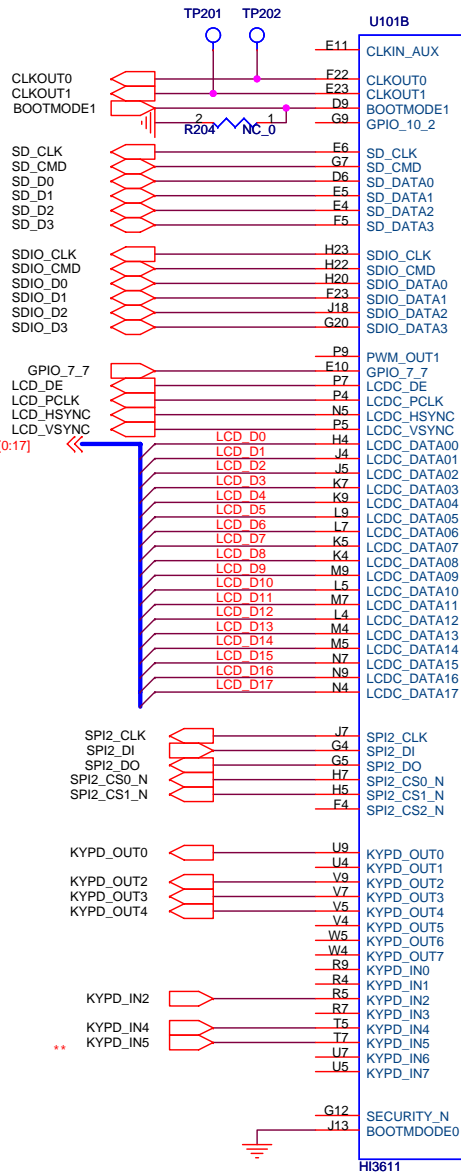
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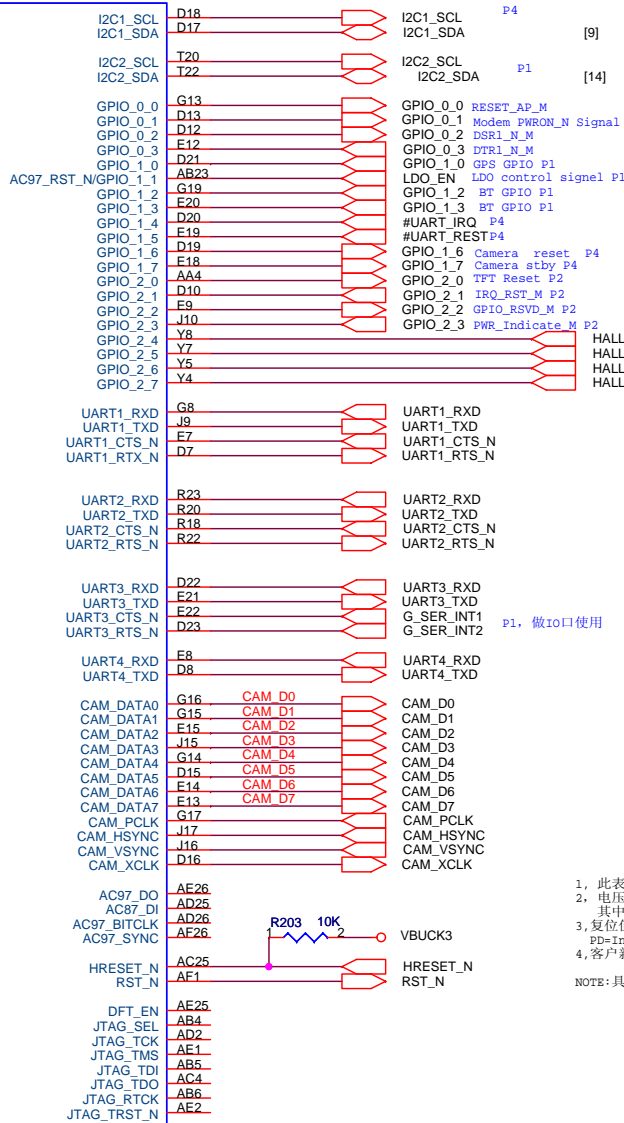
LCD_D[0:17]

SECURITY_N	Description
0	Security Mode
1	Non-Security Mode

BOOTMODE	BOOTMODE0	Description
0	0	Download via BootROM
0	1	Boot from External NAND
1	0	Boot from BootROM with NAND
1	1	Boot from External NOR



Signals(2) Interface 20F5

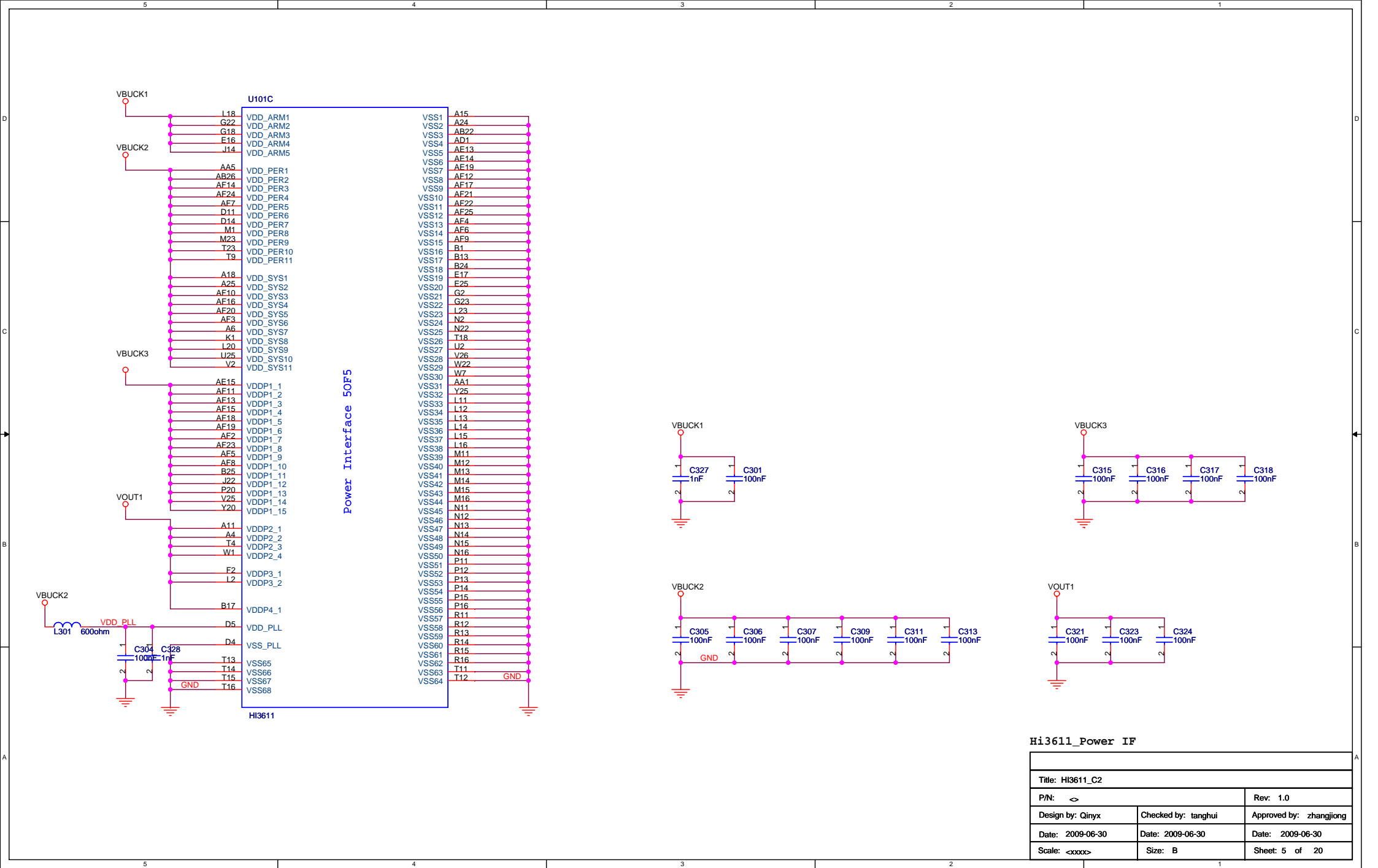


- 说明
1. 此表统计的是 AP Hi3611的GPIO使用情况，芯片的封装形式为单封；
 2. 电压域目前配置：P1=1.8V；P2=2.6V；P3=2.6V；P4=2.6V。
其中P3和P4为1.8V/2.5V可配
 3. 复位值当中，OL=Output Low Level, OH=Output High Level, PD=Input+Pull-down
PD=Input+Pull-up, OZ=高阻；IX如无特殊说明，则表示Input+Pull-down
 4. 客户新增功能可以使用的GPIO已填充为蓝色，并备注为*客户新增功能可使用*

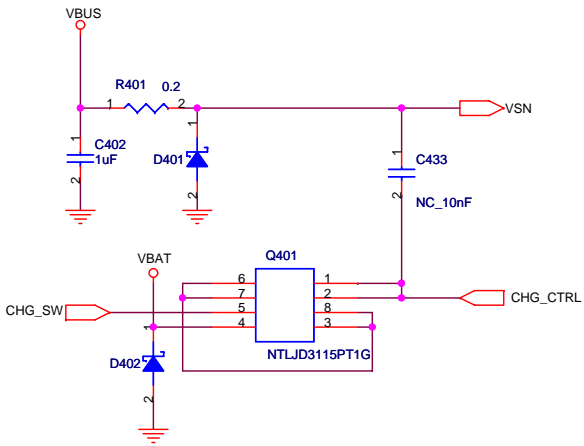
NOTE: 具体说明文档资料是：K3V100R001 GPIO分配列表(V01).xls

Hi3611_Digital IF

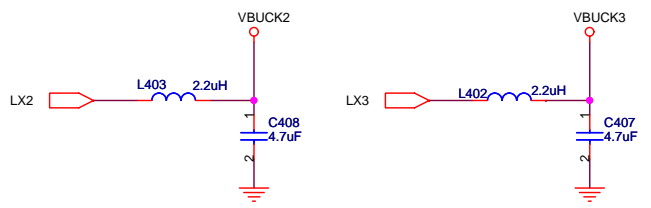
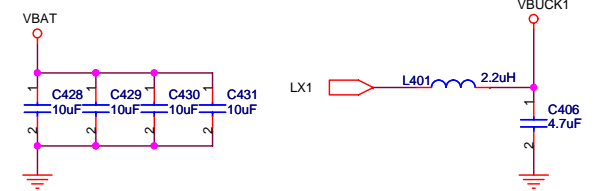
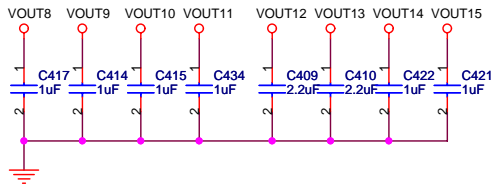
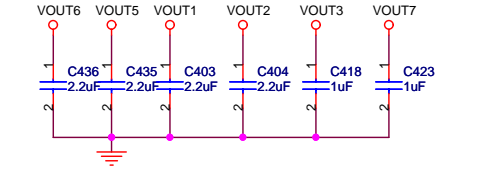
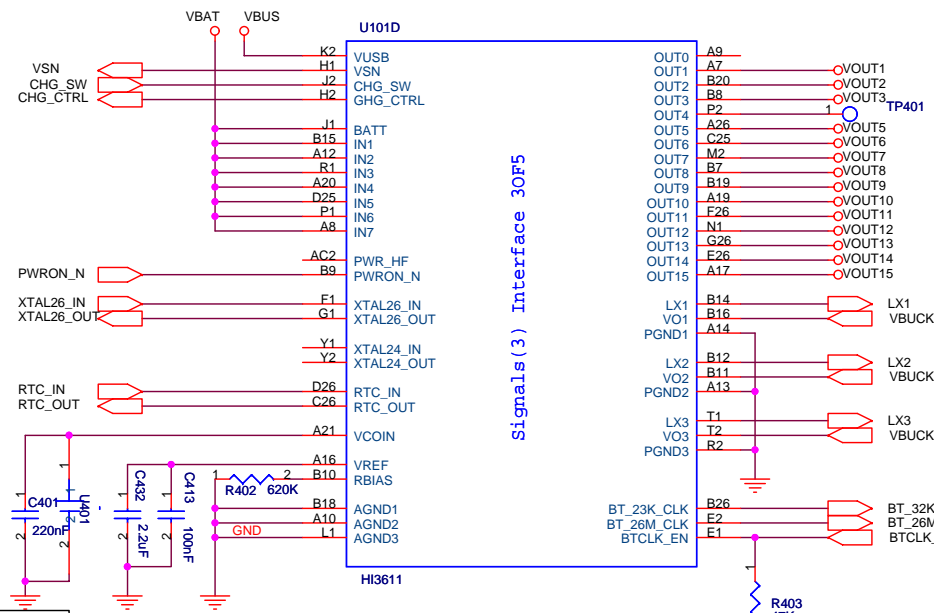
Title: HI3611_C2		
P/N: <>	Rev: 1.0	
Design by: Qinyx	Checked by: tanghui	Approved by: zhangjiog
Date: 2009-06-30	Date: 2009-06-30	Date: 2009-06-30
Scale: <xxx>	Size: B	Sheet: 4 of 20



Charger



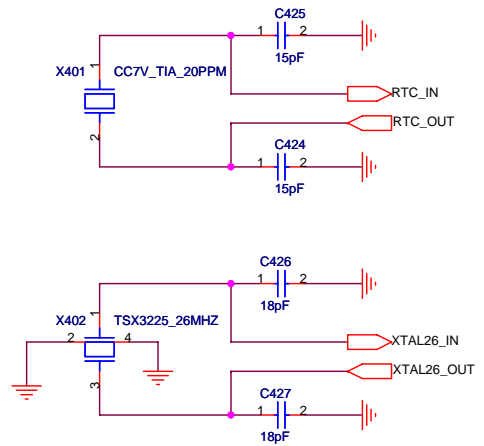
Power Management



Power management Table

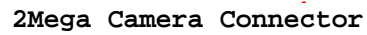
Name	Value	Current	Function
VBUCK1	1.2V	500mA	Hi3611 ARM Core
VBUCK2	1.2V	500mA	Hi3611 Peripheral Subsystem
VBUCK3	1.8V	270mA	Hi3611 IO & Memory
VOUT1	2.6V	145mA	Hi3611 IO & JOG
VOUT2	2.8V	190mA	2 Mega Camera AVDD
VOUT3	2.85V	80mA	Micro-SD
VOUT4	2.6V	200mA	CMMB
VOUT5	1.8V	164mA	WiFi VDD & IO
VOUT6	3.0V	110mA	WiFi PA & IO
VOUT7	2.8V	10mA	26MHz Clock
VOUT8	2.8V	15mA	LCD
VOUT9	1.5V	50mA	2Mega Camera Core
VOUT10	2.8V	20mA	2Mega Camera IO
VOUT11	3.0V	25mA	CMMB CA
VOUT12	3.3v	150mA	USB Transceiver
VOUT13	3.3v	300mA	Audio & HKADC
VOUT14	1.8V	5mA	RTC
VOUT15	3.0V	5mA	PMU Logic

Crystal

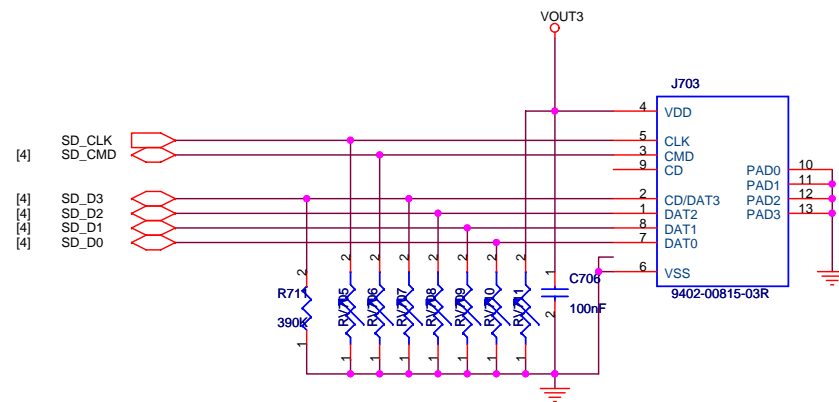
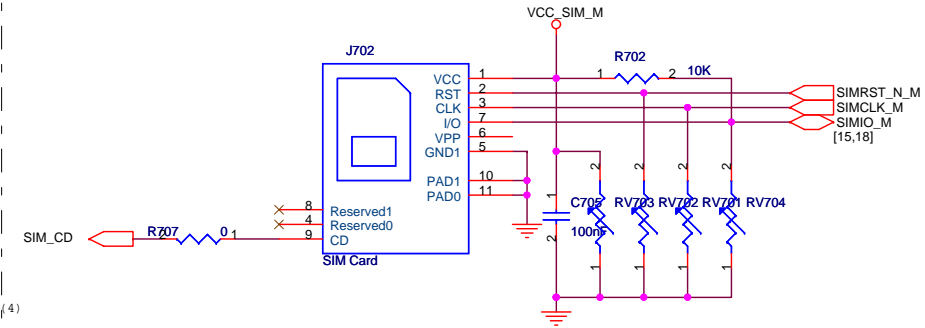


Hi3611_PMU1

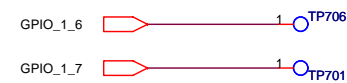
Title: Hi3611_C2		
P/N: <>		
Design by: Qinyx	Checked by: tanghui	Approved by: zhangjiog
Date: 2009-06-30	Date: 2009-06-30	Date: 2009-06-30
Scale: <xxx>	Size: B	Sheet: 6 of 20



Micro SD & SIM Card

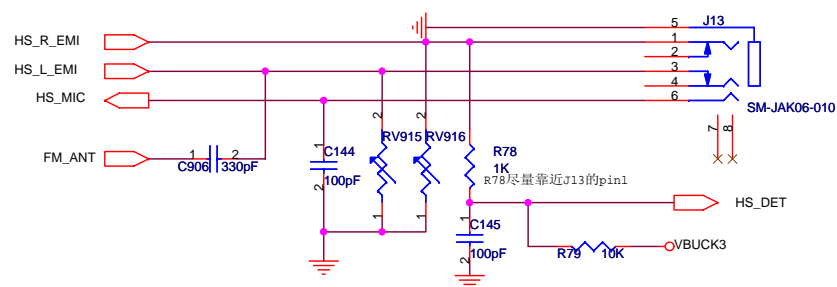
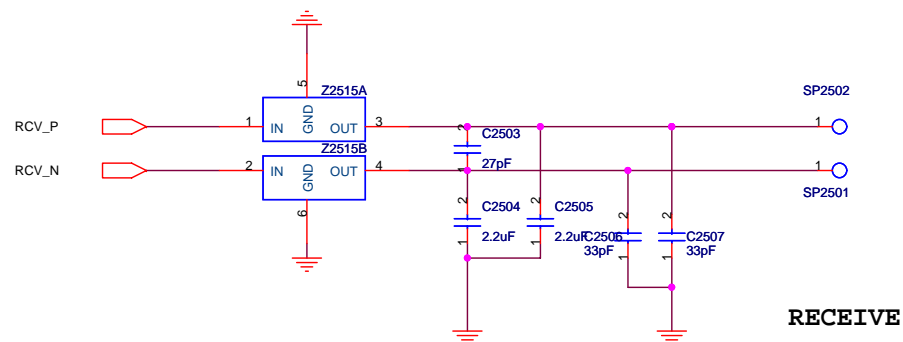
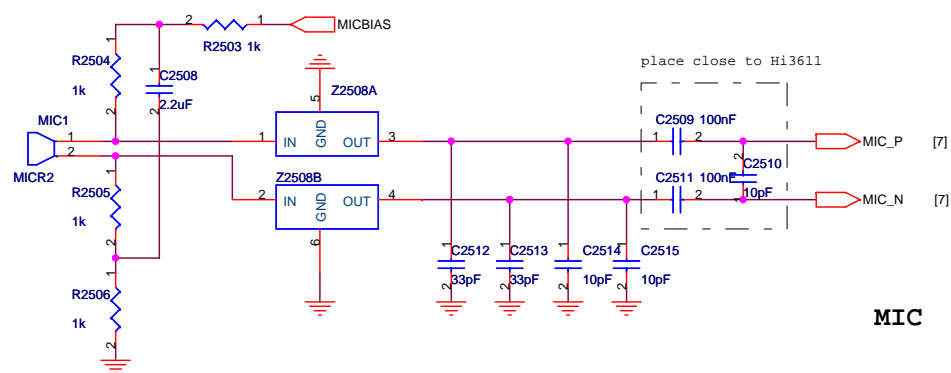
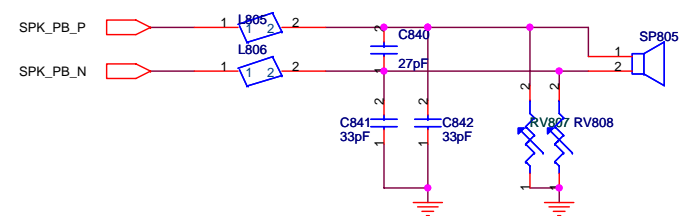
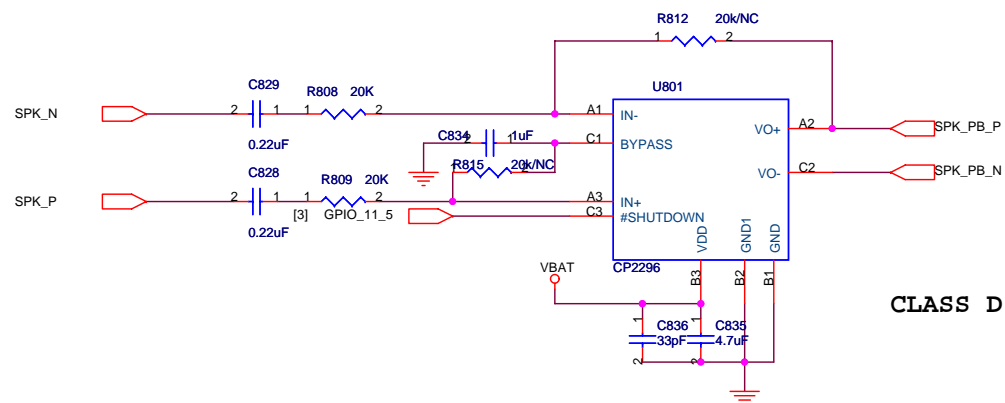


Test Point



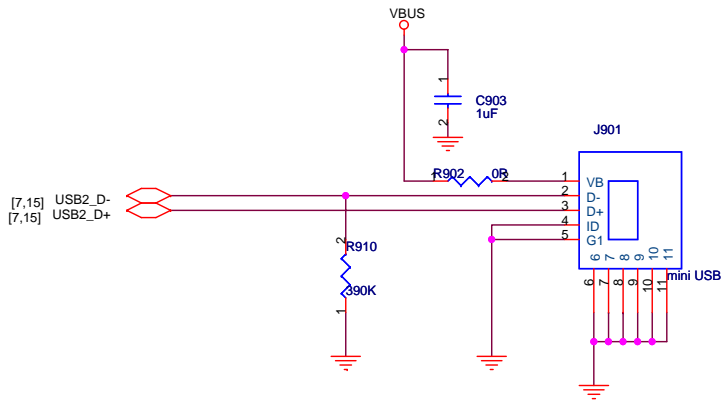
Camera Module

Title: HI3611_C2		
P/N: 		Rev: 1.0
Design by: Qinix	Checked by: tanghui	Approved by: zhangjiong
Date: 2009-06-30	Date: 2009-06-30	Date: 2009-06-30
Scale: 	Size: B	Sheet: 8 of 20

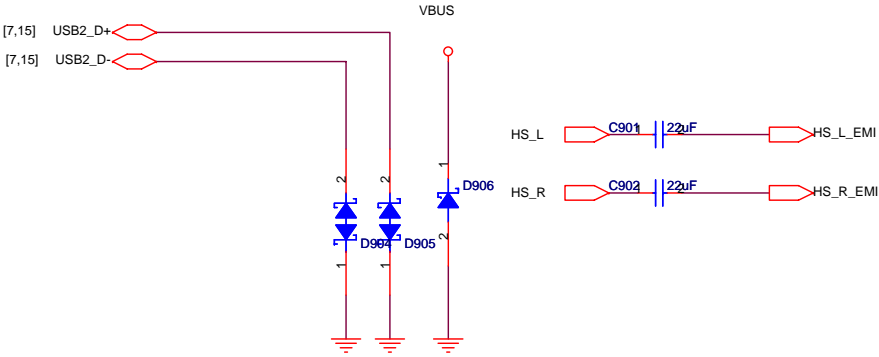


Title: HI3611_C2		
P/N: <>	Rev: 1.0	
Design by: Qinyx	Checked by: tanghui	Approved by: zhangjiong
Date: 2009-06-30	Date: 2009-06-30	Date: 2009-06-30
Scale: <xxx>	Size: B	Sheet: 9 of 20

USB & UART &Headset



ESD/EMI Circuit



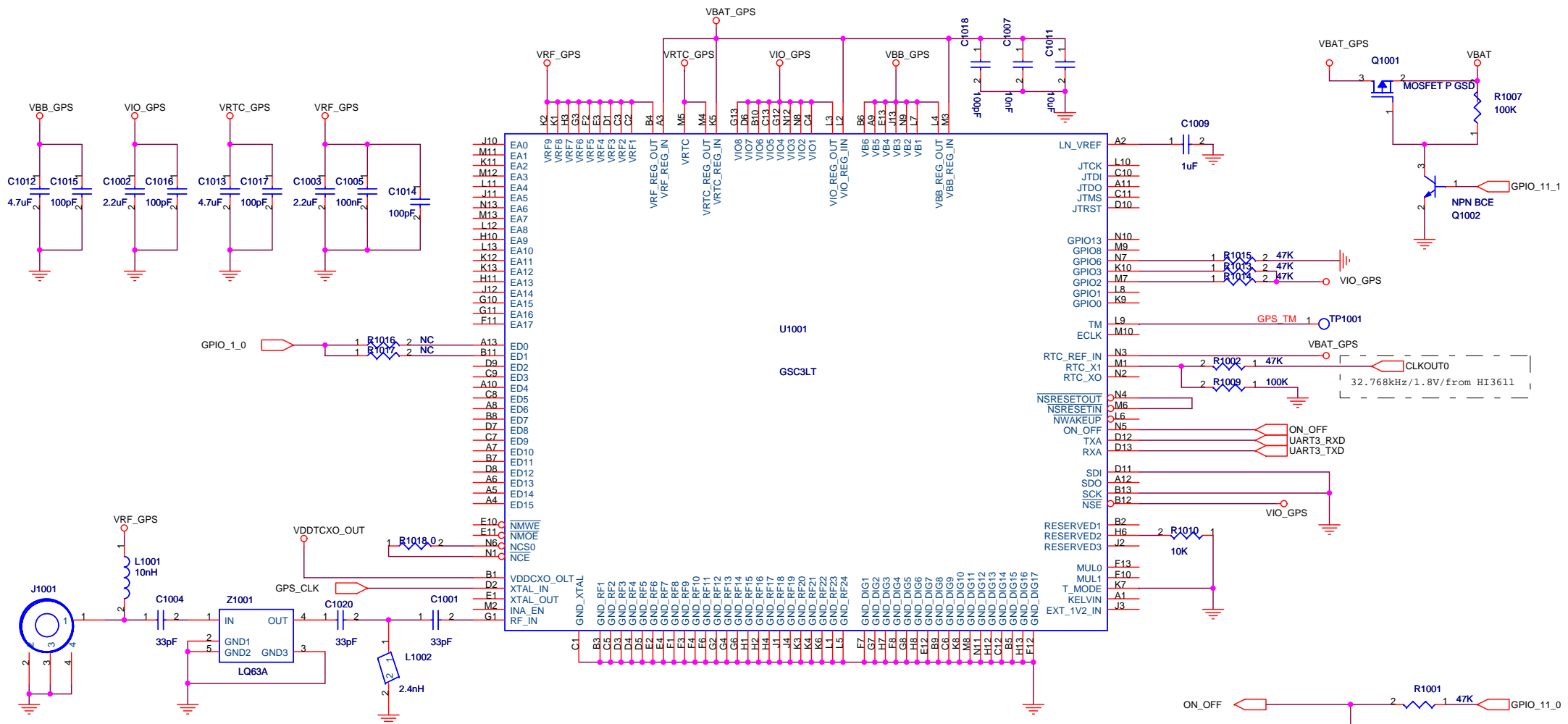
Keyboard Decoder

	KYPD_IN0	KYPD_IN1	KYPD_IN2	KYPD_IN3
KYPD_OUT0	OK1	Side_down	Side_up	/
KYPD_OUT1	Capture	VOL-	VOL+	/
KYPD_OUT2	Left Key	UP	Right Key	/
KYPD_OUT3	Left	OK2	Right	Home
KYPD_OUT4	Call	Down	End	OK3

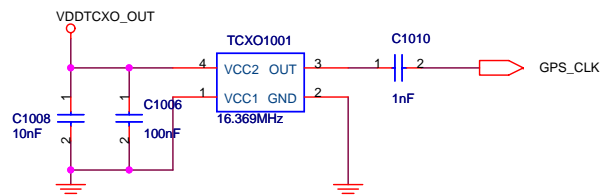
MISC

Title: HI3611_C2		
P/N: <>		Rev: 1.0
Design by: Qinyx	Checked by: tanghui	Approved by: zhangjiong
Date: 2009-06-30	Date: 2009-06-30	Date: 2009-06-30
Scale: <xxx>	Size: B	Sheet: 10 of 20

VBAT Power Gating



TCX0 for GPS



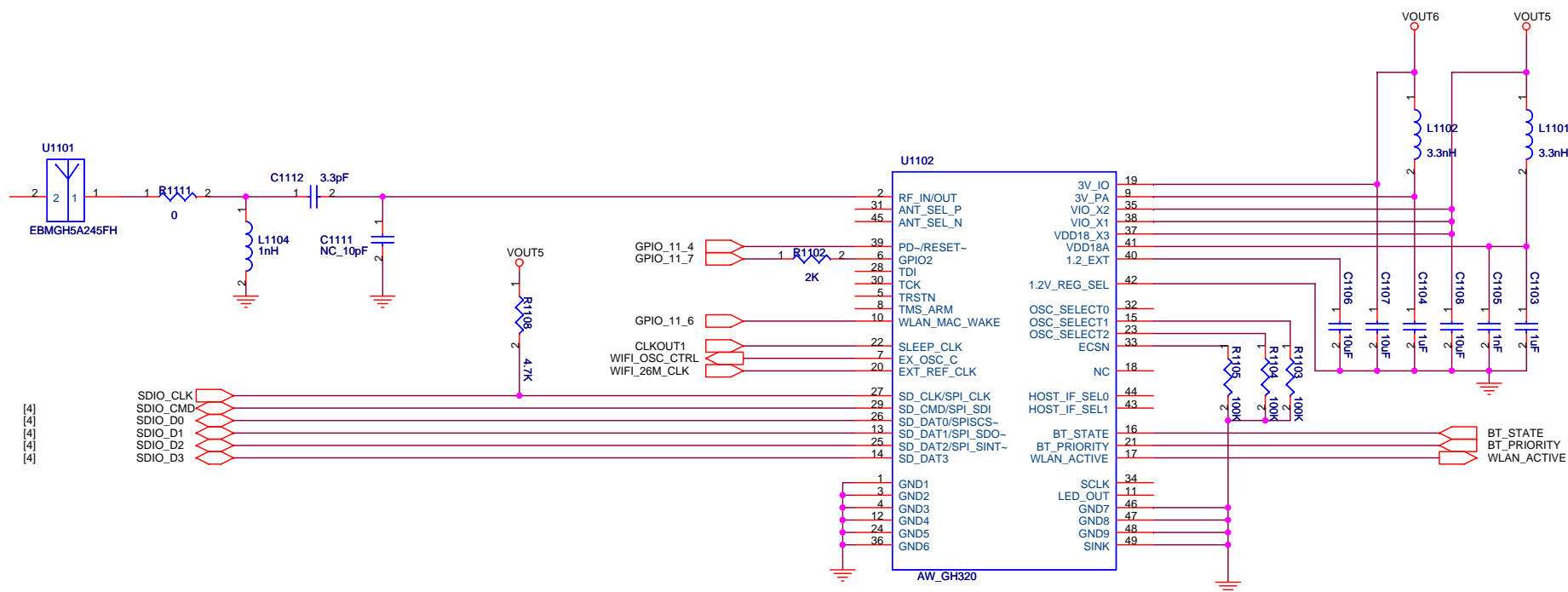
TestPoint for GPS



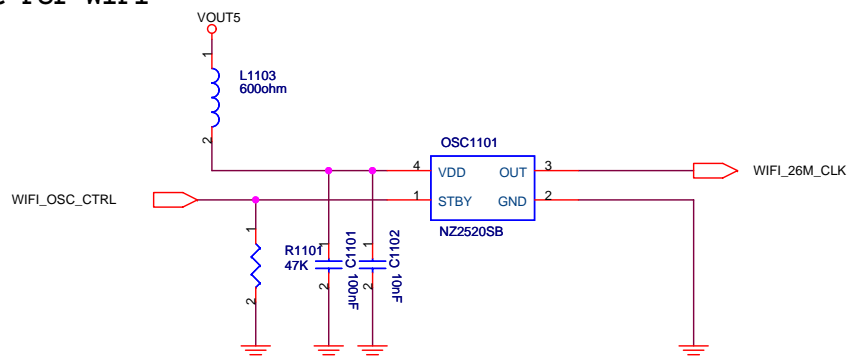
GPS

Title: HI3611_C2		
P/N: 	Rev: 1.0	
Design by: Qinyx	Checked by: tanghui	Approved by: zhangjiong
Date: 2009-06-30	Date: 2009-06-30	Date: 2009-06-30
Scale: 	Size: B	Sheet: 11 of 21


WiFi



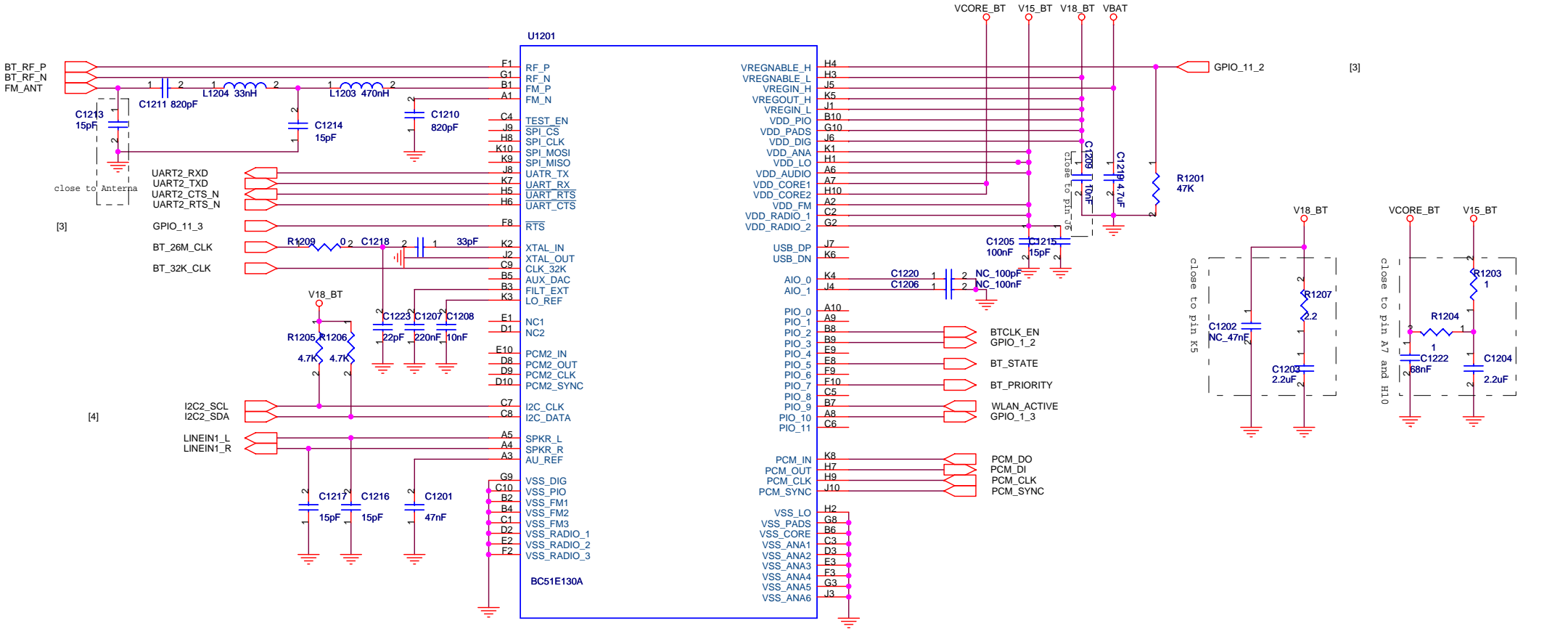
26MHz OSC For WiFi



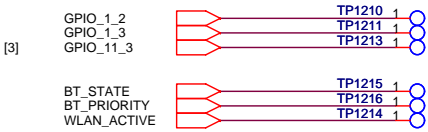
WiFi

Title: HI3611_C2		
P/N: 		Rev: 1.0
Design by: Qinyx	Checked by: tanghui	Approved by: zhangjiong
Date: 2009-06-30	Date: 2009-06-30	Date: 2009-06-30
Scale: 	Size: B	Sheet: 12 of 20

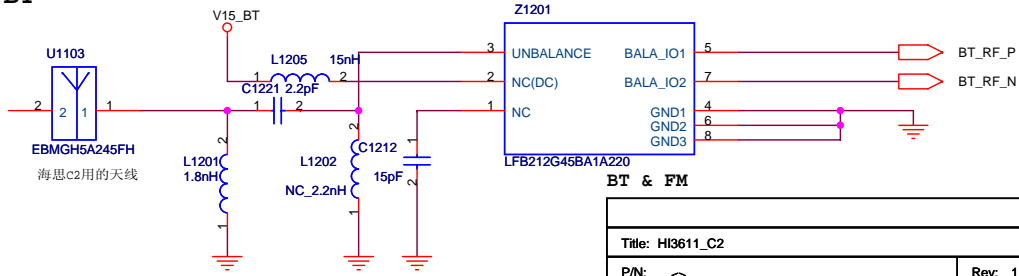
Bluetooth



Test Point

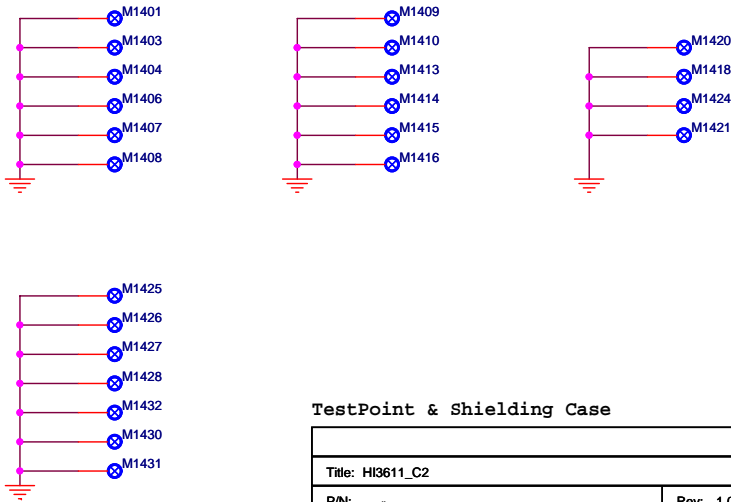
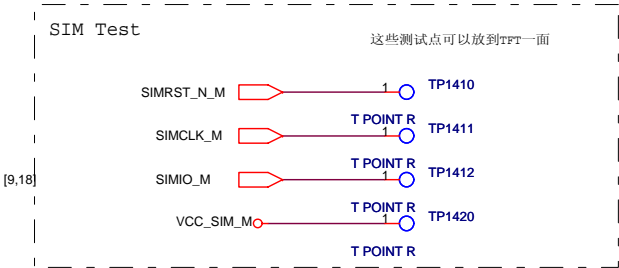
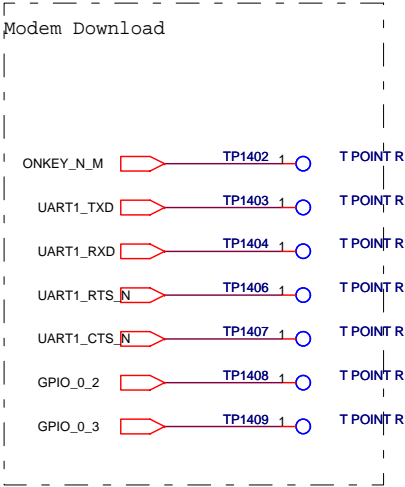
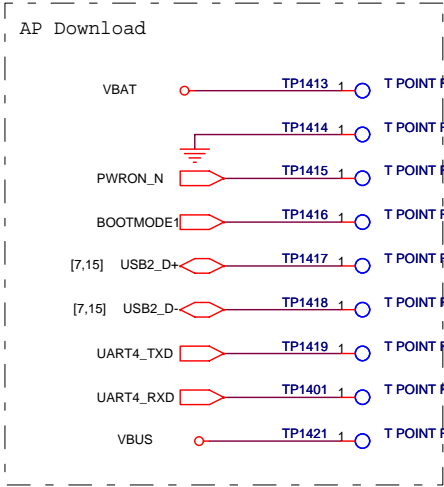


RF FOR BT

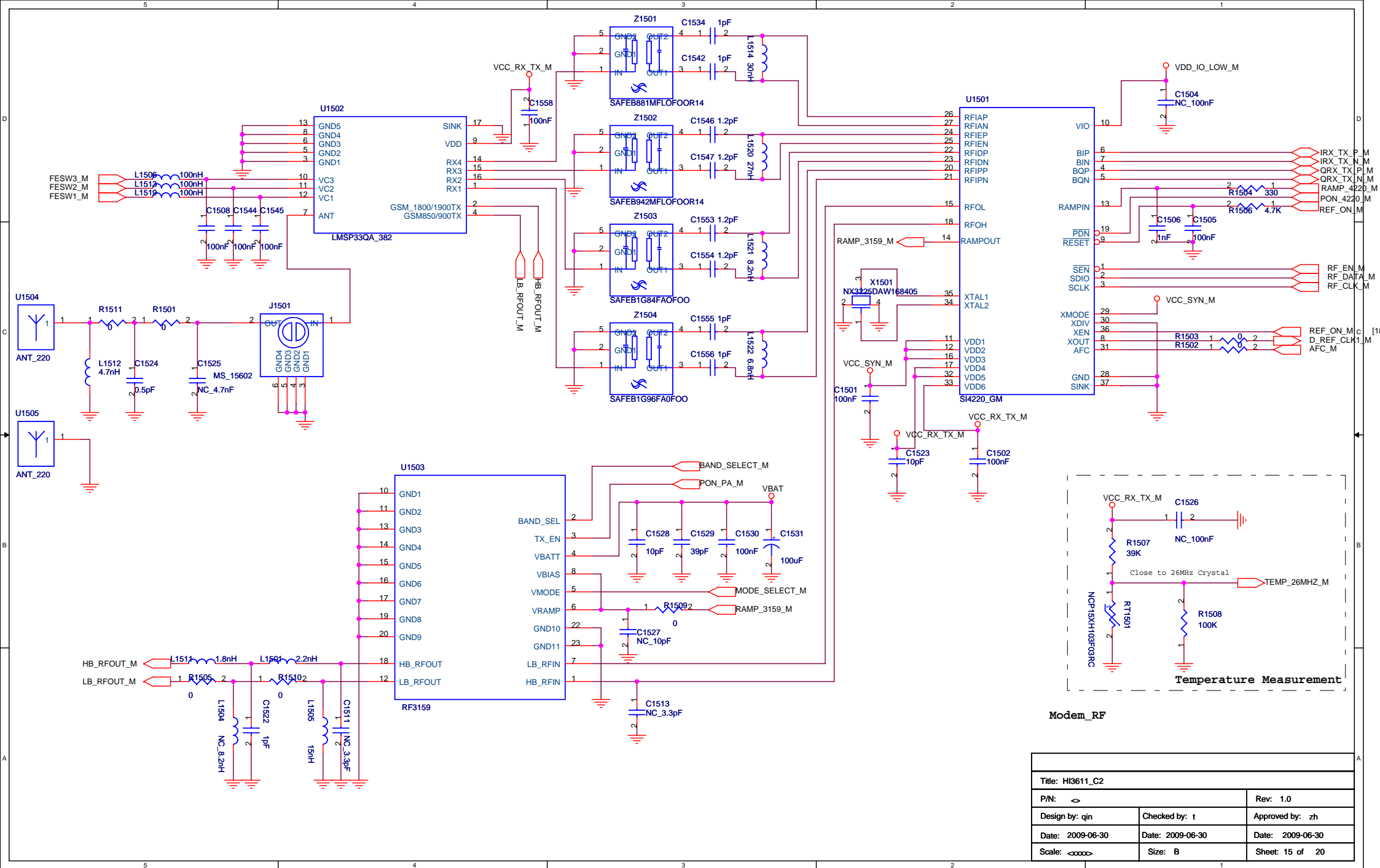


Title: HI3611_C2		
P/N: <>		
Design by: Qinyx	Checked by: tanghui	Approved by: zhangjiog
Date: 2009-06-30	Date: 2009-06-30	Date: 2009-06-30
Scale: <xxxx>	Size: B	Sheet: 13 of 20

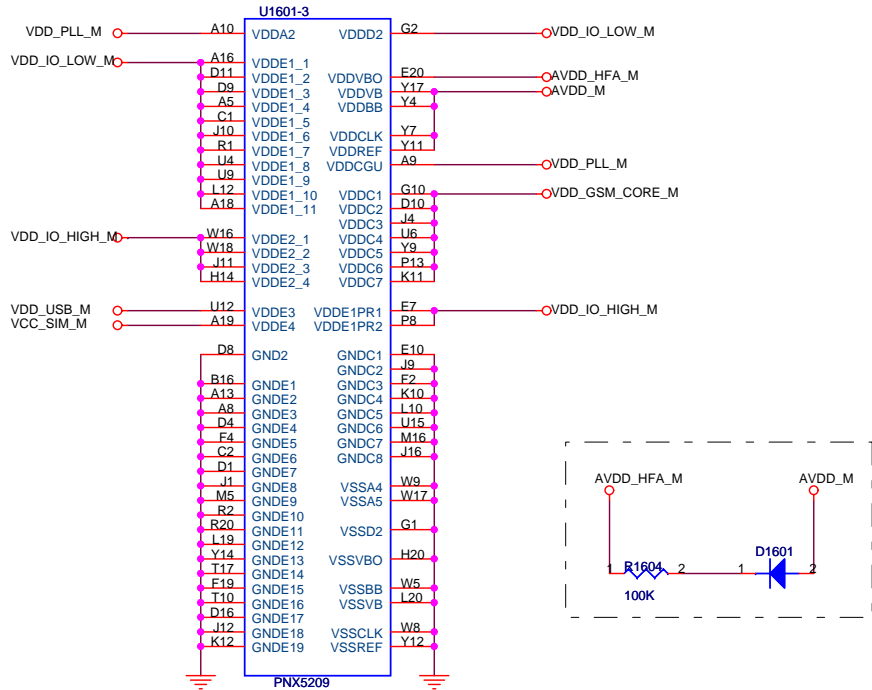
Test Point



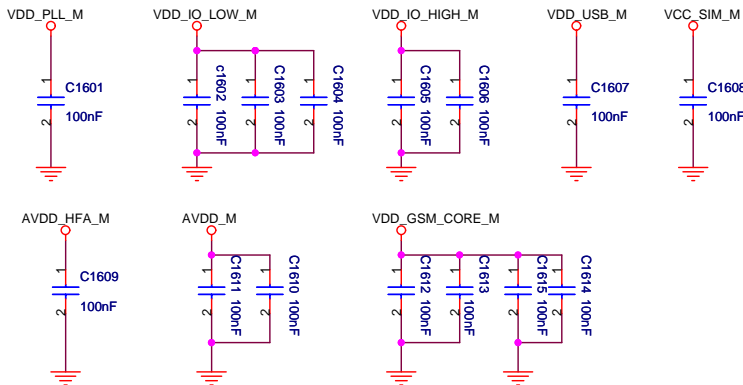
TestPoint & Shielding Case		
Title: HI3611_C2		
P/N: <>		Rev: 1.0
Design by: Qinyx	Checked by: tanghui	Approved by: zhangjiong
Date: 2009-06-30	Date: 2009-06-30	Date: 2009-06-30
Scale: <xxx>	Size: B	Sheet: 14 of 20



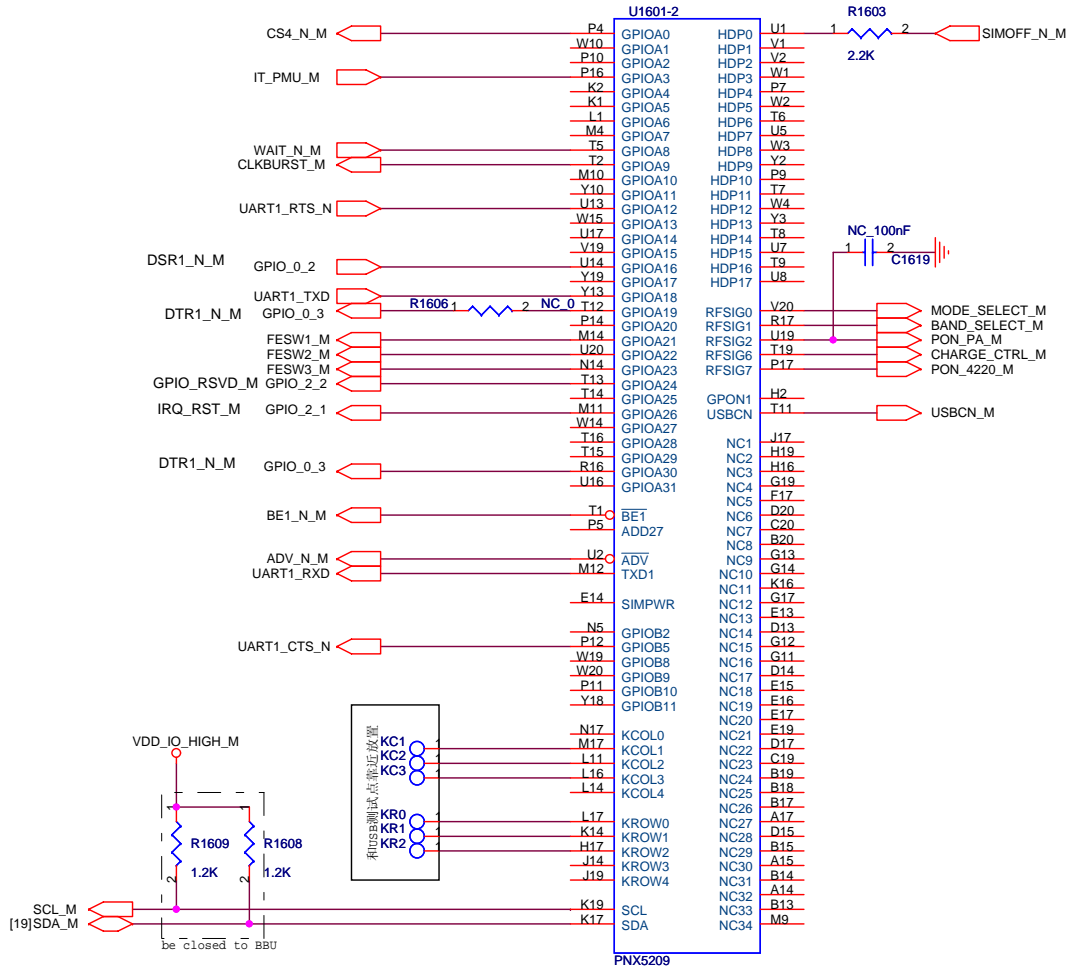
BBU Power



Power Decouple Capacitor

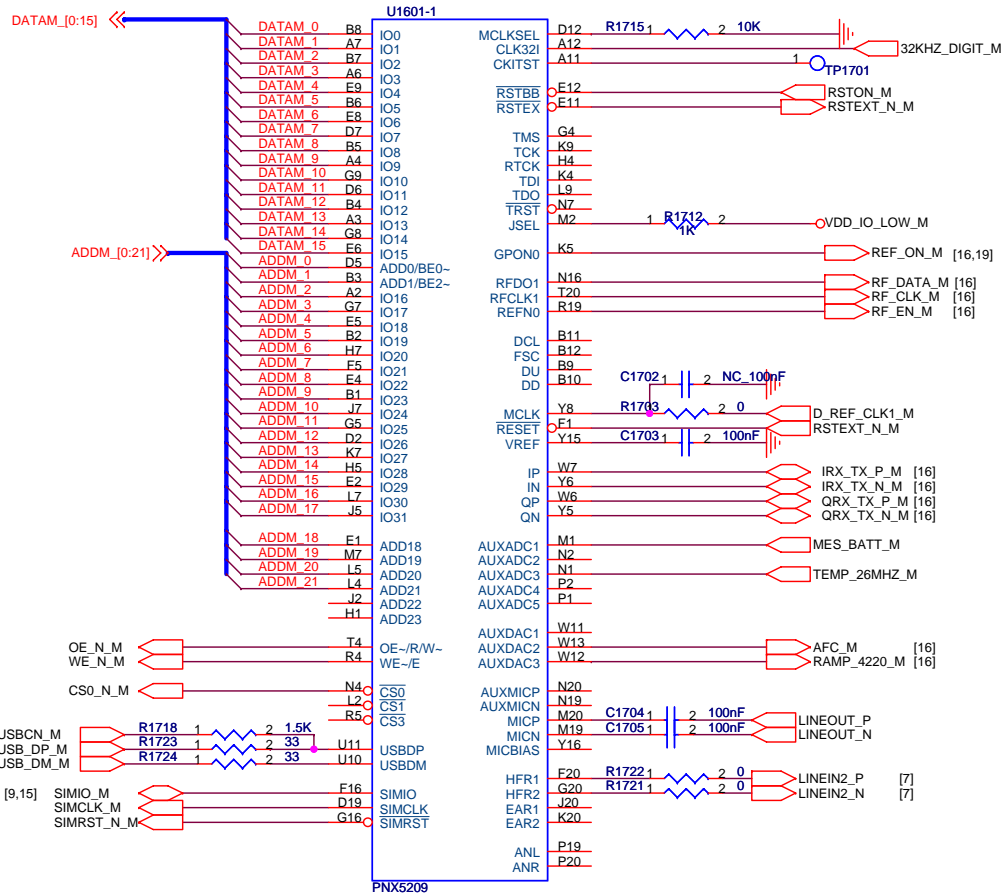


BBU IO

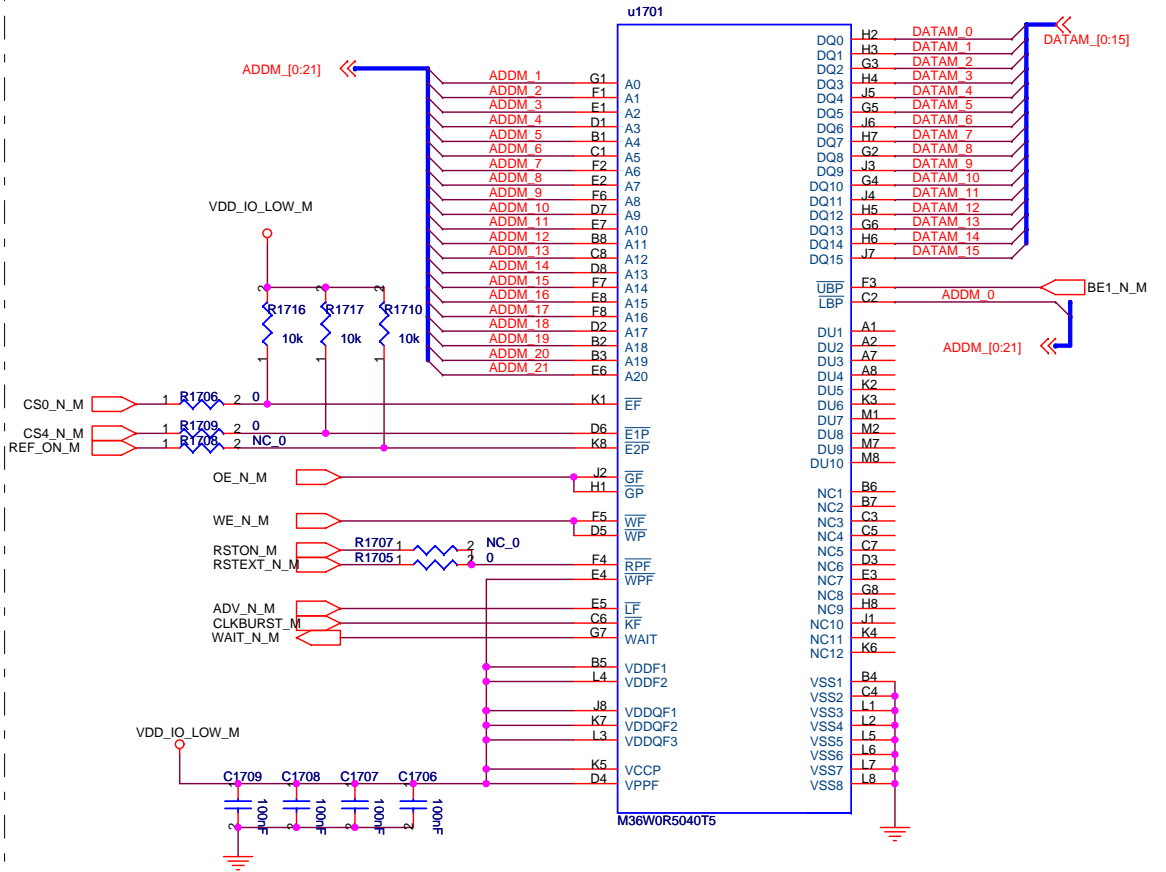


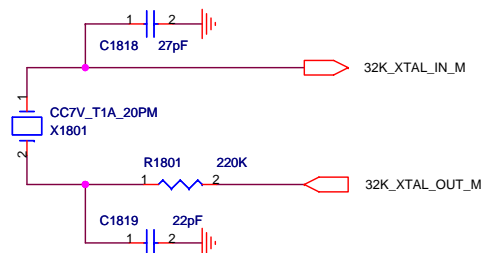
Modem_BBU		
Title: HI3611_C2		
P/N: <>	Rev: 1.0	
Design by: Qinyx	Checked by: tanghui	Approved by: zhangjiog
Date: 2009-06-30	Date: 2009-06-30	Date: 2009-06-30
Scale: <xxxx>	Size: B	Sheet: 16 of 20

BBU Memory & Misc signal

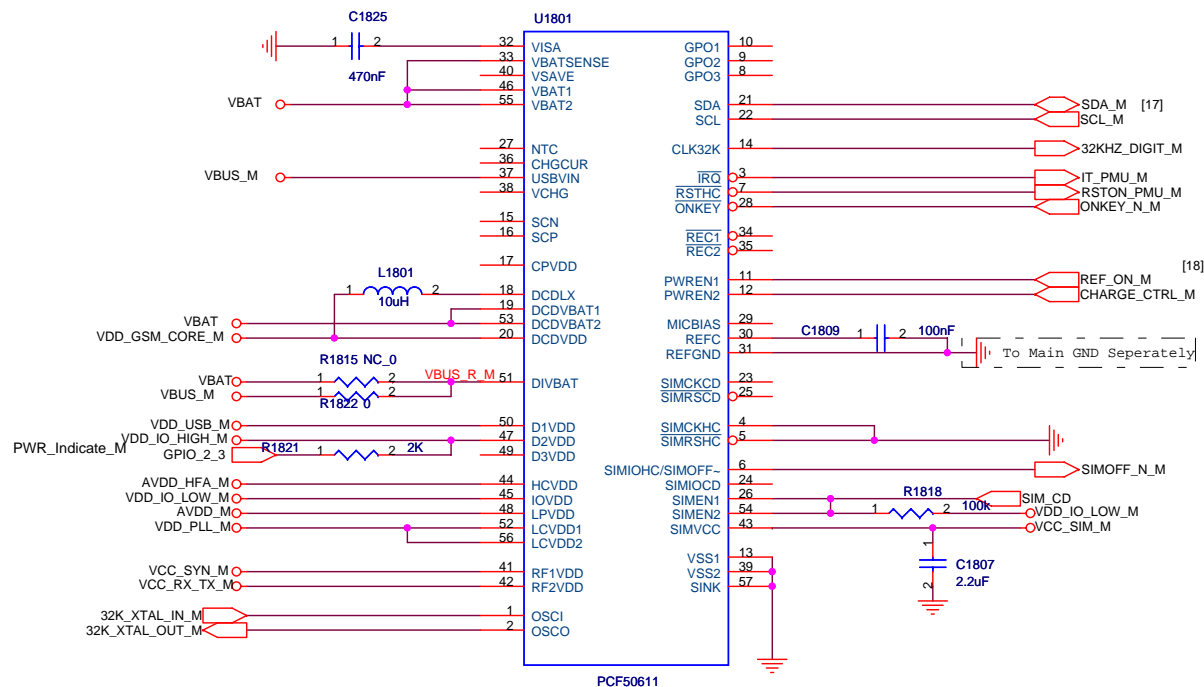


Memory(32Mbit NOR Flash+16Mbit PSRAM)

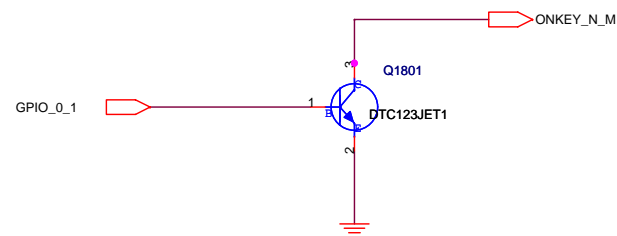





POWER MANAGEMENT TABLE			
Name	Value	Current	Function
VDD_IO_HIGH	2.90V	mA	PNX5209 High IO
VDD_GSM_CORE	1.25V	mA	PNX5209 Core
VDD_IO_LOW	1.80V	mA	PNX5209 Low IO
AVDD	2.60V	mA	PNX5209 Anlog
AVDD_HFA_M	2.60V	mA	PNX5209 Vioceband PA
VDD_PLL_M	1.2V	mA	PNX5209 CLK/PLL
VDD_USB_M	3.30V	mA	PNX5209 USB IO
VCC_SIM_M	1.8/3.0V	mA	PNX5209 SIM IO
VCC_RX_TX_M	2.8V	mA	RF Transceiver(Si4220) VDD
VCC_SYN_M	2.8V	mA	RF Transceiver VDD
VBAT	3.7V	mA	Battery Supply



Modem PWRON_N Signal

~~Modem PMU~~

Title: H13611_C2		
P/N: 		Rev: 1.0
Design by: Qinyx	Checked by: tanghui	Approved by: zhangjiong
Date: 2009-06-30	Date: 2009-06-30	Date: 2009-06-30
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