

(CWF49F1) 4.BLOCK DIAGRAM AVDD123 DVDD MXO IFI IFO LMI RSSI RV QUAD CF DEMO OPI OPO RSSI MUTI 2VppMUTE $0.1 \mathrm{Vpp}$ LNAI SPKO SpeakerAmp PA DO PAEN 100K 22K CF1 MICO 33K \$ VDDH MICI PDREG, Bias O- T/R 0.5VDD VREF VCC 33K 2.7V Programmanble 1/2048 > PFD 13 bit counter VCO MODI 100K MIC_EN T/R 0.1mA decoder >1M 0.7VppOSC2 OSC1 CHS2 CHS1 CHS0 T/R DVSS AVSS1,2,3 CPPO XO VCT