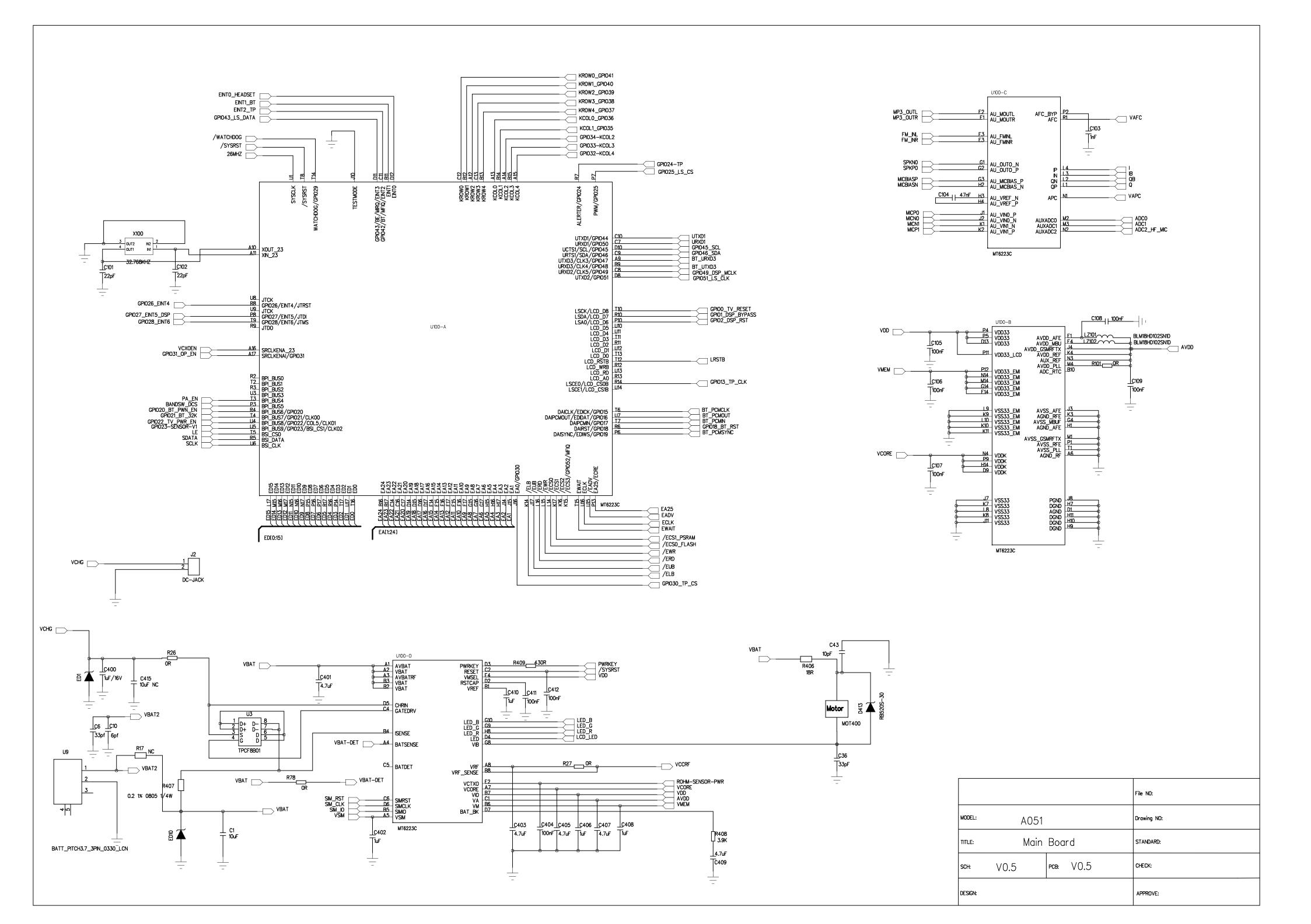
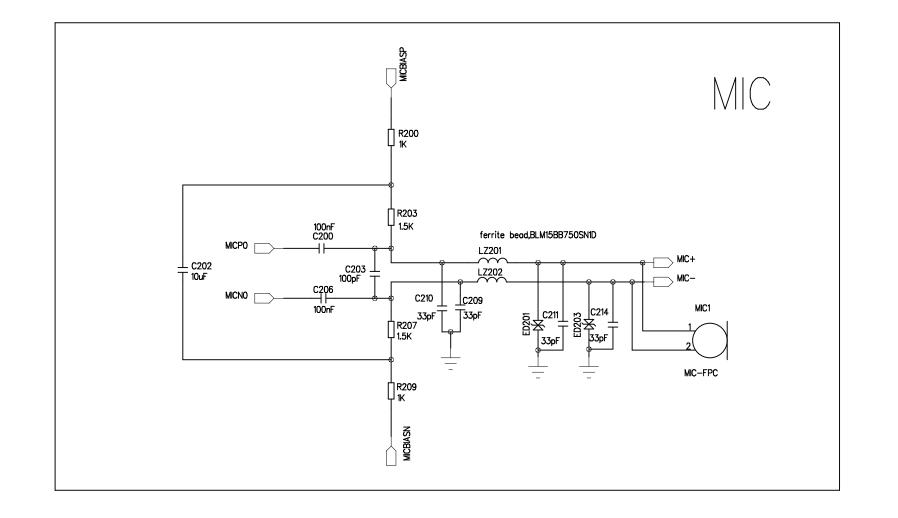
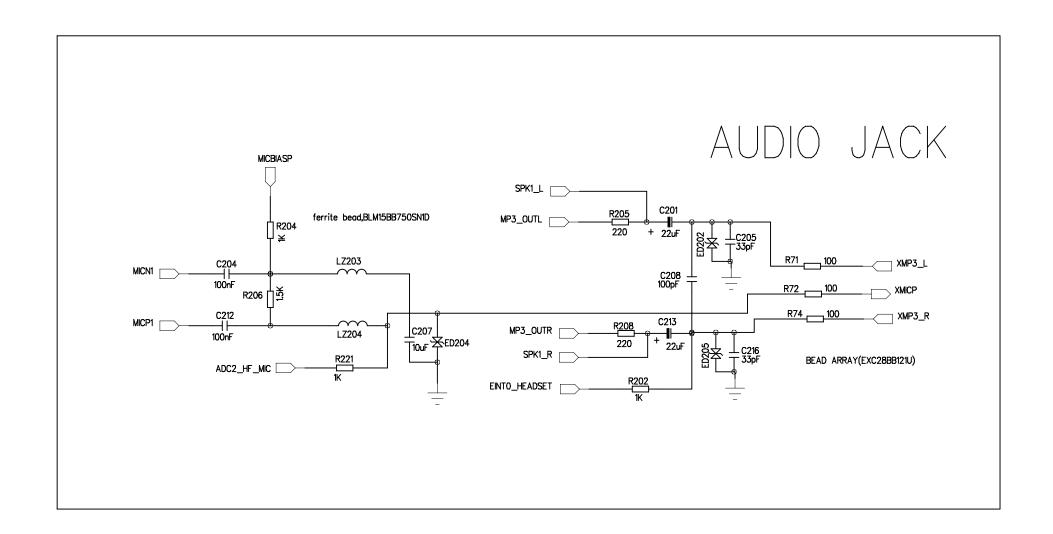
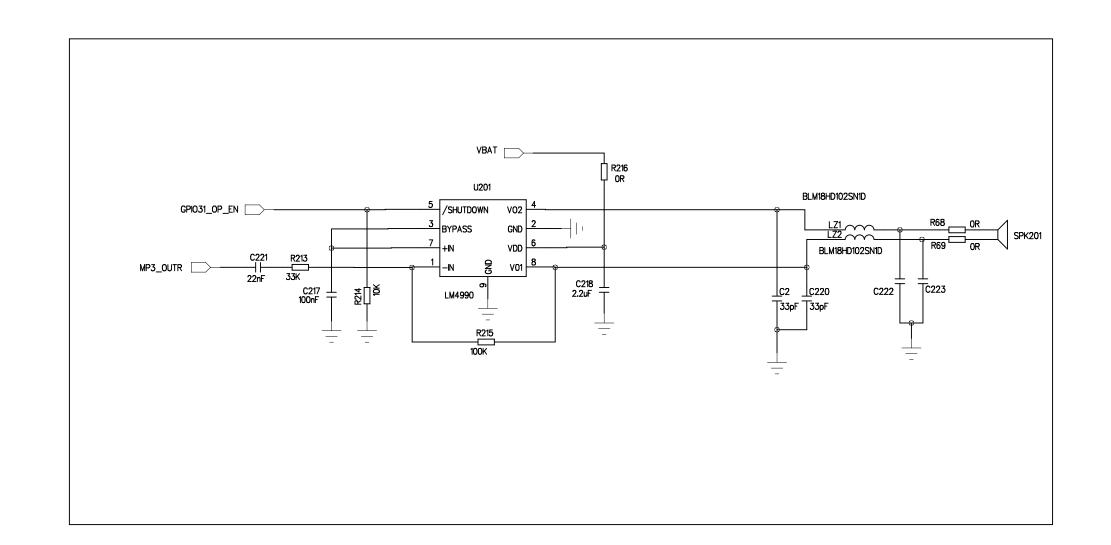


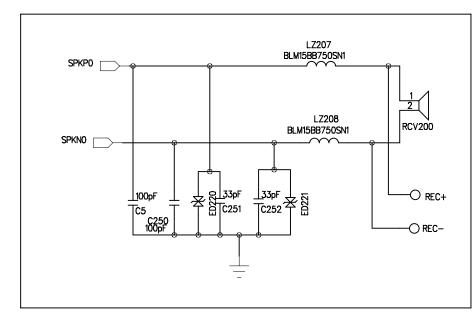
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MODEL: A051		Drawing NO:
тіть: Main Board		STANDARD:
sch: V0.5	PCB: V0.5	CHECK:
DESIGN:		APPROVE:



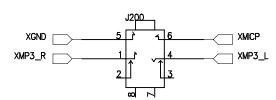






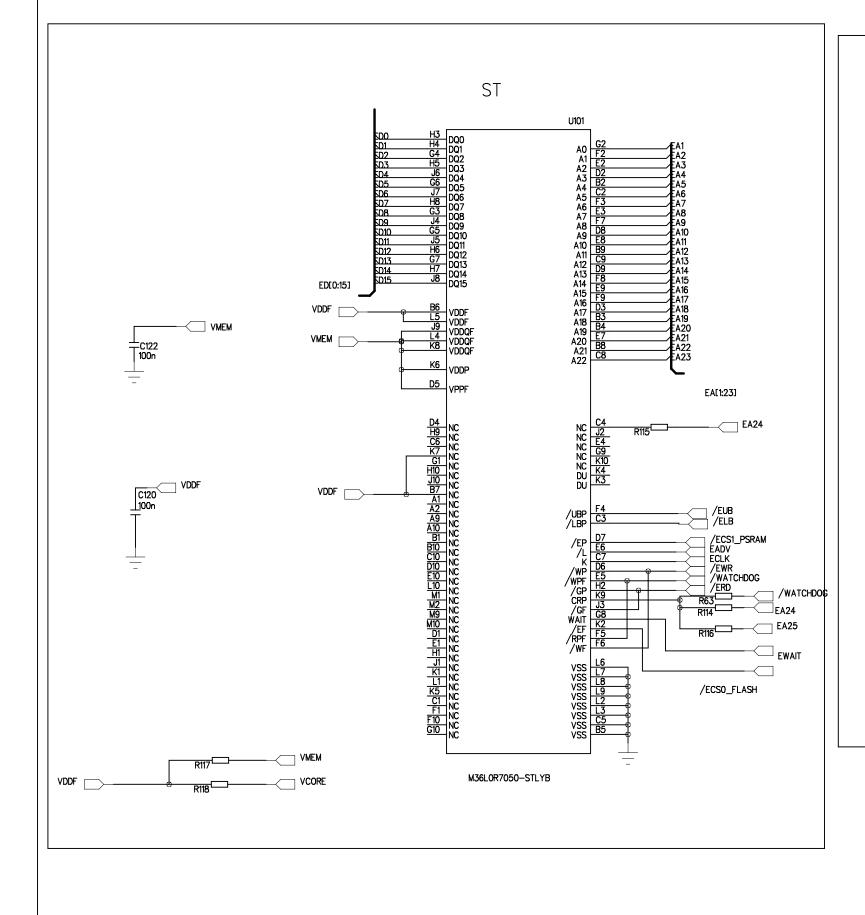


Receiver



HEADJACK_3.5MM_6PIN_06506_LCN

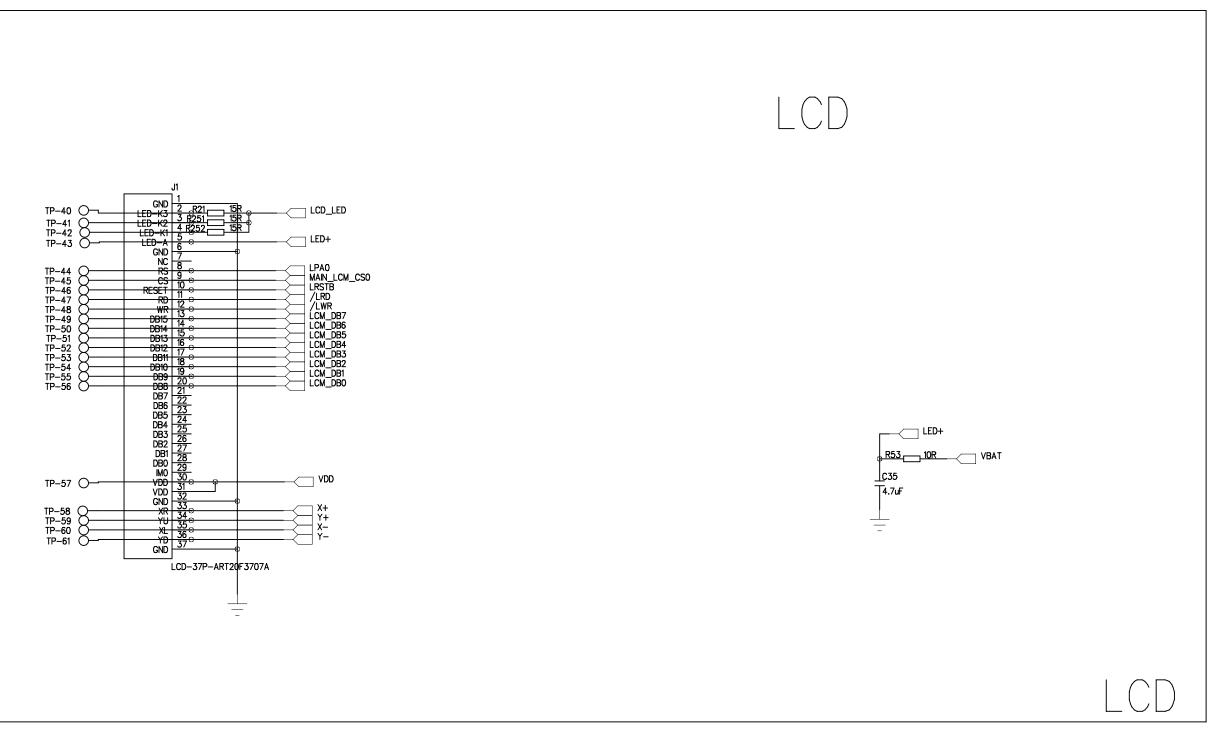
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MODEL: A051		Drawing NO:
^{тітсе:} Main Board		STANDARD:
sch: V0.5	PCB: V0.5	CHECK:
DESIGN:		APPROVE:

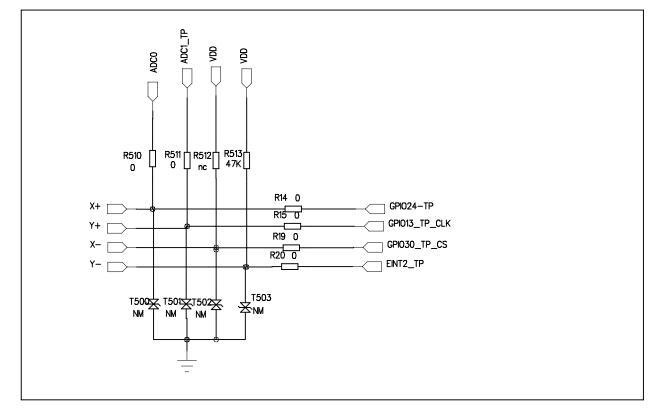


SD_DATA1 SD_DATA0 SD_CLK

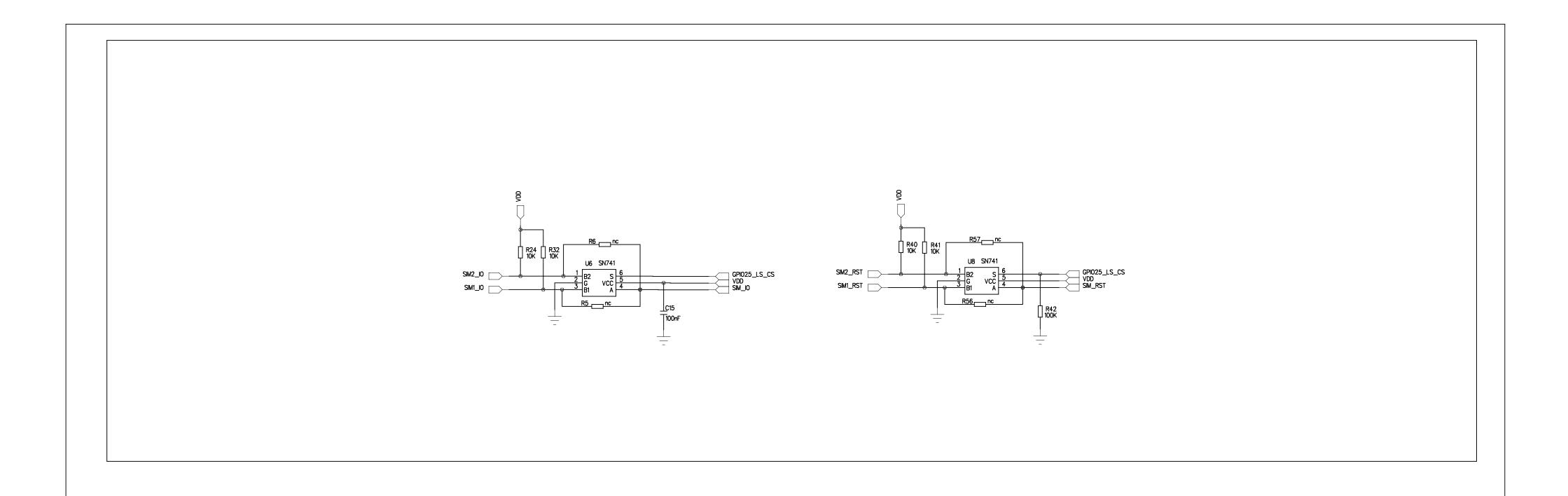
#-9948

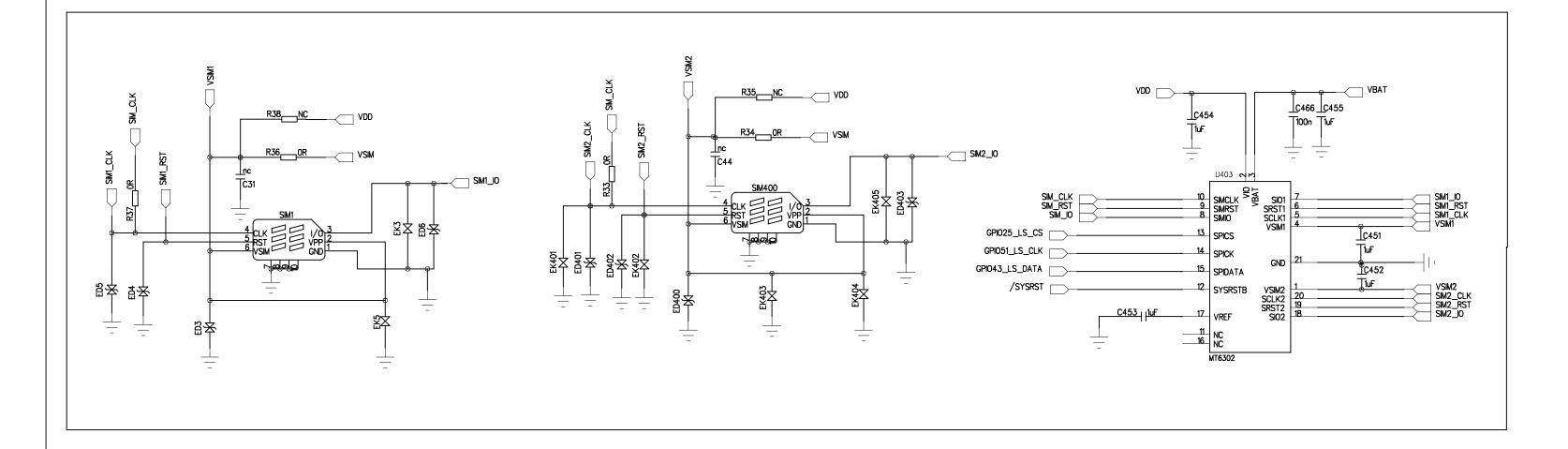
8 DAT1
DAT0
6 VSS
11
CLK
10
2 CMSND
2 CMSND
1 DAT2
1 DAT2

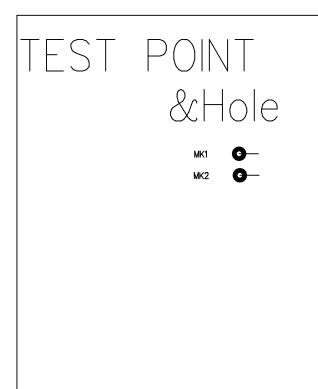




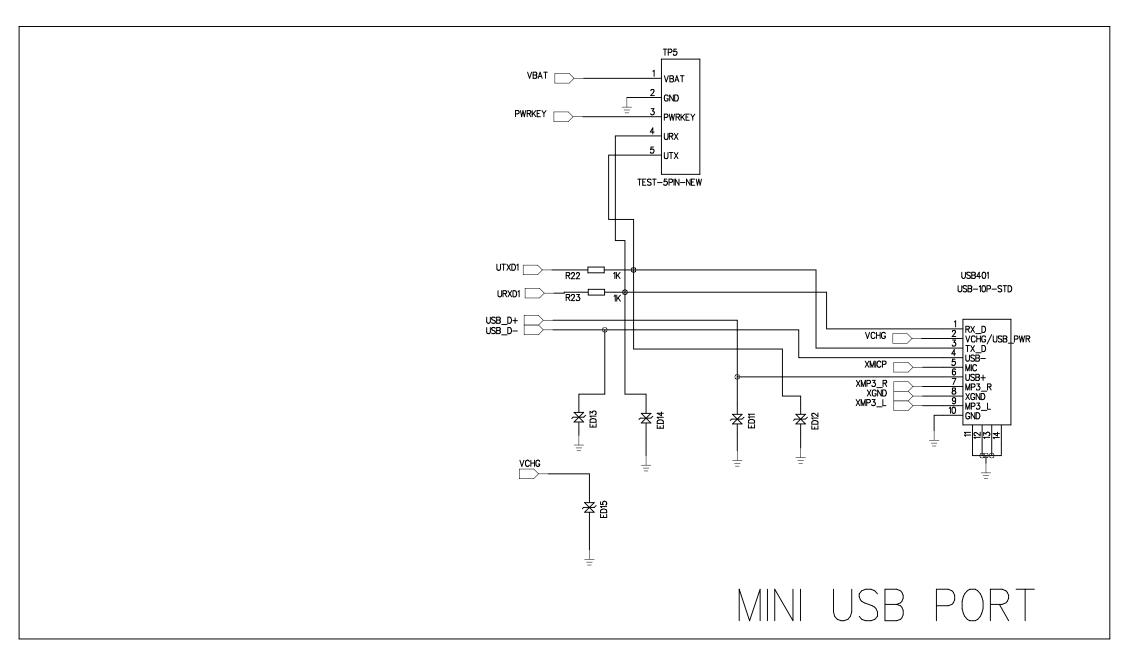
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MODEL: A05	1	Drawing NO:
тітLE: Ma	in Board	STANDARD:
sch: V0.5	PCB: V0.5	CHECK:
DESIGN:		APPROVE:

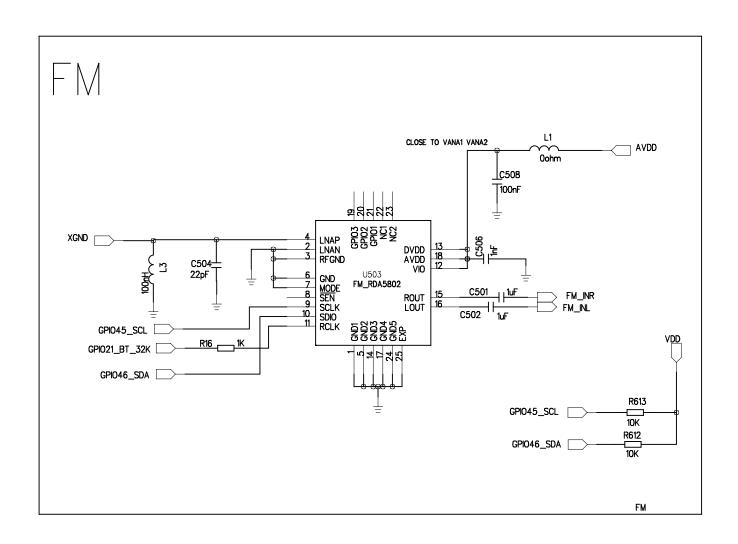


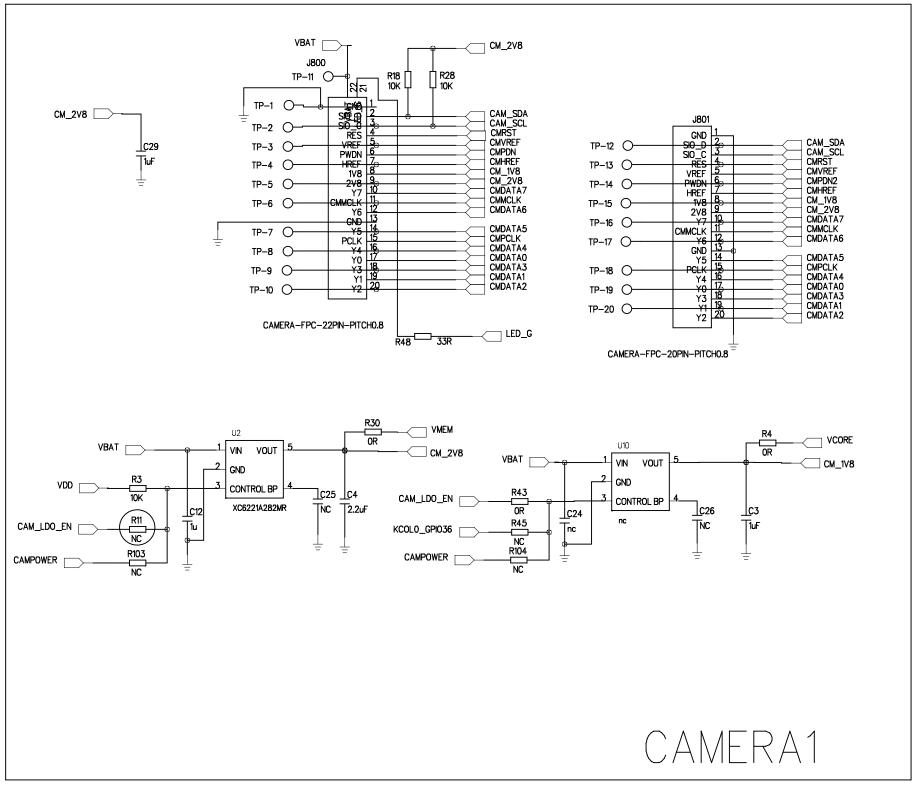




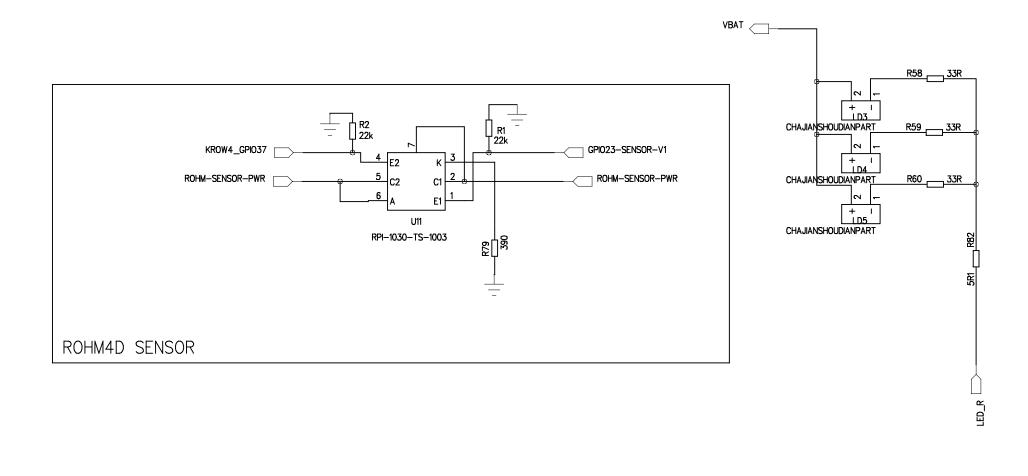
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MODEL: A05	1	Drawing NO:
^{ППLE:} Main Board		STANDARD:
sch: V0.5	PCB: V0.5	CHECK:
DESIGN:		APPROVE:

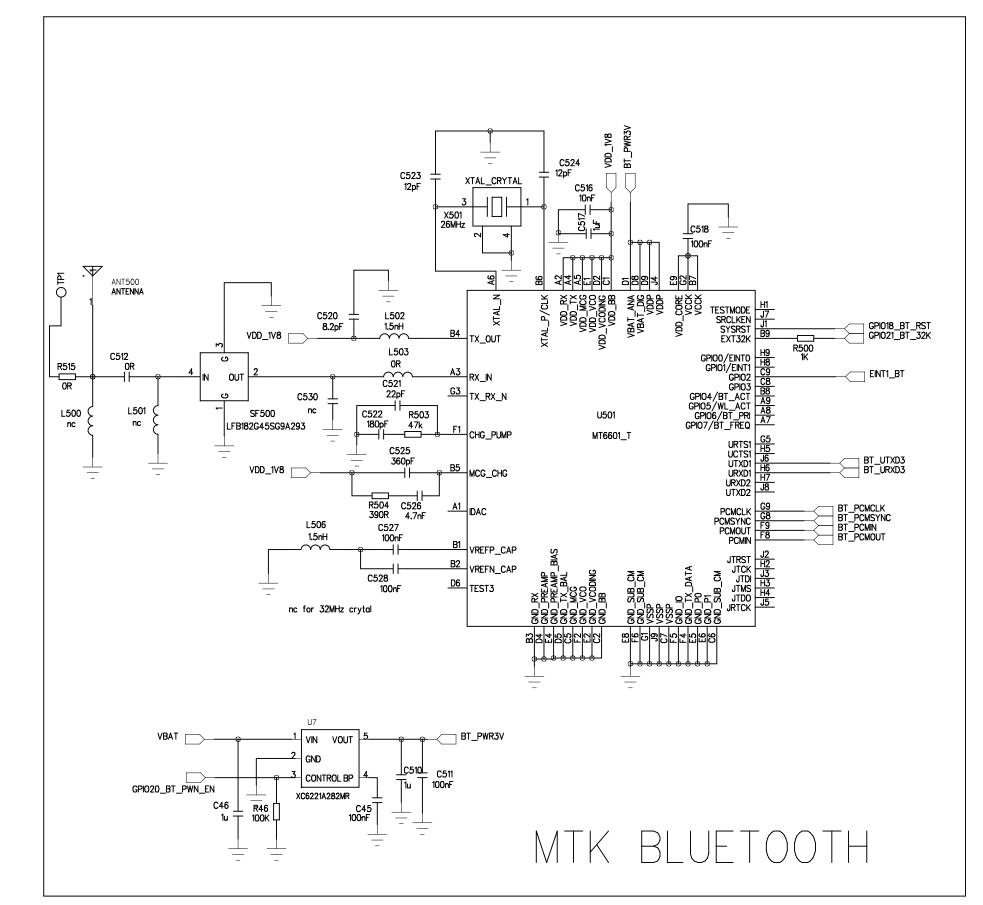






			File NO:
MODEL:	odel: A051		Drawing NO:
TITLE:	nte: Main Board		STANDARD:
SCH:	V0.5	PCB: V0.5	CHECK:
DESIGN:			APPROVE:





File NO:

Drawing NO:

STANDARD:

CHECK:

APPROVE:

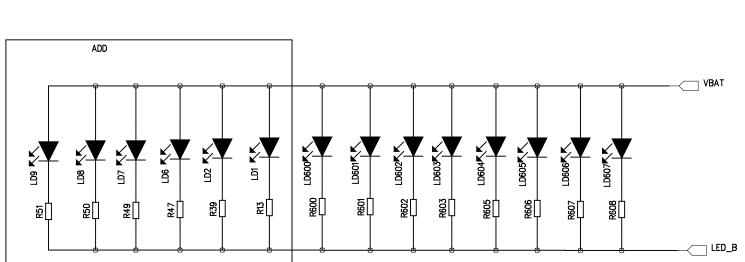
A051

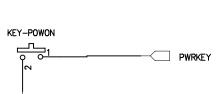
V0.5

DESIGN:

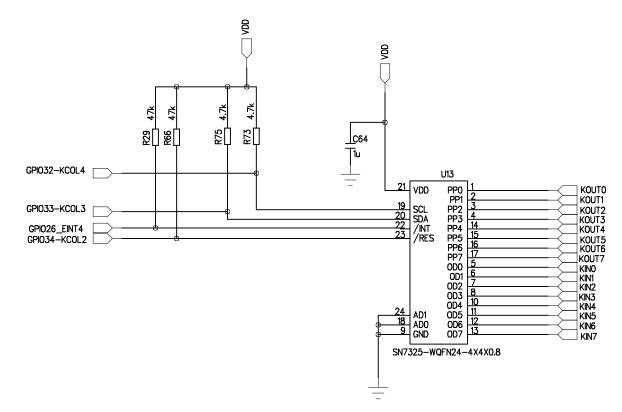
Main Board

PCB: V0.5

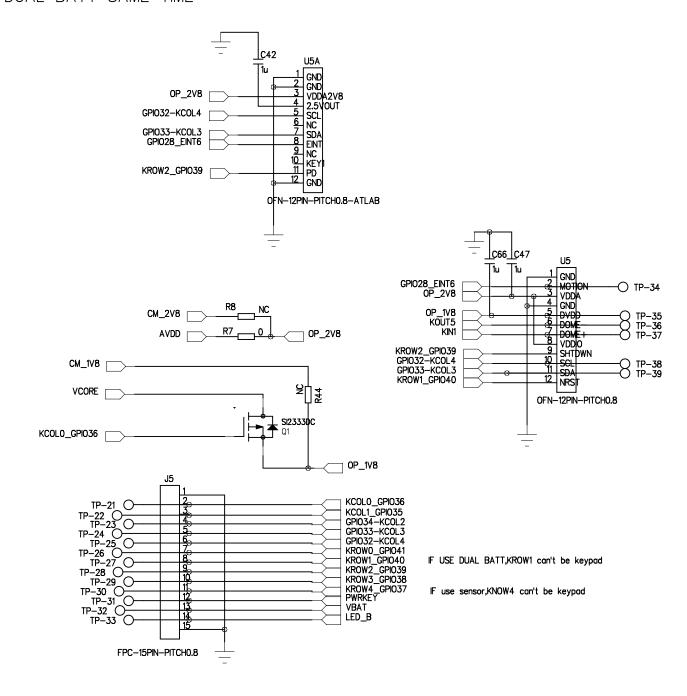


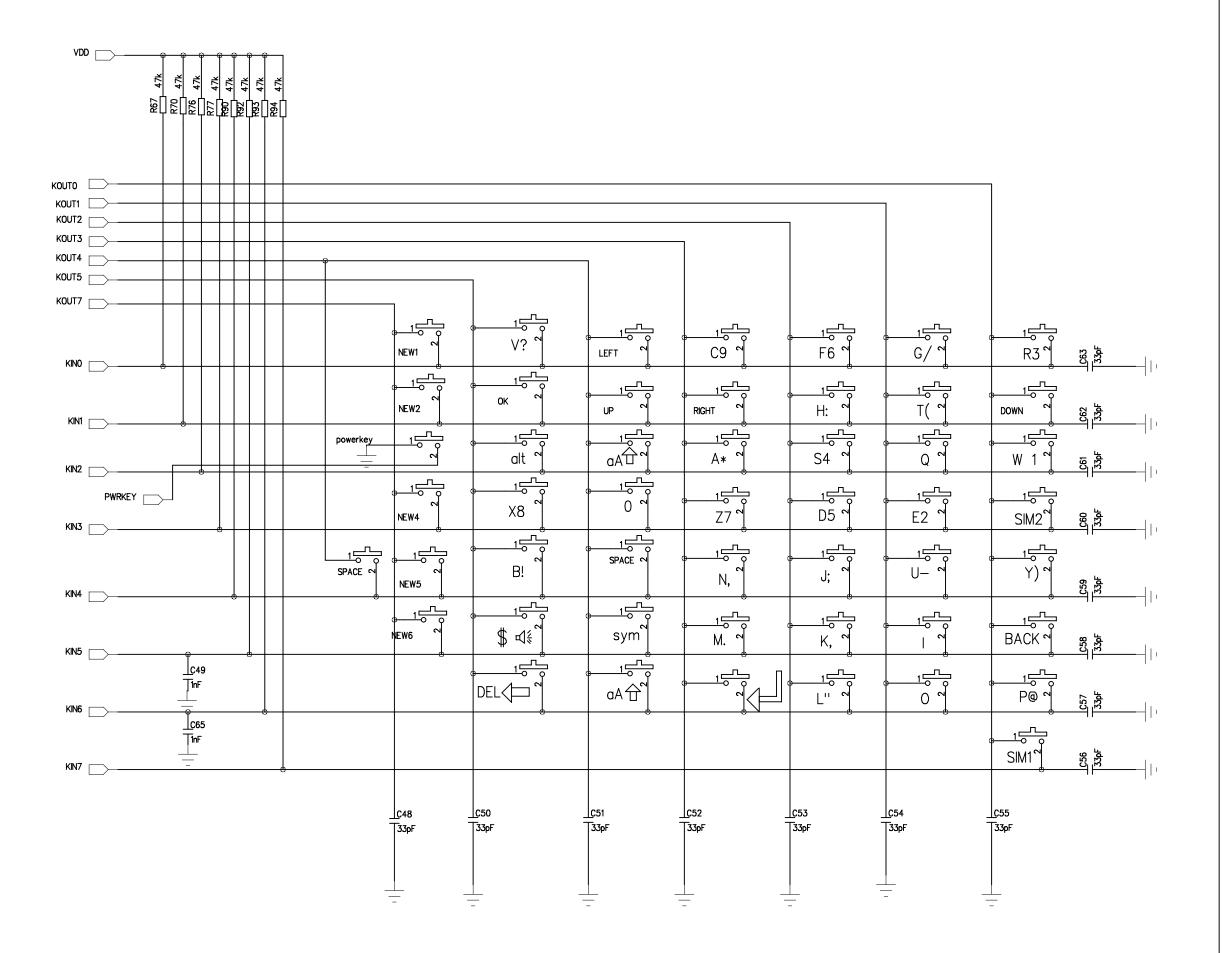


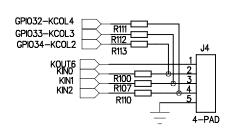




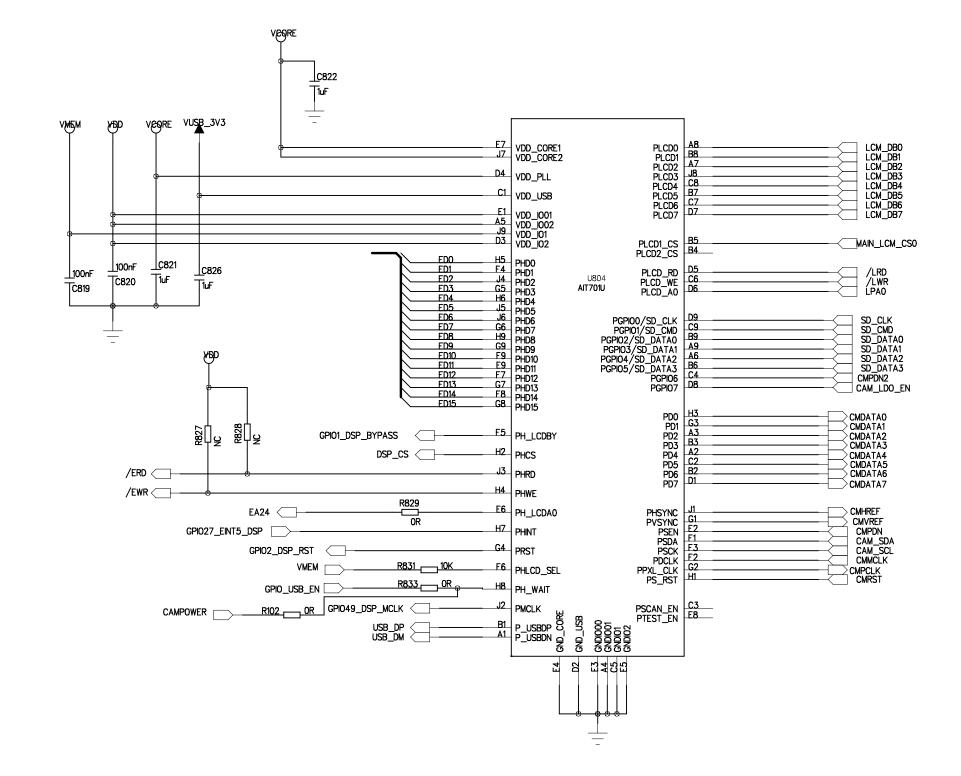
NO DUAL BATT SAME TIME

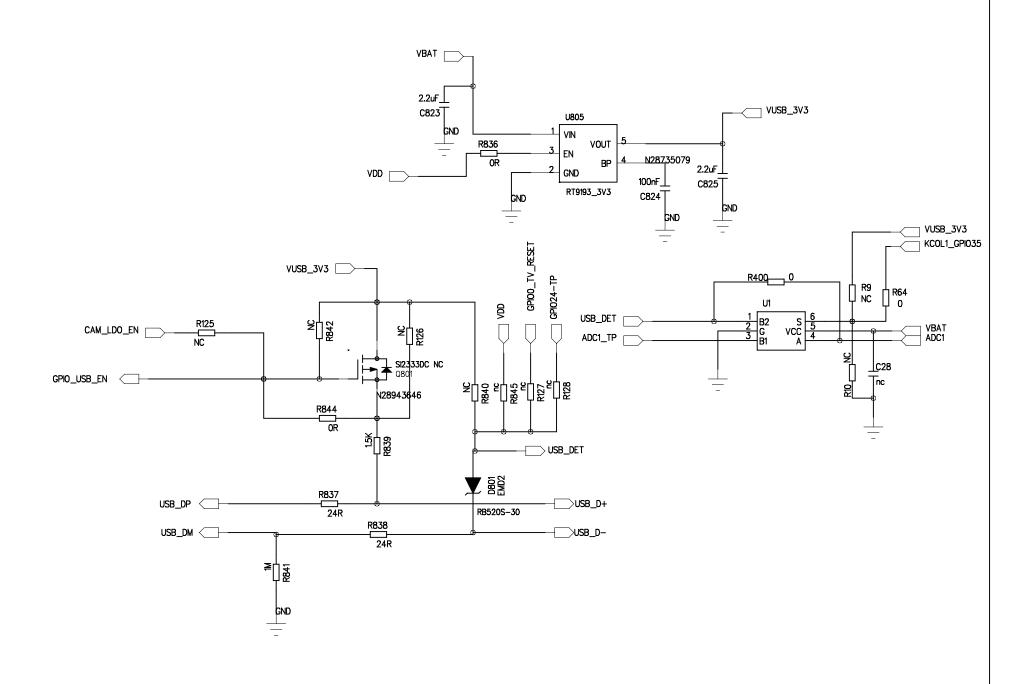






		File NO:
MODEL: A051		Drawing NO:
^{тітLE:} Main Board		STANDARD:
scn: V0.5	PCB: V0.5	CHECK:
DESIGN:		APPROVE:





	U404	ŀ	
1	NC	VCC	5 VDD
EA252	A		
GND 3	GND	Y	4DSP_CS
NC7SZ04 INVERTER_NC7SZ04			

		File NO:
MODEL: A051		Drawing NO:
^{тітLE:} Main Board		STANDARD:
sch: v0.5	PCB: V0.5	CHECK:
DESIGN:		APPROVE: