

The diagram illustrates the DSP-based Client architecture. A central block labeled "CLIENT DSP (SN93310)" is connected to several components:

- Y1**: A 12MHz clock source connected to the top of the CLIENT block.
- RX RF Module**: Connected to the CLIENT via a bidirectional **DATA** bus. It also receives **+2.5V** power and is connected to an antenna.
- Serial Flash**: Connected to the CLIENT and the RX RF Module.
- Power Management**: Provides **+3.3V** power to the CLIENT.
- USB**: Connected to the CLIENT via **USB DATA** and **USB Power** lines. A **PC** is connected to the USB port.
- MONITOR**: Connected to the PC, displaying a **Video Image**.

Microscope Main Unit

RF Module

The diagram shows a schematic for the A7121 component. At the top, there is a triangle symbol representing an antenna, labeled "L Antenna". A vertical line connects this antenna to a large rectangular block labeled "A7121". To the right of the A7121 block, there is a thick diagonal line. Below the A7121 block, there is a smaller rectangular block labeled "Y1" (crystal) with "10MHz" written inside it. Two lines connect the bottom of the A7121 block to the top of the Y1 block.

Pin configuration diagram for the SN93310 component. The diagram shows a 16-pin package with pins 1 through 16 labeled with their functions. A +2.5V supply is indicated at pin 1. A dashed line separates the pin labels from the component symbol, which is labeled SN93310. A double-headed arrow labeled DATA indicates the data bus connection between the component and the rest of the system.

Pin	Function
1	AMIC TRXD NR_IRQ
2	AMIC BB_CLK
3	AMIC_CD_TXEN
4	AMIC_SPI_TXDNR_MISO
5	AMIC_SPI_CLKNR_SCK
6	AMIC_SPI_RXDNR_MOSI
7	AMIC_SPI_CS_NR_CSN
8	AMIC_MS0 NR_CE
9	AMIC_MS1
10	AMIC_TX_SEL
11	AMIC_RX_SEL