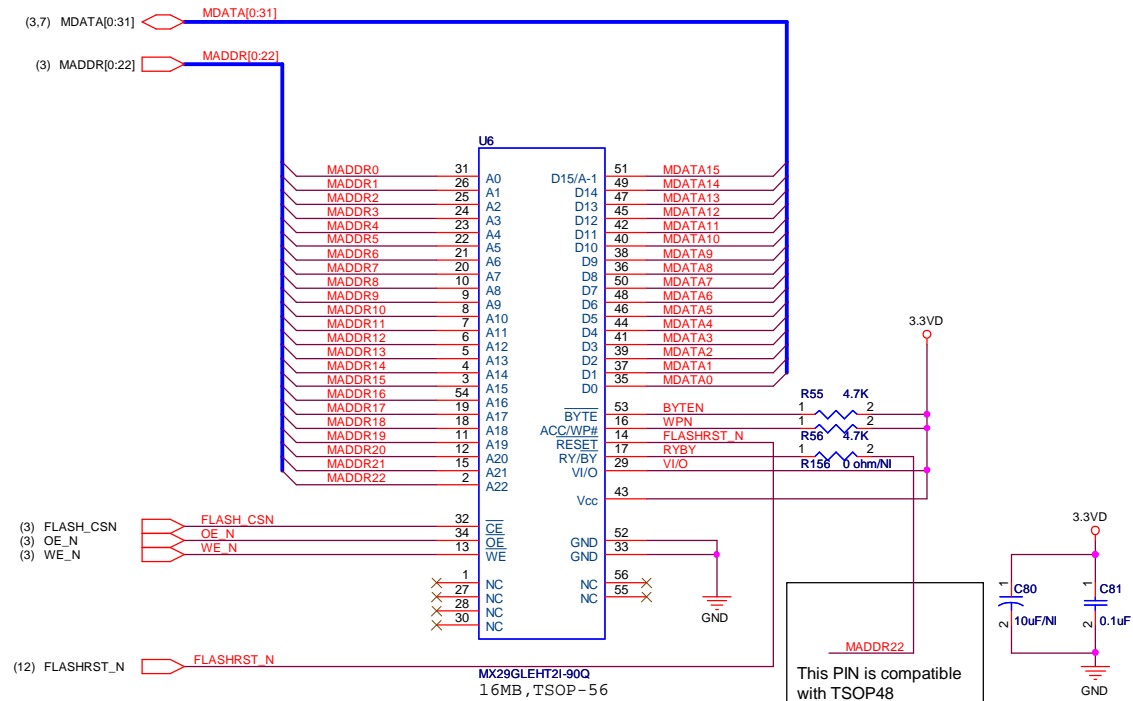
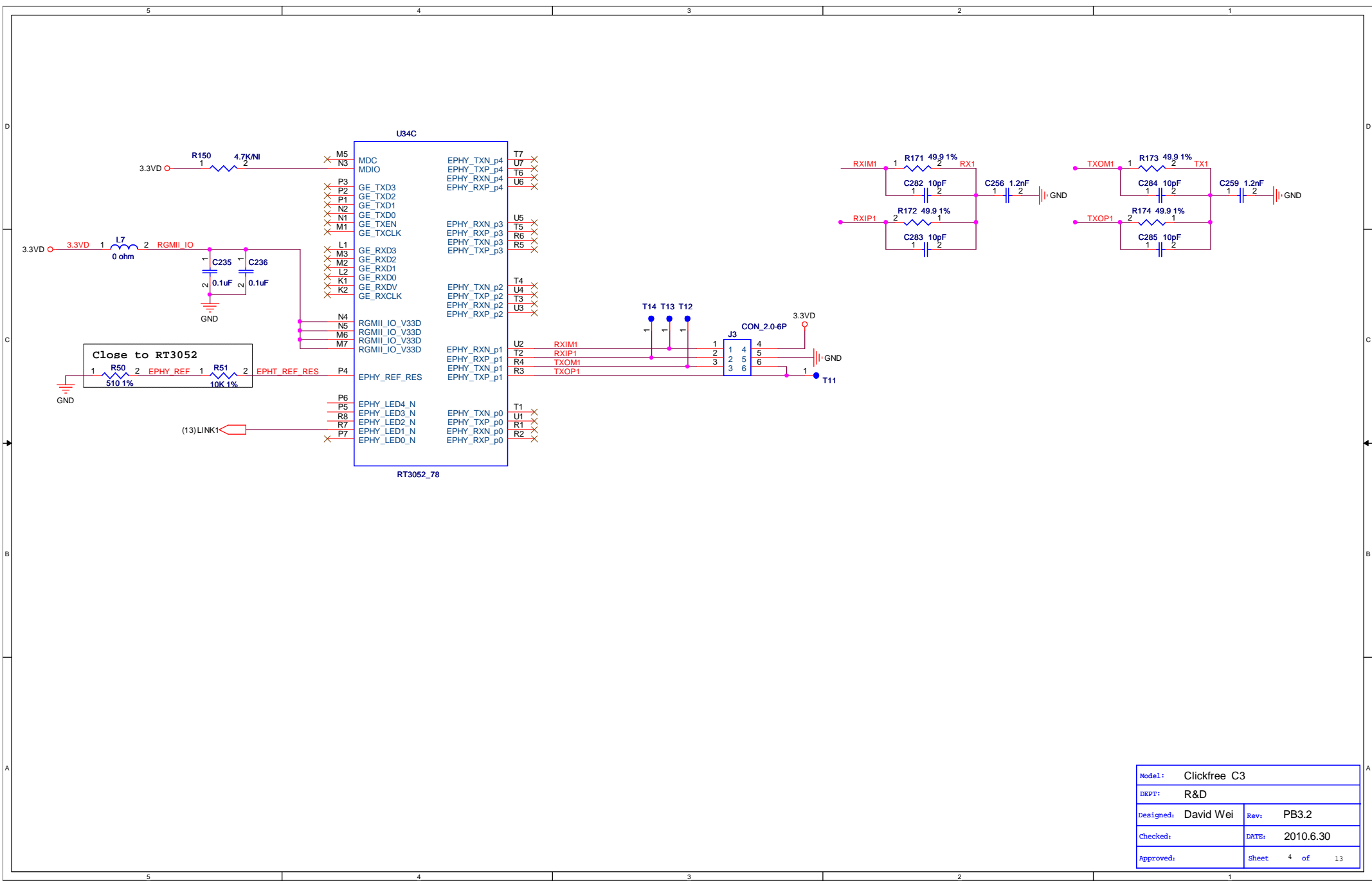
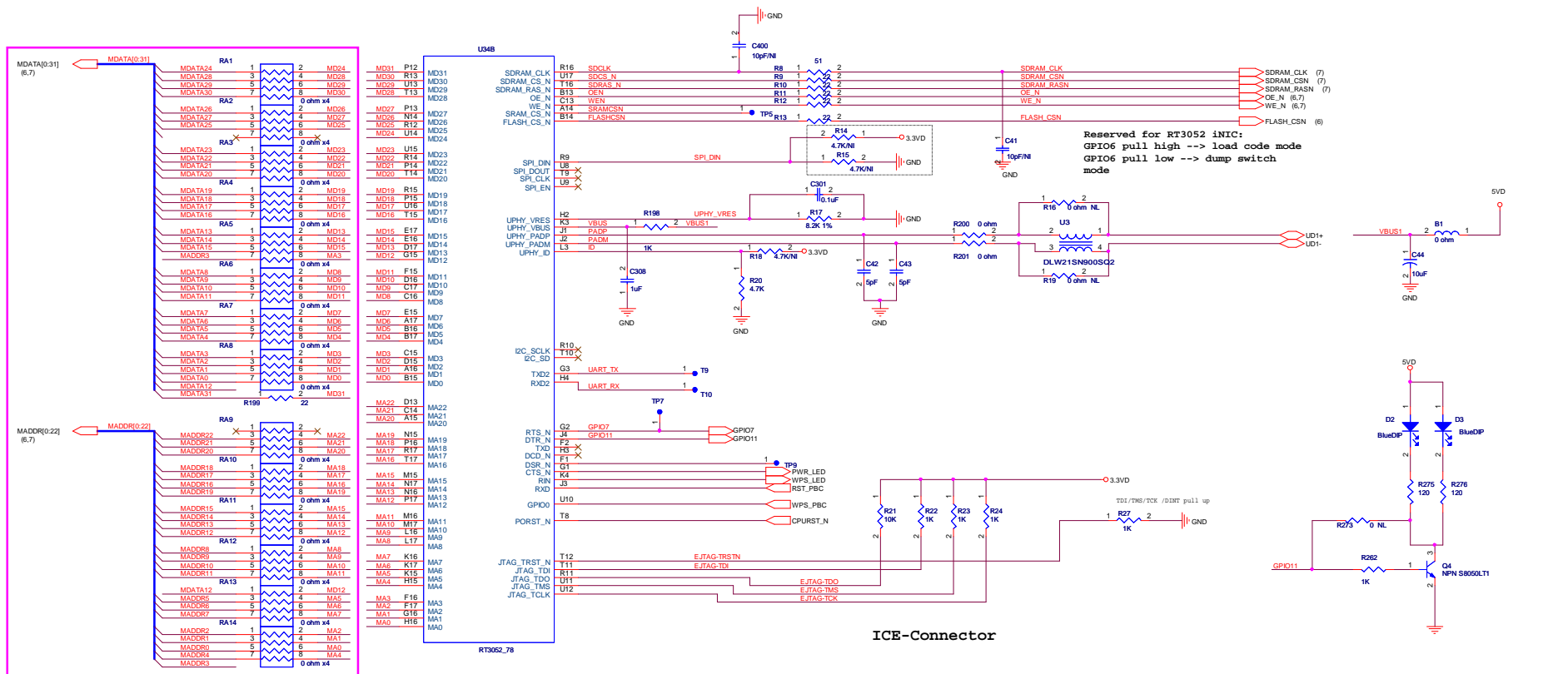


Model: Clickfree C3	
DEPT: R&D	
Designed: David Wei	Rev: PB3.2
Checked:	DATE: 2010.6.30
Approved:	Sheet 1 of 13

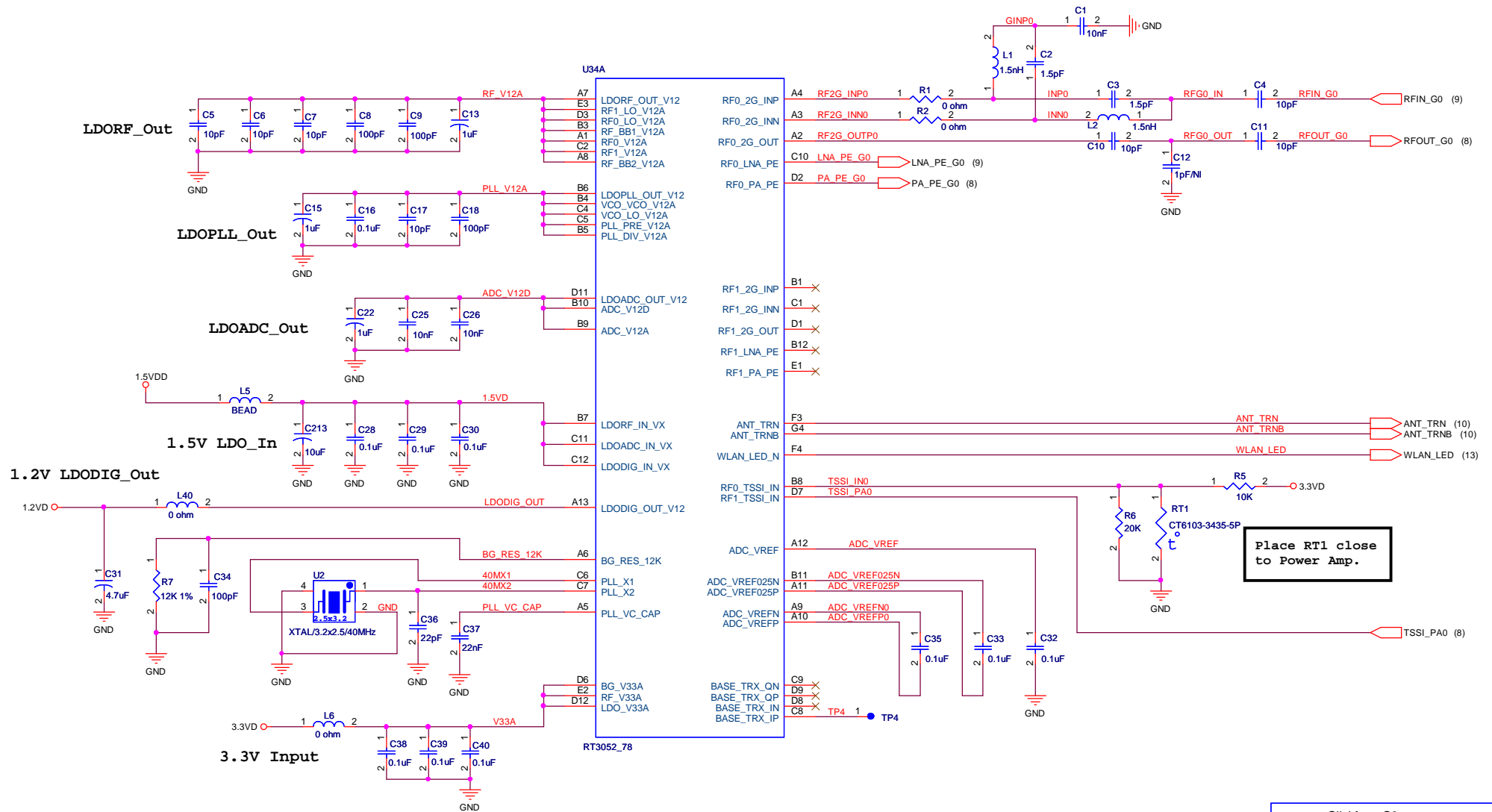


Model:	Clickfree C3		
DEPT:	R&D		
Designed:	David Wei	Rev:	PB3.2
Checked:		DATE:	2010.6.30
Approved:		Sheet	6 of 13

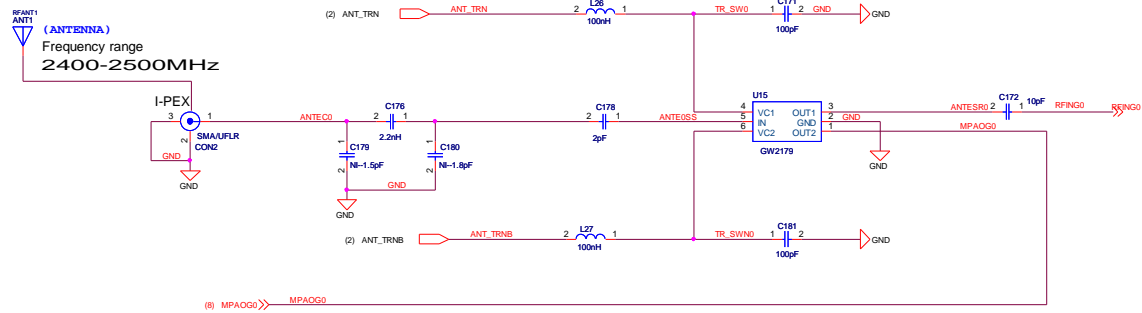




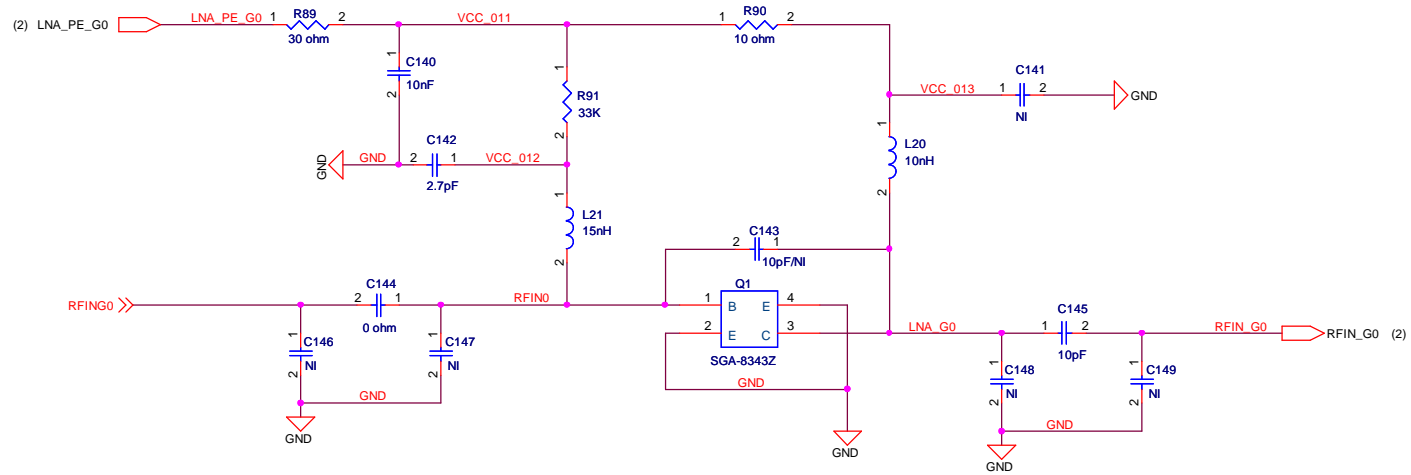
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DEPT:	R&D
Designed:	David Wei
Rev:	PB3.2
Checked:	
DATE:	2010.6.30
Approved:	
Sheet	3 of 13



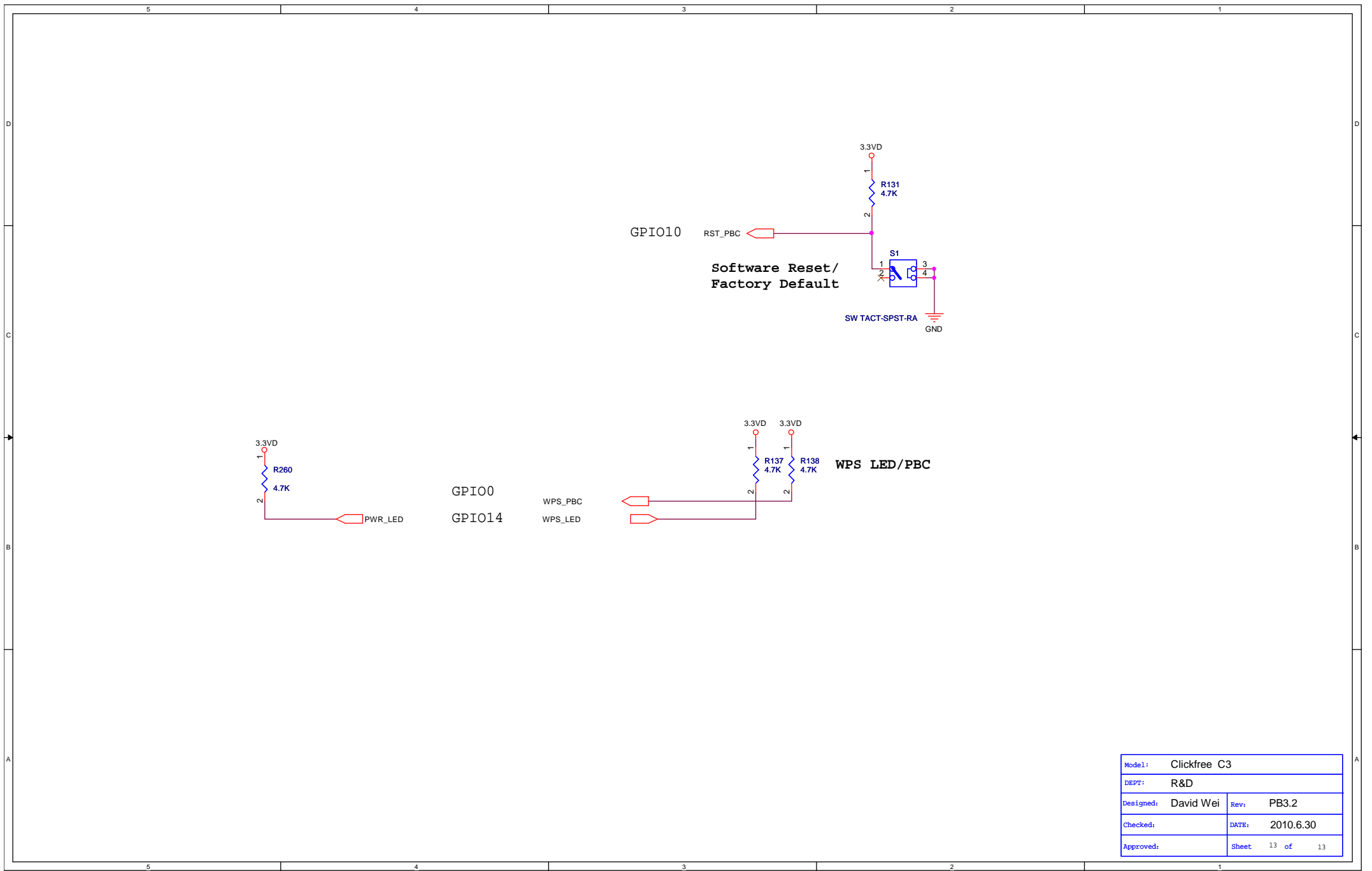
Model:	Clickfree C3		
DEPT:	R&D		
Designed:	David Wei	Rev:	PB3.2
Checked:		DATE:	2010.6.30
Approved:		Sheet	2 of 13



Model:	Clickfree C3
DEPT:	R&D
Designed:	David Wei
Rev:	PB3.2
Checked:	DATE: 2010.6.30
Approved:	Sheet 10 of 11



Model:	Clickfree C3		
DEPT:	R&D		
Designed:	David Wei	Rev:	PB3.2
Checked:		DATE:	2010.6.30
Approved:		Sheet	9 of 13



Model: Clickfree C3	
DEPT: R&D	
Designed: David Wei	Rev: PB3.2
Checked:	DATE: 2010.6.30
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- 1, 100 ohm differential impedance for TXP, TXN, RXP, & RXN
- 2, No vias for TXP, TXN, RXP, RXN, DP, & DM
- 3, Ground reference plane for DP, DM, TXP, TXN, RXP, & RXN
- 4, CLK trace width are $\geq 0.31mm$ ($\geq 12mil$)
- 5, Reference resistors are very close to ASIC. $\sim 8mm$ ($\sim 315mil$)
- 6, TXP, TXN, RXP, & RXN trace lengths should not exceed 25mm.
- 7, DP & DM trace lengths should not exceed 37mm. Prefer 20-30mm (90 ohm differential impedance)

