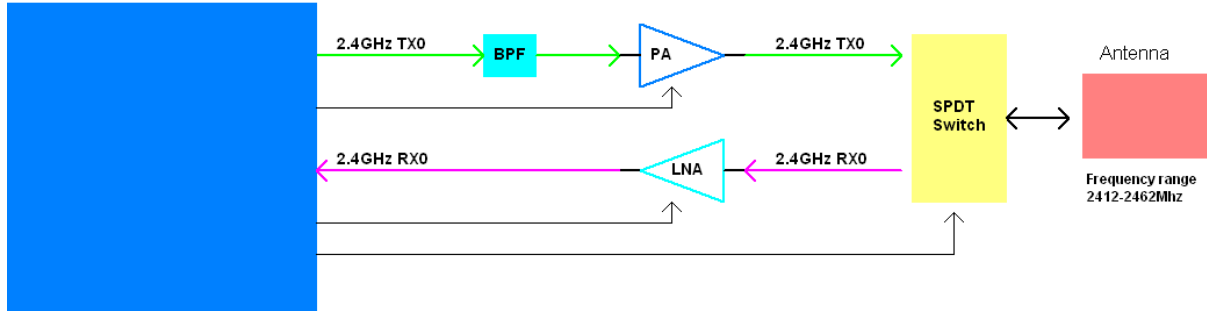
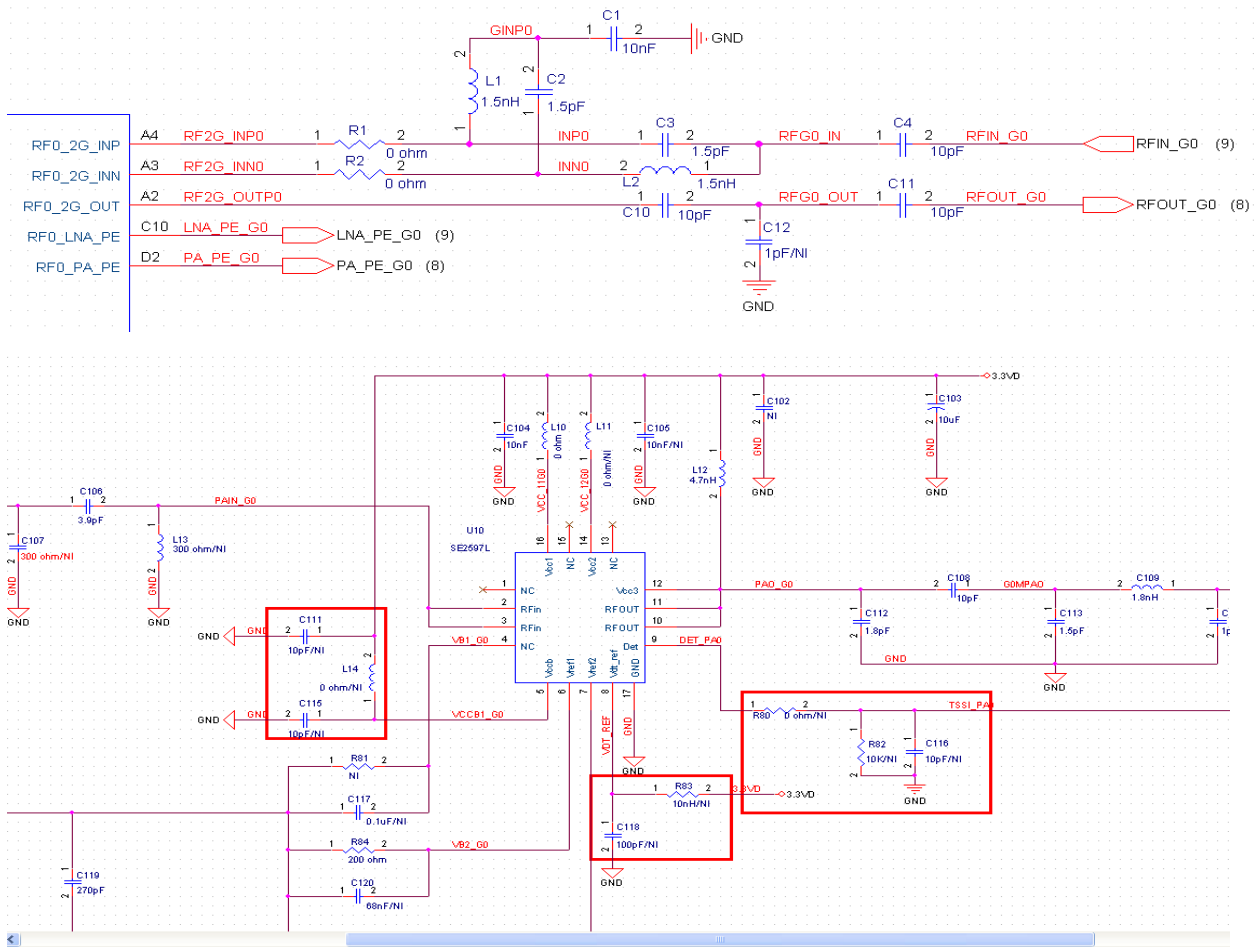


C3 Circuit Description

RT3052



RT3052 integrated wireless transceivers, 2.4G RF output TX0 signal (RF2G_OUTPO) produced by the A2 pin, the signal through the Band-Pass Filter (U9) and the Π -type matching network, output to the PA chip (U10 SE2597L). Signal amplified by the PA Resulting from MPAOG0 signal output to the SPDT Switch chip (GW2179). MPAOG0 from the power amplifier output signal, after GW2179 switched the output to PIN5, and then through the Π -type matching network output to the antennas. ANT_TRN and ANT_TRNB combination of the two signals is open fired on the control channel or turn on the receiver path. GW2179 second grounding pin.



Antenna through the I-PEX Terminal Button in the PCB board, through the matching network and Switch chip output pin is connected. RF signals through an antenna to complete the launch.

After receiving the signal from the antenna by GW2179 SWITCH, to the LNA (Q1,SGA-8343) amplified RFIN_GO converted by equalizer circuit differential signal (RF2G_INP0, RF2G_INNO) output to the RT3052, the completion of the process of receiving the signal.

