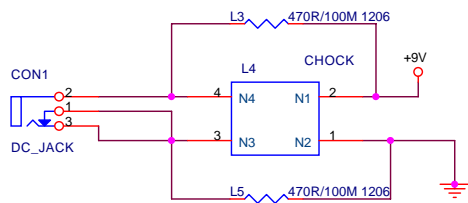
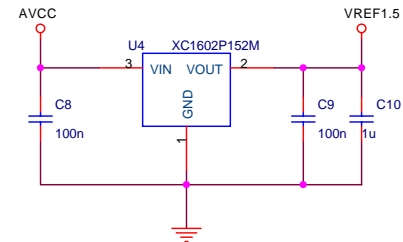
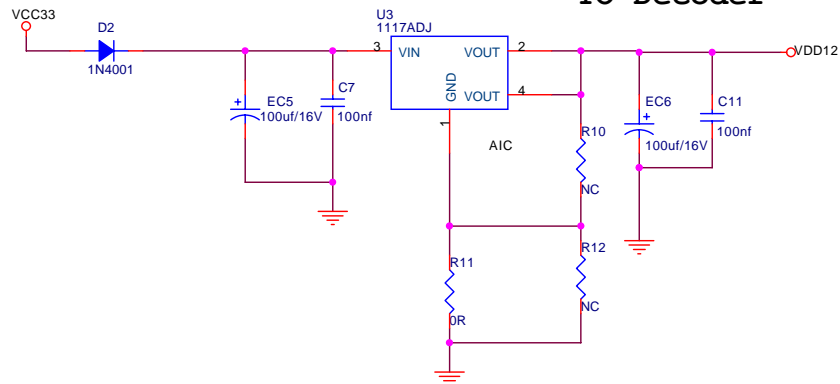
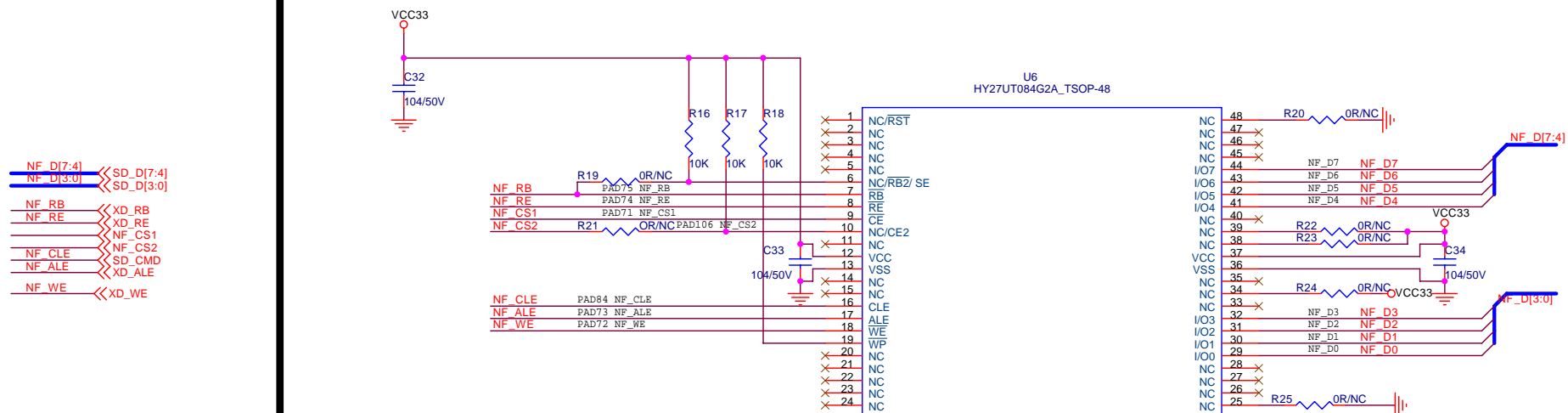
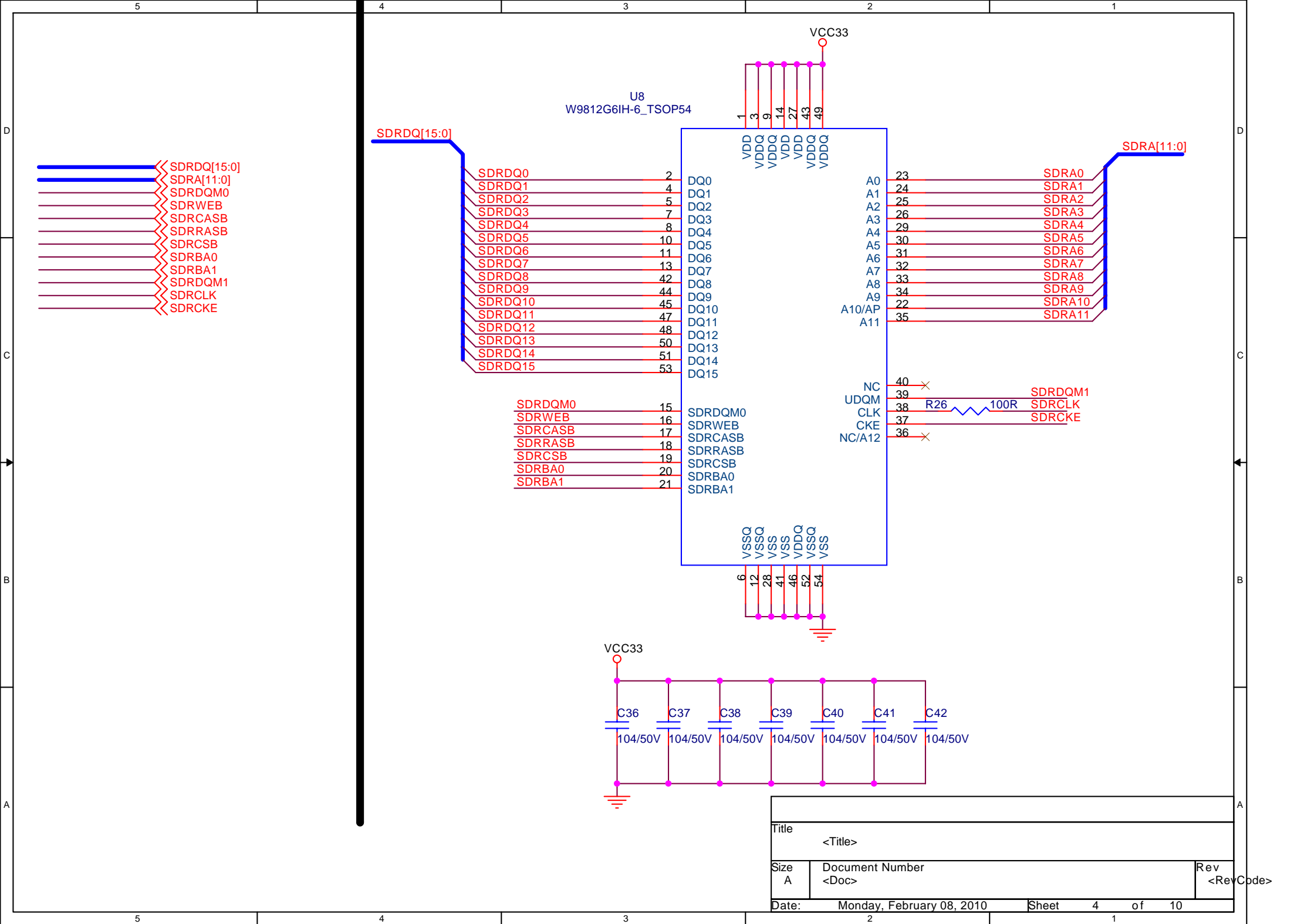


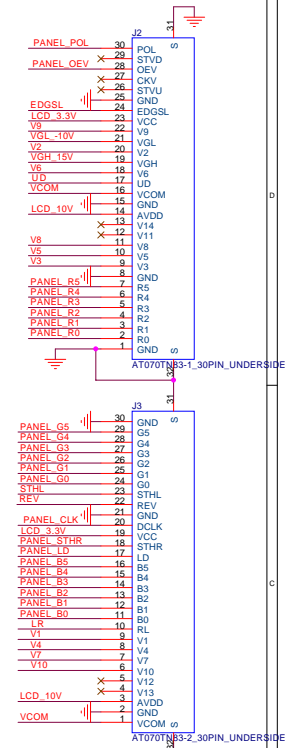
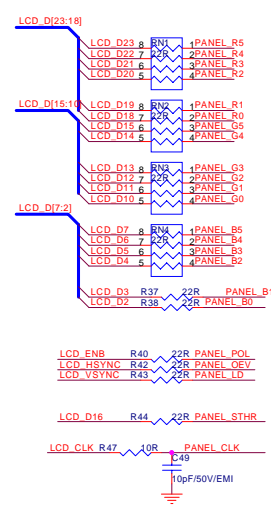
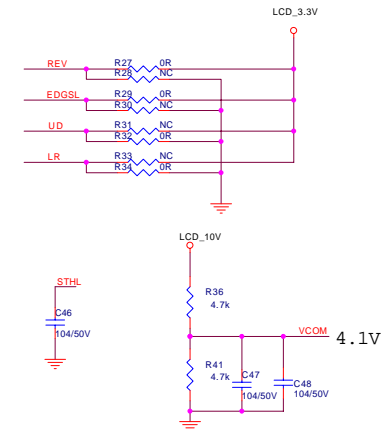
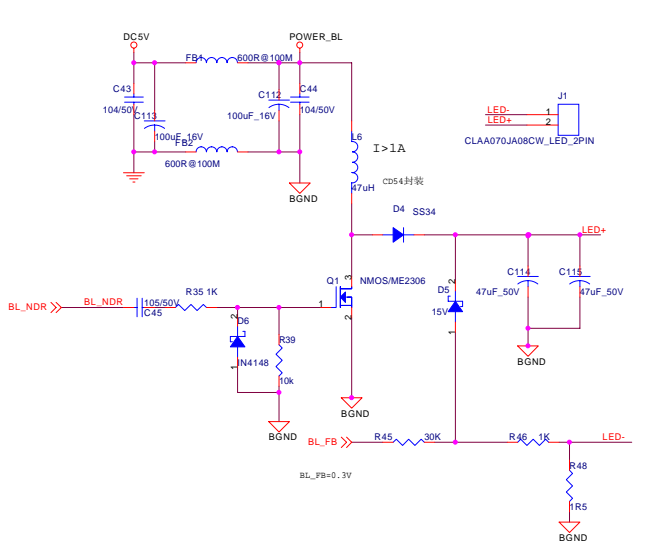
TO Decoder



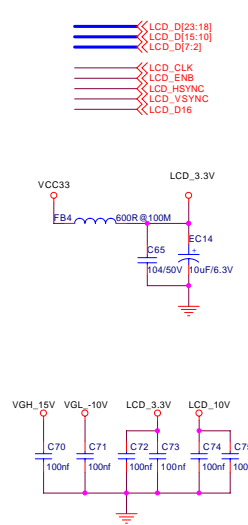
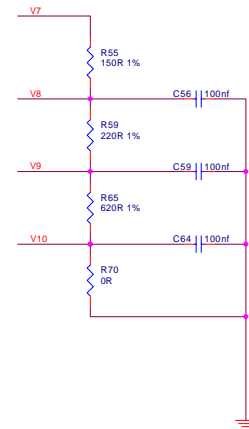
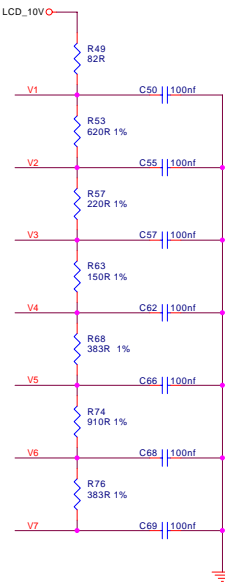
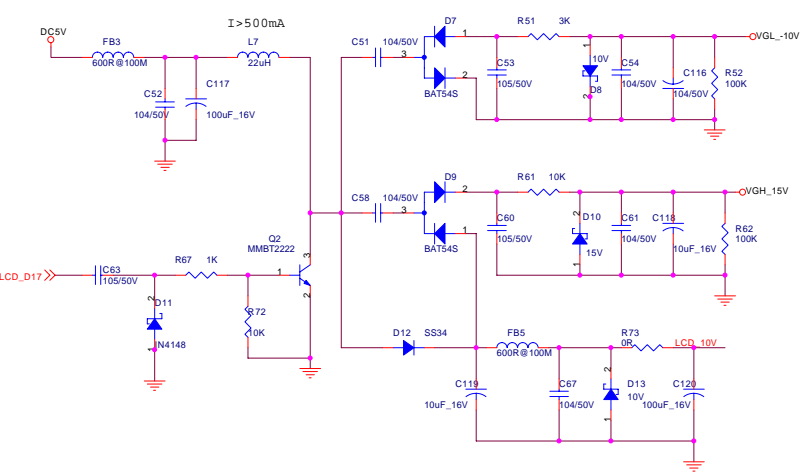
Title			<Title>
Size	Document Number	Rev	
B	<Doc>		
Date:	Saturday, March 06, 2010	Sheet	1 of 10



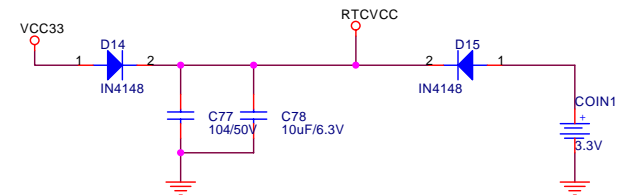
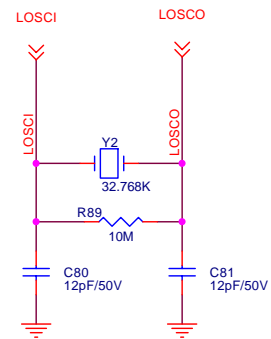
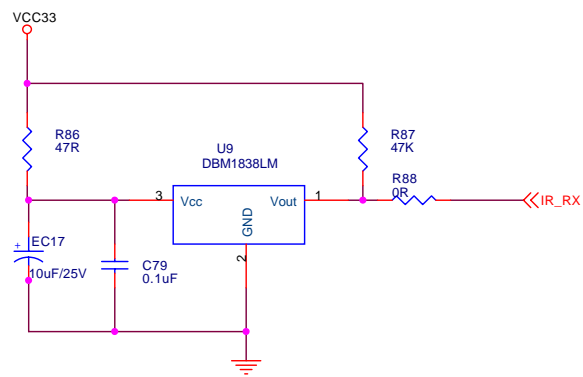
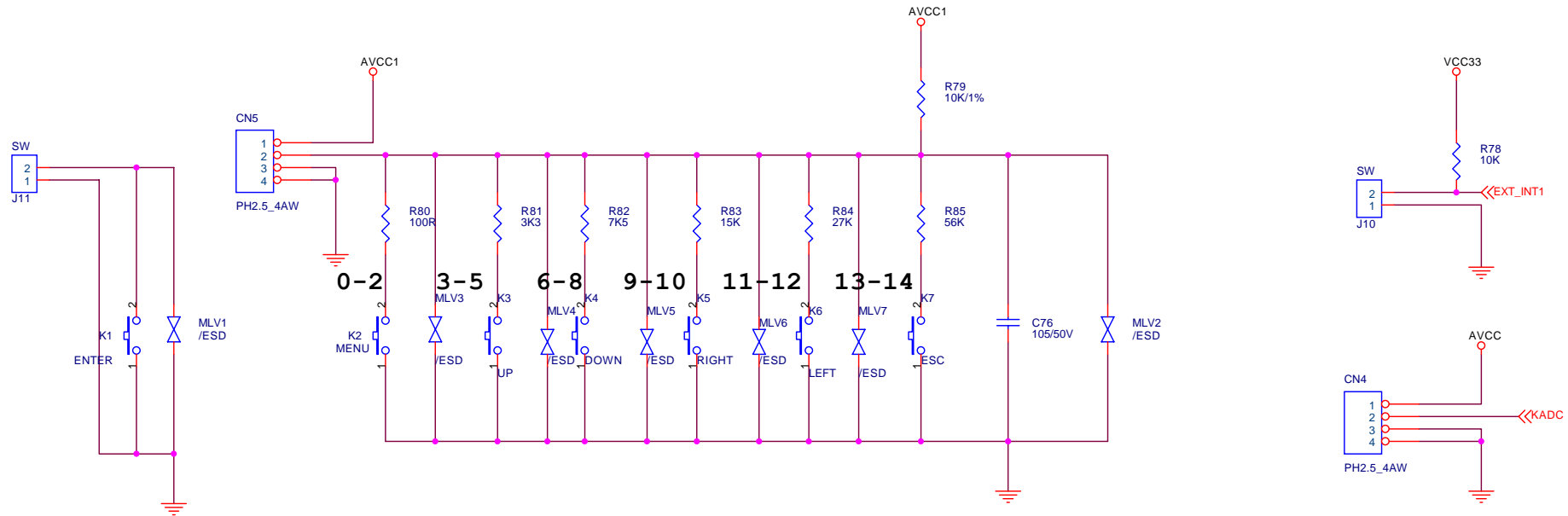




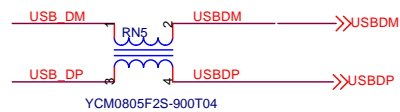
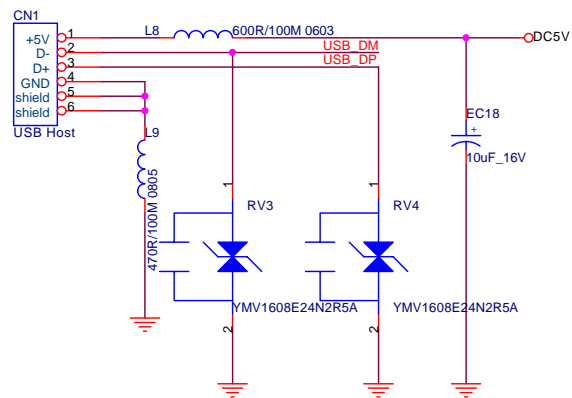
VGH/VGL



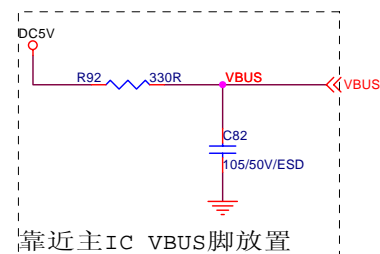
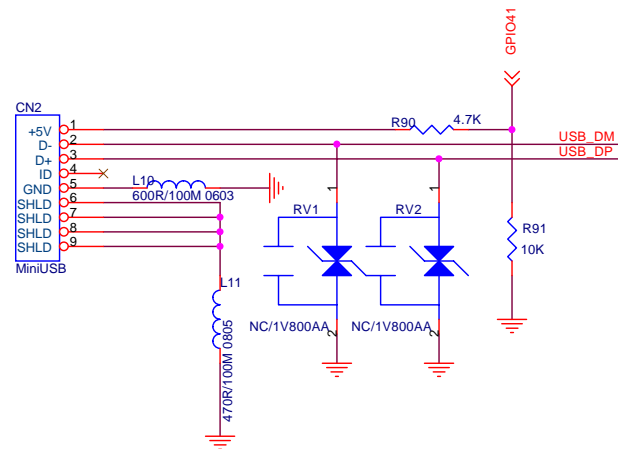
ADC&GPIO KEY



Title <Title>			
Size B	Document Number <Doc>	Rev	<Rev Code>
Date:	Monday, February 08, 2010	Sheet	6 of 10



USB_DM
USB_DP



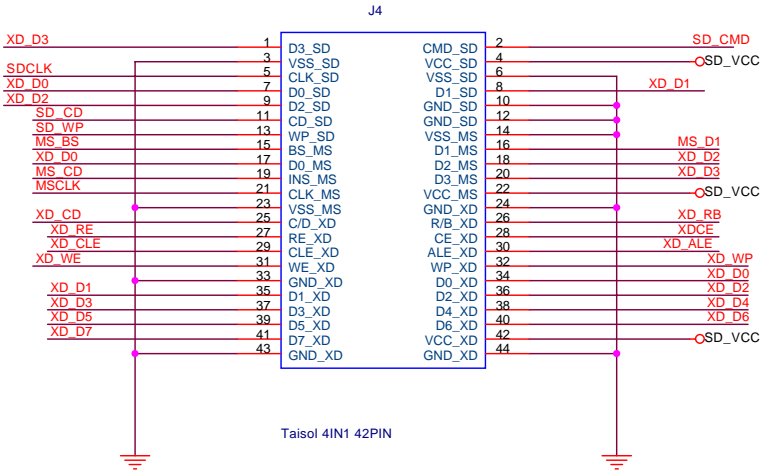
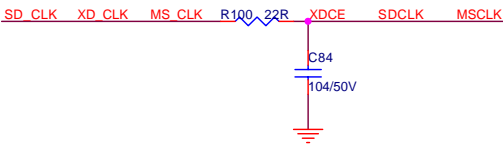
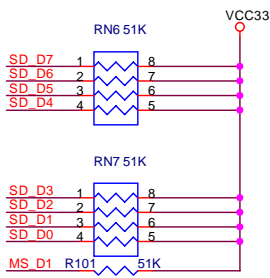
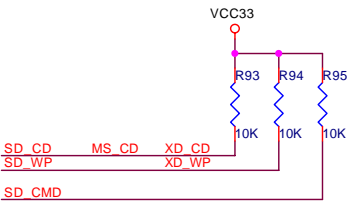
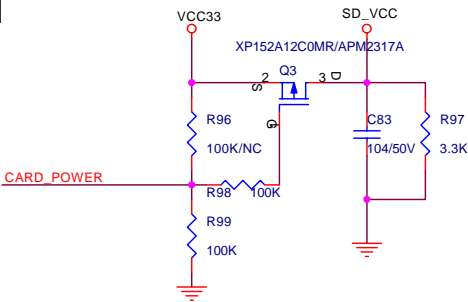
Title <Title>		
Size B	Document Number <Doc>	Rev <RevCode>
Date:	Monday, February 08, 2010	Sheet 7 of 10

SD/MMC/MS/XD 4IN1

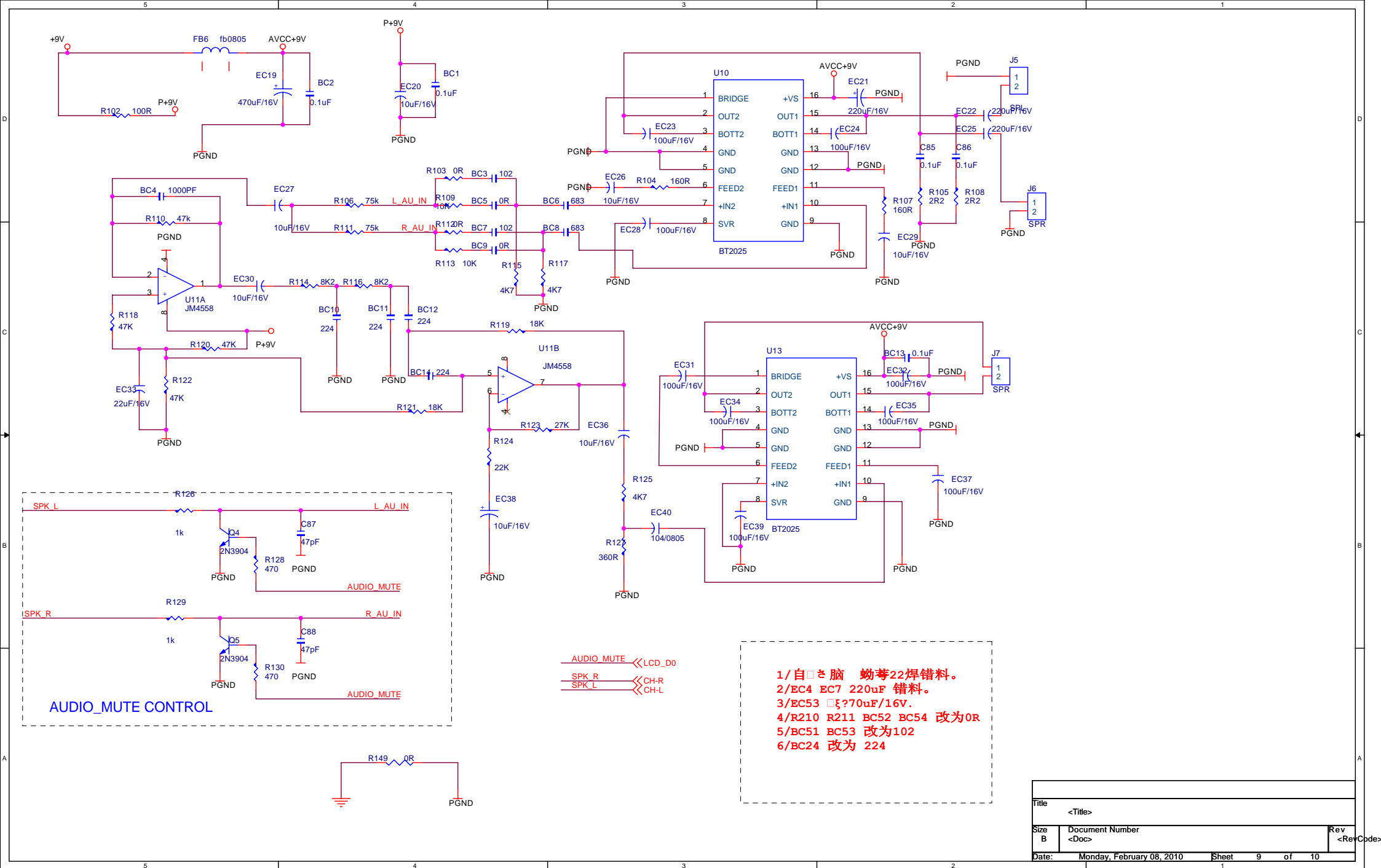
XD_D[3:0] << SD_D[3:0]
XD_D[7:4] << SD_D[7:4]

MS_D1 << MS_D1
SD_CLK MS_CLK XD_CE << SD_CLK
SD_CMD MS_BS XD_CLE << SD_CMD
SD_WP XD_WP << LCD_D1
SD_CD MS_CD XD_CD << EXT_INT0
XD_ALE << XD_ALE
XD_WE << XD_WE
XD_RB << XD_RB
XD_RE << XD_RE
CARD_POWER << P_TEST

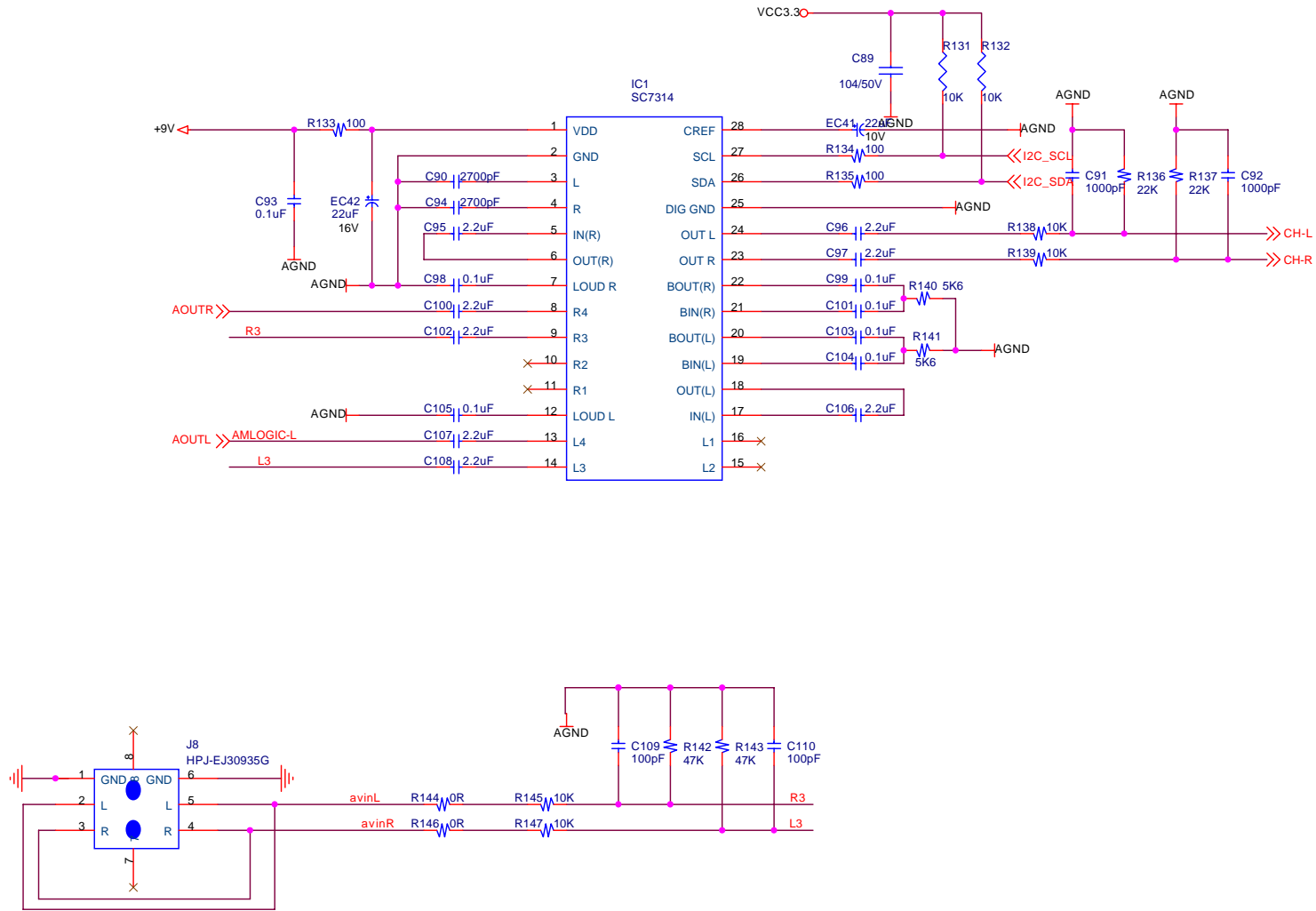
XD_D[3:0] XD_D0 XD_D1 XD_D2 XD_D3
XD_D4 XD_D5 XD_D6 XD_D7



Title			<Title>
Size	Document Number	Rev	<Rev>
B	<Doc>	Code	
Date:	Monday, February 08, 2010	Sheet	8 of 10



Title			<Title>
Size	Document Number	Rev	<RevCode>
B	<Doc>		
Date:	Monday, February 08, 2010	Sheet	9 of 10



Title			<Title>
Size	Document Number		Rev
B	<Doc>		<RevCode>
Date:	Monday, February 08, 2010		Sheet 10 of 10