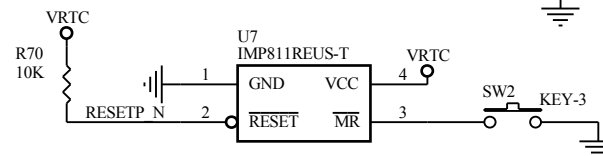
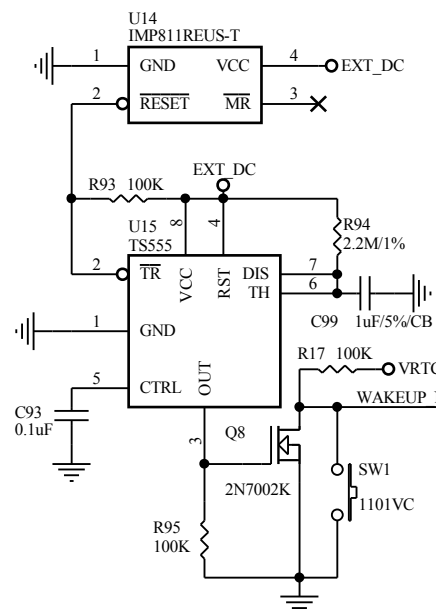
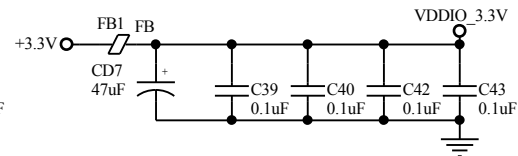
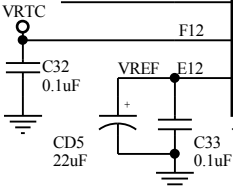
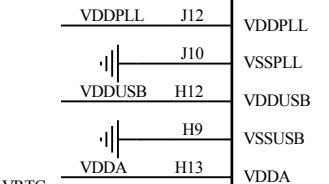
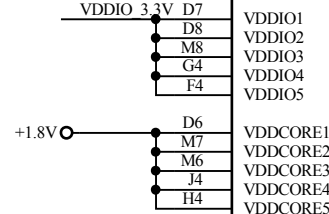
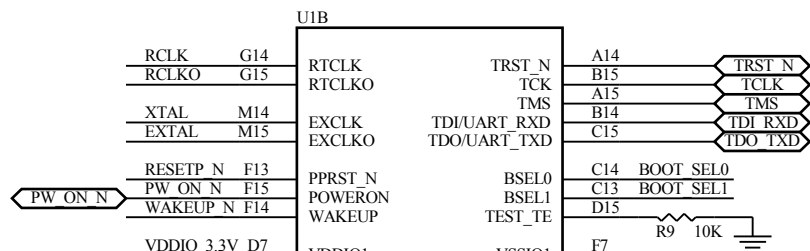
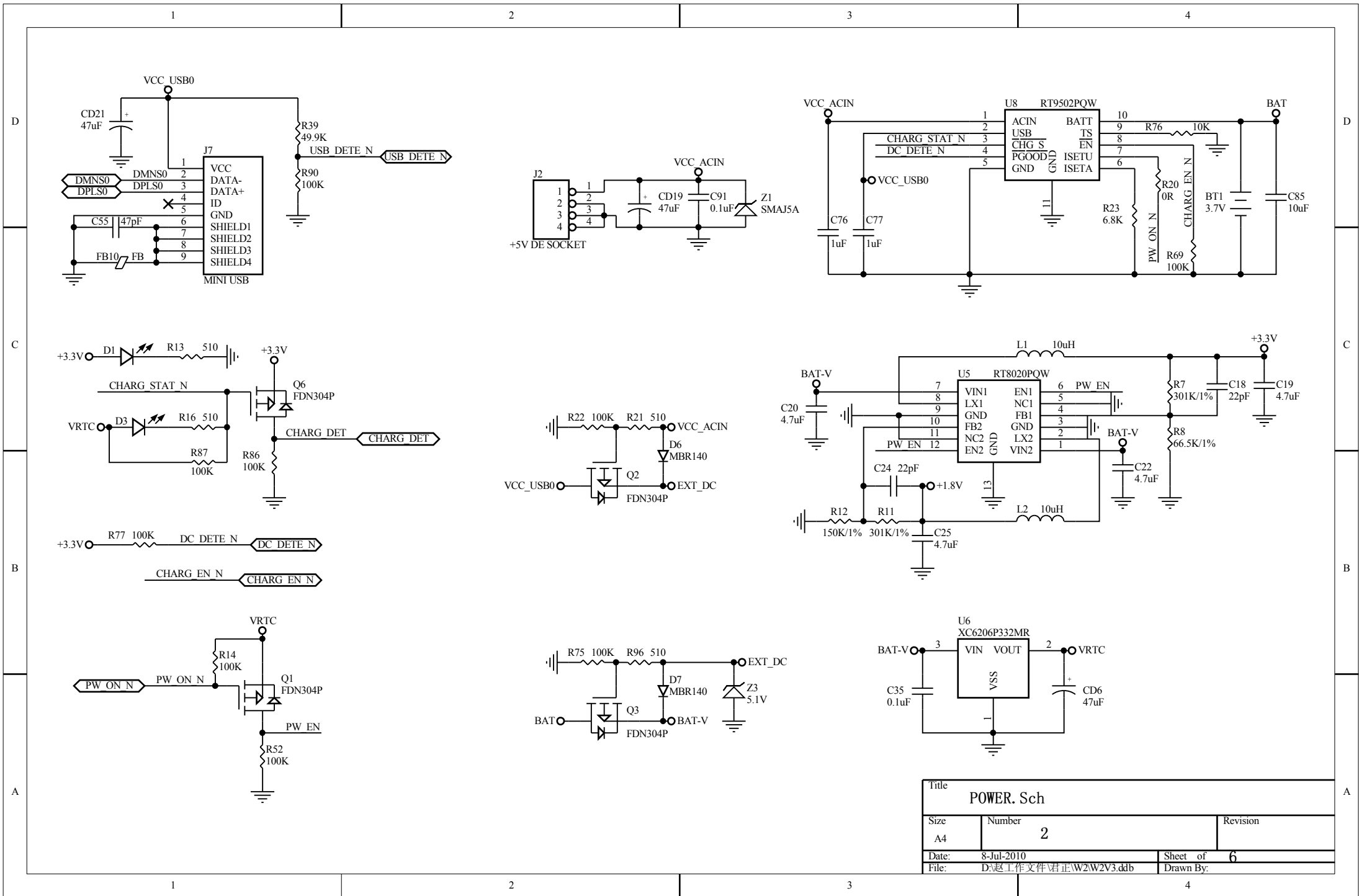


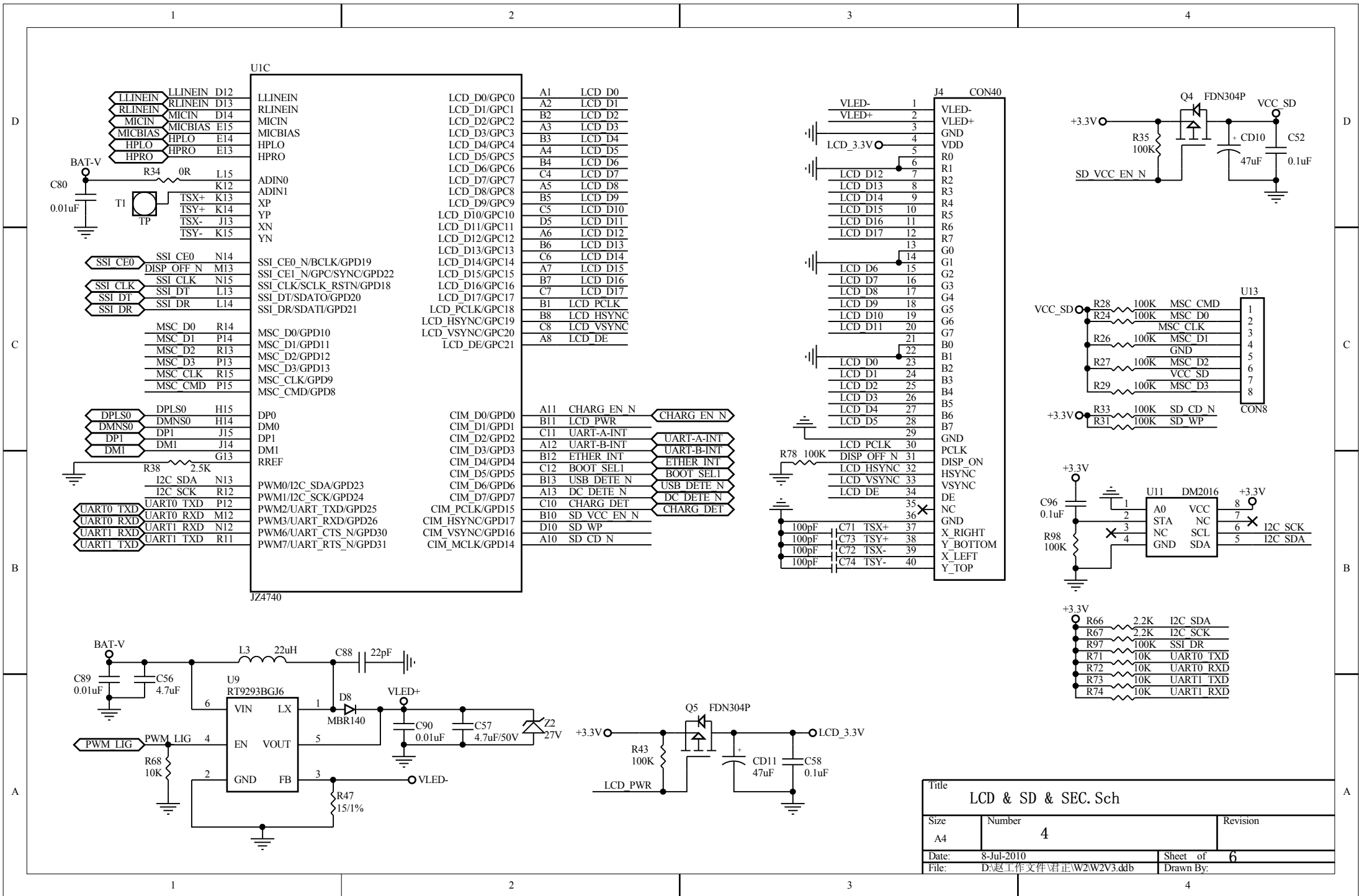
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D				D
C	<p>RESET & RTC & JTAG RESET & RTC & JTAG.Sch</p> <div> <input type="checkbox"/> TRST_N <input type="checkbox"/> TCLK <input type="checkbox"/> TMS <input type="checkbox"/> TDI_RXD <input type="checkbox"/> TDO_TXD <input type="checkbox"/> JTAG_RST <input type="checkbox"/> BOOT_SEL1 <input type="checkbox"/> PW_ON_N </div> <p>POWER POWER.Sch</p> <div> <input type="checkbox"/> CHARG_DET <input type="checkbox"/> DC_DETE_N <input type="checkbox"/> USB_DETE_N <input type="checkbox"/> DPLS0 <input type="checkbox"/> DMNS0 <input type="checkbox"/> PW_ON_N <input type="checkbox"/> CHARG_EN_N </div> <p>MEMORY & FLASH MEMORY & FLASH.Sch</p> <div> <input type="checkbox"/> PWM_LIG <input type="checkbox"/> PWM_IR <input type="checkbox"/> LCD_PS <input type="checkbox"/> LCD_REV <input type="checkbox"/> RF_RST_N <input type="checkbox"/> SHUTDOWN <input type="checkbox"/> WAIT_N <input type="checkbox"/> CS2_N <input type="checkbox"/> CS3_N <input type="checkbox"/> CS4_N <input type="checkbox"/> RD_N <input type="checkbox"/> WE_N <input type="checkbox"/> D[0..7] <input type="checkbox"/> A[0..6] </div>	<p>LCD & SD & SEC LCD & SD & SEC.Sch</p> <div> <input type="checkbox"/> ETHER_INT <input type="checkbox"/> DC_DETE_N <input type="checkbox"/> CHARG_DET <input type="checkbox"/> LLINEIN <input type="checkbox"/> RLINEIN <input type="checkbox"/> HPLO <input type="checkbox"/> HPRO <input type="checkbox"/> PWM_LIG <input type="checkbox"/> UART-A-INT <input type="checkbox"/> UART-B-INT <input type="checkbox"/> USB_DETE_N <input type="checkbox"/> BOOT_SEL1 <input type="checkbox"/> DPLS0 <input type="checkbox"/> DMNS0 <input type="checkbox"/> DP1 <input type="checkbox"/> DM1 <input type="checkbox"/> SSI_CE0 <input type="checkbox"/> SSI_CLK <input type="checkbox"/> SSI_DT <input type="checkbox"/> SSI_DR <input type="checkbox"/> UART0_TXD <input type="checkbox"/> MICIN <input type="checkbox"/> MICBIAS <input type="checkbox"/> UART1_TXD <input type="checkbox"/> UART0_RXD <input type="checkbox"/> UART1_RXD <input type="checkbox"/> CHARG_EN_N </div>	<p>AUDIO & IR & RF & COM AUDIO & IR & RF & COM.Sch</p> <div> <input type="checkbox"/> HPLO <input type="checkbox"/> HPRO <input type="checkbox"/> PWM_IR <input type="checkbox"/> SHUTDOWN <input type="checkbox"/> UART1_TXD <input type="checkbox"/> UART1_RXD <input type="checkbox"/> RF_RST_N <input type="checkbox"/> UART0_TXD <input type="checkbox"/> UART0_RXD <input type="checkbox"/> TDI_RXD <input type="checkbox"/> TDO_TXD <input type="checkbox"/> TRST_N <input type="checkbox"/> TMS <input type="checkbox"/> TCLK <input type="checkbox"/> JTAG_RST <input type="checkbox"/> DP1 <input type="checkbox"/> DM1 <input type="checkbox"/> MICIN <input type="checkbox"/> MICBIAS <input type="checkbox"/> SSI_CE0 <input type="checkbox"/> SSI_CLK <input type="checkbox"/> SSI_DT <input type="checkbox"/> SSI_DR <input type="checkbox"/> LCD_PS <input type="checkbox"/> LCD_REV </div>	
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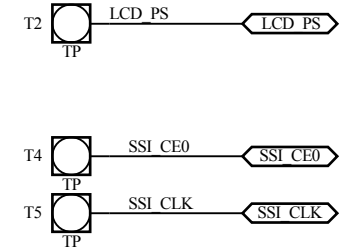
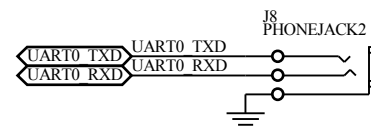
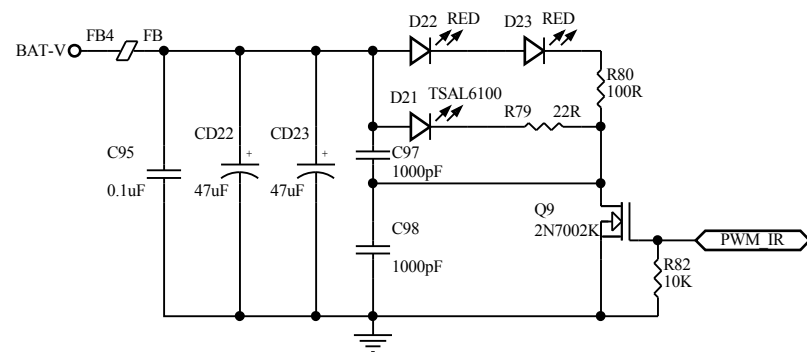
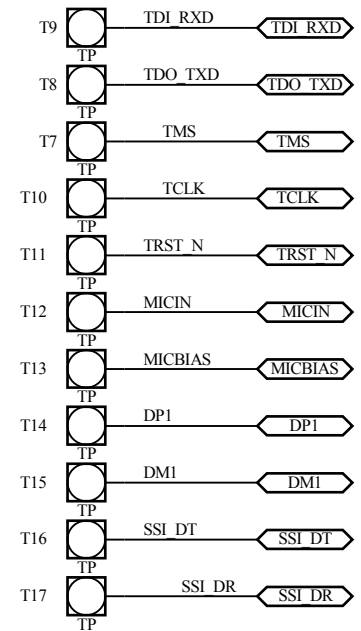
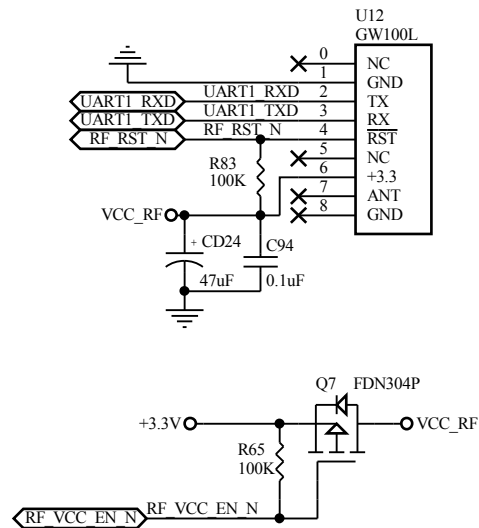
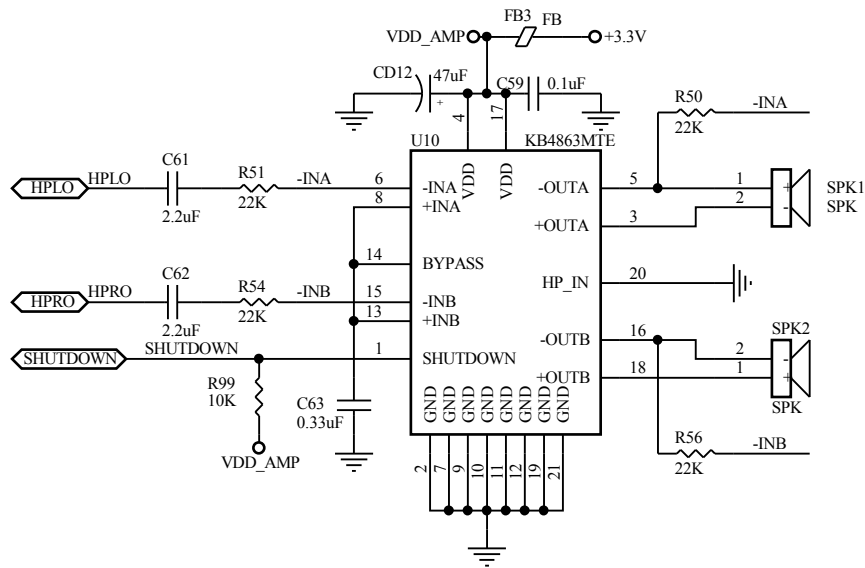
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AUDIO & IR & RF & COM. Sch		
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