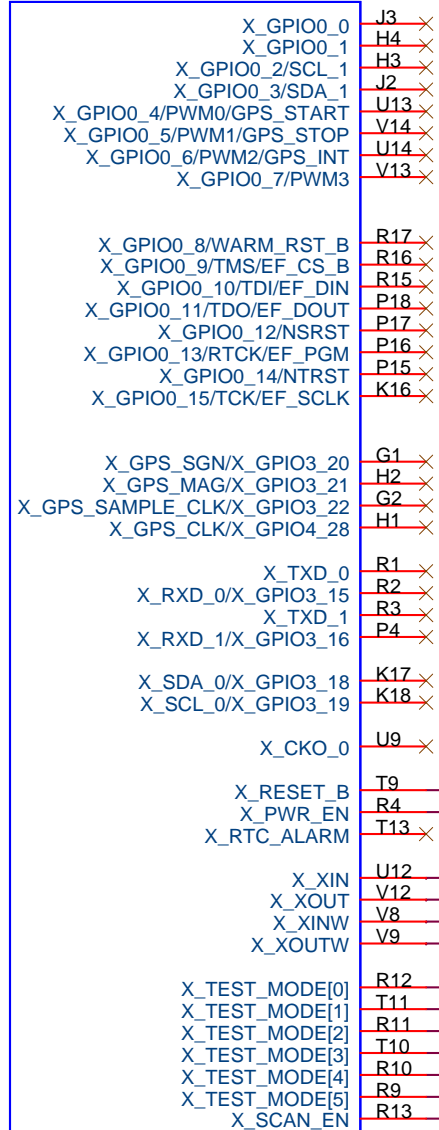
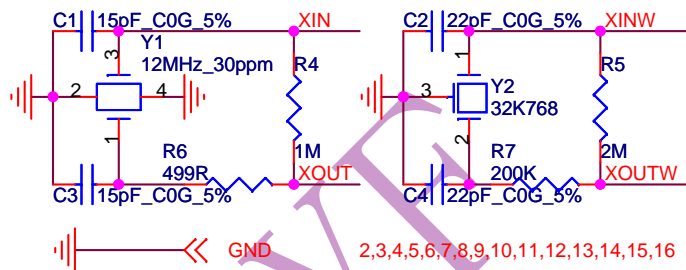


U1A

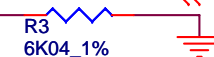
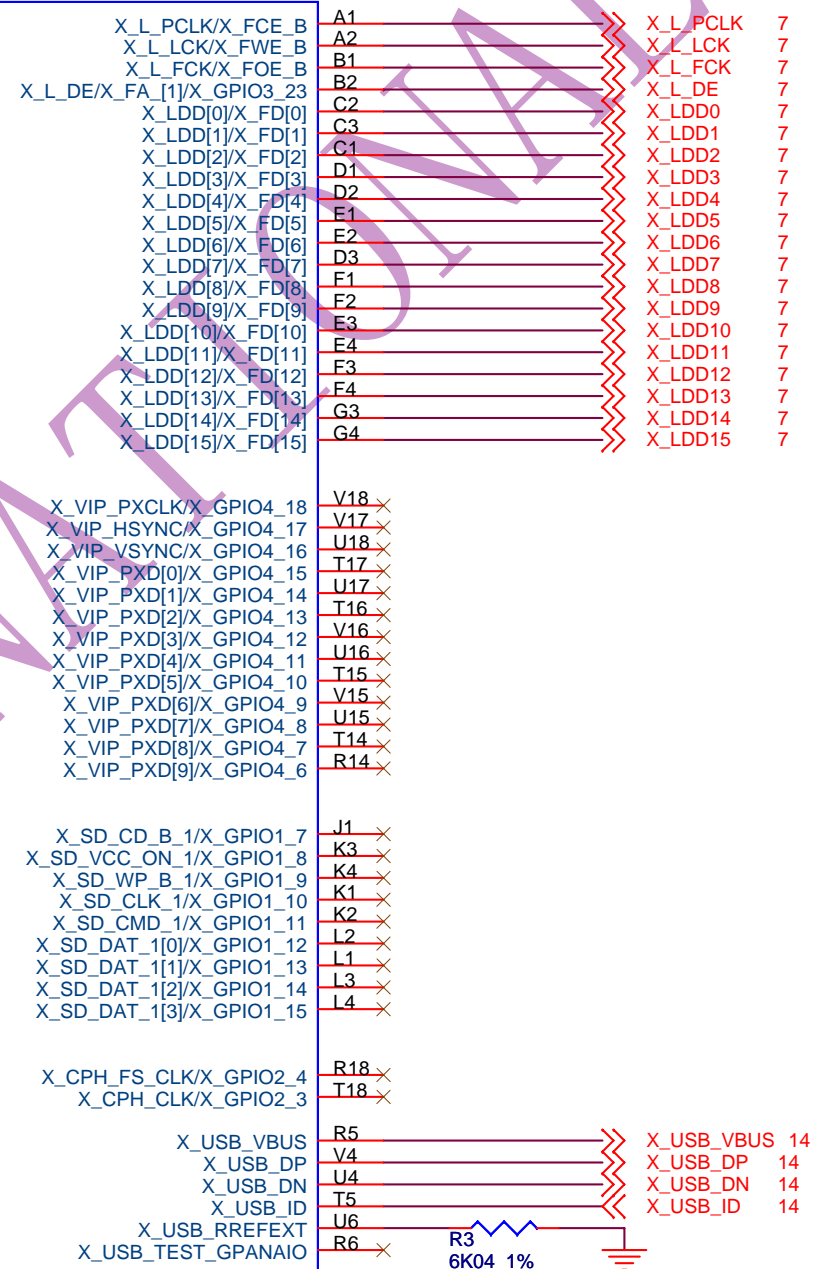


X_RESET_B 15
X_PWR_EN 5,15

X_TEST_MODE0 5
X_TEST_MODE1 2,3,4,5,6,7,8,9,10,11,12,13,14,15,16
X_TEST_MODE2 5
X_TEST_MODE3 5
X_TEST_MODE4 2,3,4,5,6,7,8,9,10,11,12,13,14,15,16
X_TEST_MODE5 2,3,4,5,6,7,8,9,10,11,12,13,14,15,16



U1B



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Title

YF Atlas-IV 标准平台 (PA05)

Size

Document Number

CPU(1)

Rev

V01

Date:

Friday, September 04, 2009

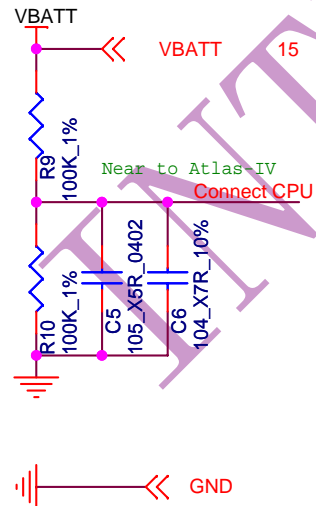
Sheet

1

of 17

U1C

X_MCS_B[0]	H18	X_MCS0	4
X_MCLK_O	A11	X_MCLK_O	4
X_MCLKB_O	B11	X_MCLKB_O	4
X_MCKE	J15	X_MCKE	4
X_MRAS_B	J16	X_MRAS	4
X_MCAS_B	J17	X_MCAS	4
X_MWE_B	J18	X_MWE	4
X_M_BA[0]	C10	X_MBA0	4
X_M_BA[1]	C11	X_MBA1	4
X_MA[0]	A7	X_MA0	4
X_MA[1]	B8	X_MA1	4
X_MA[2]	B7	X_MA2	4
X_MA[3]	A8	X_MA3	4
X_MA[4]	C9	X_MA4	4
X_MA[5]	C8	X_MA5	4
X_MA[6]	B10	X_MA6	4
X_MA[7]	D8	X_MA7	4
X_MA[8]	B9	X_MA8	4
X_MA[9]	A10	X_MA9	4
X_MA[10]	D9	X_MA10	4
X_MA[11]	D10	X_MA11	4
X_MA[12]	A9	X_MA12	4
X_MD[0]	B12	X_MD0	4
X_MD[1]	D14	X_MD1	4
X_MD[2]	C12	X_MD2	4
X_MD[3]	C13	X_MD3	4
X_MD[4]	D12	X_MD4	4
X_MD[5]	D13	X_MD5	4
X_MD[6]	A12	X_MD6	4
X_MD[7]	A13	X_MD7	4
X_MD[8]	A14	X_MD8	4
X_MD[9]	C15	X_MD9	4
X_MD[10]	B14	X_MD10	4
X_MD[11]	A15	X_MD11	4
X_MD[12]	B15	X_MD12	4
X_MD[13]	A16	X_MD13	4
X_MD[14]	C16	X_MD14	4
X_MD[15]	D16	X_MD15	4
X_MD[16]	B17	X_MD16	4
X_MD[17]	B18	X_MD17	4
X_MD[18]	C18	X_MD18	4
X_MD[19]	C17	X_MD19	4
X_MD[20]	D18	X_MD20	4
X_MD[21]	D17	X_MD21	4
X_MD[22]	E16	X_MD22	4
X_MD[23]	E17	X_MD23	4
X_MD[24]	E18	X_MD24	4
X_MD[25]	G17	X_MD25	4
X_MD[26]	G16	X_MD26	4
X_MD[27]	F15	X_MD27	4
X_MD[28]	F17	X_MD28	4
X_MD[29]	G18	X_MD29	4
X_MD[30]	H17	X_MD30	4
X_MD[31]	H16	X_MD31	4
X_MDQM[0]	C14	X_MDQM0	4
X_MDQM[1]	B16	X_MDQM1	4
X_MDQM[2]	F16	X_MDQM2	4
X_MDQM[3]	H15	X_MDQM3	4
X_MDQS[0]	B13	X_MDQS0	4
X_MDQS[1]	D15	X_MDQS1	4
X_MDQS[2]	E18	X_MDQS2	4
X_MDQS[3]	G15	X_MDQS3	4



U1D

X_DF_CS_B[0]	A3	X_FCE_B0	5
X_DF_CS_B[1]	B3		
X_DF_ALE/X_SD_CMD0	B5	X_DF_ALE	5
X_DF_CLE/X_SD_CLK0	A5	X_DF_CLE	5
X_DF_RE_B/X_SD_CMD2	C4	X_DF_RE_B	5
X_DF_WE_B/X_SD_CLK2	A4	X_DF_WE_B	5
X_DF_RY_BY	B4	X_DF_RY_BY	5
X_DF_WP_B/X_SD_VCC_ON0	D4	X_DF_WP_B	5
X_DF_AD[0]/X_SD_DAT_0_2[0]	D5	X_DF_AD0	5
X_DF_AD[1]/X_SD_DAT_0_2[1]	C5	X_DF_AD1	5
X_DF_AD[2]/X_SD_DAT_0_2[2]	D6	X_DF_AD2	5
X_DF_AD[3]/X_SD_DAT_0_2[3]	A6	X_DF_AD3	5
X_DF_AD[4]/X_SD_DAT_0_2[4]	C6	X_DF_AD4	5
X_DF_AD[5]/X_SD_DAT_0_2[5]	B6	X_DF_AD5	5
X_DF_AD[6]/X_SD_DAT_0_2[6]	D7	X_DF_AD6	5
X_DF_AD[7]/X_SD_DAT_0_2[7]	C7	X_DF_AD7	5
X_SD_CLK_3/X_GPIO1_2	M1		
X_SD_CMD_3/X_GPIO1_3	M2		
X_SD_DAT_3[0]/X_SPI_EN_0/X_GPIO1_28	M3		
X_SD_DAT_3[1]/X_SPI_CLK_0/X_GPIO1_29	M4		
X_SD_DAT_3[2]/X_SPI_DIN_0/X_GPIO1_0	N2		
X_SD_DAT_3[3]/X_SPI_DOUT_0/X_GPIO1_1	N3		
X_CK0_1/I2S_MCLK/X_GPIO1_20	N1		
X_AC97_BIT_CLK/I2S_BCLK	P1		
X_AC97_SYNC/I2S_LRCLK	P3		
X_AC97_DIN/I2S_DIN	P2		
X_AC97_DOUT/I2S_DOUT[0]	N4		
X_USCLK_0/I2S_EXTCLK/X_GPIO0_18	V1		
X_UTXD_0/X_GPIO0_19	T1		
X_URXD_0/X_GPIO0_20	U1		
X_UTFS_0/USB1_UTMI_DRVVBUS/X_GPIO0_21	U2		
X_URFS_0/I2S_DOUT[1]/X_GPIO0_22/RTS_0	V2		
X_USCLK_1/X_GPIO0_23	V3		
X_UTXD_1/X_GPIO0_24	U3		
X_URXD_1/X_GPIO0_25	T2		
X_UTFS_1/X_GPIO0_26	T3		
X_URFS_1/I2S_DOUT[2]/X_GPIO0_27/CTS_0	T4		
X_AUX0	L16		
X_AUX1	N16		
X_AUX2	M15		
X_XN	N18	X_XN	7
X_XP	N17	X_XP	7
X_YN	L18	X_YN	7
X_YP	L17	X_YP	7

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Title

YF Atlas-IV 标准平台 (PA05)

Size

Document Number

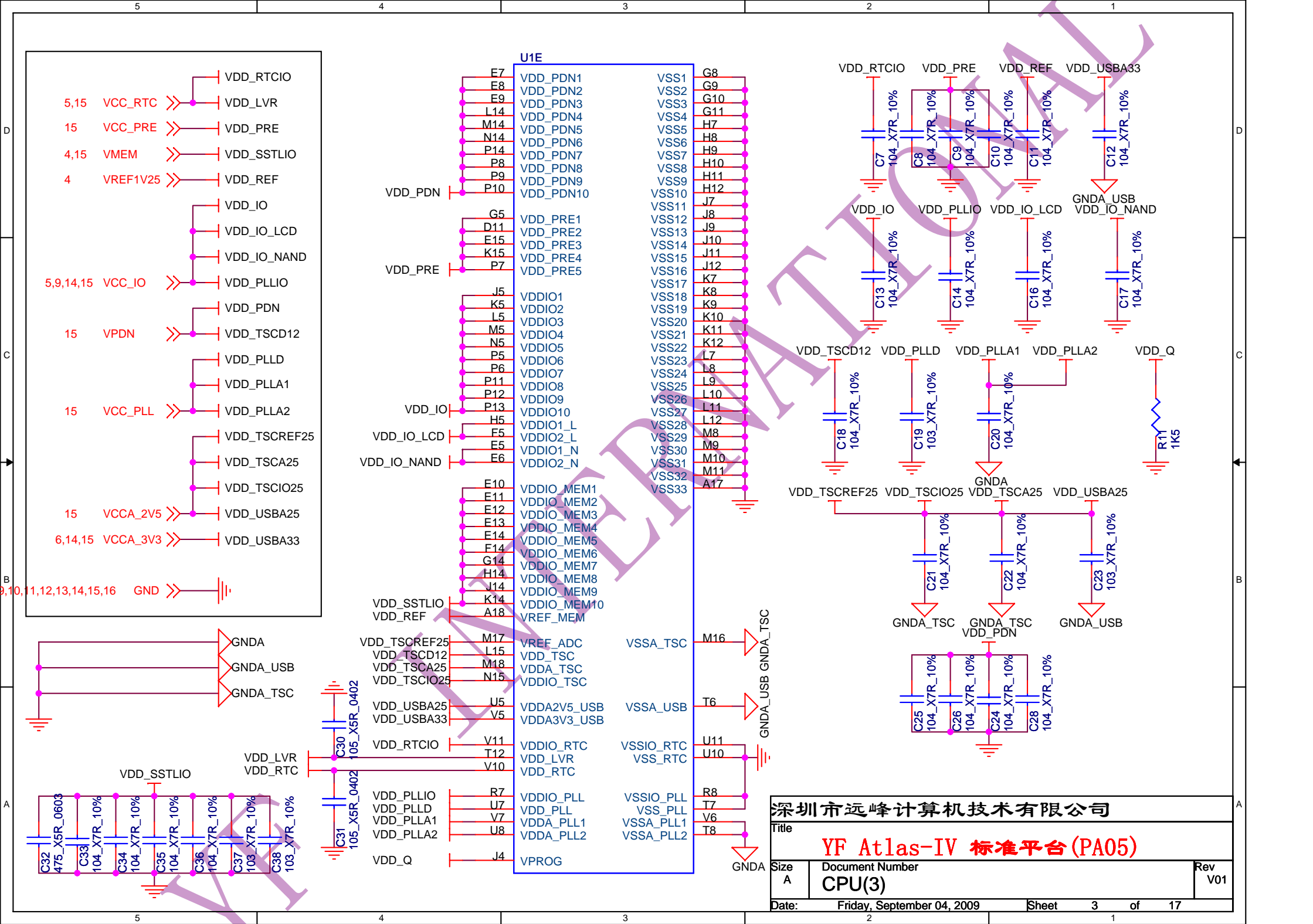
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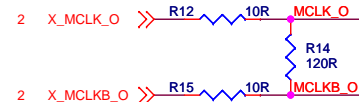
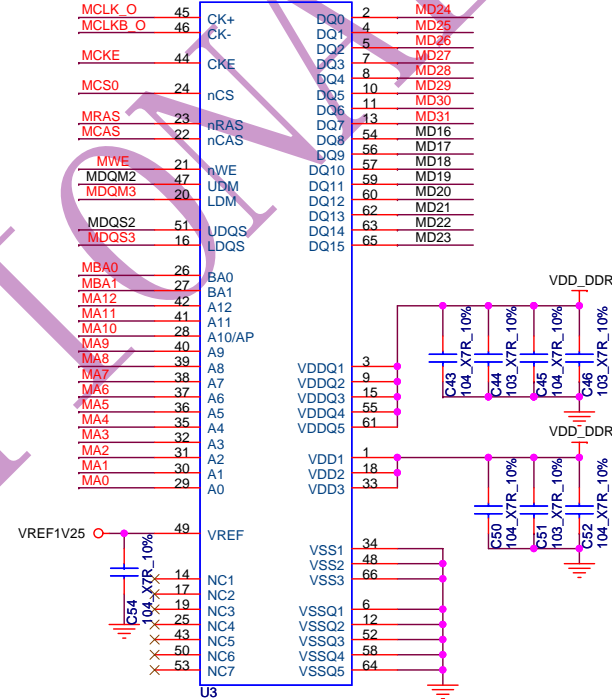
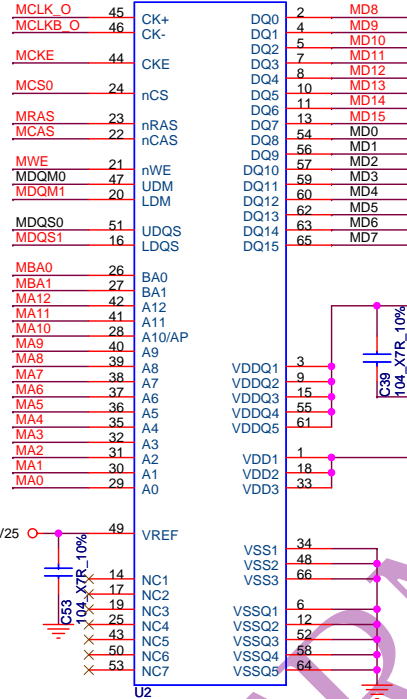
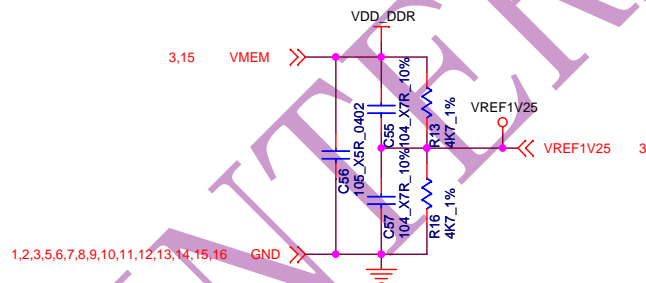
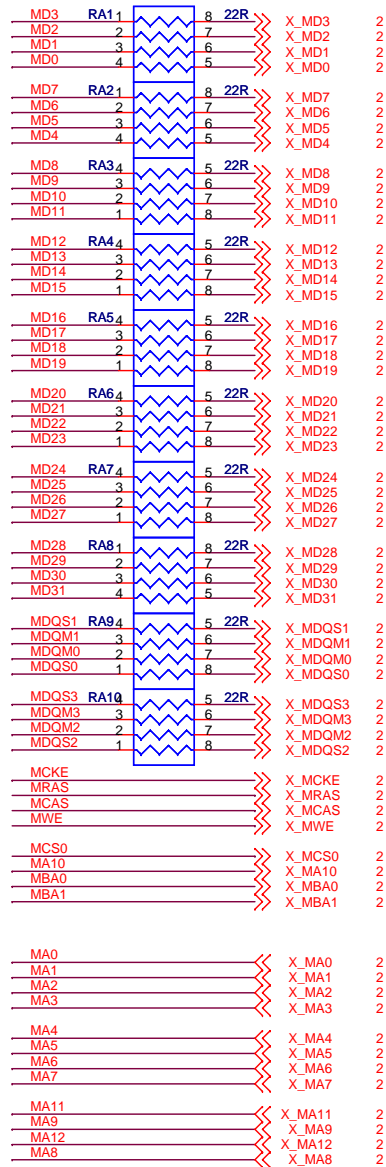
CPU(2)

V01

Date: Friday, September 04, 2009

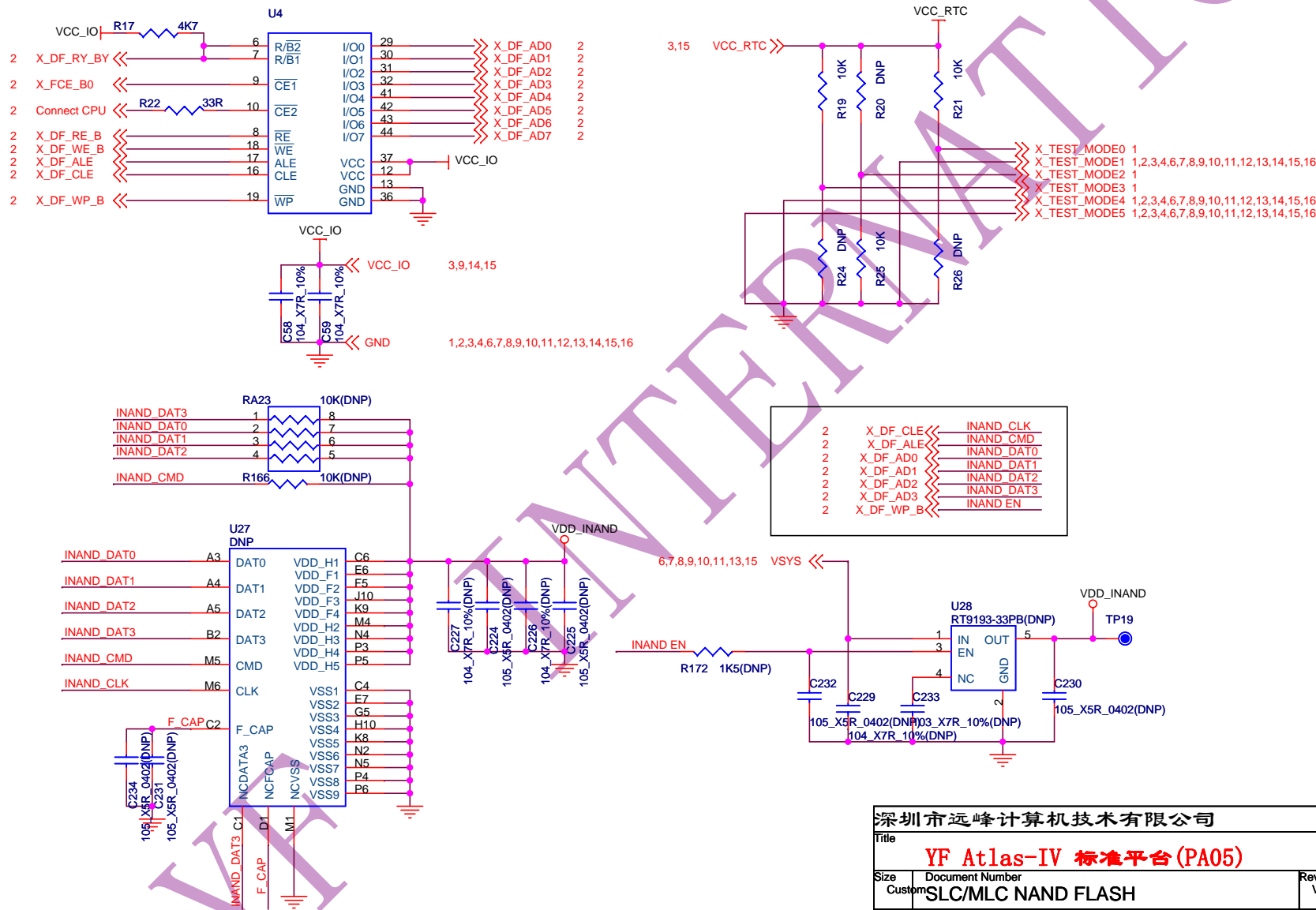
Sheet 2 of 17

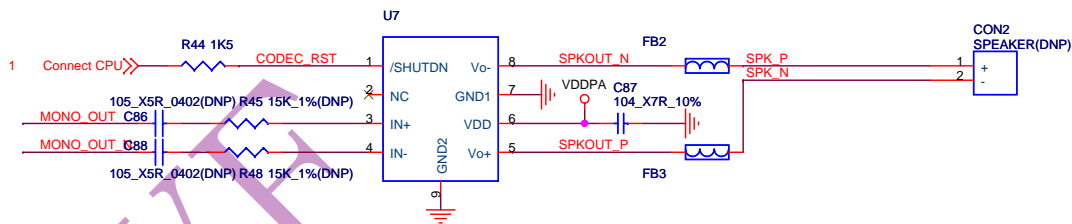
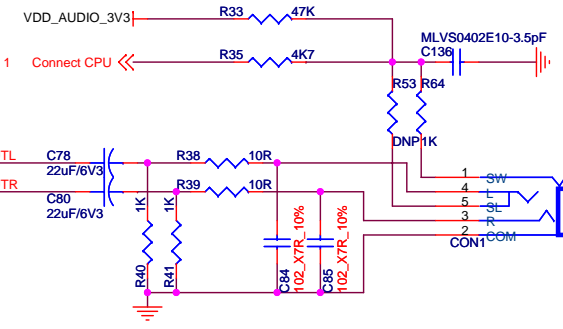
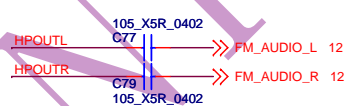
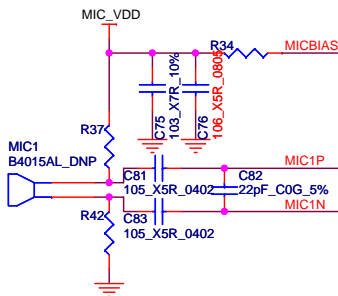
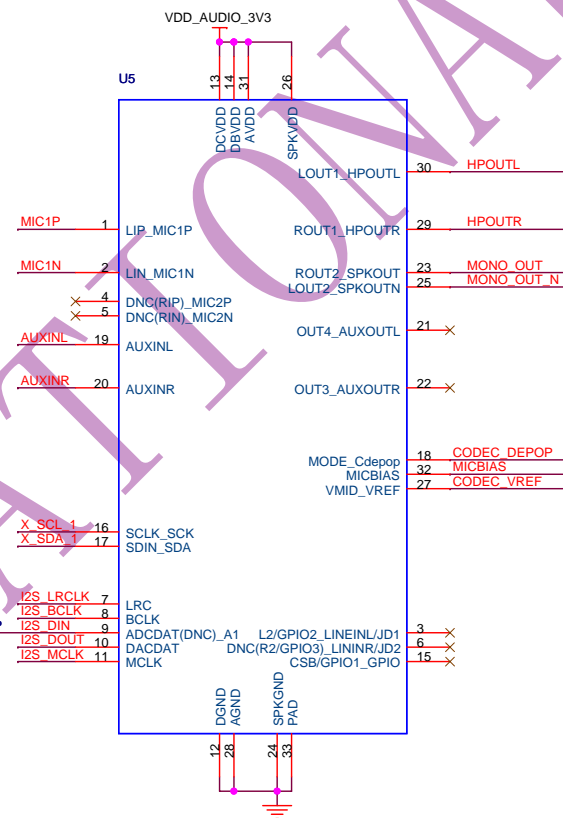
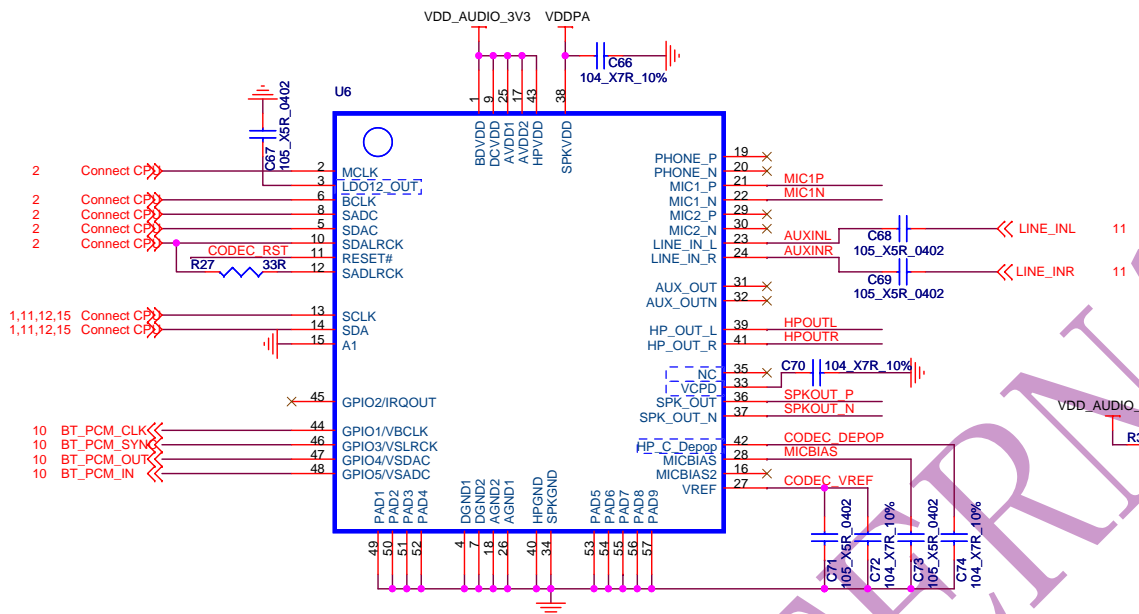
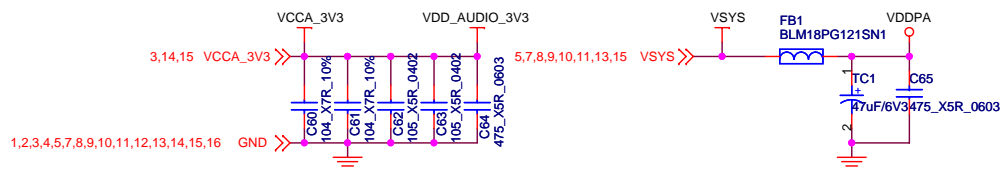


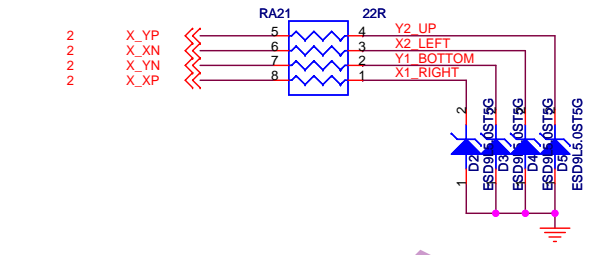
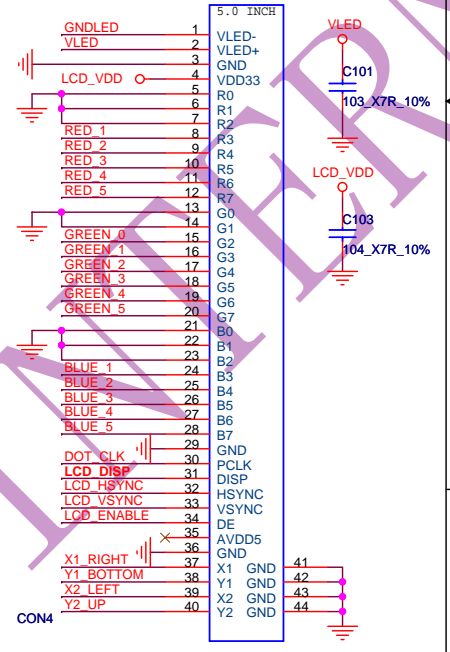
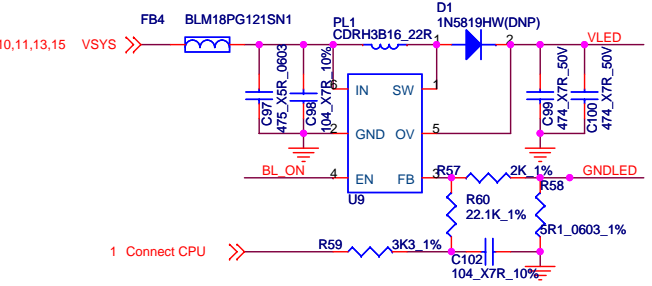
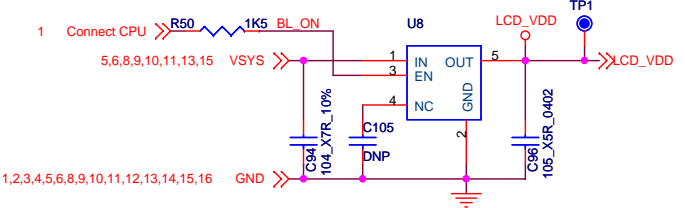
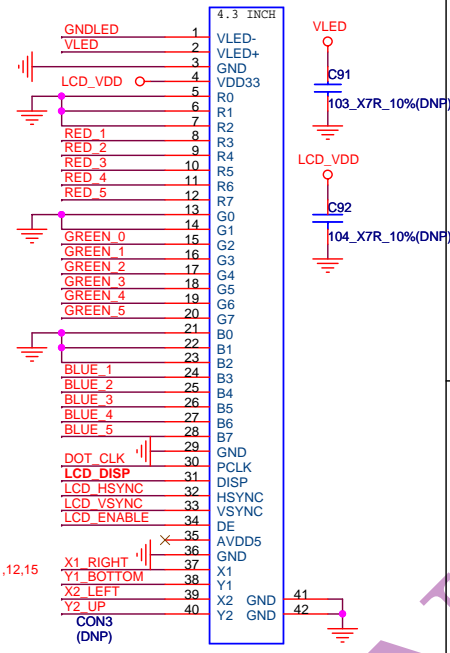
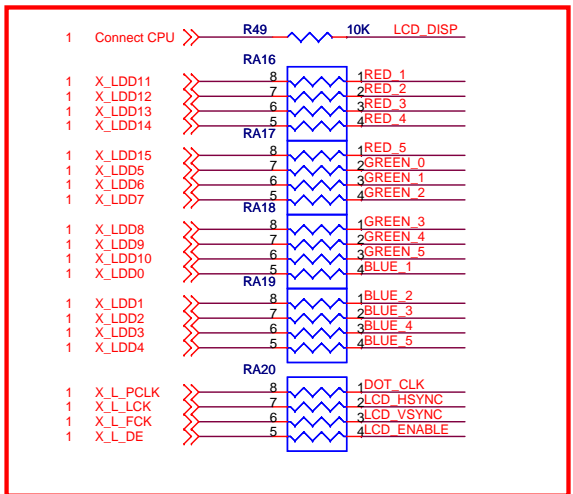


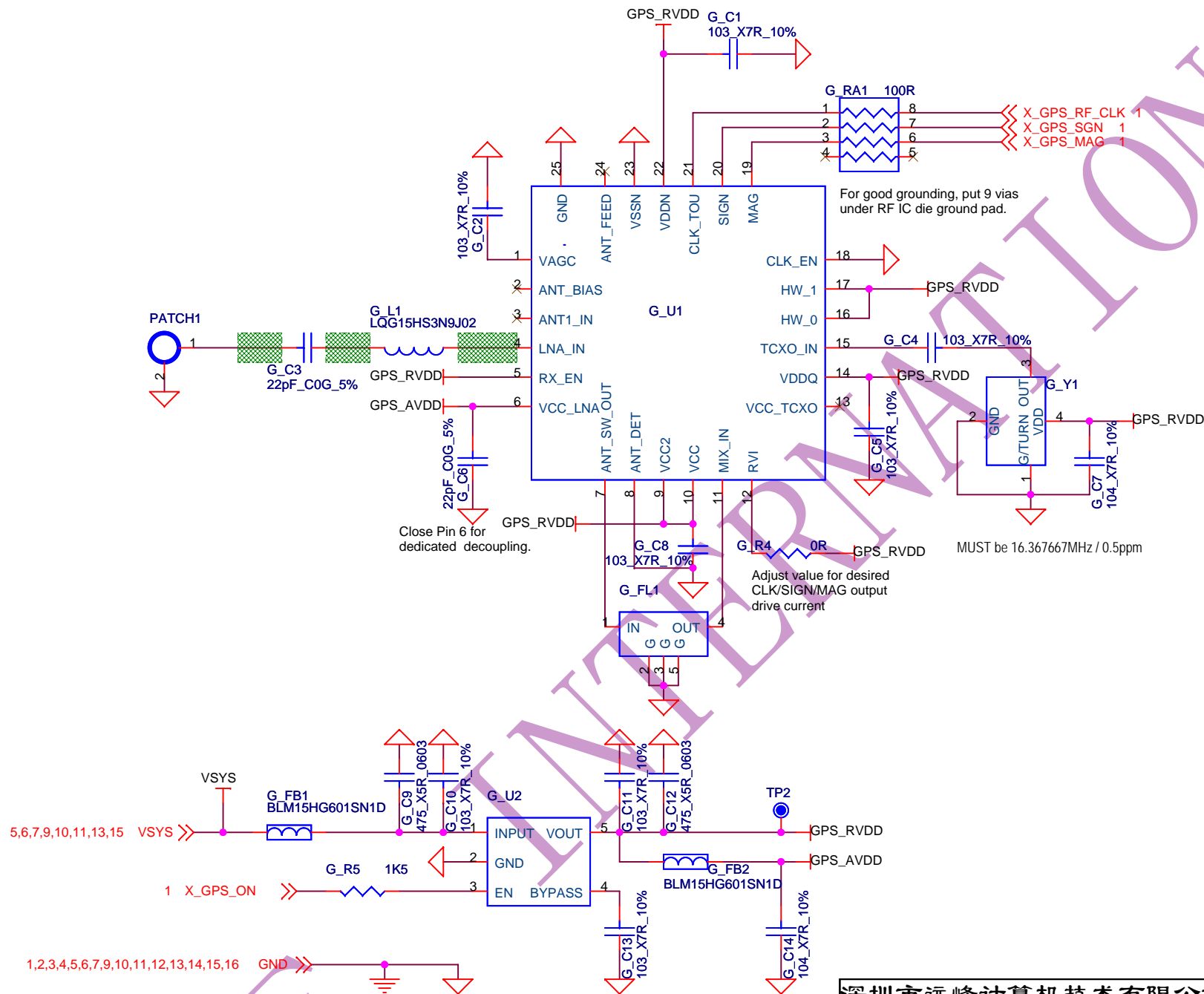
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Title				
YF Atlas-IV 标准平台 (PA05)				
Size	Document Number			Rev
Custom	DDR RAM			V01
Date:	Friday, September 04, 2009	Sheet	4	of 17

WORKING MODE X_TEST_MODE[5:4]	SUB MODE X_TEST_MODE[3:2]	FEATURE1 X_TEST_MODE[1]	FEATURE0 X_TEST_MODE[0]
2'b00: Normal 2'b01: Normal with ARM JTAG 2'b10: Function ATE	2'b00: Embedded ROM NAND boot (SLC)	1'b0: 8-bit	1'b0: 2 KB page
		1'b1: 16-bit	1'b1: 4 KB page
	2'b01: NAND boot (SLC)	1'b0: 8-bit	1'b0: 512 B page
		1'b1: 16-bit	1'b1: 2 KB page
	2'b10: Embedded ROM NAND boot (MLC)	1'b0: 8-bit	1'b0: 2 KB page
		1'b1: 16-bit	1'b1: 4 KB page
2'b11: Embedded ROM SD/MMC boot	-	-	
2'b11: Test/eFuse	2'b00: Pin scan	1'b0: Boundary scan	-
		1'b1: eFuse (X_RESET_B should be 1'b0)	-

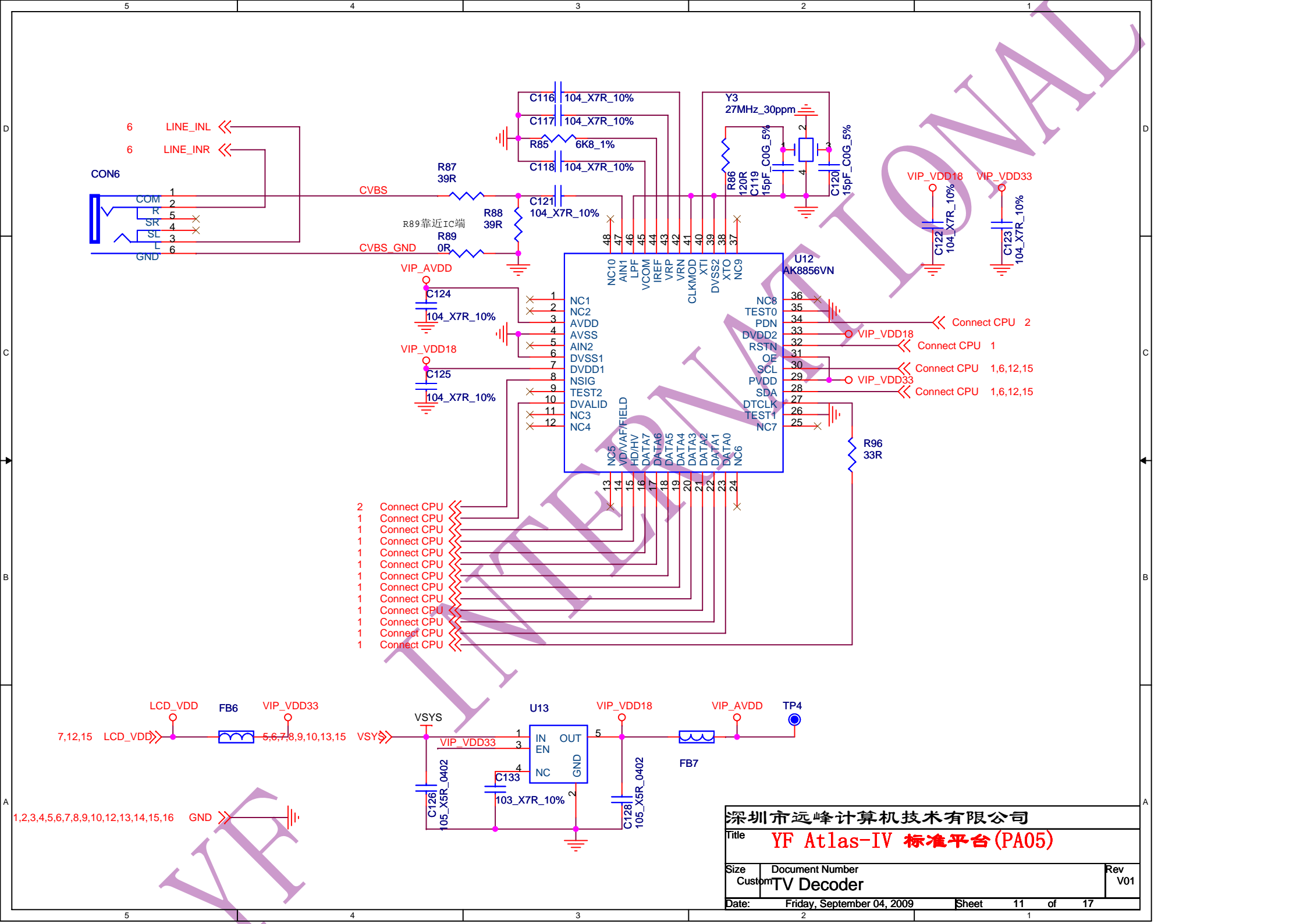


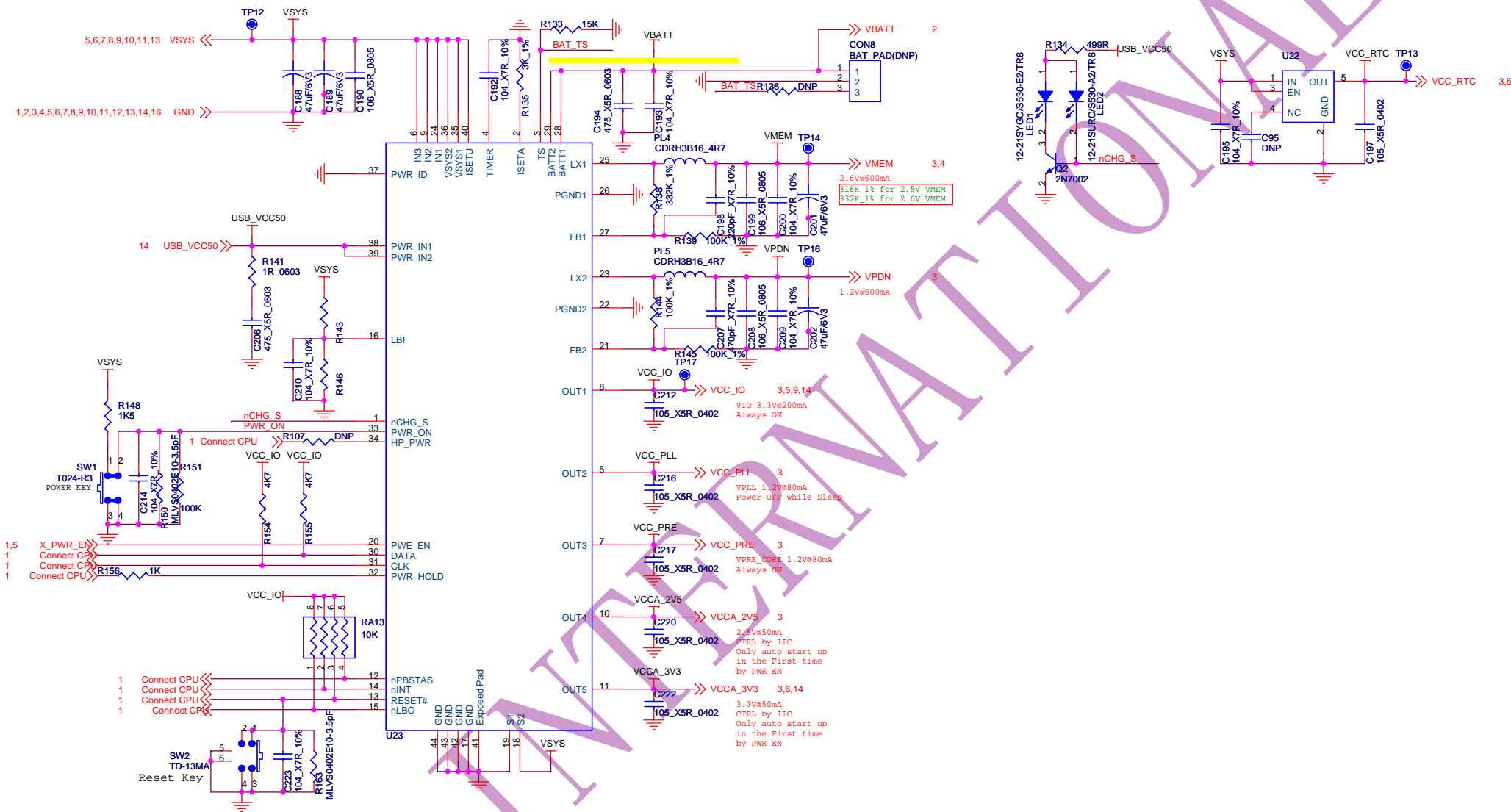






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Title			
YF Atlas-IV 标准平台 (PA05)			
Size	Document Number		Rev
Custom	GPS RF		V01
Date:	Friday, September 04, 2009		Sheet 8 of 17





深圳市远峰计算机技术有限公司			
Title			
YF Atlas-IV 标准平台 (PA05)			
Size	Document Number	Rev	
Custom	Power Management	V01	
Date:	Friday, September 04, 2009	Sheet	15 of 17

