Technical Description:

The Equipment Under Test (EUT) is a tablet, equipped with HDMI, WiFi, Bluetooth 3.0 & 4.0 and SD Interface. The EUT operates in the frequency range from 2412MHz to 2462MHz at 802.11b,g,n HT20 (11 channels with 5MHz spacing) and The EUT operates in the frequency 2402MHz to 2480MHz at Bluetooth 3.0 (79 channels with 1MHz spacing) while 2402MHz to 2480MHz at Bluetooth 4.0 (40 channels with 2MHz spacing). The EUT is powered by an external AC/DC adaptor or USB 5.0VDC or / and 3.7 VDC (1 x 3.7V rechargeable battery). The adaptor accepts 100-120VAC only.

An internal, integral antenna has been used.

1. WiFi & Bluetooth RF module (AP6210)

AP6210 is a WLAN USB module which fully supports the features and functional compliance of IEEE 802.11b/g/n(HT20) and Bluetooth 3.0 and Bluetooth 4.0 standards. This module operates in 2.4GHz frequency bands. Ap6210 consists of:

- 1) U5 (BCM43362) is a single-chip IEEE802.11b/g/n(HT20) 1T1R WLAN controller.
- 2) U6 (BCM20710) is a single-chip Bluetooth 4.0 + EDR compliant processor.
- 3) Y3 is 26MHz crystal oscillator providing clock for U5 and U6.

2. Main Processor Portion

- 1) U1 (AML8726-MXS) is main processor.
- 2) Y1 is 24MHz crystal oscillator for U1.
- 3) Y2 is 32.768kHz crystal oscillator for U1.

3. Memory Portion

- 1) U5, U6, U7, U8 (H5TQ2G83EFR) are 2Gbit DDR3 memories.
- 2) U19 (TC58TEG5DCJTA00) is 4GB flash memory.

4. Video Portion

1) U26 and U28 (SPE6V8UDFN10RGB) is HDMI Transceiver.

Average Power Specifications

Operating Mode	Date Rate	Channel	Freq. (MHz)	Pov	ged Output wer
/ Band				Nominal (dBm)	Maximum (dBm)
		1	2412		
	1Mbps	6	2437		
		11	2462		
		1	2412		
	2Mbps	6	2437		
IEEE 802.11b		11	2462	+13.0 dBm	+13.5 dBm
(2.4GHz)		1	2412	+13.0 dbiii	+13.3 dDill
(2.40112)	5.5Mbps	6	2437		
		11	2462		
		1	2412		
	11Mbps	6	2437		
		11	2462		
	6Mbps	1	2412		
		6	2437		
		11	2462		
	9Mbps	1	2412		
		6	2437	+13 dBm	+13.5 dBm
		11	2462		
	12Mbps	1	2412		
	•	6	2437		
		11	2462		
IEEE 000 44	18Mbps	1	2412		
IEEE 802.11g	•	6	2437		
(2.4GHz)		11	2462		
	24Mbps	1	2412	+11.5 dBm	+12 dBm
	•	6	2437		
		11	2462		
	36Mbps	1	2412		
	•	6	2437		
		11	2462		
	48Mbps	1	2412		
	•	6	2437	+9.5 dBm	+10 dBm
		11	2462		
	54Mbps	1	2412		
	•	6	2437		
		11	2462		

Operating Mode	MCS Index	Channel	Freq. (MHz)	Pov	ged Output wer
/ Band				Nominal (dBm)	Maximum (dBm)
	MCS0	1	2412		
		6	2437		
		11	2462		
	MCS1	1	2412		
		6	2437	+12 dBm	+13 dBm
		11	2462		
	MCS2	1	2412		
		6	2437		
		11	2462		
IEEE 802.11n	MCS3	1	2412		
(HT20) (2.4GHz)		6	2437		
(11120) (2.40112)		11	2462		
	MCS4	1	2412	+10.5 dBm	+11.5 dBm
		6	2437		
		11	2462		
	MCS5	1	2412		
		6	2437		
		11	2462		
	MCS6	1	2412	0 5 15	0.5.15
		6	2437	+8.5 dBm	+9.5 dBm
		11	2462		
	MCS7	1	2412		
		6	2437		
		11	2462		

Operating Mode	Channel	Freq. (MHz)	Time-averaged Outp Power	
/ Band			Nominal (dBm)	Maximum (dBm)
	0	2402		
Bluetooth 4.0	19	2440	+7 dBm	+8 dBm
LE	39	2480		
	0	2402		
Bluetooth	39	2441	-6 dBm	-5 dBm
	78	2480		





正基科技股份有限公司 SPECIFICATION

SPEC. NO.:	REV: 1.2
DATE : 12. 26.20	12
PRODUCT NAME:	AP6210

	APPROVED	CHECKED	PREPARED	DCC ISSUE
NAME				

AMPAK Technology Inc.
Doc. NO:

www.ampak.com.tw

Proprietary & Confidential Information



AMPAK

AP6210

WiFi 802.11b/g/n + BT4.0 SIP Module Spec Sheet



Revision History

Date	Revision Content	Revised By	Version
2012/09/18	- Preliminary	Bart	1.0
2012/09/25	- Modify Pin Definition	Bart	1.1
2012/12/26	- Modify Pin name 10,11	Brian	1.2
			111
		. 0	11 16
		0. 5	130
	100	1 10 14	M.





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1. Introduction

AMPAK Technology would like to announce a low-cost and low-power consumption module which has all of the WiFi and Bluetooth functionalities. The highly integrated tiny module makes the possibilities of web browsing, VoIP, Bluetooth headsets and portable navigation applications. With seamless roaming capabilities and advanced security, also could interact with different vendors' 802.11b/g/n Access Points in the wireless LAN.

The module complies with IEEE 802.11 b/g/n standard and it could achieve up to a speed of 72.2Mbps with single stream in 802.11n draft, 54Mbps as specified in 802.11g, or 11Mbps for 802.11b to connect to the wireless LAN. The integrated module provides SDIO interface for WiFi, UART / PCM for Bluetooth.

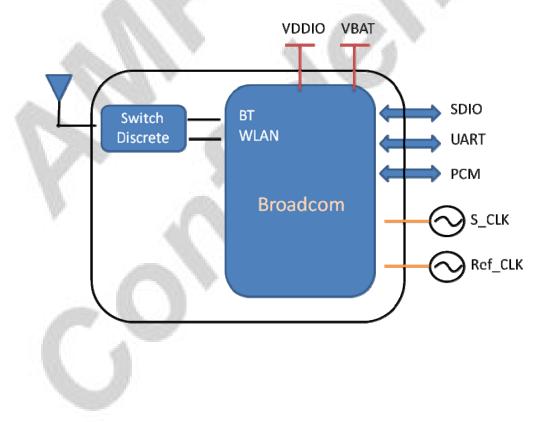
This compact module is a total solution for a combination of WiFi + BT technologies. The module is specifically developed for Tablet, Smart phones and Portable devices.



2. Features

- 802.11b/g/n single-band radio
- Bluetooth V4.0 + EDR with integrated Class 1.5 PA Concurrent Bluetooth and WLAN operation
- Simultaneous BT / WLAN receive with single antenna
- WLAN host interface options:
 - SDIO v2.0 up to 50 MHz clock rate
- BT host digital interface:
 - UART (up to 4 Mbps)
- IEEE Co-existence technologies are integrated die solution

A simplified block diagram of the module is depicted in the figure below.





3. Deliverables

3.1 Deliverables

The following products and software will be part of the product.

- Module with packaging
- **Evaluation Kits**
- Software utility for integration, performance test.
- Product Datasheet.
- Agency certified pre-tested report with the adapter board.

3.2 Regulatory certifications

The product delivery is a pre-tested module, without the module level certification. For module approval, the platform's antennas are required for the certification.



4. General Specification

4.1 General Specification

Model Name	AP6210
Product Description	Support WiFi/Bluetooth functionalities
Dimension	L x W x H: 12.0 x 12.0 x 1.5 (typical) mm
WiFi Interface	SDIO V2.0
BT Interface	UART/ PCM
Operating temperature	-30°C to 85°C
Storage temperature	-40°C to 85°C
Humidity	Operating Humidity 10% to 95% Non-Condensing

4.2 Voltages

4.2.1 Absolute Maximum Ratings

Symbol	Description	Min.	Max.	Unit
VBAT	Input supply Voltage	-0.5	5.5	V
VDDIO	Digital/Bluetooth/SDIO/SPI I/O Voltage	-0.5	3.6	V

4.2.2 Recommended Operating Rating

The module requires two power supplies: VBAT and VDDIO

	Min.	Тур.	Max.	Unit
Operating Temperature	-30	25	85	deg.C
VBAT	3.0	3.6	4.8	V
VDDIO	1.71	3.3	3.6	V



5. WiFi RF Specification

5.1 2.4GHz RF Specification

Conditions: VBAT=3.6V; VDDIO =3.3V; Temp:25°C

Feature	Description		
WLAN Standard	IEEE 802.11b/g/n, WiFi compliant		
Frequency Range	2.400 GHz ~ 2.497 GHz (2.4 GHz ISM Band)		
Number of Channels	2.4GHz: Ch1 ~ Ch14		
Modulation	802.11b : DQPSK, DBPSK, CCK		
Modulation	802.11 g/n : OFDM /64-QAM,16-QAM, QPSK, BPSK		
	802.11b /11Mbps : 16 dBm \pm 1.5 dB @ EVM \leq -9dB		
Output Power	802.11g /54Mbps : 15 dBm \pm 1.5 dB @ EVM \leq -25dB		
	802.11n /65Mbps : 14 dBm ± 1.5 dB @ EVM ≤ -28dB		
	- MCS=0 PER @ -85 dBm, typical		
A 11 V	- MCS=1 PER @ -84 dBm, typical		
Receive Sensitivity	- MCS=2 PER @ -82 dBm, typical		
17 ACC 1 1 TO	- MCS=3 PER @ -80 dBm, typical		
(11n,20MHz) @10% PER	- MCS=4 PER @ -77 dBm, typical		
@10701 LIX	- MCS=5 PER @ -73 dBm, typical		
	- MCS=6 PER @ -71 dBm, typical		
W	- MCS=7 PER @ -69 dBm, typical		
	- 6Mbps PER @ -86 dBm, typical		
	- 9Mbps PER @ -85 dBm, typical		
	- 12Mbps PER @ -85 dBm, typical		
Receive Sensitivity (11g)	- 18Mbps PER @ -83 dBm, typical		
@10% PER	- 24Mbps PER @ -81 dBm, typical		
	- 36Mbps PER @ -78 dBm, typical		
	- 48Mbps PER @ -73 dBm, typical		
	- 54Mbps PER @ -72 dBm, typical		
	- 1Mbps PER @ -90 dBm, typical		
Receive Sensitivity (11b)	- 2Mbps PER @ -89 dBm, typical		
@8% PER	- 5.5Mbps PER @ -88 dBm, typical		
	- 11Mbps PER @ -85 dBm, typical		
Data Rate	802.11b: 1, 2, 5.5, 11Mbps		
Dala Nale	802.11g: 6, 9, 12, 18, 24, 36, 48, 54Mbps		





Data Rate	802.11n: 6.5, 13, 19.5, 26, 39, 52, 58.5, 65Mbps
(20MHz ,Long GI,800ns)	
Data Rate	802.11n: 7.2, 14.4, 21.7, 28.9, 43.3, 57.8, 65,72.2Mbps
(20MHz ,short GI,400ns)	
Maximum Input Laval	802.11b : -10 dBm
Maximum Input Level	802.11g/n : -20 dBm
Antenna Reference	Small antennas with 0~2 dBi peak gain





6. Bluetooth Specification

6.1 Bluetooth Specification

Conditions · VBAT=3 6V · VDDIO=3 3V · Temp·25°C

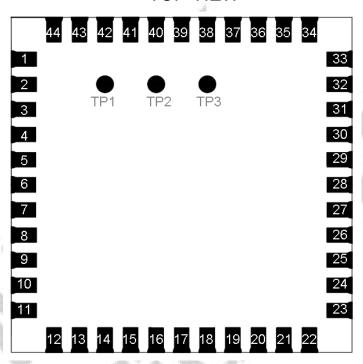
Feature	Description				
General Specification	AL				
Bluetooth Standard	Bluetooth V4.0	of 1, 2 and 3 Mbps.	ONT		
Host Interface	UART		CU		
Antenna Reference	Small antennas	s with 0~2 dBi peak	gain		
Frequency Band	2.400 GHz ~ 2	483.5 GHz	<i>b</i>		
Number of Channels	79 channels	11 11			
Modulation	FHSS, GFSK,	FHSS, GFSK, DPSK, DQPSK			
RF Specification		VI			
. 11.00.	Min.	Typical.	Max.		
Output Power (Class 1.5)	200	10dBm			
Sensitivity @ BER=0.1% for GFSK (1Mbps)	X.II	-86 dBm			
Sensitivity @ BER=0.01% for π/4-DQPSK (2Mbps)	1/2	-86 dBm			
Sensitivity @ BER=0.01% for 8DPSK (3Mbps)		-80 dBm			
GFSK (1Mbps):-20dBm					
Maximum Input Level	π/4-DQPSK (2	Mbps) :-20dBm			
	8DPSK (3Mbps	s) :-20dBm			



7. Pin Assignments

7.1 Pin Outline





7.2 Pin Definition

NO	Name	Туре	Description
1	GND	-	Ground connections
2	WL_BT_ANT	I/O	RF I/O port
3	GND	W.	Ground connections
4	NC	_	Floating (Don't connected to ground)
5	NC	_	Floating (Don't connected to ground)
6	BT_WAKE	I	HOST wake-up Bluetooth device
7	BT_HOST_WAKE	0	Bluetooth device to wake-up HOST
8	NC		Floating (Don't connected to ground)
9	VBAT	Р	Main power voltage source input
10	NC	_	Floating (Don't connected to ground)
11	NC		Floating (Don't connected to ground)
12	WL_REG_ON	I	Internal regulators power enable/disable
13	WL_HOST_WAKE	0	WLAN to wake-up HOST
14	SDIO_DATA_2	I/O	SDIO data line 2

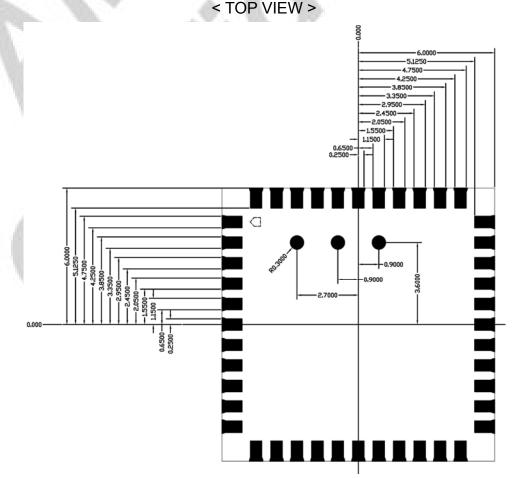


15	SDIO_DATA_3	I/O	SDIO data line 3
16	SDIO_DATA_CMD	I/O	SDIO command line
17	SDIO_DATA_CLK	I/O	SDIO CLK line
18	SDIO_DATA_0	I/O	SDIO data line 0
19	SDIO_DATA_1	I/O	SDIO data line 1
20	GND	_	Ground connections
21	VIN_LDO_OUT	Р	Internal Buck voltage generation pin
22	VDDIO	Р	I/O Voltage supply input
23	VIN_LDO	Р	Internal Buck voltage generation pin
24	LPO	JA	External Low Power Clock input (32.768KHz)
25	PCM_OUT	I/O	PCM Data output
26	PCM_CLK	I/O	PCM Clock
27	PCM_IN	I/O	PCM data input
28	PCM_SYNC	I/O	PCM sync signal
29	WL_VDD_TCXO	Р	1.7V to 3.3V supply for the TCXO driver
30	TCXO_IN	L	Reference clock input
31	GND	V _	Ground connections
32	NC	_	Floating (Don't connected to ground)
33	GND	-01	Ground connections
34	BT_RST_N	10	Low asserting reset for Bluetooth core
35	NC	- Pu	Floating (Don't connected to ground)
36	GND	4.	Ground connections
37	BT_XTAL_IN	1	Crystal input for BT
38	BT_XTAL_OUT	0	Crystal output for BT
39	NC	V-,	Floating (Don't connected to ground)
40	NC	7	Floating (Don't connected to ground)
41	UART_RTS_N	0	Bluetooth UART interface
42	UART_TXD	0	Bluetooth UART interface
43	UART_RXD	I	Bluetooth UART interface
44	UART_CTS_N	I	Bluetooth UART interface
45	TP1 (NC)	_	Floating (Don't connected to ground)
46	TP2 (NC)	_	Floating (Don't connected to ground)
47	TP3 (NC)	_	Floating (Don't connected to ground)



8. Dimensions

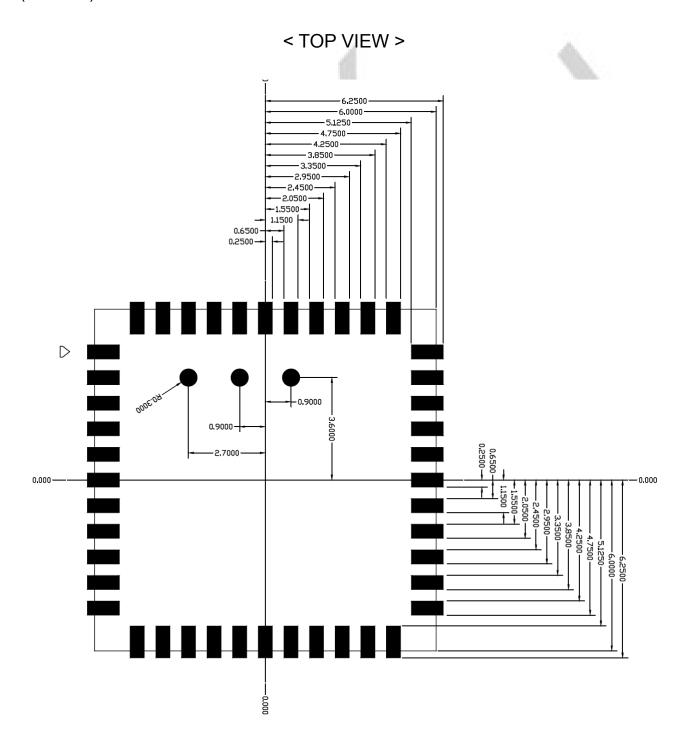
8.1 Physical Dimensions (Unit: mm) < TOP VIEW > < Side View > - 12 +/- 0.1 -12 +/- 0.1 1.5 +/- 0.1 < TOP VIEW >





8.2 Layout Recommendation

(Unit: mm)





External clock reference

External LPO signal characteristics

Parameter	Specification	Units
Nominal input frequency	32.768	kHz
Frequency accuracy	±30	ppm
Duty cycle	30 - 70	%
Input signal amplitude	1600 to 3300	mV, p-p
Signal type	Square-wave or sine-wave	B :
Innut impedance	>100k	Ω
Input impedance	<5	pF
Clock jitter (integrated over 300Hz – 15KHz)	<1	Hz
Output high voltage	0.7Vio - Vio	V

External Ref CLK signal characteristics

No.	Item	Cumb	Electrical Specification				Barrank
NO.	item	Symb.	Min.	Туре	Max.	Units	Remark
1	Nominal Frequency	F0		26.00000		MHz	
2	Mode of Vibration			Funda	mental	•	
3	Frequency Tolerance	ΔF/F0	-10	-	10	ppm	at 25°C±3°C
4	Operating Temperature Range	Topr	-30	-	85	°C	
5	Frequency Stability	TC	-10	-	10	ppm	
6	Storage Temperature	T _{STG}	-55	-	125	°C	
7	Load capacitance	CL	-	16		pF	
8	Equivalent Series Resistance	ESR	-	-	50	Ω	
9	Drive Level	DL	-	100	200	μW	
10	Insulation Resistance	IR	500	-	-	ΜΩ	At 100V _{DC}
11	Shunt Capacitance	C0	-	-	3	pF	
12	Aging Per Year	Fa	-2	-	2	ppm	First Year

9.1 SDIO Pin Description

The module supports SDIO version 2.0 for 4-bit modes It has the ability to stop the SDIO clock and map the interrupt signal into a GPIO pin. This 'out-of-band' interrupt signal notifies the host when the WLAN device wants to turn on the SDIO interface. The ability to force the control of the gated clocks from within the WLAN chip is also provided.



- Function 0 Standard SDIO function (Max BlockSize / ByteCount = 32B)
- Function 1 Backplane Function to access the internal System On Chip (SOC) address space (Max BlockSize / ByteCount = 64B)
- Function 2 WLAN Function for efficient WLAN packet transfer through DMA (Max BlockSize/ByteCount=512B)

SDIO Pin Description

	SDIO 4-Bit Mode
DATA0	Data Line 0
DATA1	Data Line 1 or Interrupt
DATA2	Data Line 2 or Read Wait
DATA3	Data Line 3
CLK	Clock
CMD	Command Line

10. Host Interface Timing Diagram

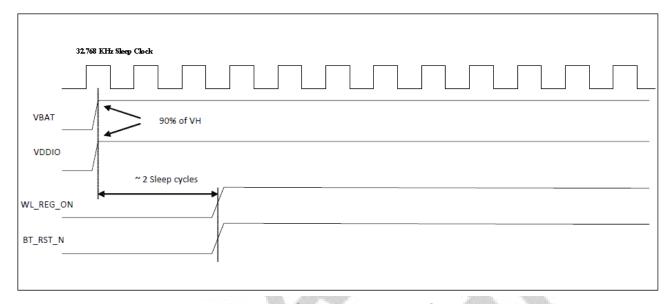
10.1 Power-up Sequence Timing Diagram

The module has two signals that allow the host to control power consumption by enabling or disabling the Bluetooth, WLAN and internal regulator blocks. These signals are described below.

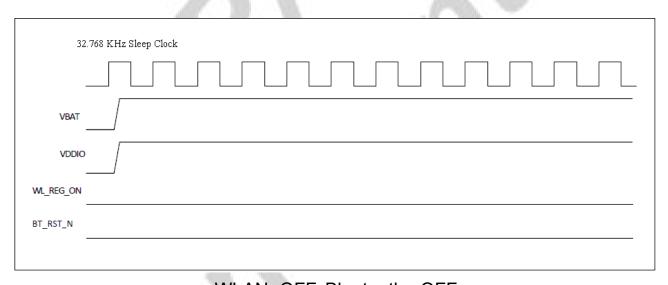
Additionally, diagrams are provided to indicate proper sequencing of the signals for carious operating states. The timing value indicated are minimum required values: longer delays are also acceptable.

- WL_REG_ON: Used by the PMU to power up the WLAN section. It is input to control the internal WLAN regulators. When this pin is high, the regulators are enabled and the WLAN section is out of reset. When this pin is low the WLAN section is in reset.
- BT_RST_N: Used by the PMU to power up the internal Bluetooth regulators. If the
 BT_RST_N pins are low, the regulators are disabled.

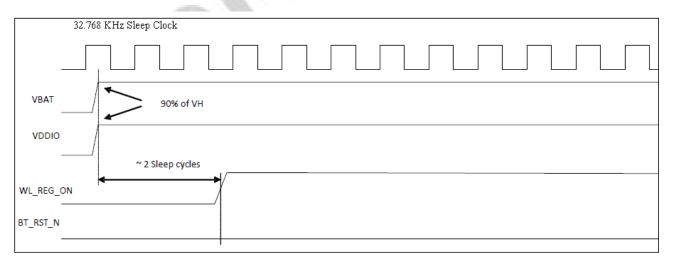




WLAN=ON, Bluetooth =ON

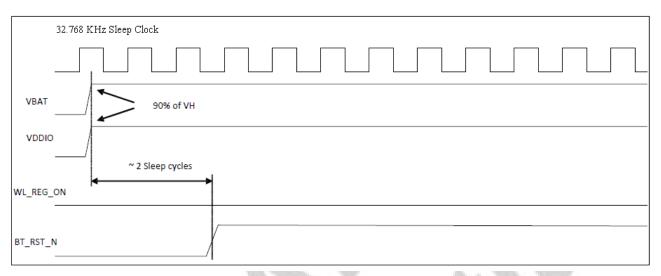


WLAN=OFF, Bluetooth =OFF



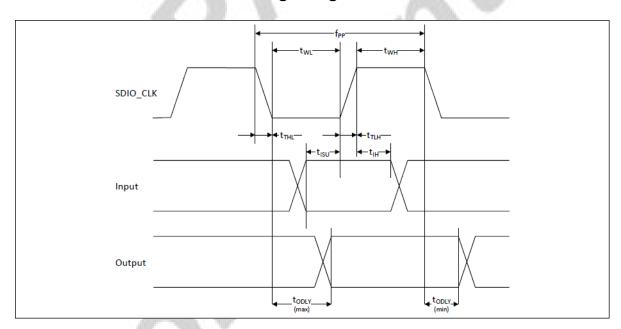
WLAN=ON, Bluetooth =OFF





WLAN=OFF, Bluetooth=ON

10.2 SDIO Default Mode Timing Diagram



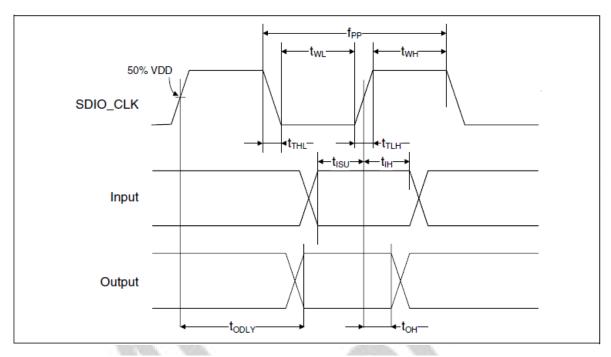
Parameter	Symbol	Minimum Typical Maximun		Maximum	Unit
SDIO CLK (All values are refferred to mini	imum VIH and	d maximum VI	L ^b)		
Frequency-Data Transfer mode	fPP	0	-	25	MHz
Frequency-Identification mode	fOD	0	-	400	kHz
Clock low time	tWL	10	-	-	ns
Clock high time	tWH	10	-	-	ns
Clock rise time	tTLH	-	-	10	ns
Clock low time	tTHL	-	-	10	ns
Inputs: CMD, DAT (referenced to CLK)					
Input setup time	tISU	5	-	-	ns
Input hold time	tIH	5	-	-	ns
Outputs: CMD, DAT (referenced to CLK)					
Output delay time - Data Transfer mode	tODLY	0	-	14	ns
Output delay time - Identification mode	tODLY	0	-	50	ns

a. Timing is based on CL \leq 40pF load on CMD and Data.

b. $min(Vih) = 0.7 \times VDDIO$ and $max(Vil) = 0.2 \times VDDIO$.



10.3 SDIO High Speed Mode Timing Diagram



Parameter	Symbol	Minimum	Typical	Maximum	Unit
SDIO CLK (All values are refferred to mini	mum VIH an	d maximum VI	L ^b)		
Frequency-Data Transfer mode	fPP	0	-	50	MHz
Frequency-Identification mode	fOD	0	-	400	kHz
Clock low time	tWL	7	-	-	ns
Clock high time	tWH	7	-	-	ns
Clock rise time	tTLH	-	-	3	ns
Clock low time	tTHL	-	-	3	ns
Inputs: CMD, DAT (referenced to CLK)					
Input setup time	tISU	6	-	-	ns
Input hold time	tIH	2	-	-	ns
Outputs: CMD, DAT (referenced to CLK)					
Output delay time - Data Transfer mode	tODLY	-	-	14	ns
Output hold time	tOH	2.5	-	-	ns
Total system capacitance (each line)	CL	-	-	40	pF

a. Timing is based on CL ≤ 40pF load on CMD and Data.

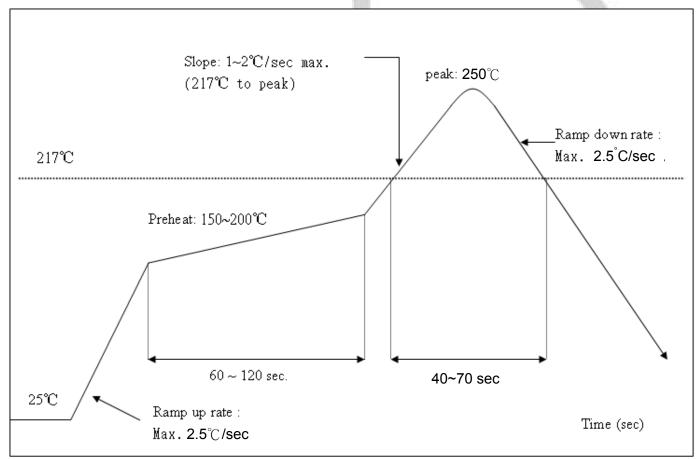
b. min(Vih) = 0.7 x VDDIO and max(ViI) = 0.2 x VDDIO.



11. Recommended Reflow Profile

Referred to IPC/JEDEC standard.

Peak Temperature: <250°C Number of Times : ≤2 times







12. Package Information

12.1Label

Label A→ Anti-static and humidity notice



Label B→ MSL caution / Storage Condition

(Caution This bag contains MOISTURE-SENSITIVE DEVICES H blank, see adjace bar code label
1.	Calculated shelf life in sealed bag: 12 months at <40°C and <90% relative humidity (RH)
2.	Peak package body temperature: # blank, see adjacent bar code label
3.	After bag is opened, devices that will be subjected to reflow solder or other high temperature process must be
	a) Mounted within: hours of factory conditions ≤30°C/60% RH, or
	b) Stored per J-STD-033
4.	Devices require bake, before mounting, if:
	a) Humidity Indicator Card reads >10% for level 2a - 5a devices or >60% for level 2 devices when read at $23\pm5^\circ$
	b) 3a or 3b are not met
5.	If baking is required, refer to IPC/JEDEC J-STD-033 for bake procedure
Ba	ag Seal Date:
	Note: Level and body temperature defined by IPC/JEDEC J-STD-020

Label C→ Inner box label.

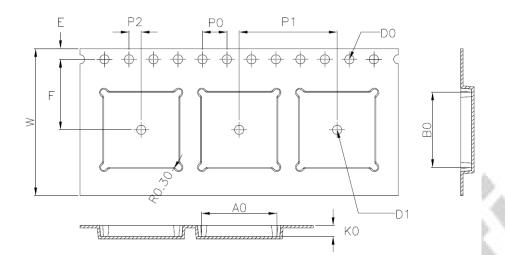
Model: P/N: 99P-W01-0048R Qty: Date Code :

Label D→ Carton box label .



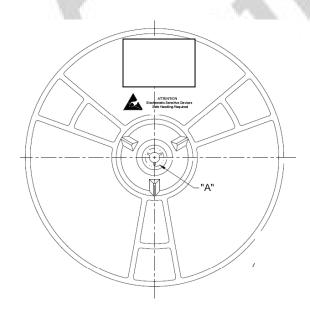


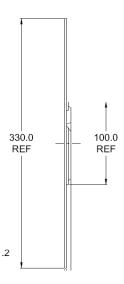
12.2 Dimension



W	24.00±0.30
Α0	12.30±0.10
В0	12.30±0.10
K0	1.80±0.10
E	1.75±0.10
F	11.50±0.10
P0	4.00±0.10
P1	16.00±0.10
P2	2.00±0.10
D0	1.50 +0.10
D1	Ø1.50MIN

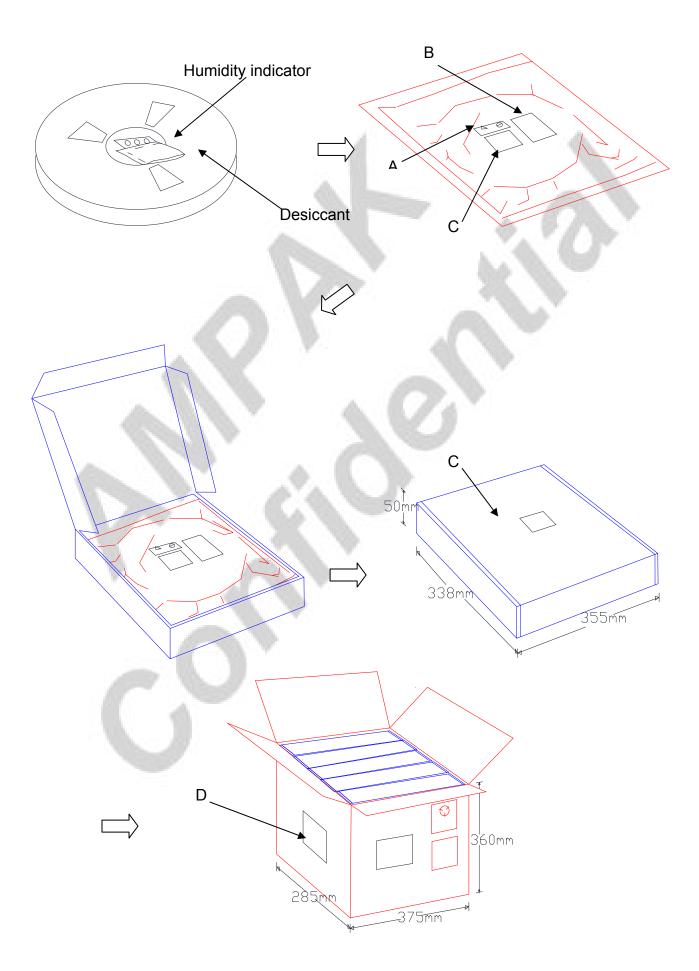
- 1. 10 sprocket hole pitch cumulative tolerance ± 0.20 .
- 2. Carrier camber is within 1 mm in 250 mm.
- 3. Material: Black Conductive Polystyrene Alloy.
- 4. All dimensions meet EIA-481-D requirements.
- 5. Thickness: 0.30±0.05mm.
- 6. Packing length per 22" reel: 98.5 Meters.(1:3)
- 7. Component load per 13" reel: 1500 pcs.













12.3 MSL Level / Storage Condition

LEVEL
Caution
This bag contains 4
MOISTURE-SENSITIVE DEVICES
Do not open except under controlled conditions
 Calculated shelf life in sealed bag: 12 months at< 40℃ and 90% relative humidity(RH)
225 C 240 C 250 C 260 C
2. Peak package body temperature:
 After bag is opened, devices that will be subjected to reflow solder or other high temperature process must a) Mounted within: 48 hours of factory conditions <30°C/60% RH, OR b) Stored at <10% RH
 Devices require bake, before mounting, if: a)Humidity Indicator Card is>10%when read at 23±5℃ b)3a or 3b not met
5. If baking is required, devices may be baked for 24 hours at 125±5°C
Note: If device containers cannot be subjected to high temperature or shorter bake times are desired,
reference IPC/JEDEC J-STD-033 for bake procedure
Bag Seal Date: See-SEAL DATELABEL
Note:Level and body temperature defined by IPC/JEDED J-STD-020

※NOTE: Accumulated baking time should not exceed 96hrs