

Model:NINR257 (Receiver portion)

Technical Description of Receiver

The receiver operates at 49.860 MHz, and controls by the corresponding transmitter to move forward, backward, left, right and carry out charging.

The circuit of the receiver comprises of,

1. Super-regenerative circuit

Consisting: L2, C1, C2, C3, C5, C6, C13, Q1, T1, R1, R3

2. Decoder

Consisting: TX10

3. Drive Motor Circuit

Consisting: C14

4. Steer Motor Circuit

Consisting: C13

5. A Power Supply

Consisting: 1 x 2.4V Ni-MH battery pack

6. Regulator and Charging Circuit

Consisting: 1N5819, C12, C16, C17, EC1, D1, L2

7. Clock Frequency

Consisting: Y1

The receiver uses an internal, integral and non-detachable antenna.

FCC ID: YPS-MRC201249R