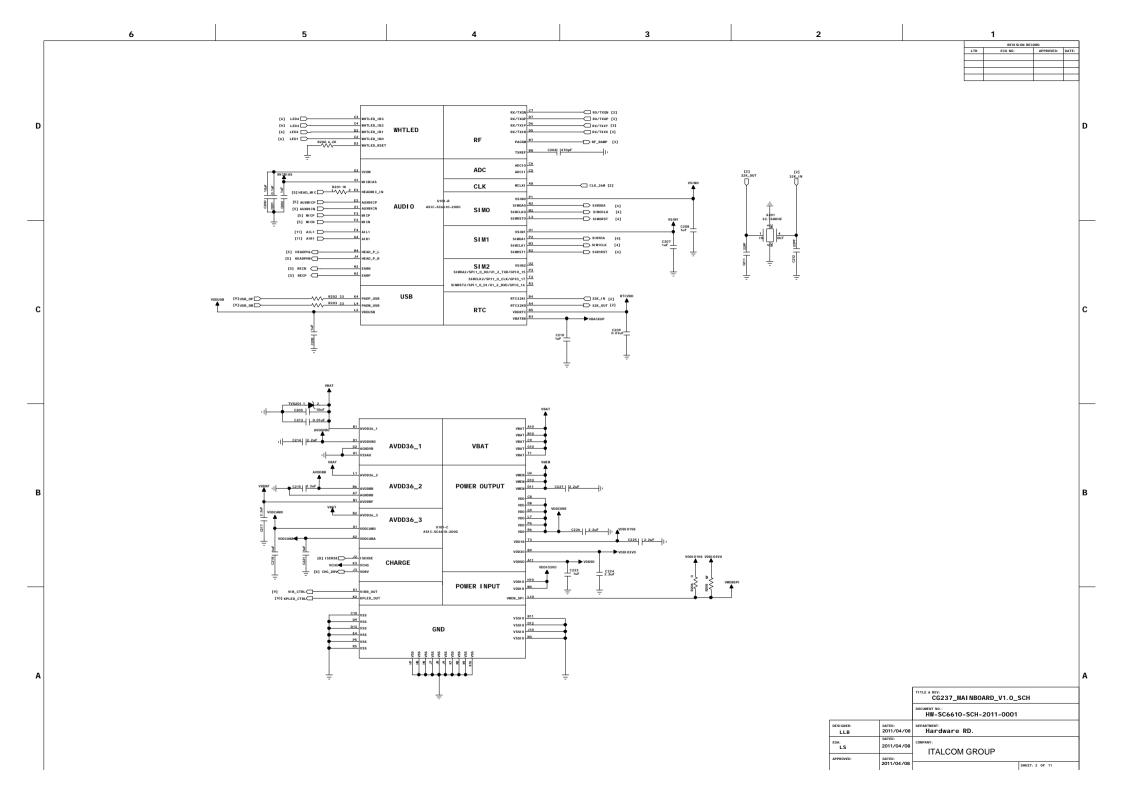
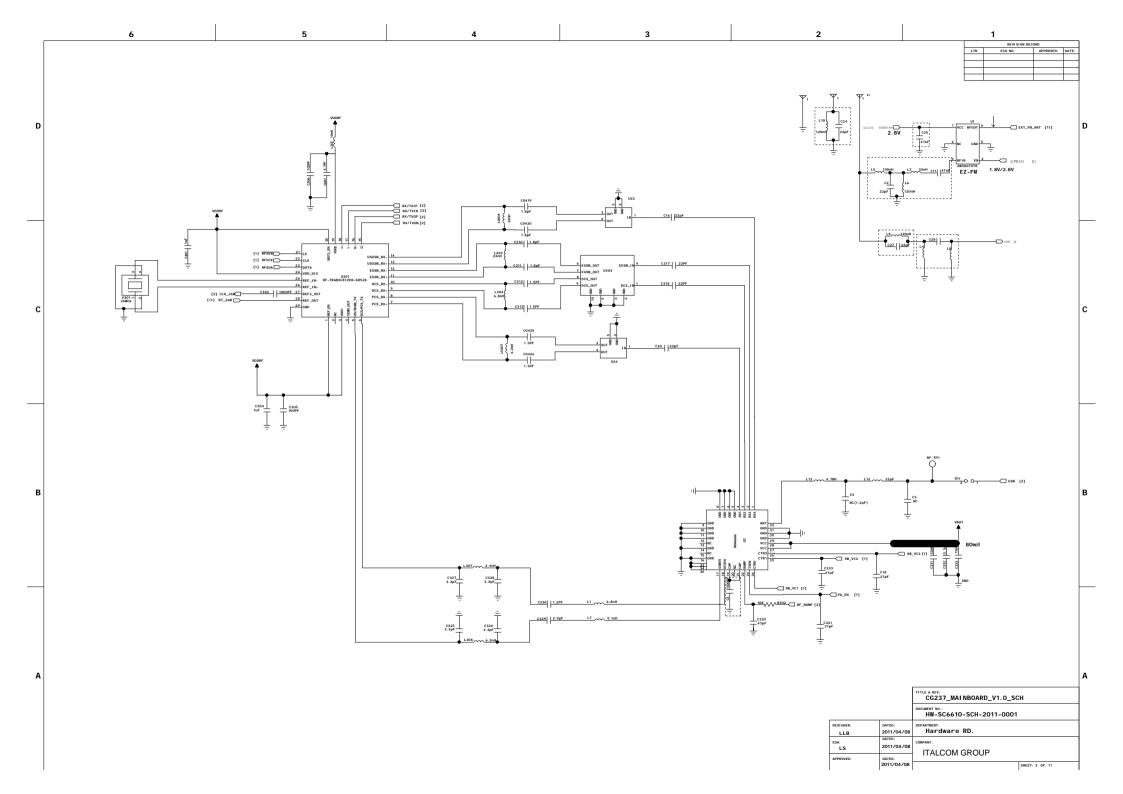
REVISION RECORD ECO NO: D H XTLEN/GPIO_39 B15 N17 ESMD[1] [4] OPI_IO1 — I PESDA/GPIO 40 C11 RFSDA [3] M15 ESMD[2] L RFSCK/GPIO_41 B12 RFSCK [3] MEMORY RESEN 131 [4] OPI 103 D H RESEN/GPIO 42 C12 L RF_PAEN/GPI 0_48 A13 PA_EN [3] RF DIGITAL IO LRF PAMODE/GP10 47 C14 LRF_PABAND/GPI 0_43 C13 L RF_VBS3/GPI0_46 A14 SW_VC3 [3] [4] QPI_CLK — LRF_VBS2/GPI0_45 B14 LRF_VBS1/GPI 0_44 → SW_VC1 [3] [6] LCM_DO CMD[0]/GPI0_53/SPI1_1_DI L L MTMS/GPIO_19 JTAG → ATV SDA [11] K17 LCMD[1]/GPI0 54/SPI1 1 D0 [6] LCM_SPI1_DO/LCM_DT LMTDI/RXQD/GPI 0_20 T8
L MTCK/CLK_32K/GPI 0_21 T7 GP10_20_HLED [7] L17 LCMD[2]/GPI0_55/SPI1_1_CLK L J16 LCMDF31/GPI0 56/SPI1 1 CS0 I TAT LOW SPIT CS/LOW D3CT L MTDO/SDA2/GP10_22 R7 LMTRST_N/SCL2/GP10_23 P7 K15 LCMD[4]/GPI0_57/SPI1_1_CS1 L FN SCL [11] M14 | CMDF51/GPIO 58 | [6] LCM_D5 [6] LCM_D6 L14 LCMD[6]/GP10_59 L

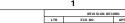
J13 LCMD[7]/GP10_60/SDA3 L LCM [6] LCM_D7 H14 LCMD[8]/GPI0_61/SCL3 L | IISDO/GPIO_7 R4 IIS J14 LCMRD/SPI 1_2_DI LIISCLK/VBDAC_BCLK/GPIO_8 BT_PCM_CLK [11] J15 LCMWR/SPI1_2_D0 [6] LCM WR√□— HIISLRCK/VBDAC_LRO/GPIO_9 I BT_PCM_SYNC [11] [6] LCM_CD -K14 LCMCD/SPI 1_2_CLK H15 LCMCSO/SPI1_2_CLK [6] LCM_CS --H16 LCMCS1/SPI1_2_CS1/CLK_32K L SPIO_DO/U1TXD/GPIO_10 U11 U11 [6] LCM_RST___ CMRSTN/GPI 0_62 SP10_D0 [4] SP10_D1 [4] SPI L SPIO_CLK/GPIO_12 R10 H SPIO_CSO/GPIO_13 T11 SPIO_CLK [4] [7] CAM_MCLK _______R100_33 C16 CCIRMCK/CLK_DSP/CLK_32K SPIO CSN [4] D15 CAM_PD/GPIO_37 H [7] CAM_BACK_PWDNC U101-A ASI C-SC6610-2000 C15 CAM_RSTN D16 CCIRCK(SCK) [7,11] CAM_SPI_SCK/CAM_PCEK E17 CCIRHS [7,11] CAM_HS [7.11] CAN SPI SSN/CAN VSYNC> UOTXD [11] UORXD [11]
UOCTSN/U1RXD [9] [7,11] CAM_SPI_SDI/CAM_DD UART F16 CCIRD[1]/VBDAC_SDATA_T [7,11] CAM_D1 -CAMERA MIORTS/U1 0 TXD/GPI0 5 UNRTSN/NROOT/UITXD [9] E15 CCIRD[2]/VBDAC_BLCK [7,11] CAM_D2 E14 CCIRD[3] [7,11] CAM_D3 -[7,11] CAM_D4 --F15 CCIRDF41/GP10 36 HTP_XL/GPI0_49 B17 TP_XL [6] G16 CCIRD[5]/GPIO_35 L 17.111 CAM D5 □ TOUCH PANEL — TP XR [6] G17 CCIRD[6] [7,11] CAM_D6 -LTP_YU/GPI0_51 B16 F14 CCIRD[7] F7 111 CAN D7 LTP_YD/GPI0_52 TP_YD [6] G15 SDA/GPIO_33 H G14 SCL/GP10_32 H [7] CAM_SCL -L GPIO_O R PA_AUDIO_EN [5] L GPIO_1 P8 OP101_BT_LD0 [11] EYOUTO R16
EYOUT1 U16
EYOUT2 U17
EYOUT3 T15
EYOUT[3] GPIO 12C has been pulled up by VDDCAMA L GP10_3 U13 GP034_TV_1.2V_EN [11] don't pull up it again HSPI1_0_CS0/GPI0_18 U7 L GPIO_63/BT_XTL_EN R11 BT_CLK_REQUEST [11] YOUTS R15 KEYOUT[4]
:YOUTS R15 KEYOUT[5]/GPI0_26 H MITA R14 KEYOUT[6]/GPI 0_27 H EXTINTO R12 (INO P17 KEYIN[0]
(IN1 P16 KEYIN[1]
(IN2 N15 KEYIN[2] KEYPAD SYSTEM CONTROL R101 1K 1 IN3 P15 KEYIN[3] 1 2 C100 100PF INA T17 KEYIN[4]/GPIO_30 H INS N14 KEYIN[5]/GPIO_31 H GP10_25_LD00N [11] I DESTRUCT ANY CORD OF TO IN6 P13 KEYIN[6]/SDA1/GP10_28 H LCLK_AUX/CLK_DSP/GPIO_24 ATV SCL [11] N7 P12 KEYIN[7]/SCL1/GPI0_29 H CG237_MAI NBOARD_V1.0_SCH HW-SC6610-SCH-2011-0001 2011/04/08 Hardware RD. LLB

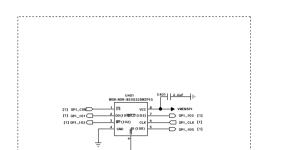
С

LS ITALCOM GROUP APPROVED 2011/04/08

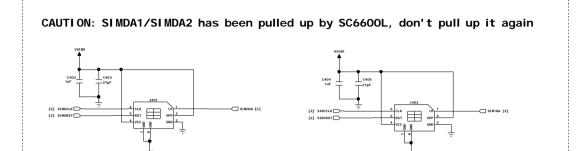




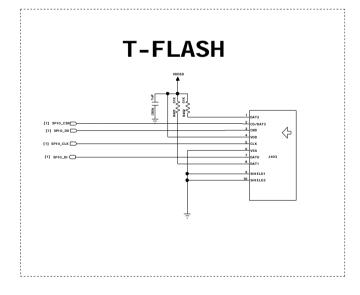




Serial Flash WSON-8



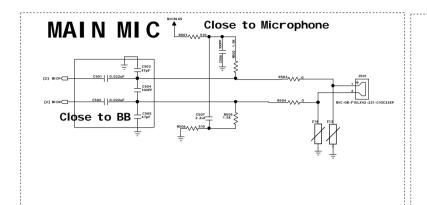
SIM1

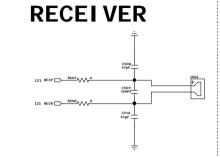


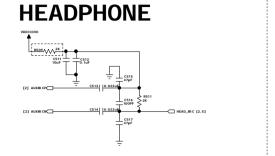
		TITLE & REV: CG237_MAI NBOARD_V1.0_SCH
		DOCUMENT NO.: HW-SC6610-SCH-2011-0001
DESIGNER:	DATED: 2011/04/08	DEPARTMENT: Hardware RD.
EQA: LS	DATED: 2011/04/08	COMPANY: ITALCOM GROUP
APPROVED:	DATED: 2011/04/08	SHEET: 4 OF 11
•		'

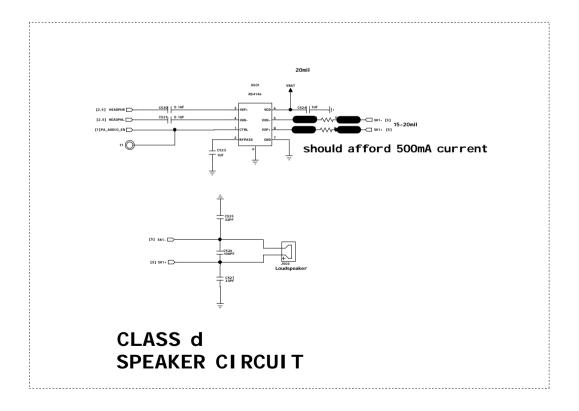
6 5 4 3 2

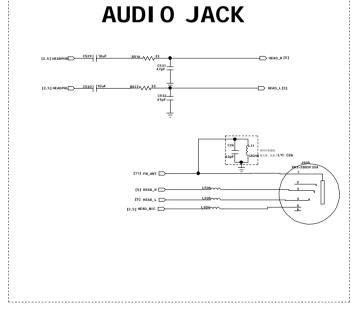
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:











		TITLE & REV: CG237_MAINBOARD_V1.0_SCH
		DOCUMENT NO.: HW-SC6610-SCH-2011-0001
DESIGNER:	DATED: 2011/04/08	DEPARTMENT: Hardware RD.
EGA: LS	DATED: 2011/04/08	ITALCOM GROUP
APPROVED:	DATED: 2011/04/08	SHEET: 5 OF 11

6 5 4 3 2 1

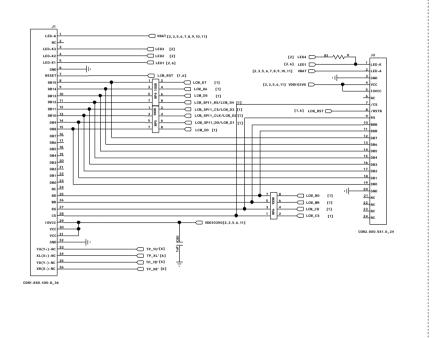
	REVISION REC		
LTR	ECO NO:	APPROVED:	DATE:

TP

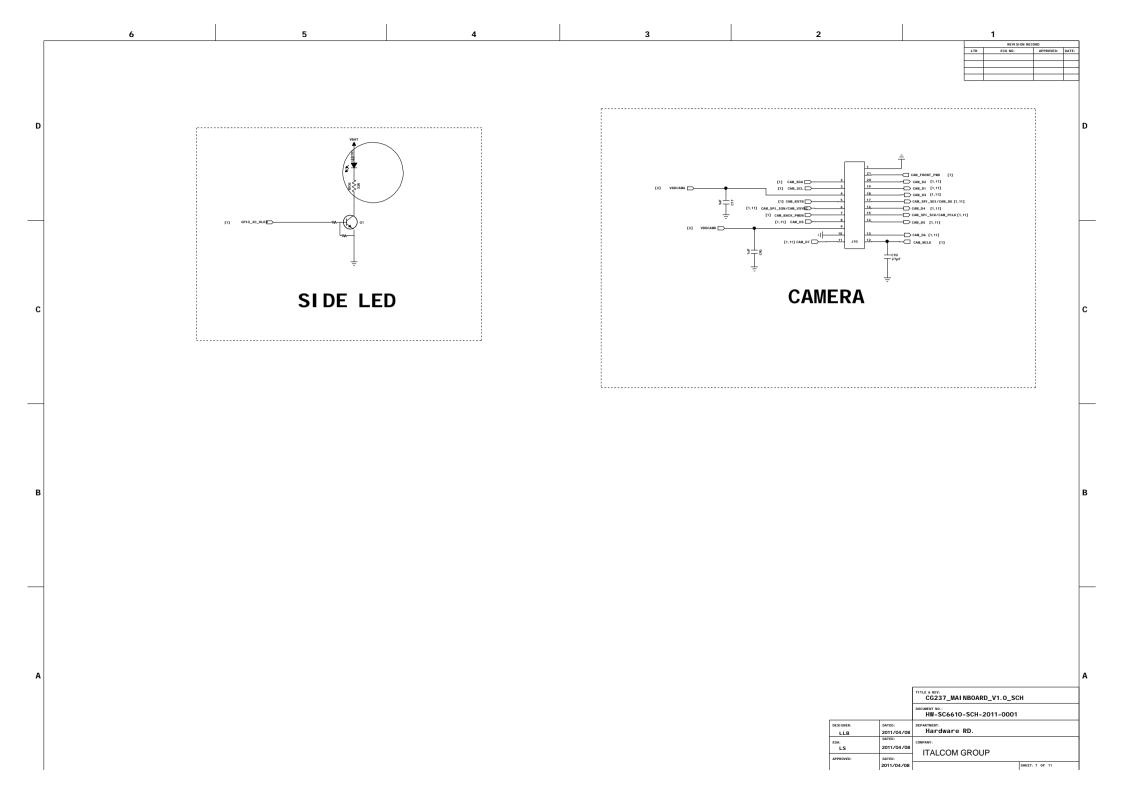
NOTE: Do not change the Touchpanel Circuit



LCD



		TITLE & REV: CG237_MAI NBOARD_V1. O_SCH
		DOCUMENT NO.: HW-SC6610-SCH-2011-0001
DESIGNER:	DATED: 2011/04/08	DEPARTMENT: Hardware RD.
EQA: LS	DATED: 2011/04/08	COMPANY: ITALCOM GROUP
APPROVED:	DATED: 2011/04/08	SHEET: 6 OF 11



CHARGE [2]I SENSE R801.1 VV 2 10 should afford 1A current should afford 1A current All trace connectted to the MOSFET should be as wide as possible BAT CONNECT TTLE & REV: CG237_MAI NBOARD_V1.0_SCH CUMENT NO.: HW-SC6610-SCH-2011-0001 DESIGNER: PARTMENT: Hardware RD.

> EQA: LS

ITALCOM GROUP

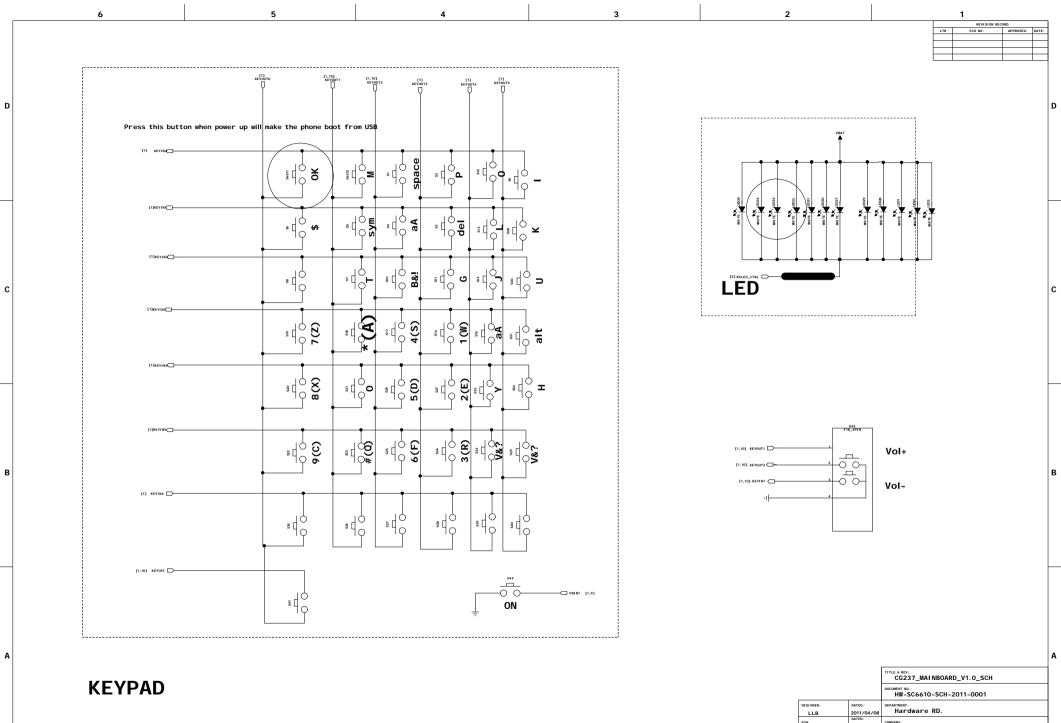
VI BRATOR 5PIN_I/O CONNECTOR Boot select: UORTSN/NBOOT/U1TXD = Iow --> boot from USB UORTSN/NBOOT/U1TXD = high --> boot from NOR oMUJARTused by the two USB HOLE and MARK POINT [1,2,3,4,5,6,7,8,10,11]CND [2,9] USB DP [2,9] USB_DM TEST POINT CG237_MAI NBOARD_V1.0_SCH HW-SC6610-SCH-2011-0001

Hardware RD.

ITALCOM GROUP

LLB EQA: LS

2011/04/08



EQA: LS 2011/04/08 ITALCOM GROUP DATED: 2011/04/08

