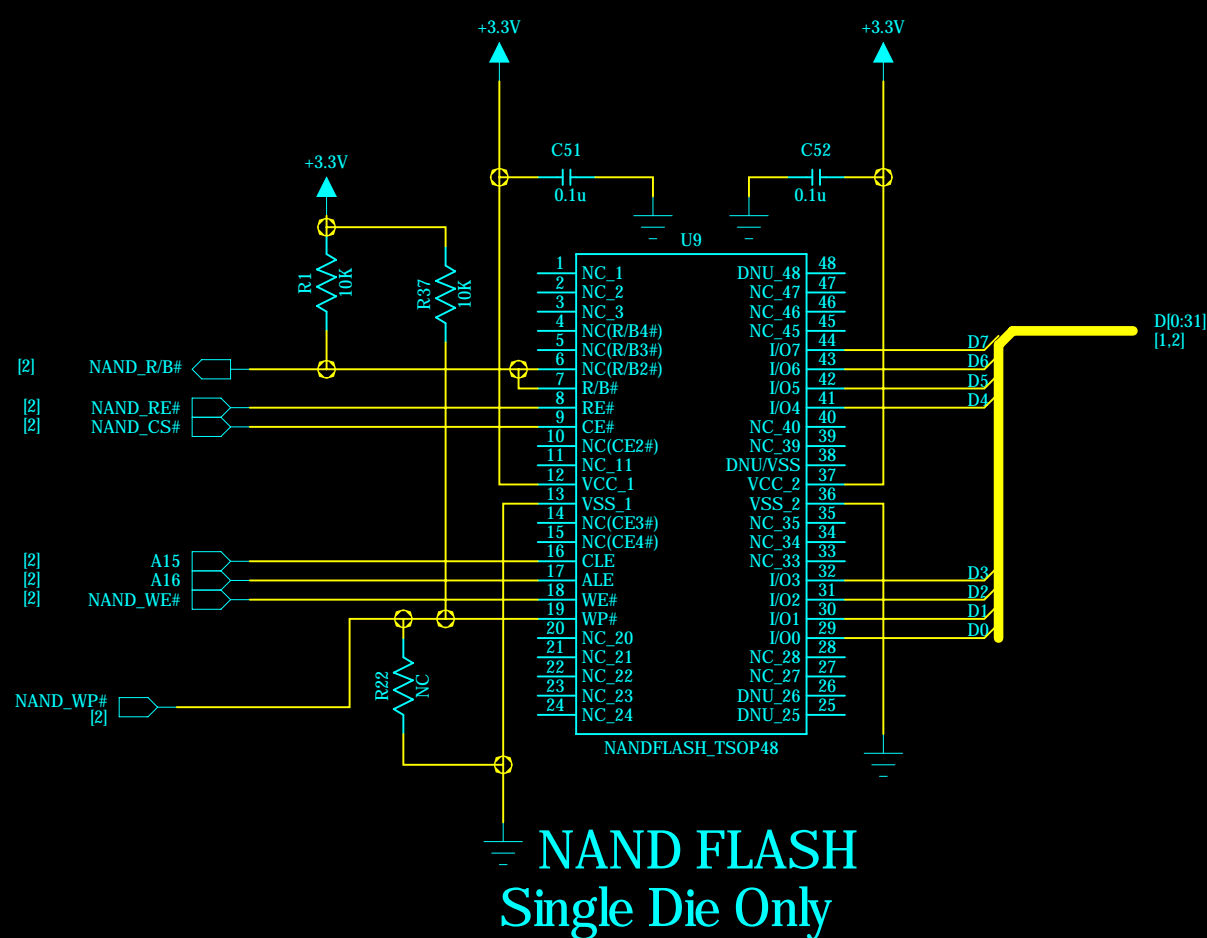


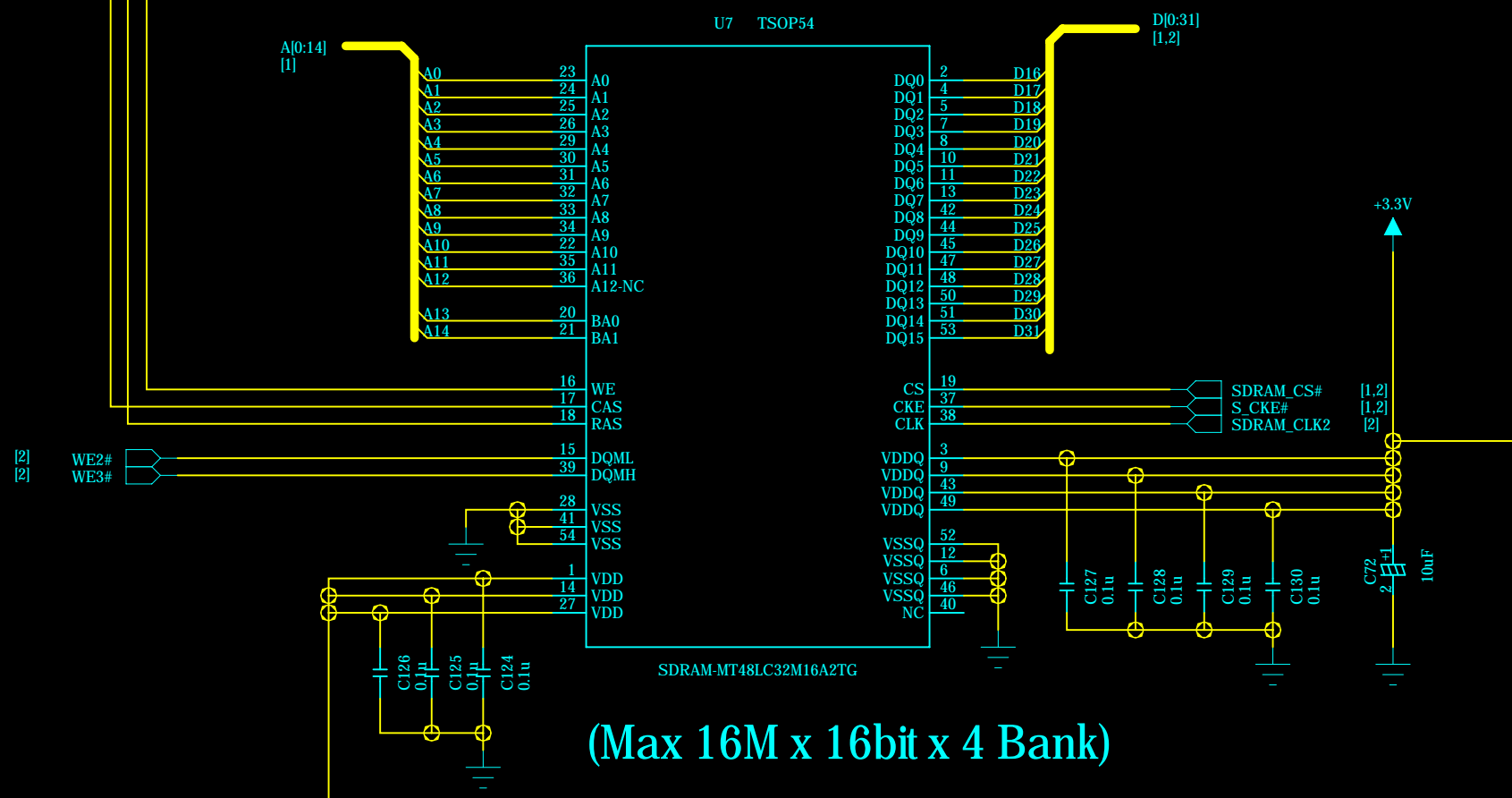
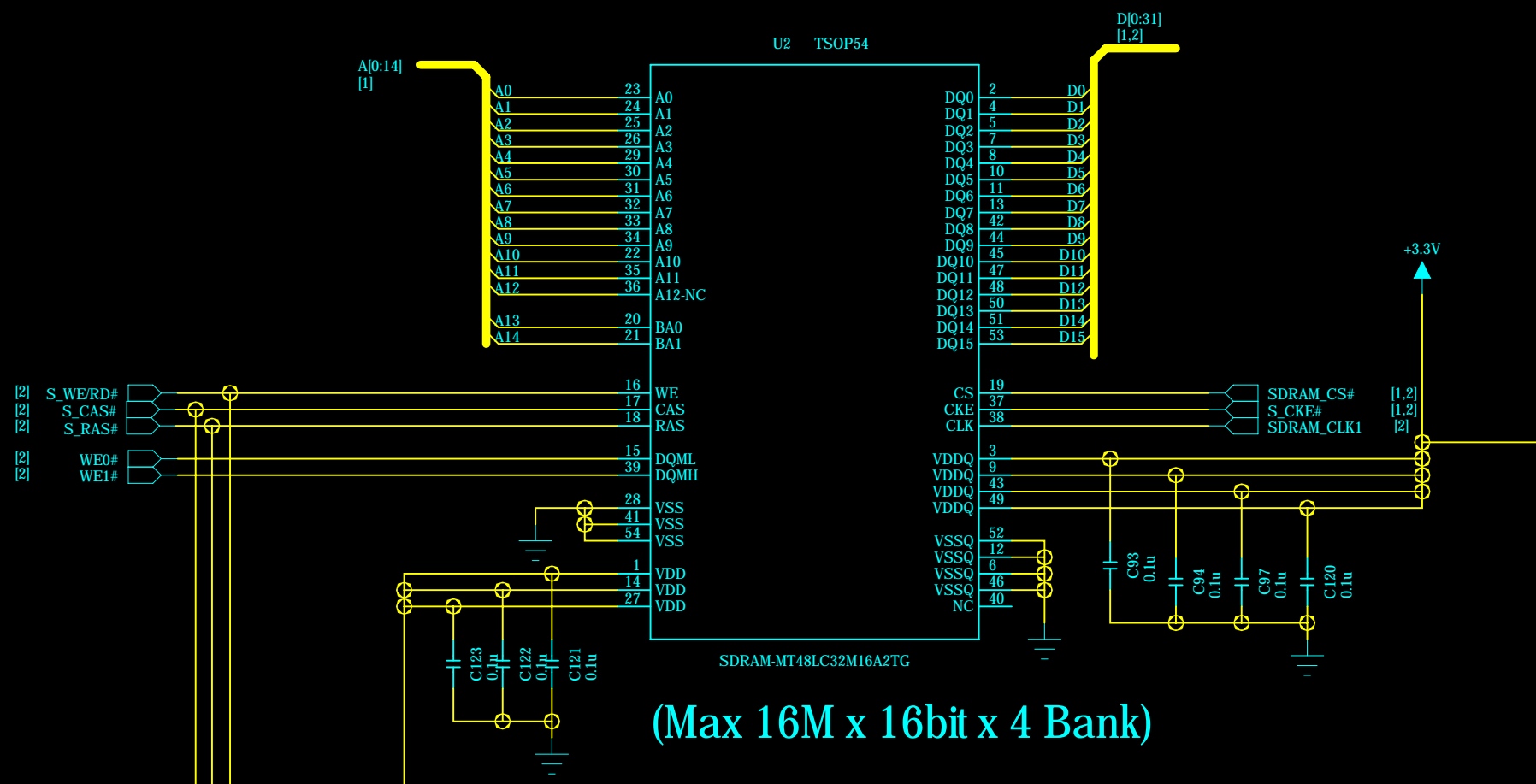
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

D

D



When Programing,J1 should connected.



B

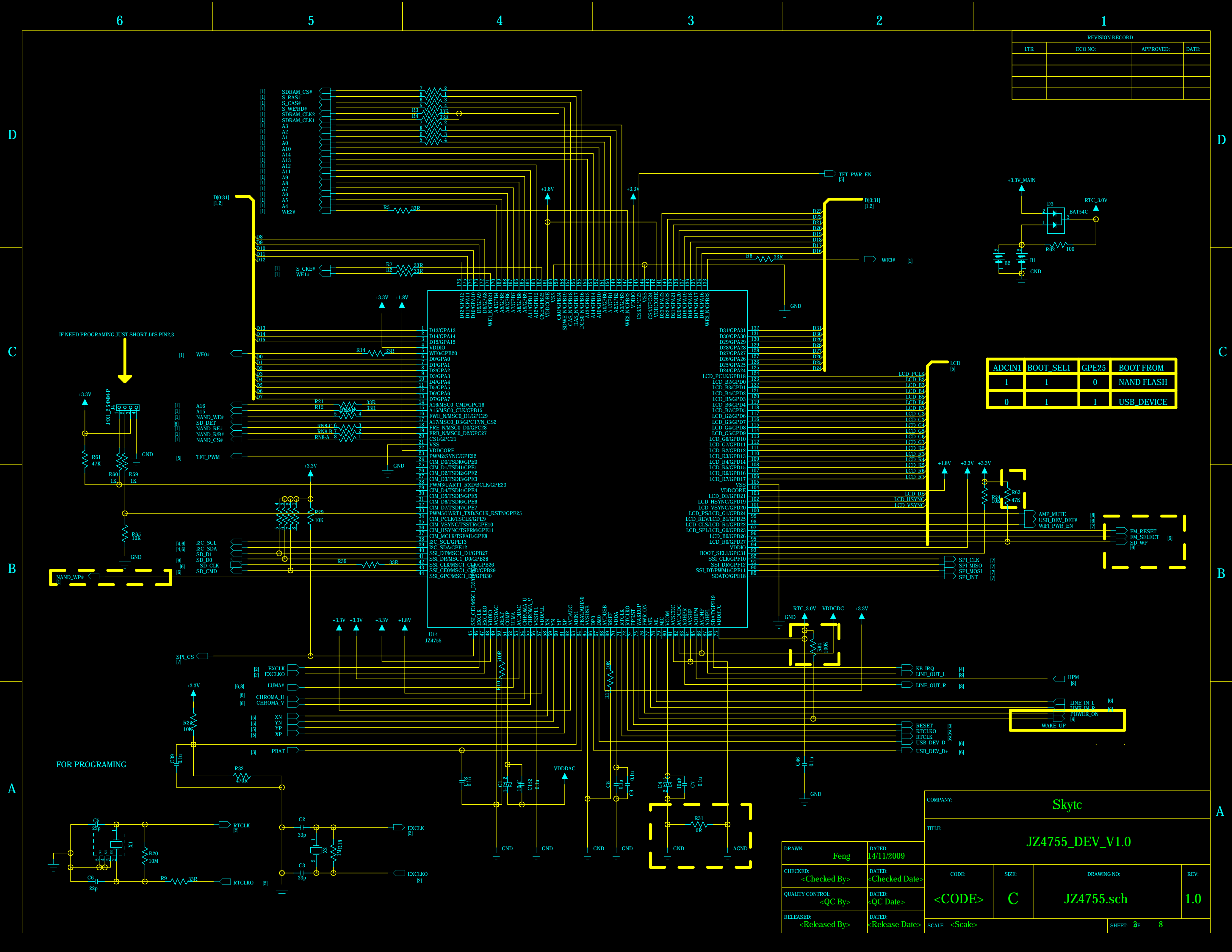
B

A

A

DRAWN: <b>Feng</b>	DATED: <b>14/11/2009</b>
CHECKED: <b>&lt;Checked By&gt;</b>	DATED: <b>&lt;Checked Date&gt;</b>
QUALITY CONTROL: <b>&lt;QC By&gt;</b>	DATED: <b>&lt;QC Date&gt;</b>
RELEASED: <b>&lt;Released By&gt;</b>	DATED: <b>&lt;Release Date&gt;</b>

COMPANY: Skytc			
TITLE: JZ4755_DEV_V1.0			
CODE: <b>&lt;CODE&gt;</b>	SIZE: <b>C</b>	DRAWING NO: <b>JZ4755.sch</b>	REV: <b>1.0</b>
SCALE: <b>&lt;Scale&gt;</b>		SHEET: <b>0F</b>	<b>8</b>



REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

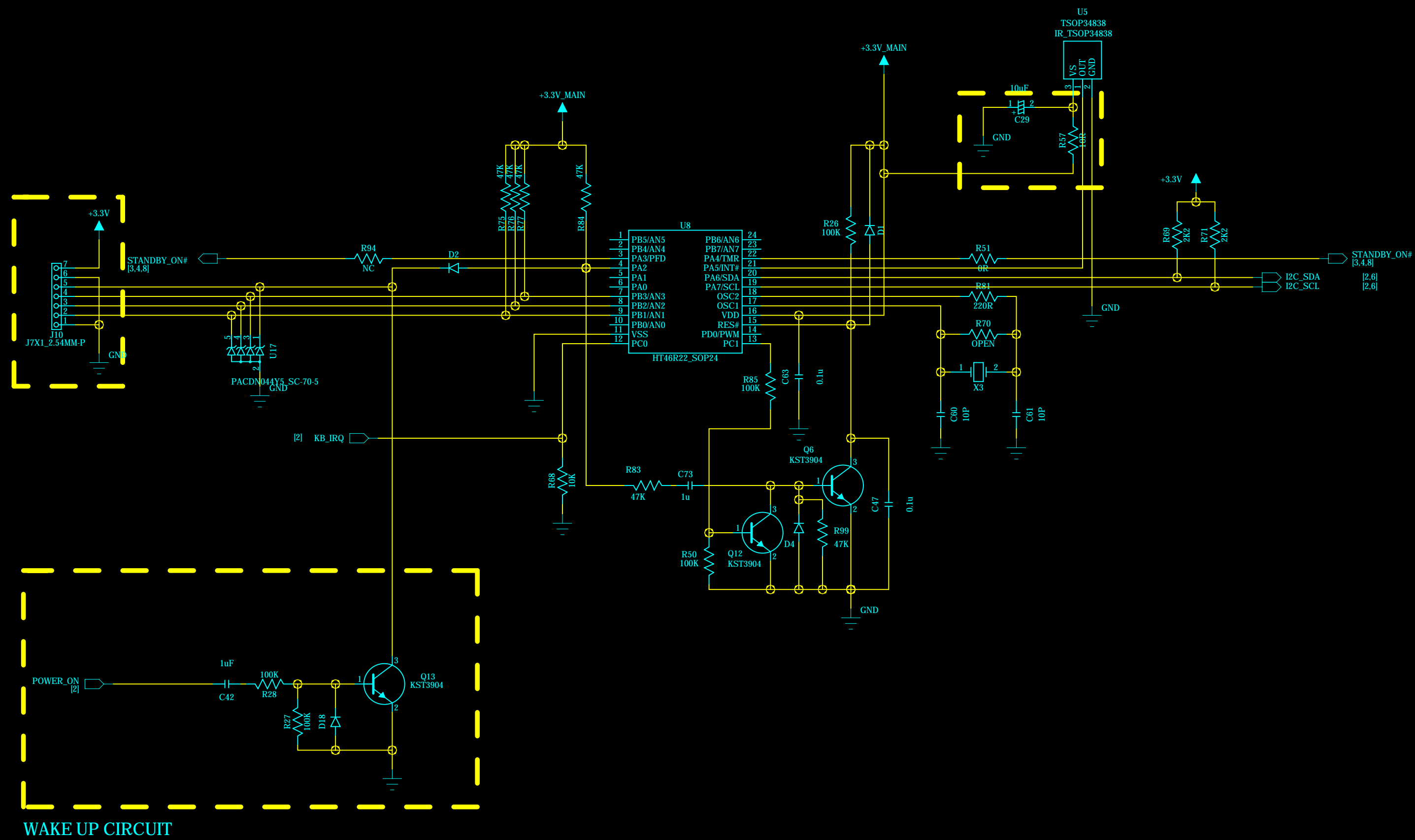
ADCIN1	BOOT_SEL1	GPE25	BOOT FROM
1	1	0	NAND FLASH
0	1	1	USB_DEVICE

COMPANY: Skytc			
TITLE: JZ4755_DEV_V1.0			
CODE:	SIZE: C	DRAWING NO: JZ4755.sch	REV: 1.0
SCALE: <Scale>		SHEET: 8f 8	

DRAWN: Feng	DATED: 14/11/2009
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



## WAKE UP CIRCUIT

DRAWN: <b>Feng</b>	DATED: <b>14/11/2009</b>
CHECKED: <b>&lt;Checked By&gt;</b>	DATED: <b>&lt;Checked Date&gt;</b>
QUALITY CONTROL: <b>&lt;QC By&gt;</b>	DATED: <b>&lt;QC Date&gt;</b>
RELEASED: <b>&lt;Released By&gt;</b>	DATED: <b>&lt;Release Date&gt;</b>

COMPANY: Skytc			
TITLE: JZ4755_DEV_V1.0			
CODE:  <CODE>	SIZE:  C	DRAWING NO:  JZ4755.sch	REV:  1.0
SCALE: <Scale>		SHEET: 0f	8

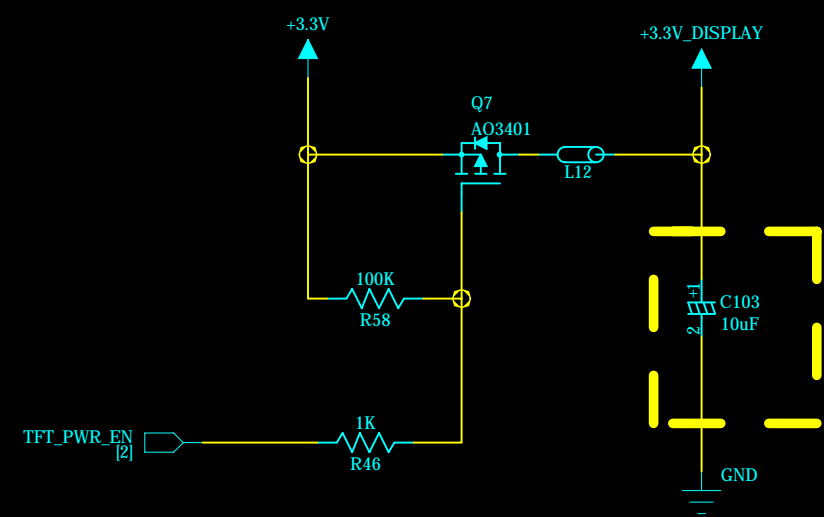
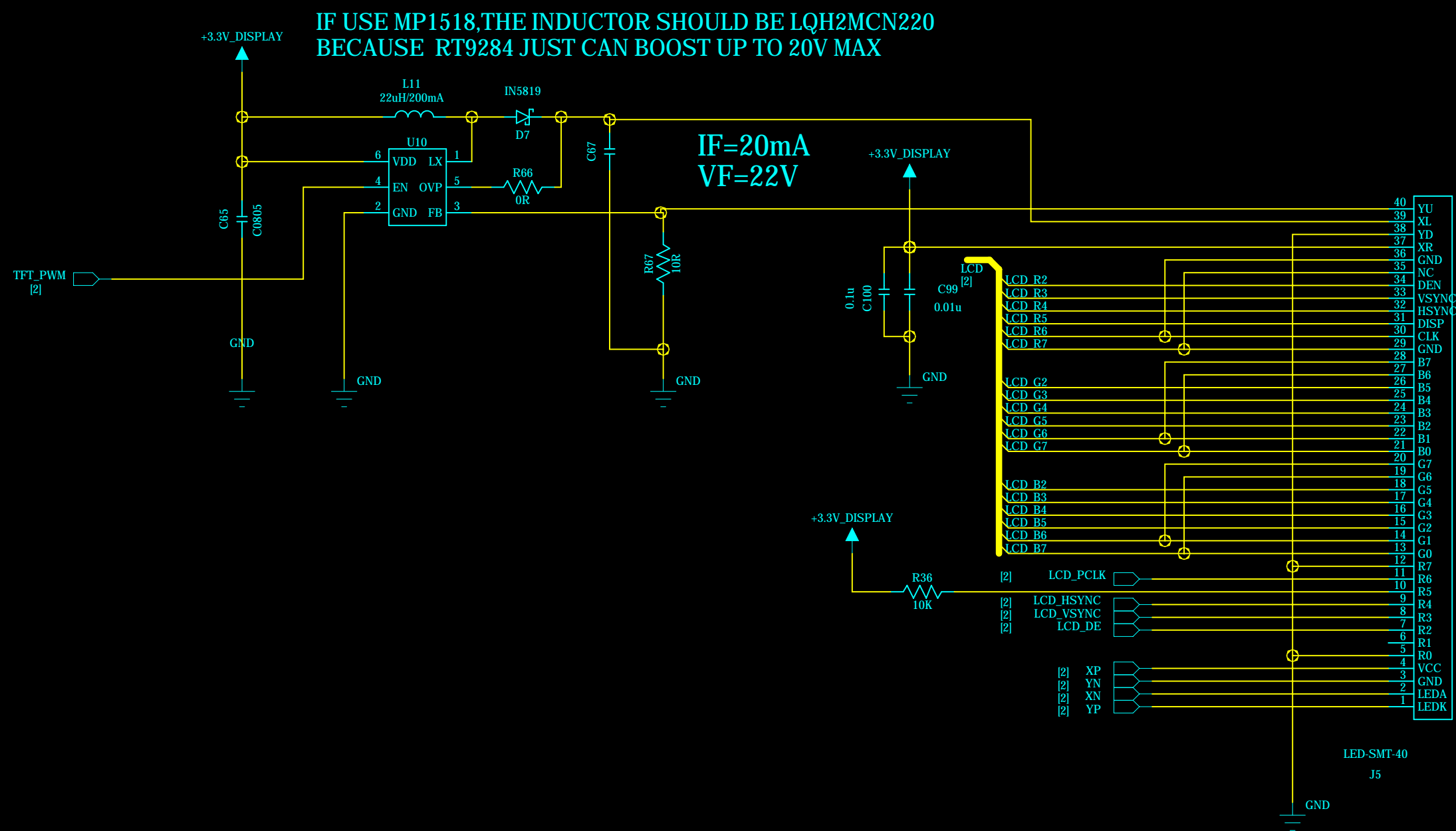
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

D

D

C

C



B

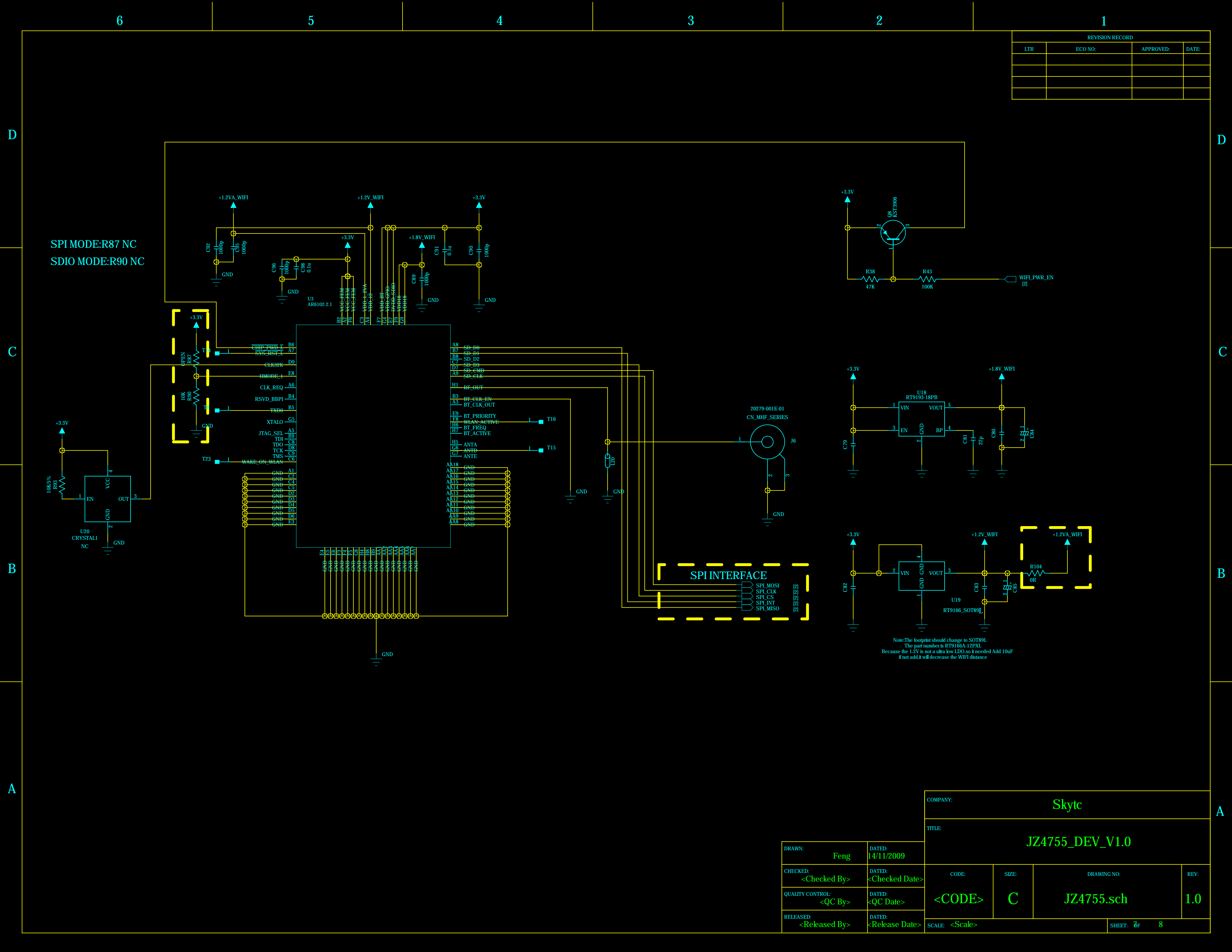
B

A

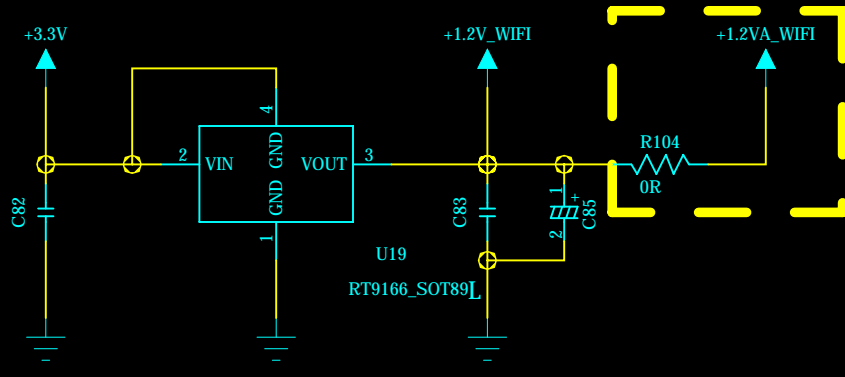
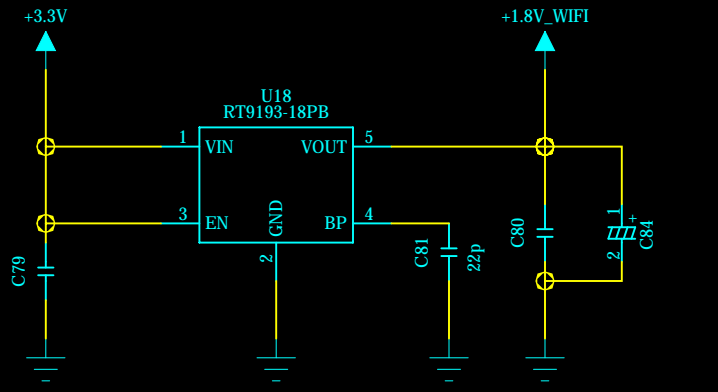
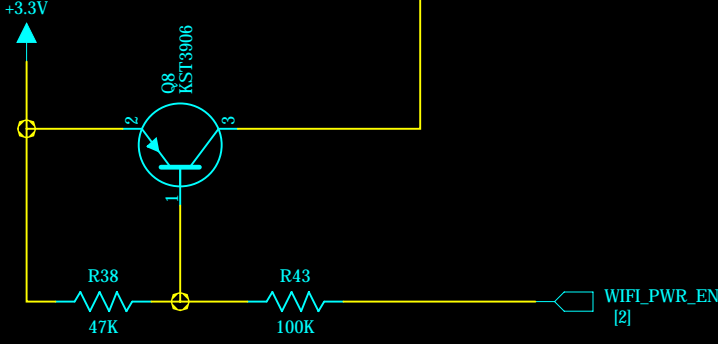
A

		COMPANY: <div>Skytc</div>				
		TITLE: <div>JZ4755_DEV_V1.0</div>				
DRAWN: <div>Feng</div>	DATED: <div>14/11/2009</div>	CODE: <div>&lt;CODE&gt;</div>	SIZE: <div>C</div>	DRAWING NO: <div>JZ4755.sch</div>	REV: <div>1.0</div>	
CHECKED: <div>&lt;Checked By&gt;</div>	DATED: <div>&lt;Checked Date&gt;</div>					
QUALITY CONTROL: <div>&lt;QC By&gt;</div>	DATED: <div>&lt;QC Date&gt;</div>					
RELEASED: <div>&lt;Released By&gt;</div>		DATED: <div>&lt;Release Date&gt;</div>			SCALE: <Scale>	SHEET: 6F 8





REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:



Note:The footprint should change to SOT89L  
The part number is RT9166A-12PXL  
Because the 1.2V is not a ultra low LDO,so it needed Add 10uF  
if not add,it will decrease the WIFI distance

COMPANY: Skytc			
TITLE: JZ4755_DEV_V1.0			
CODE:	SIZE: C	DRAWING NO: JZ4755.sch	REV: 1.0
SCALE: <Scale>			SHEET: 0f 8

DRAWN: Feng	DATED: 14/11/2009
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

