

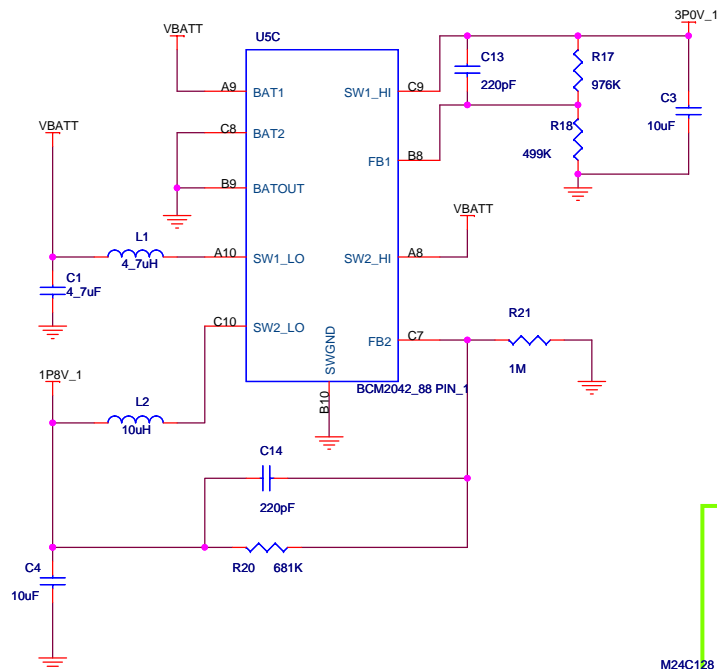
REVISION HISTORY

Revision 0000-P2 -- EEPROM for BCM2042A1 set switcher VDDA output to 1.85v, and using VDDA for on chip LDO input  
Revision 0020-P1 -- Put additional resistor R22 to avoid Xtal startup issue  
Revision 0020-P2 -- update the components value on "pi" low pass filter for 2nd and 3rd harmonic reduction

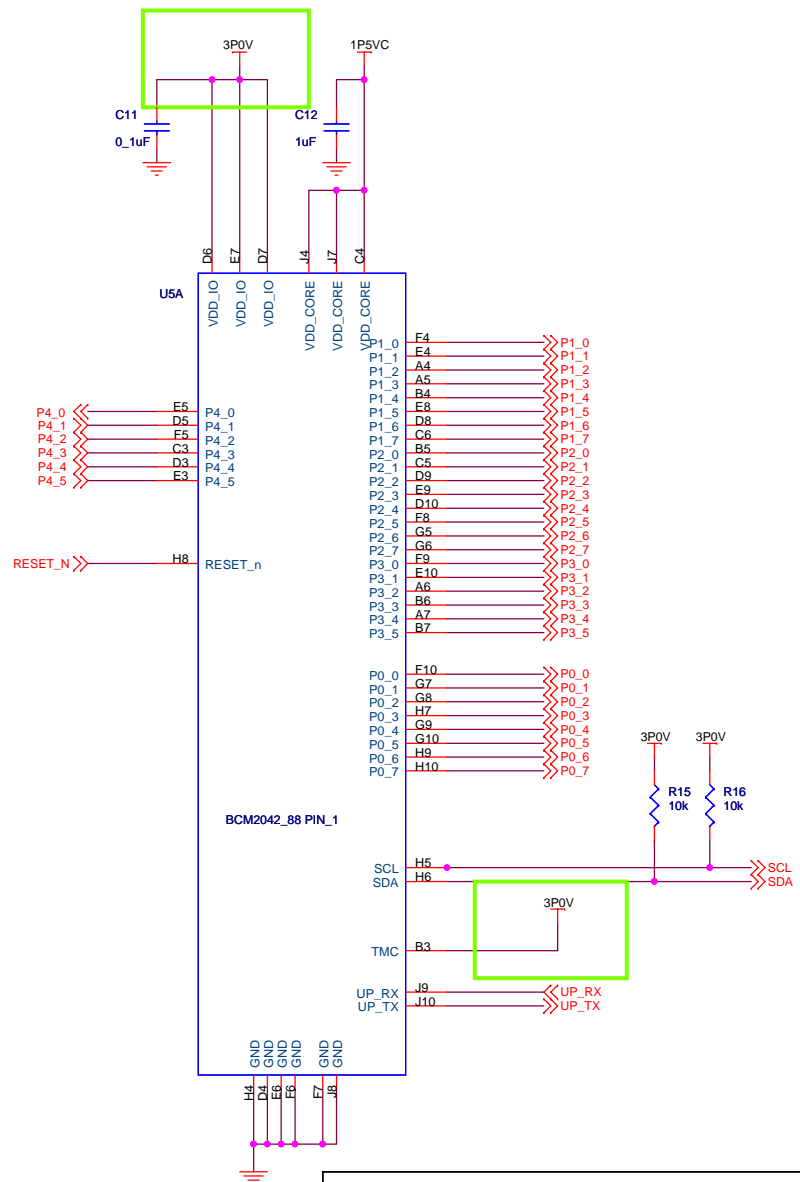
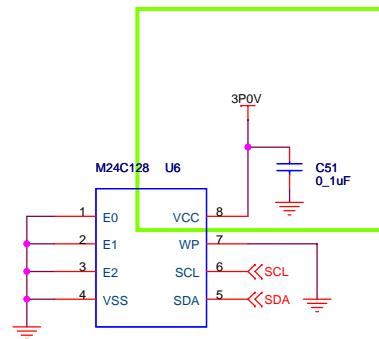


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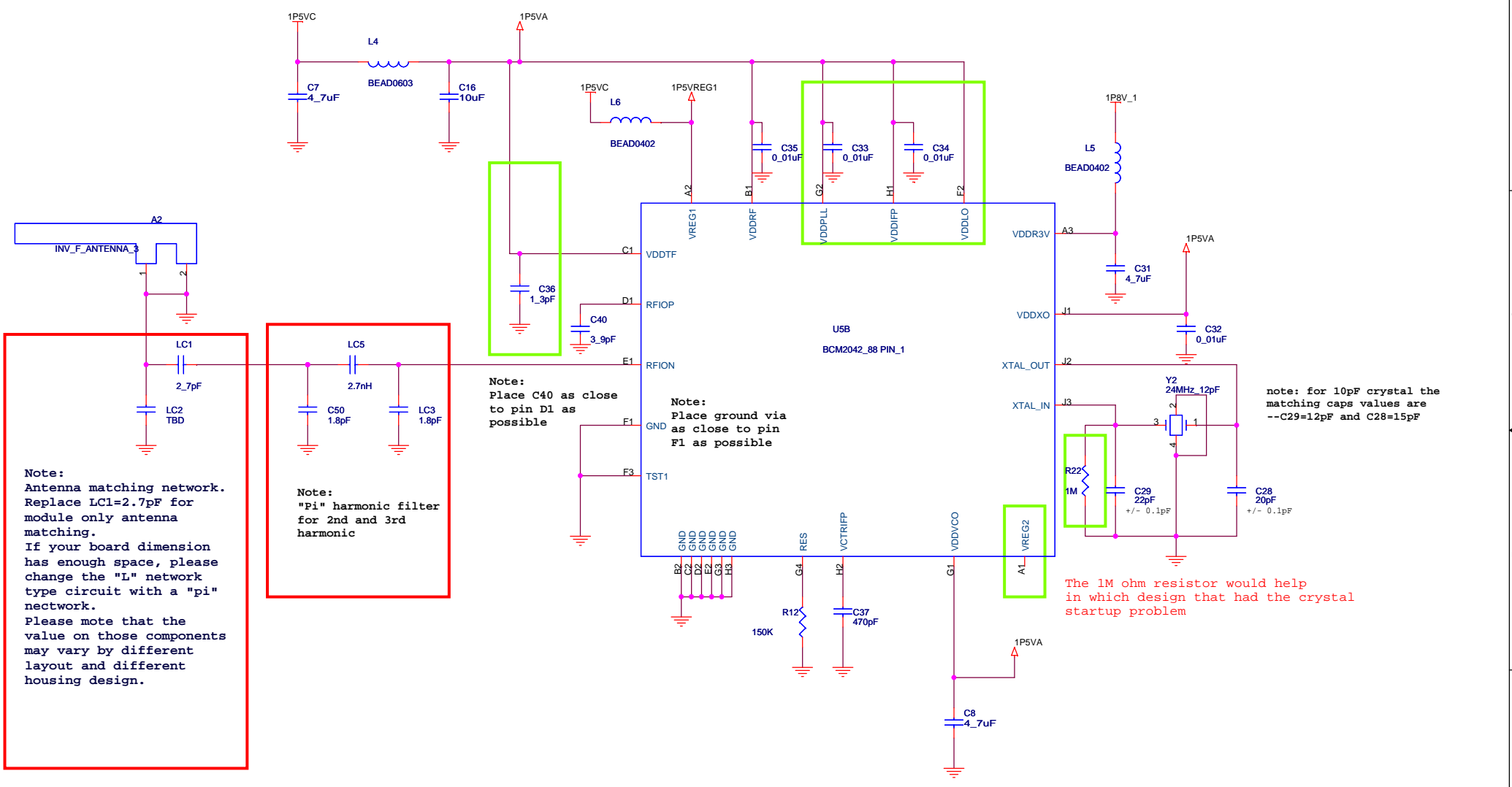


Note: The switching regulator is configured for 2 battery cells in serial.



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