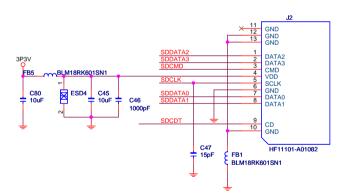
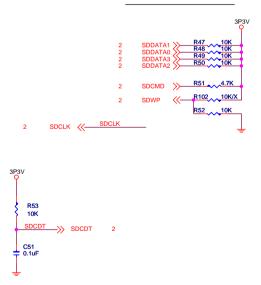
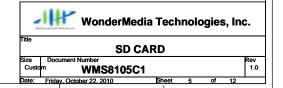


MicroSD Socket

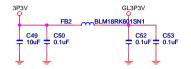


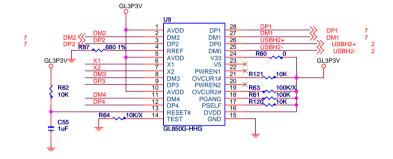
PU Resistors:

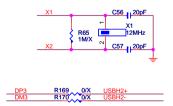


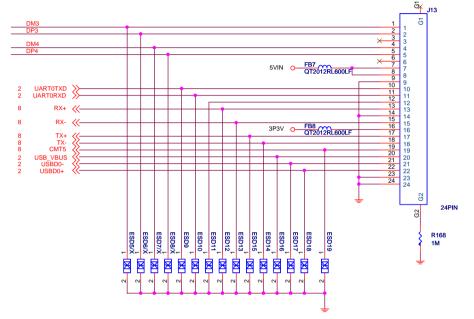


USB HUB/24pin CONN

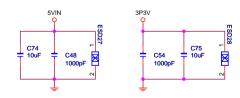


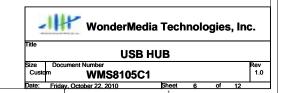




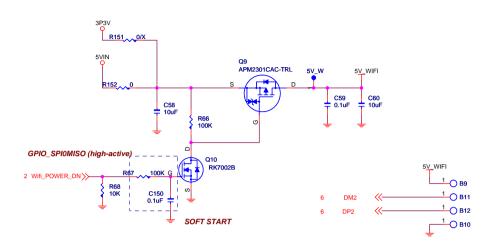


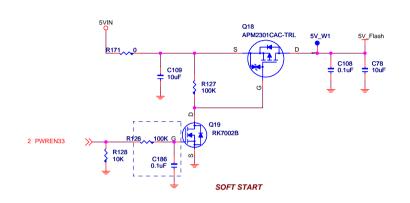
ESD5, ESD6, ESD7, ESD8 default to unmount



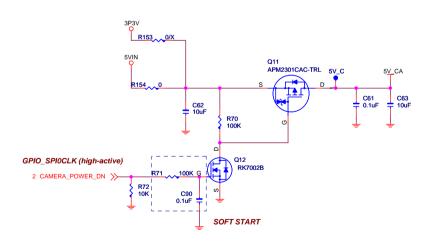


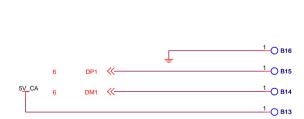
USB WiFi/CAMERA



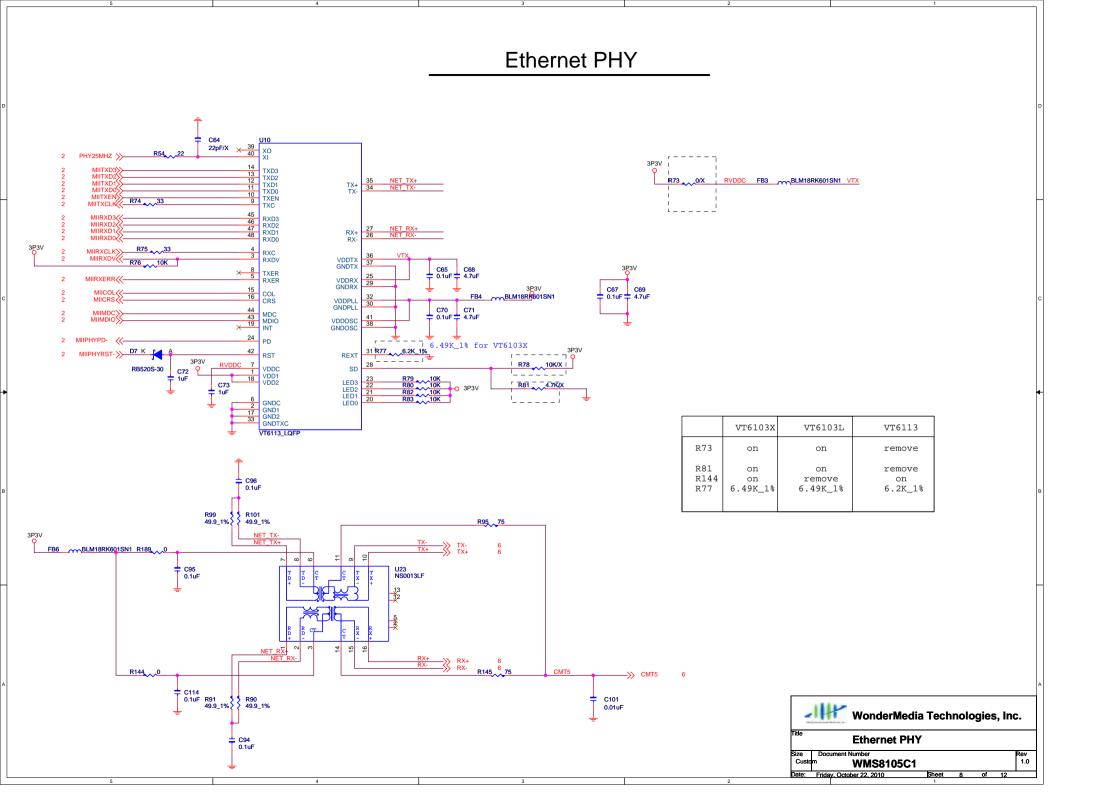


1 B18





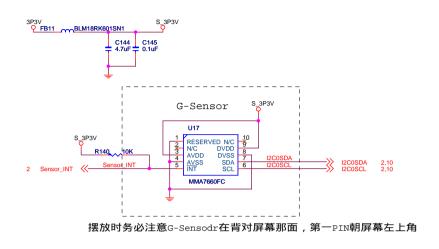


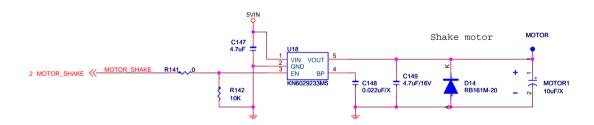


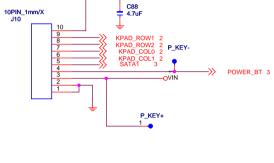
LCD PANEL 9631S-50Y913 16V D9 BAT54S C93 22pF/X R100 51K_1% AVDD_10V R107 18K/X C97 C98 C99 C100 0.1uF 0.1uF 0.1uF R215 3.3K_1% C105 C106 T0.1uF T4.7uF R110 10K/X ESD close to J8 R58 100K_1% 2 LCD_BL_DIM <<-Close to CS7146 R105 1.8 R106 1.8 WonderMedia Technologies, Inc. LCD PANEL WMS8105C1

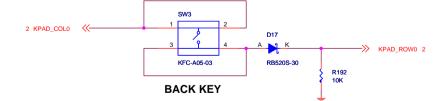
R117 **I2S CODEC** HeadPhone FB14 R118 4.7K R119 4.7K LCD TOUCH POWER 2,3,4,9 PWREN33 C185 BLM18RK601SN1 C107 -R122 100K HP_DECT R131____1K AMP_EN R133 10K LINEOUT_2L HP_INL LINEOUT_2R HP_INR I2S_LRCLK R214 100 DACLRC MIC_IN_L C195 1uF R181 R180 49.9_1% 49.9_1% C178 1uF MICBIAS VMID R112 0 C142 C143 C134 270pF/X LINEOUTL C136, 2200pF R137, 200K_1% MCIE: master clock. I SCHE andio date bit clock. I/O DIENCE NO. audio date left and right clock. I/O ASDOUR: DAC audio date left and right clock. I/O ASDOUR: ACC audio data. O AMENCE ACC audio data. O 10uF/X SPKL1 BYPASS VDD 1000pF 1 SHDN GND 7 C139 10uF C141 AMP_AGND U25 EUA2036_TDF Onboard MIC I _ _ _ _ AMP_AGND I GND CLOSED R123 closed to MIC MICBIAS R123___1K WonderMedia Technologies, Inc. **I2S CODEC** WMS8105C1

G-Sensor & Shake



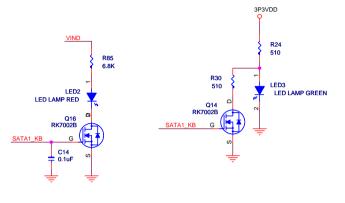


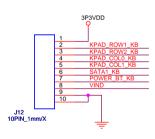


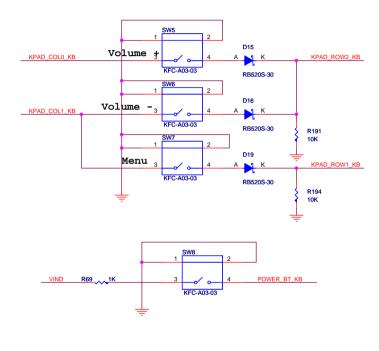


	WonderMedia Technologies, Inc.								
Title	G-Sensor & shake								
Size Custo	Document Number WMS81050	C1				Rev 1.0			
Date:	Friday, October 22, 2010	Sheet	11	of	12	•			

Keypad board



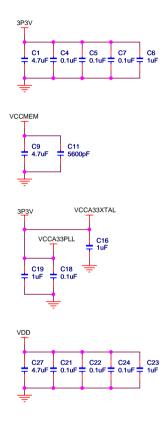


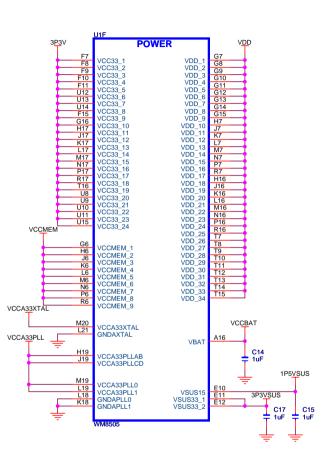


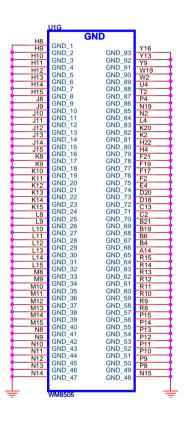
WonderMedia Technologies, Inc.							
Title	Title Keypad Board						
Size	Document Number					Rev	
А3	WMS8105C1					1.0	
Date:	Friday, October 22, 2010	Sheet	12	of	12		

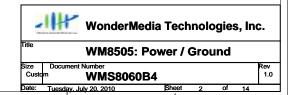


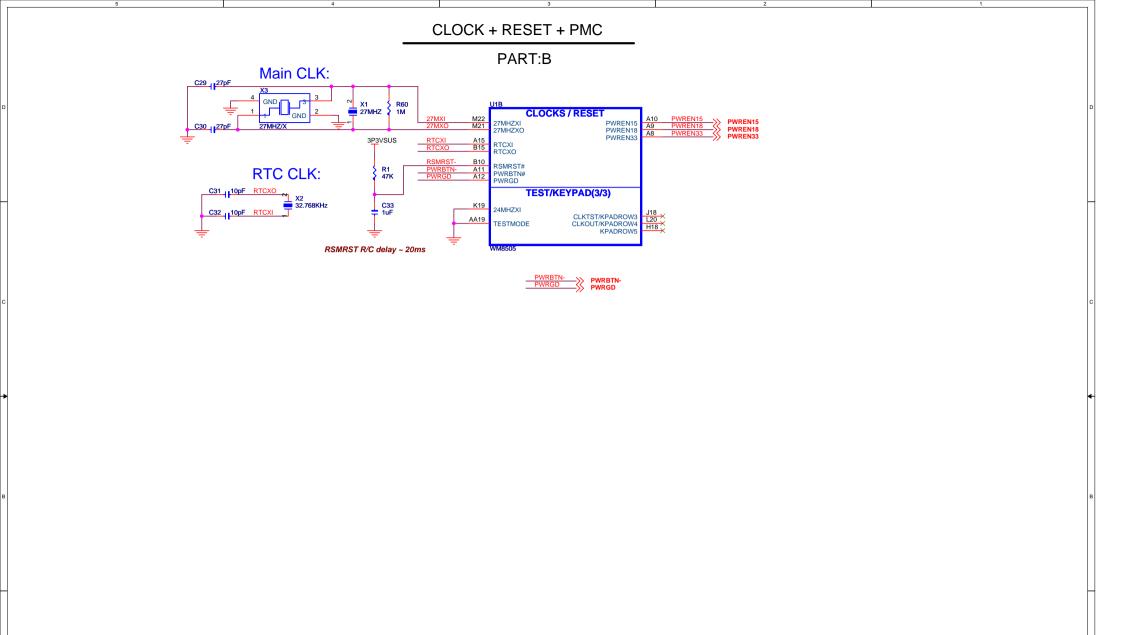
PART: F+G

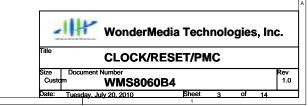


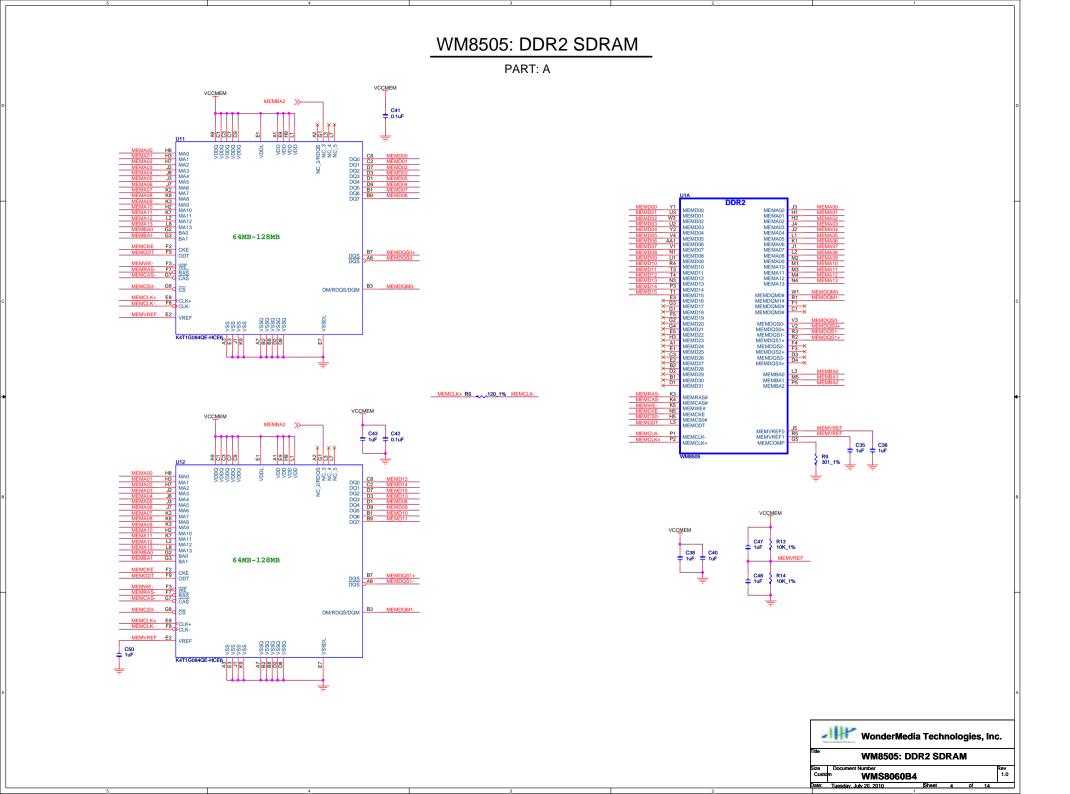




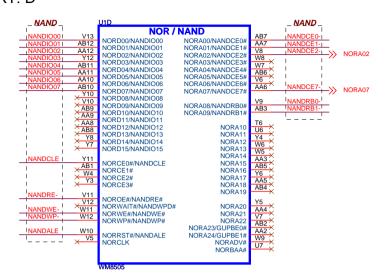




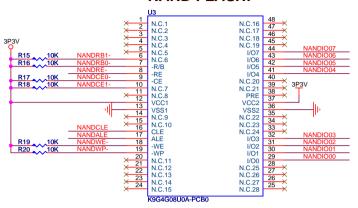








NAND FLASH:

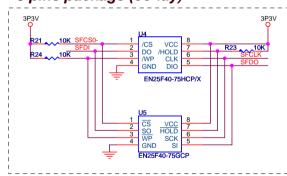


WM8505: SPI FLASH

PART: O

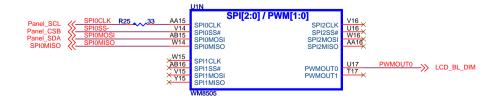


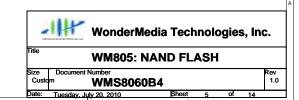
8 pins package (co-lay)

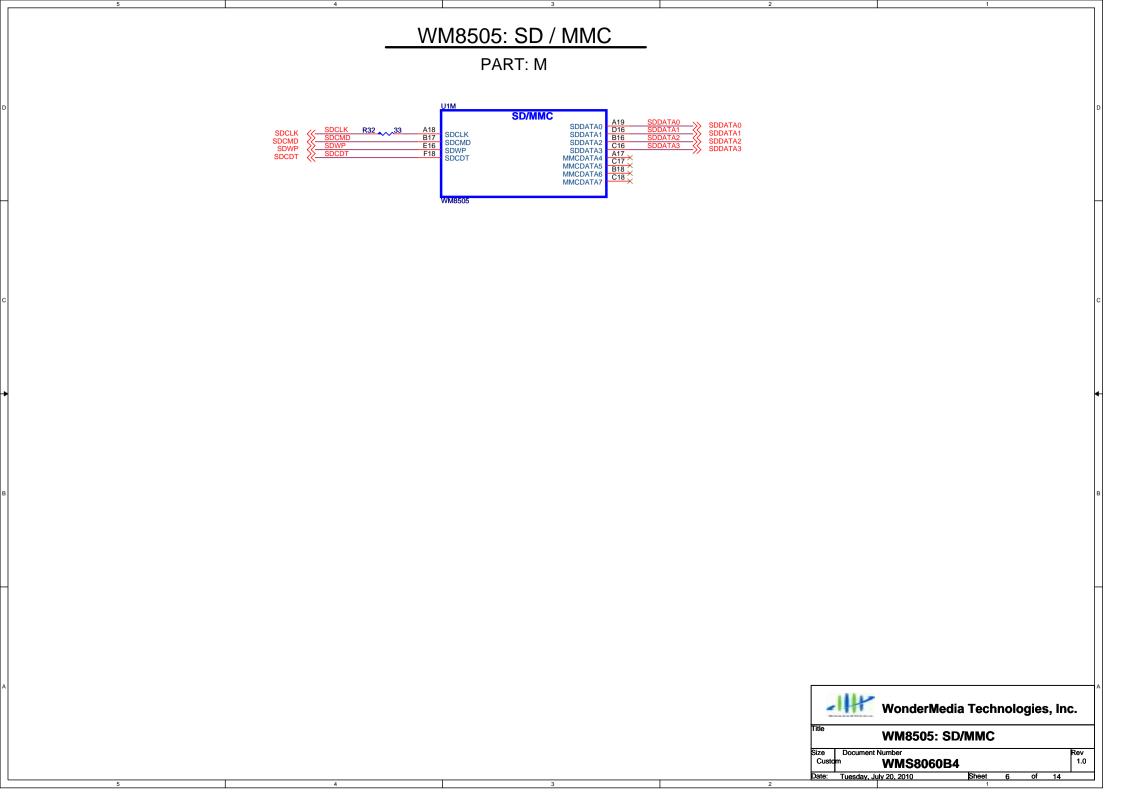


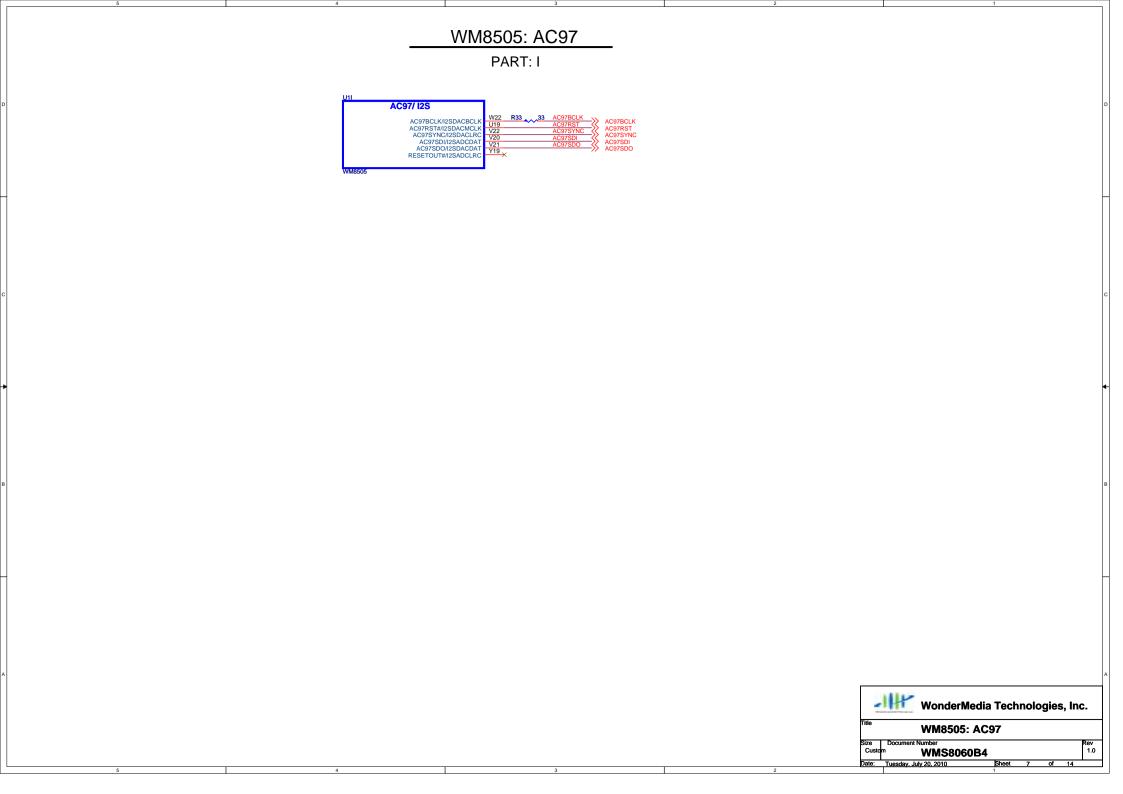
WM8505: SPI + PWM

PART: N









WM8505: USB (HOST+DEVICE) PART: K





VCCA33USBPLL (suspend power)



VCCA15USBPLL (suspend power)

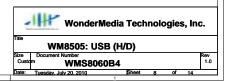


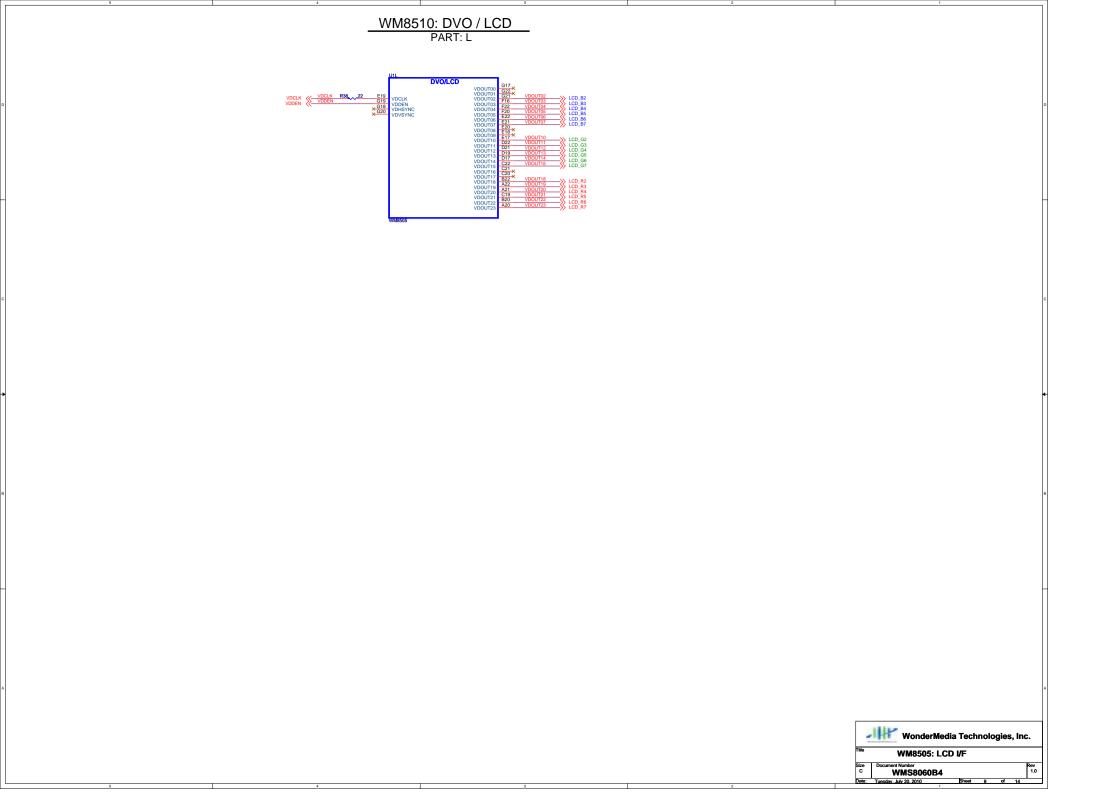
VCC33USB (suspend power)



(USBH2) to USB A TYPE HOST single right-angle connector. (USBH1) to WiFi miniCard connector (USBD0) to USB mini-B connector.







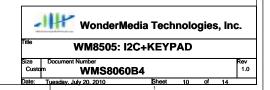
WM8505: I2C / KEYPAD PART: R



WM8505: VDIN / CMOS / KEYPAD

PART: J





WM8505: GPIO+STRAPPING+WAKEUP



* IMPORTANT: If IO board not use GPIO, please connect GND;

GPIO Pins: Function:

GPIO0 DCIN Detect
GPIO1 Charging Status
GPIO2 Wifi_POWER_DN

GPIO3 AC97/I2S Amp ShutDown/Mute GPIO:

GPIO4 Battery Detect
GPIO5 Reserved
GPIO6 Reserved

GPIO6 Reserved
GPIO7 SD Card Power

Strapping Option: NORA02 NORA02 NORA02 R44 A7K Enable Keypad function,

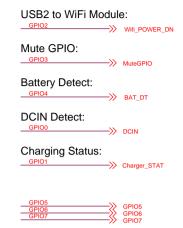
* Note: If IO board not use Keypad, please connect KPAD_ROW0~KPAD_ROW2 to GND.

STRAPPING TABLE:

OTTOAL TIME TA	<i></i>			
PIN_NAME	RS	ON/OFF	Internal	DESCRIPTIONS
NORA[2:1]	R221,R109		PD,PD	NOR Boot
		ON-OFF		=>SPI Flash Boot,
NORA7	R118		PD	Disable Keypad function, default N.C.
		ON		=>Enable Keypad function,

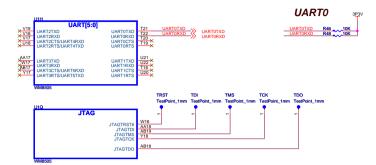
WARMING:

ALL others strappings not listed above recommend to use internal settings as default. Otherwise un-expected results may encounter.

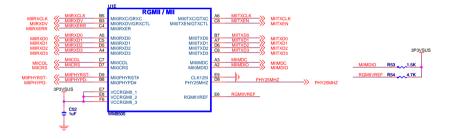




WM8505: UART/JTAG PART: H+Q



WM8505: RGMII/MII PART: E

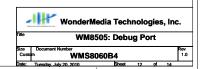


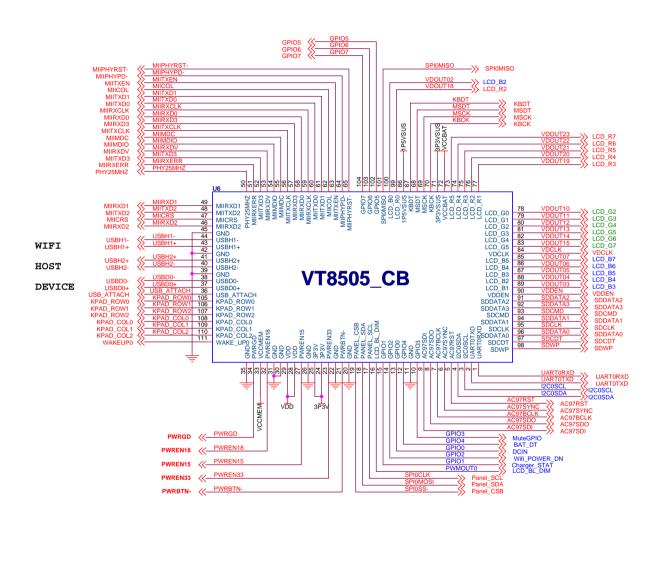
WM8505: PS2 KBDC PART: S

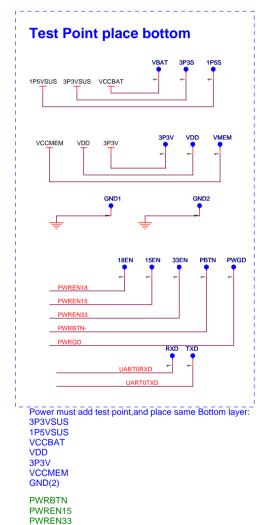


WM8505: VGA (DAC) PART: C









WonderMedia Technologies, Inc.
WM8505: CORE BOARD PIN BALL

WMS8060B4

1.0

PWREN18

