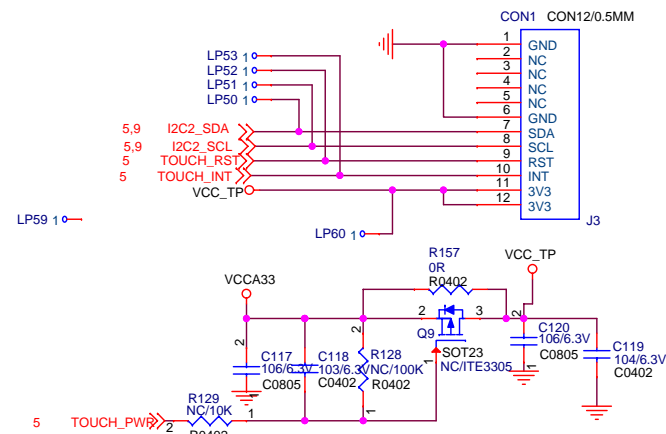
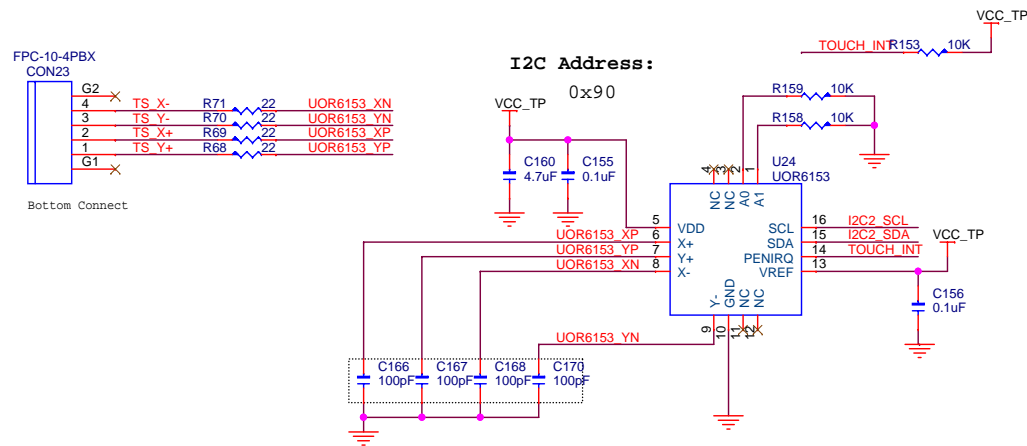


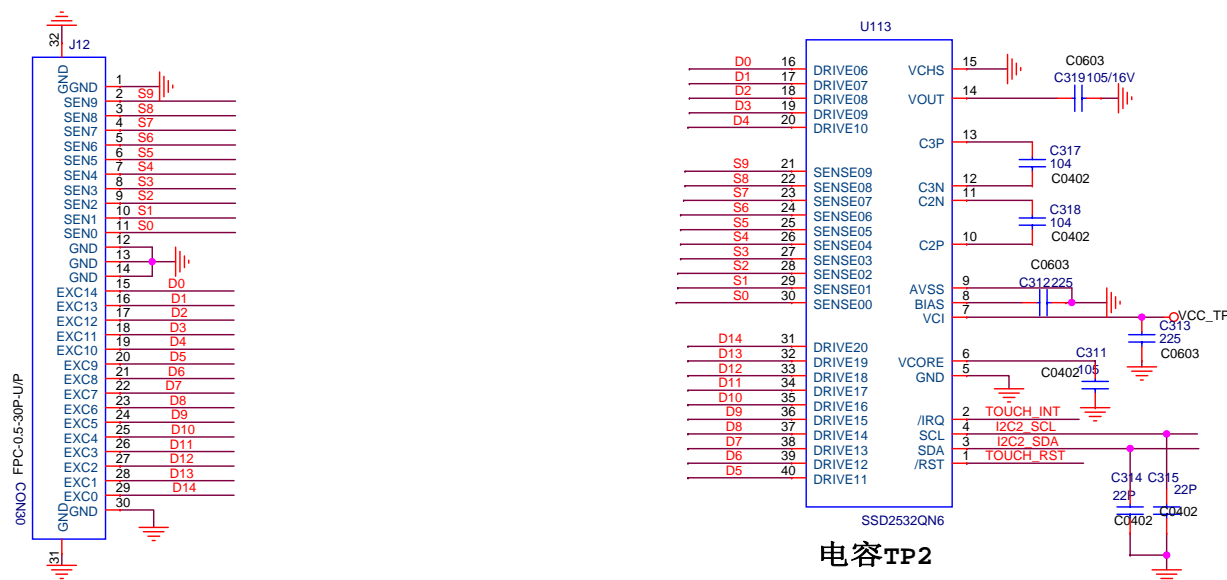
Version	Date	Author	Change Note	Approved
V1.0	20120117	HCH	First edictor	

NOTES:
1.I2C0, I2C1 only used for the regular power supply module ;
2.UART1 only use for debugging;
3.The IO be defauled as input port, please note that the pull-up port can not be set to pull-down function;
the drop-down port can not be set to pull-up function;
4.The default status of GPIO please reference RK2906 IO list from ROCKCHIP;



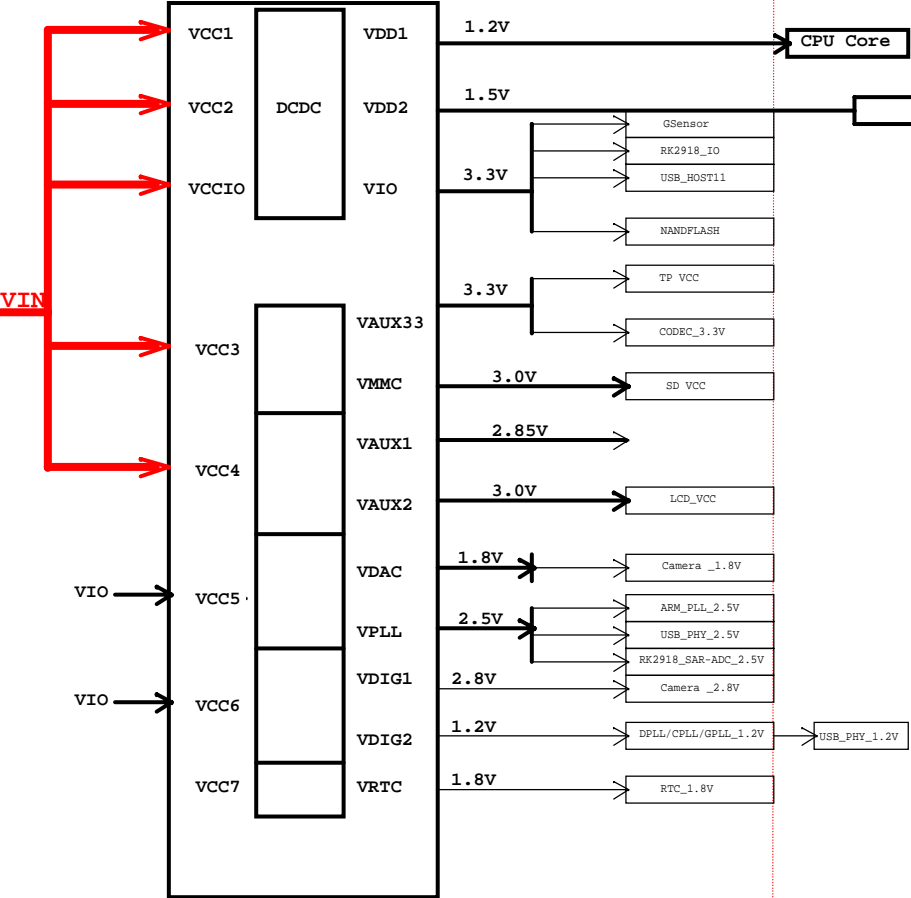
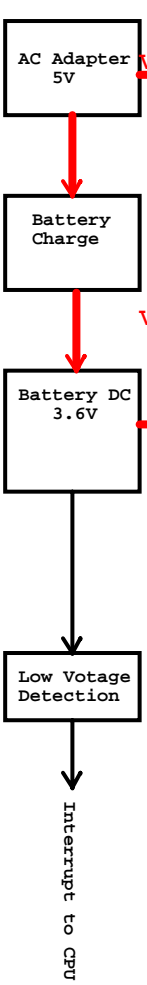
部分TP的电源需要控制，请参考相关Datesheet！
在这种情况下，建议I2C接RK2918的I2C3口！

电容TP1



电容TP2

Title		
<Title>		
Size	Document Number	Rev
B	<Doc>	<RevCode>
Date:	Friday, June 08, 2012	Sheet 1 of 1



TPS659102

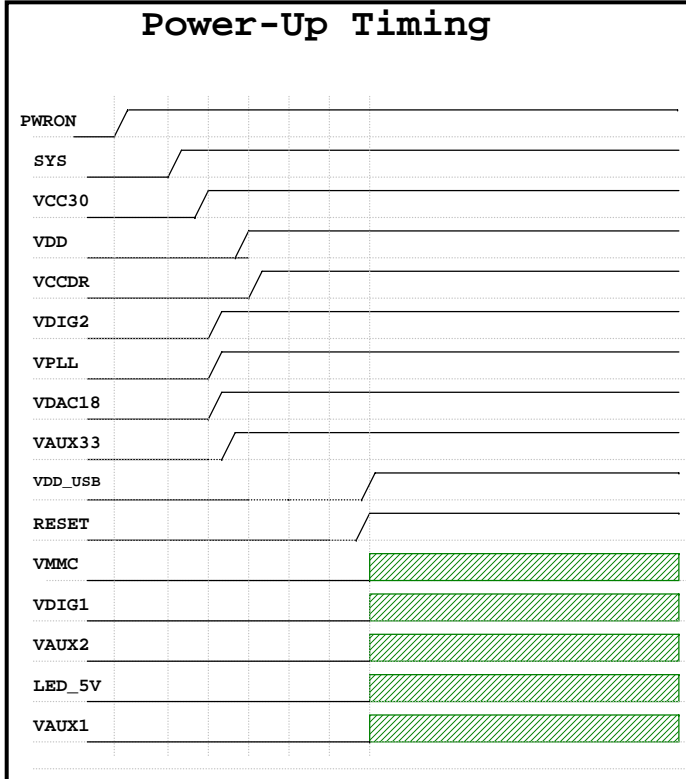
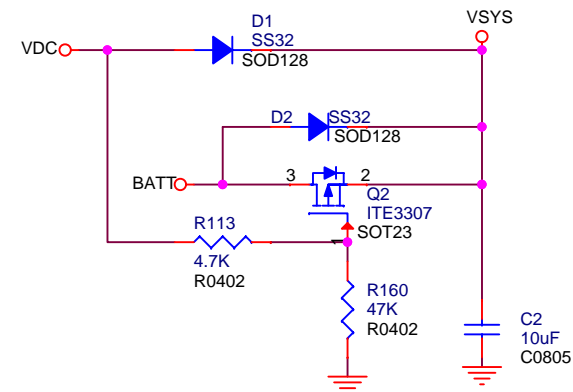
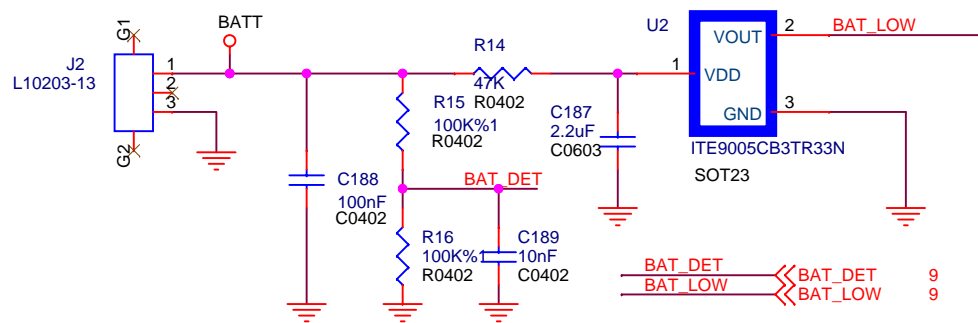
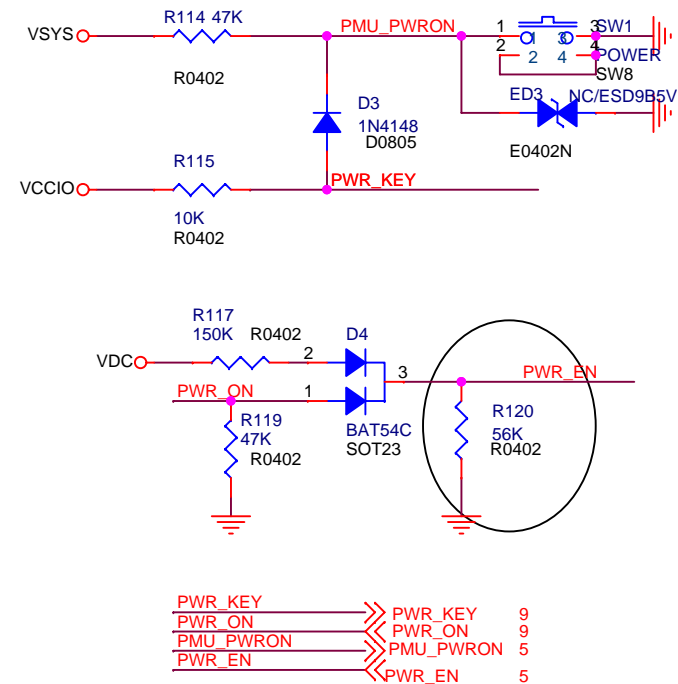
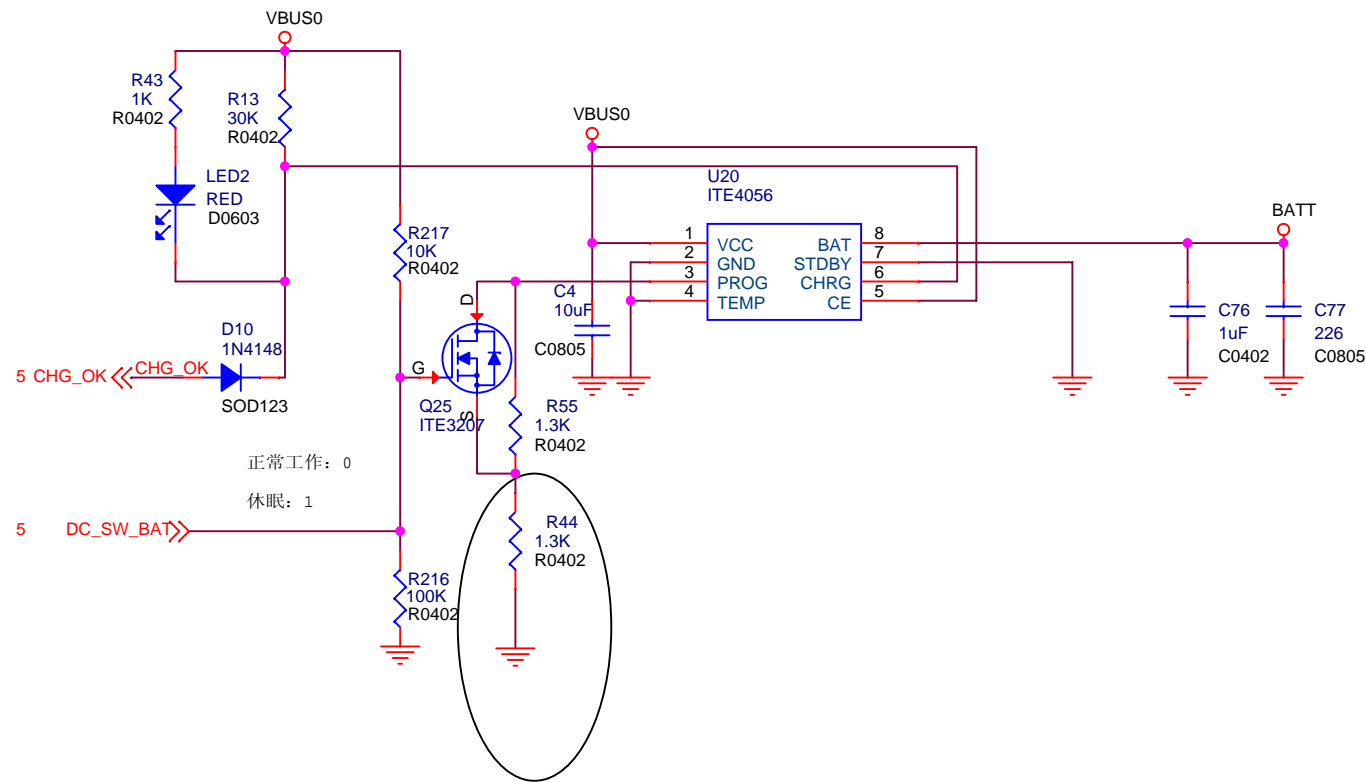


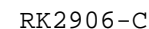
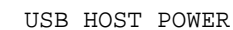
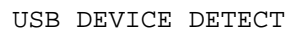
Table 12. Power Sources

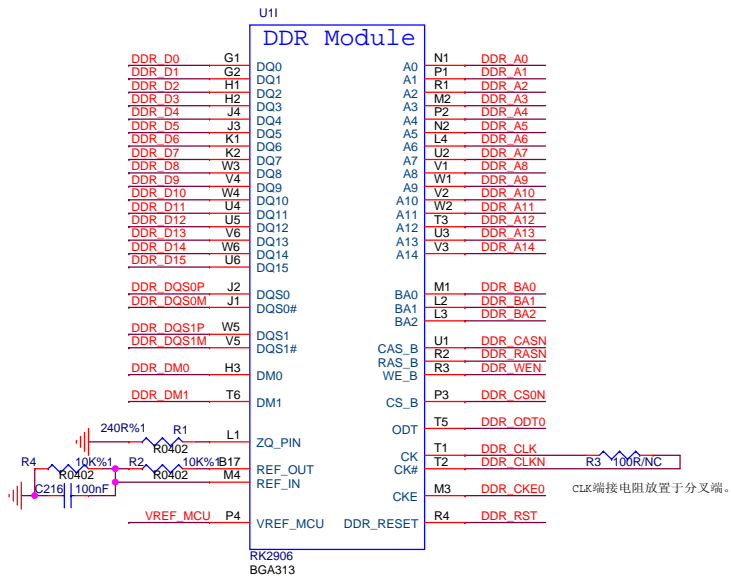
RESOURCE	TYPE	VOLTAGES	POWER
VIO	SMPS	1.5 V / 1.8 V / 2.5 V / 3.3 V	1000 mA
VDD1	SMPS	0.6 ... 1.5 in 12.5-mV steps Programmable multiplication factor: x2, x3	1500 mA
VDD2	SMPS	0.6 ... 1.5 in 12.5-mV steps Programmable multiplication factor: x2, x3	1500 mA
VDD3	SMPS	5 V	100 mA
VDIG1	LDO	1.2 V, 1.5 V, 1.8 V, 2.7 V	300 mA
VDIG2	LDO	1 V, 1.1 V, 1.2 V, 1.8 V	300 mA
VPLL	LDO	1.0 V, 1.1 V, 1.8 V, 2.5 V	50 mA
VDAC	LDO	1.8 V, 2.6 V, 2.8 V, 2.85 V	150 mA
VAUX1	LDO	1.8 V, 2.5 V, 2.8 V, 2.85 V	300 mA
VAUX2	LDO	1.8 V, 2.8 V, 2.9 V, 3.3 V	150 mA
VAUX33	LDO	1.8 V, 2.0 V, 2.8 V, 3.3 V	150 mA
VMMC	LDO	1.8 V, 2.8 V, 3.0 V, 3.3 V	300 mA

Power Diagram

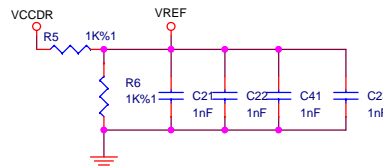
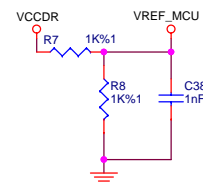
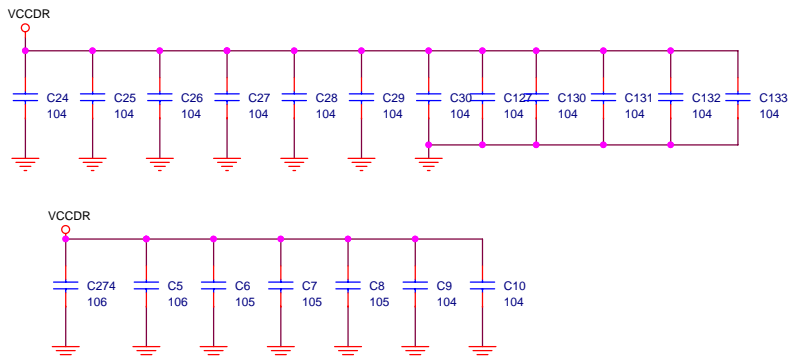
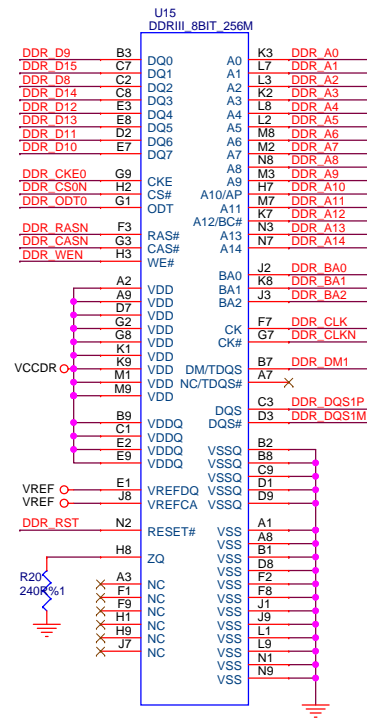
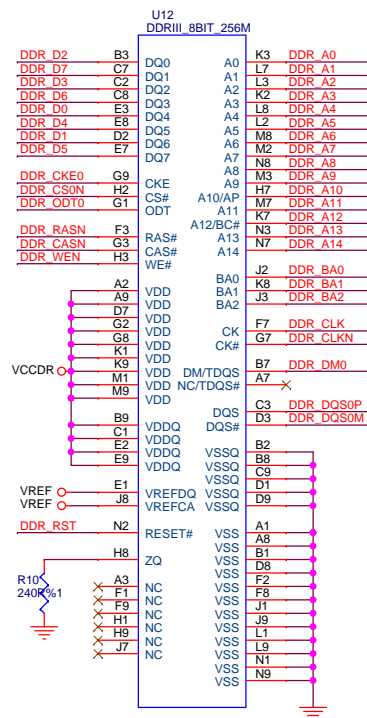


BATTER IN &LOW BAT LEVEL DET

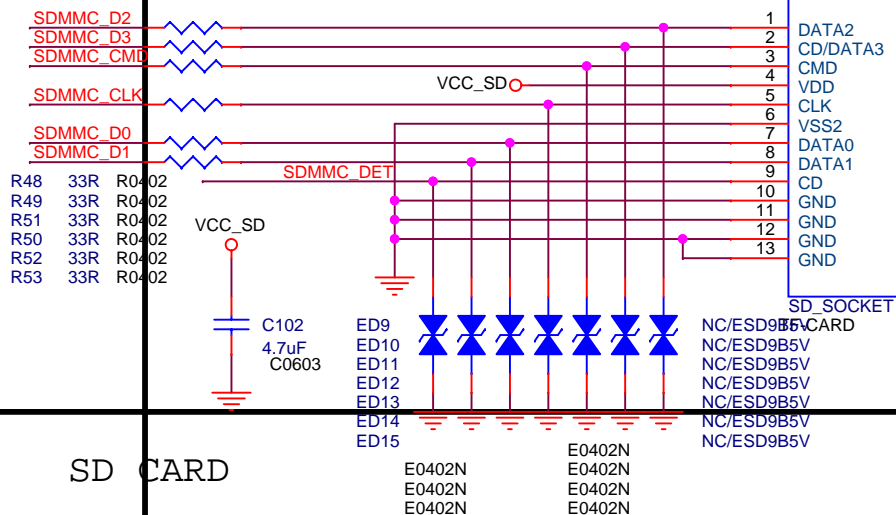
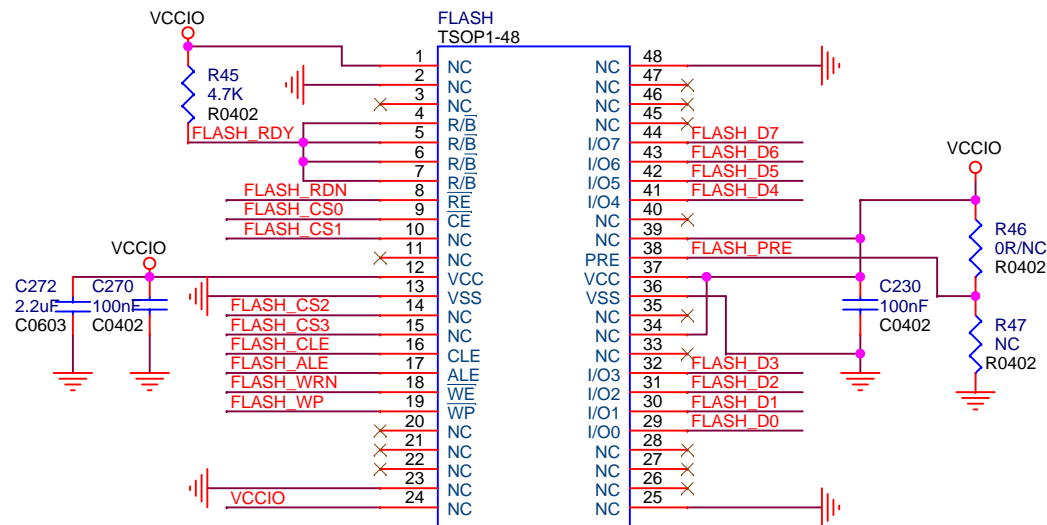




RK2906-I



U23

FLASH
TSOP1-48

U1D

NAND FLASH
Module

FLASH_DATA[0]
FLASH_DATA[1]
FLASH_DATA[2]
FLASH_DATA[3]
FLASH_DATA[4]
FLASH_DATA[5]
FLASH_DATA[6]
FLASH_DATA[7]

FLASH_RDN
FLASH_RDY
FLASH_WRN
FLASH_WP
FLASH_CLE
FLASH_ALE

GPIO0_A[5]/FLASH_DQS

FLASH_CSN0
GPIO0_D[2]/FLASH_CSN1
GPIO0_D[3]/FLASH_CSN2
GPIO0_D[4]/FLASH_CSN3

RK2906
BGA313

RK2906-D

U1E

SDMMC Module

GPIO1_D[2]/SDMMC0_DATA0
GPIO1_D[3]/SDMMC0_DATA1
GPIO1_D[4]/SDMMC0_DATA2
GPIO1_D[5]/SDMMC0_DATA3

GPIO1_D[0]/SDMMC0_CLKOUT
GPIO1_D[1]/SDMMC0_CMD
GPIO2_A[2]/SDMMC0_DETECT_N
GPIO5_D[5]/SDMMC0_PWR_EN

GPIO1_C[3]/SDMMC1_DATA0
GPIO1_C[4]/SDMMC1_DATA1
GPIO1_C[5]/SDMMC1_DATA2
GPIO1_C[6]/SDMMC1_DATA3

GPIO1_C[7]/SDMMC1_CLKOUT
GPIO1_C[2]/SDMMC1_CMD
GPIO5_D[6]/SDMMC1_PWR_EN

RK2906
BGA313

RK2906-E

Rockchip
瑞芯微电子

福州瑞芯微电子有限公司

Title: RK2906_RK610_PMIC_2DDR_REF

File: MEMORY

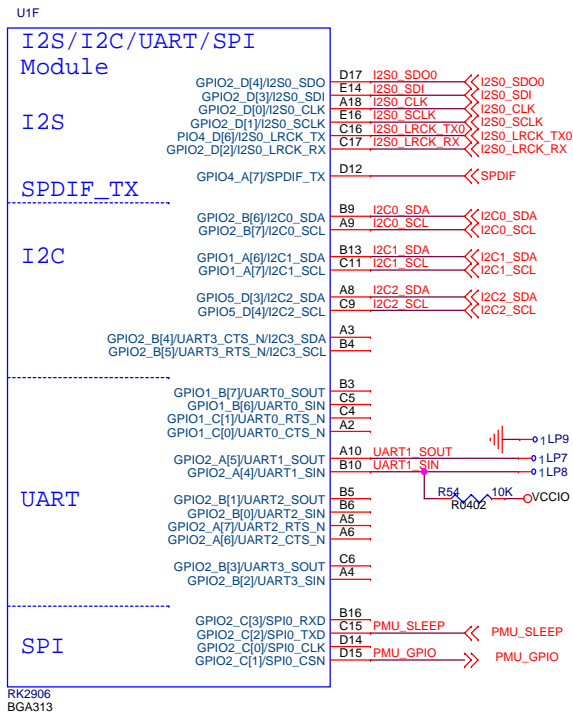
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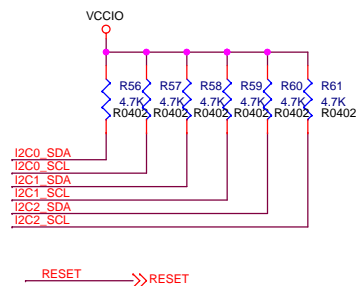
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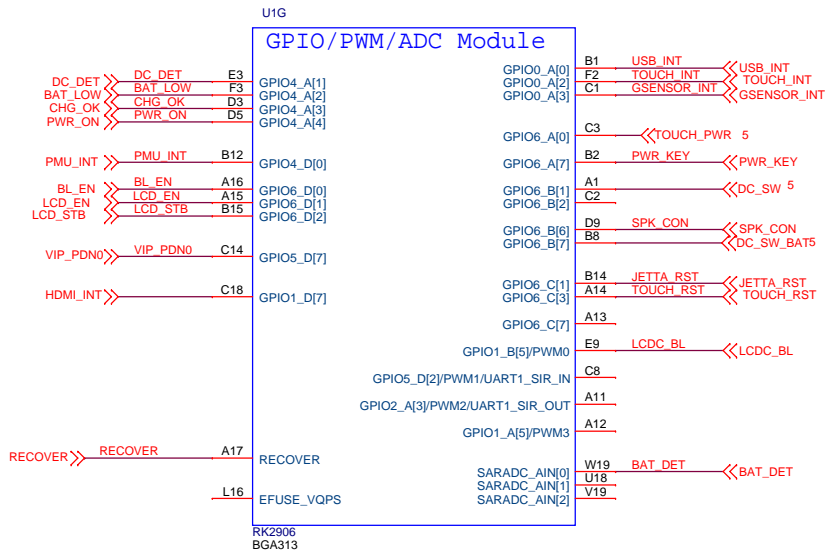
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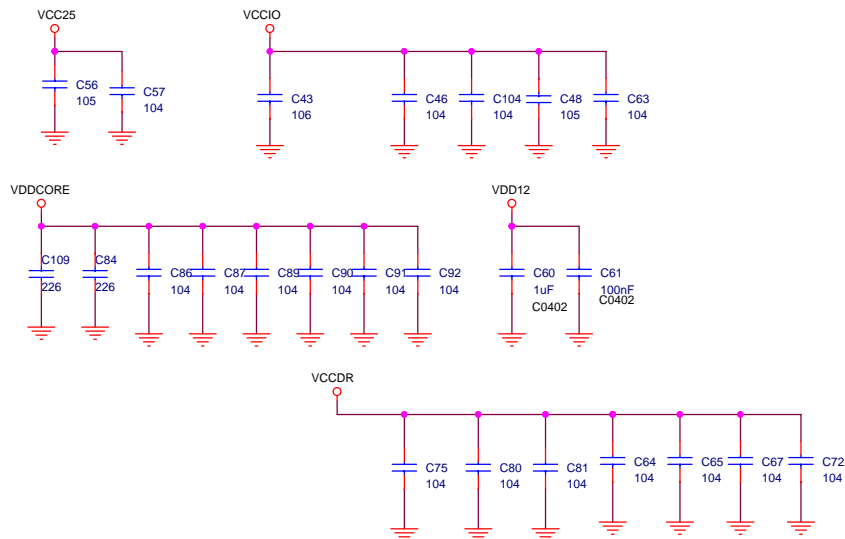
RK2906-F



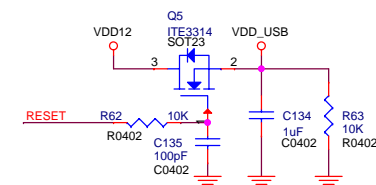
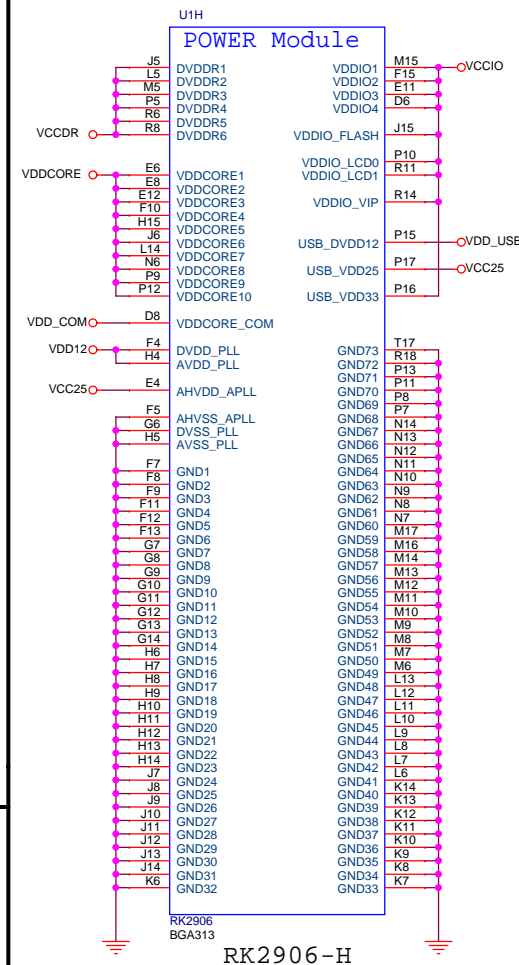
PULL UP FOR I2C



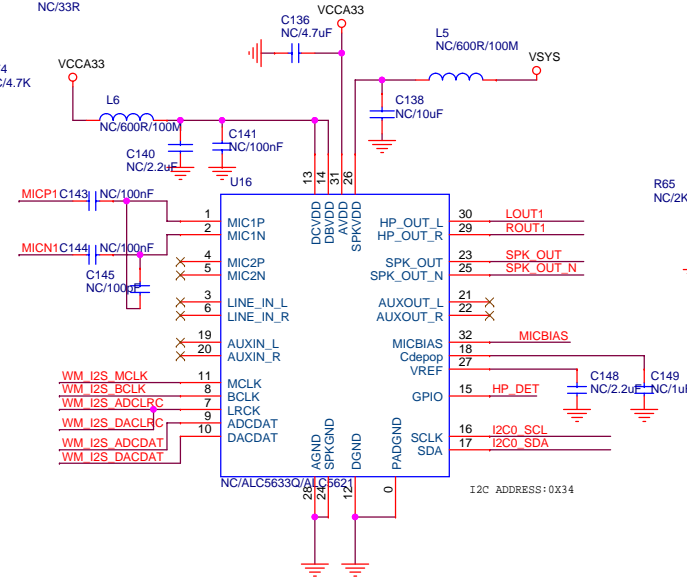
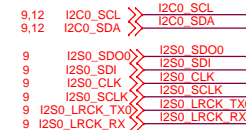
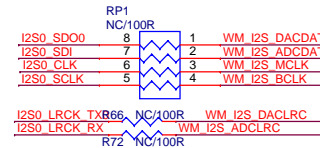
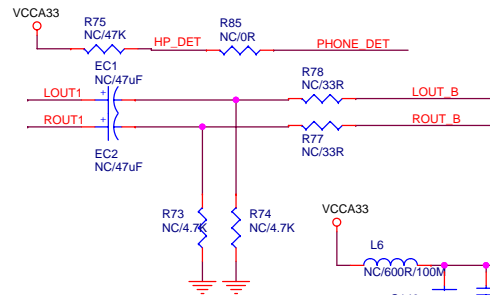
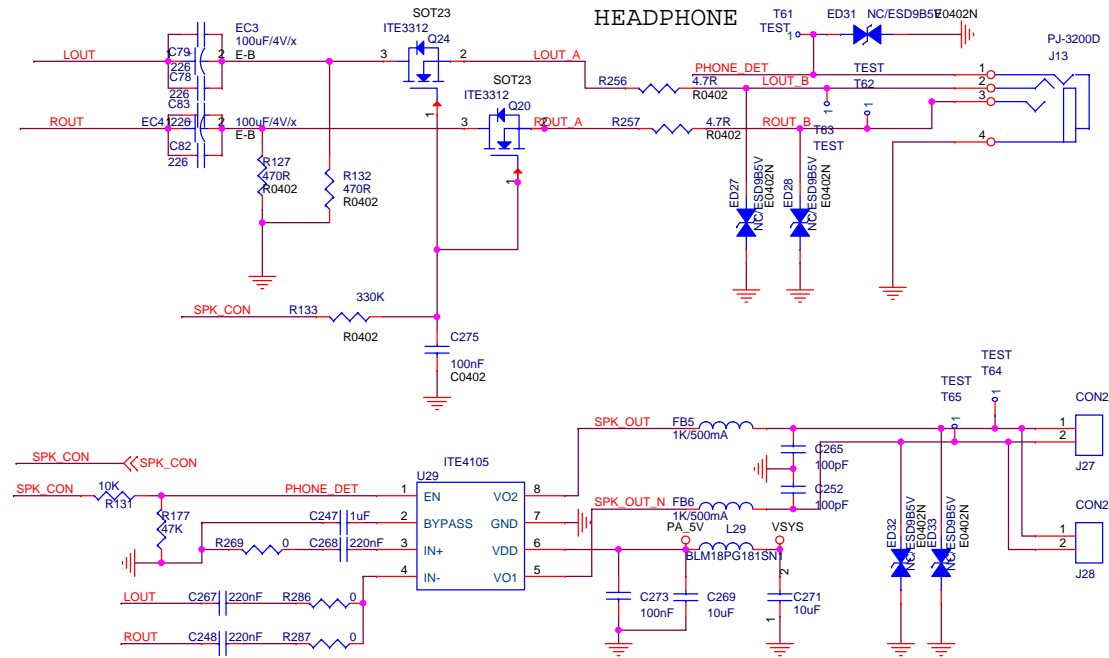
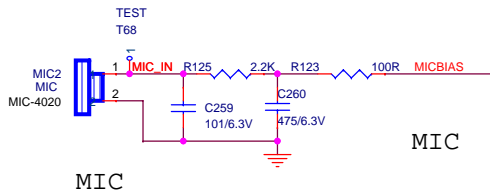
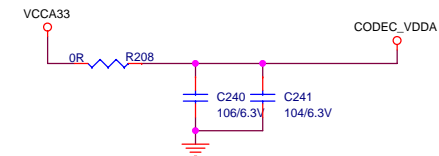
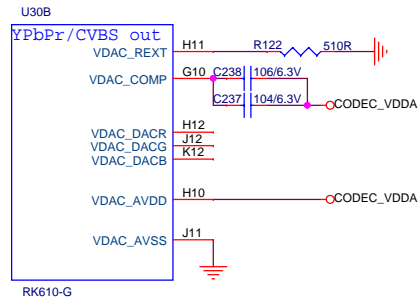
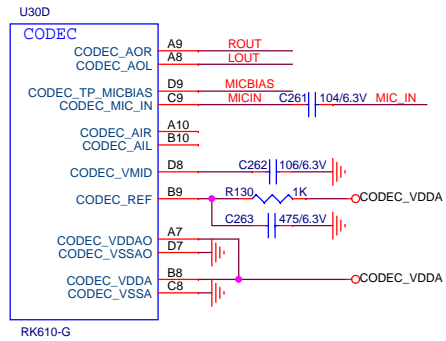
RK2906-G



RK2918 POWER FILTER



Note: The Vgs of NMOS must be less than 1.5V.



Note:
Please place these 4 components as close to CODEC as possible.

