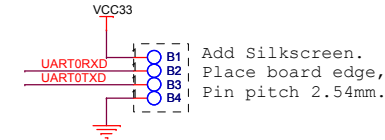


# Core Board Package

## UART0 Connector



Place near Core Board.

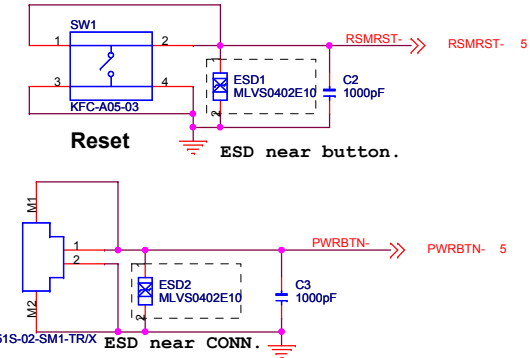
## WMS8200B1

134Pin 46.74\*63mm (1.84\*2.48inch)

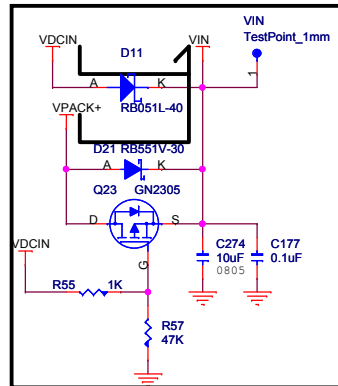
Place near Core Board.

USB PORT 0 Configuration Table

	USBID0
HOST	0 default
Device	1



### DC Switch

[illegible]

Battery Low Detect:

3.4V

VPACK+

R10 1K\_1%

R12 150K\_1%

C7 4.7uF

SC-82

Co-lay 3.0V

U2

OUT 3.0V

VSS

VDD NC

S-80830CNNB-B8PT2G

U20 3.0V

VIN

VOUT

S-80830CNMA-B8PT1G/X

VSS

R125 0/X

VSUS33

R7 10K

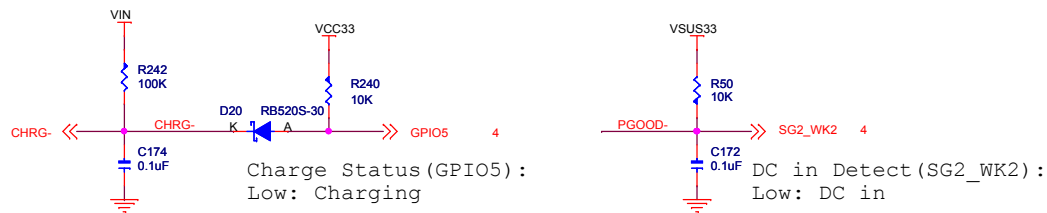
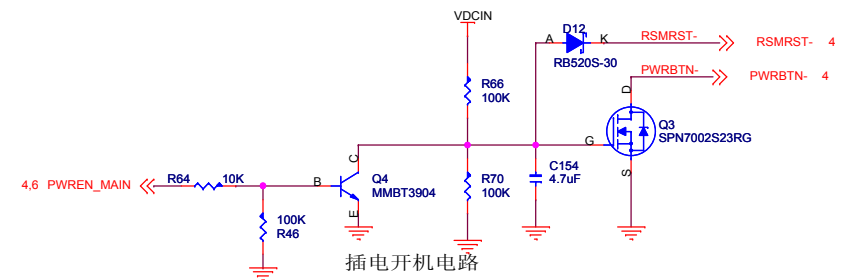
WAKEUP0

R124 0

RSMRST-

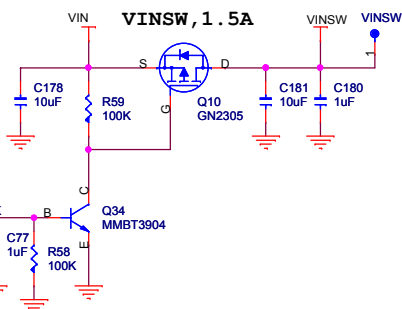
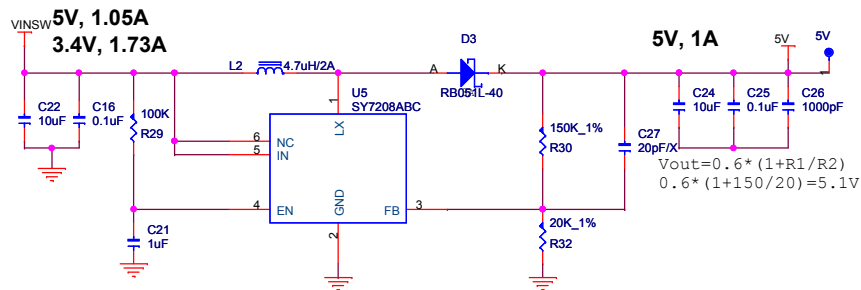
Charge Current (SUS\_GPIO1):  
Low: ~0.5A  
High: ~1.0A

- Silkscreen layer  
add +/-  
Battery Connector

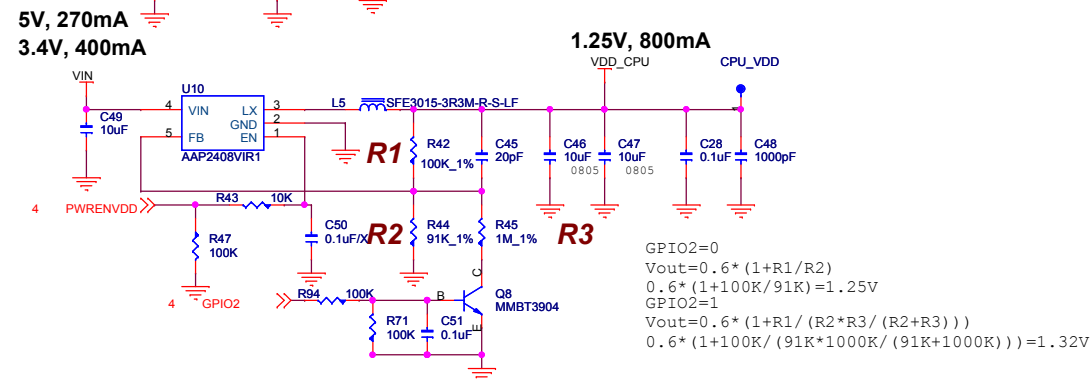
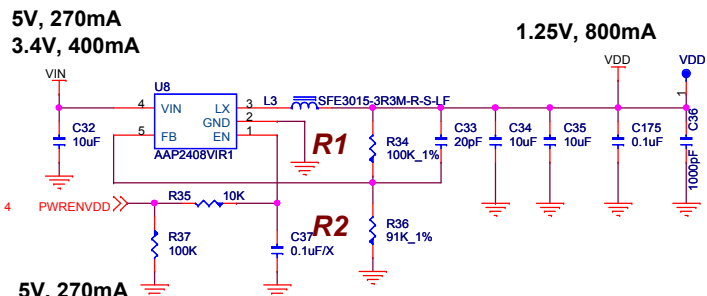


# Voltage Regulators

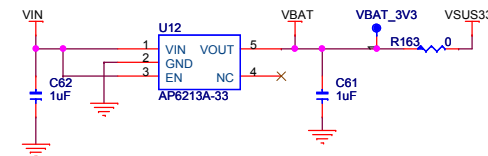
## VINSW->5.2V, 1A



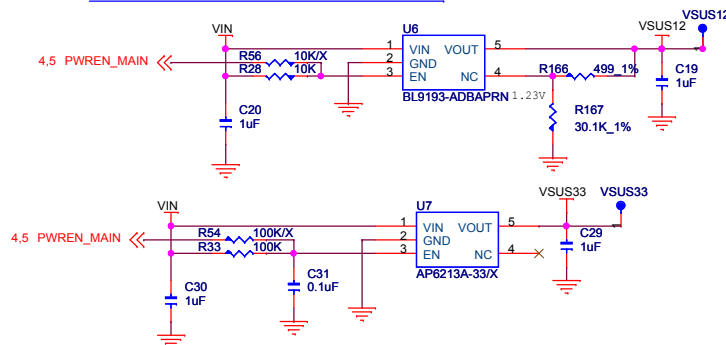
## VIN->1.2V, 800mA x 2



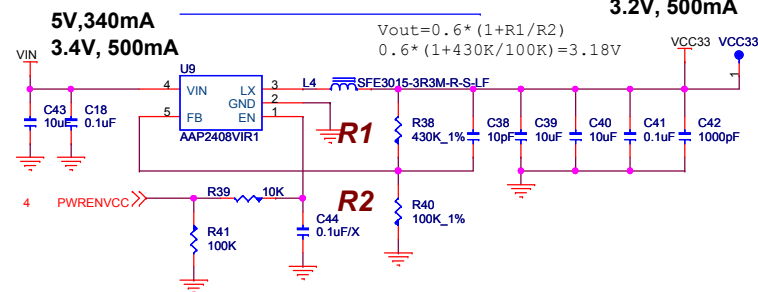
## VIN->VCCBAT 3.3V ?uA



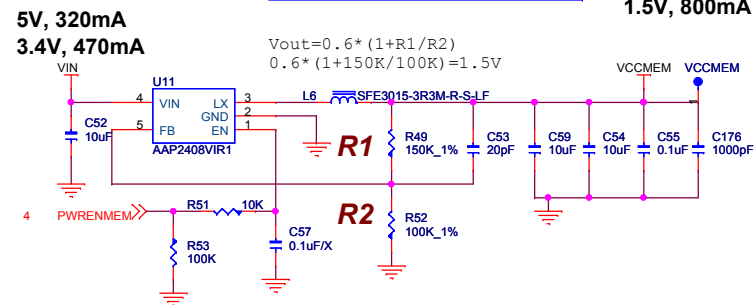
## VIN->Suspend Power



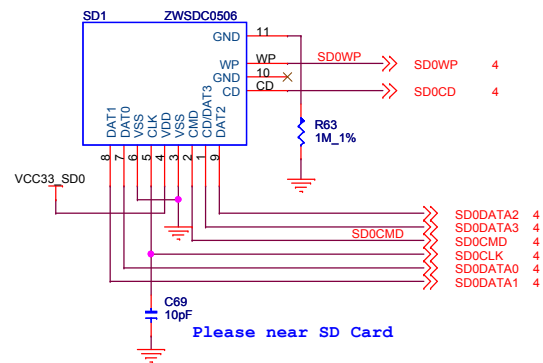
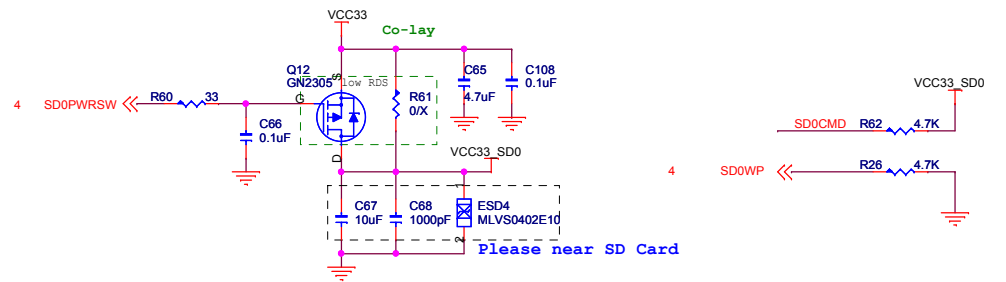
## VIN->3.3V 800mA



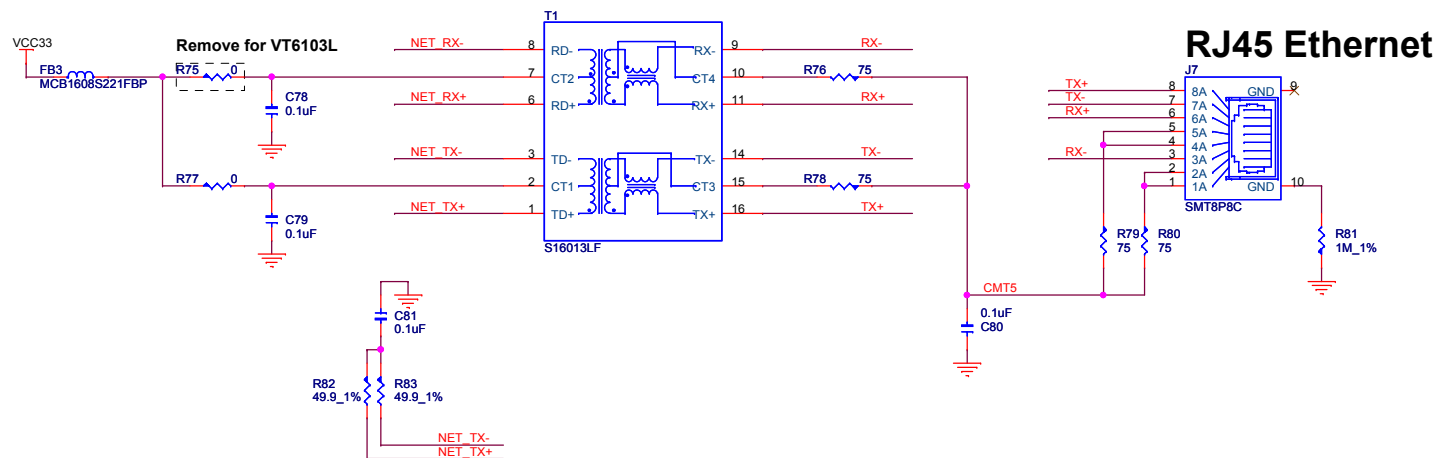
## VIN->1.5V, 800mA



# SD Card



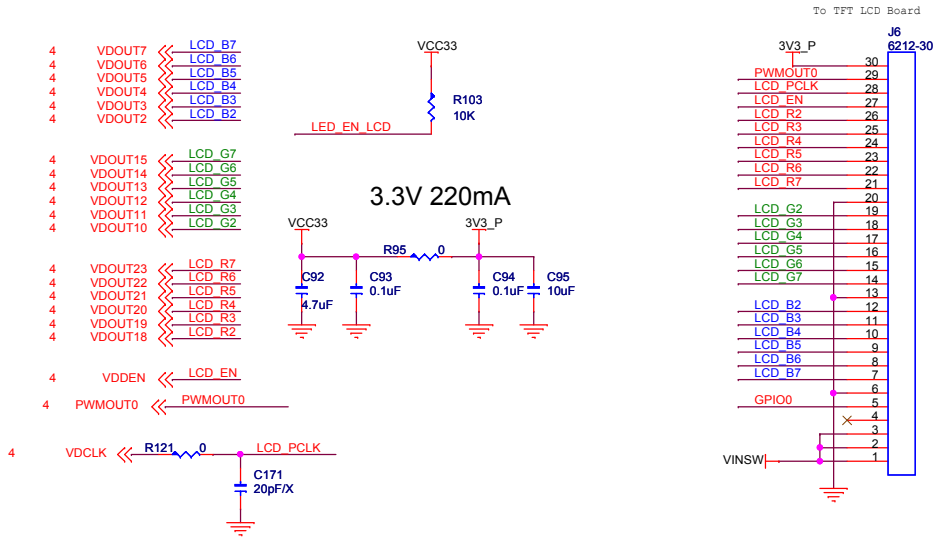
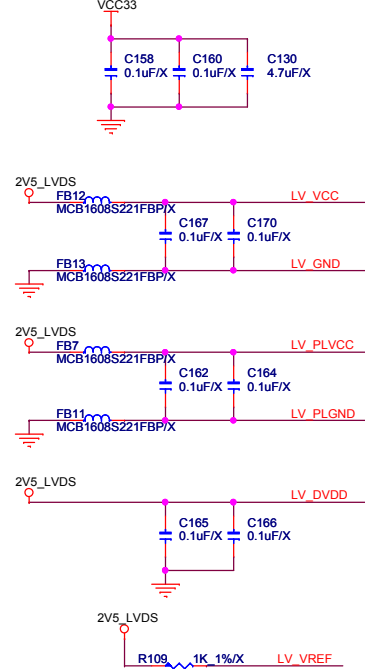
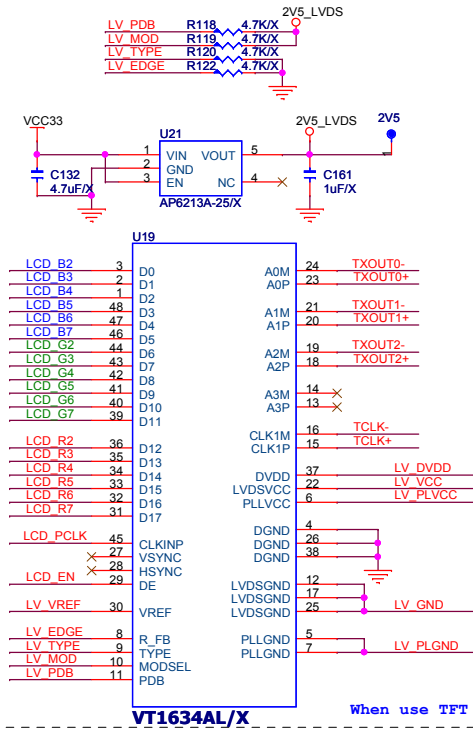
# Ethernet



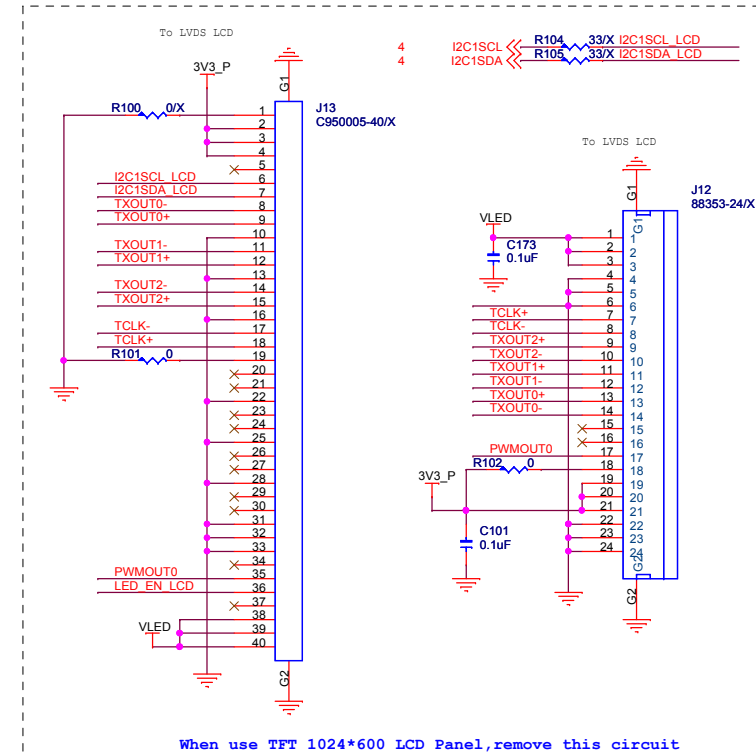
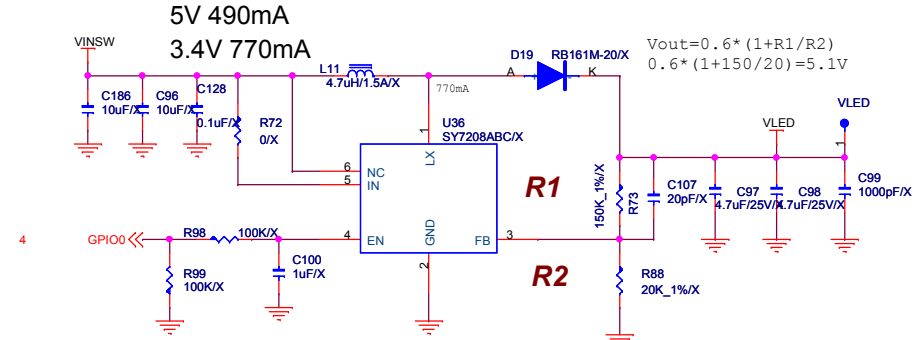


Title				<b>USB Host/OTG</b>			
Size	Document Number					Rev	
Custom	<b>WMS8199E</b>					1.0	
Date:	Friday, August 17, 2012		Sheet	10	of	16	

	0	1
PDB	Power Down Mode	Normal Mode
MODESEL	12bit DDR Mode	18bit SDR Mode
TYPE	L3B (Conventional)	MSB (non-Conventional)
EDGE	Rising Edge	Falling Edge

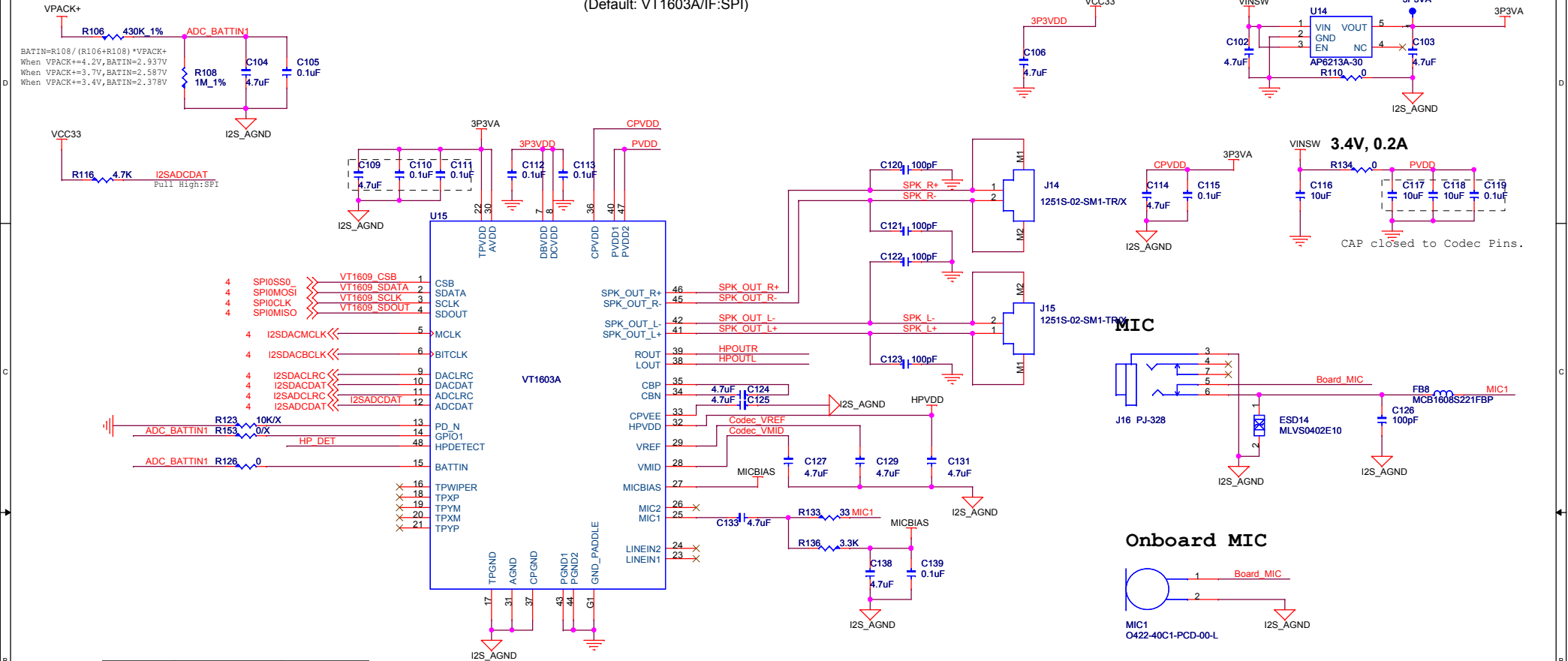


## LCD Panel



# I2S CODEC

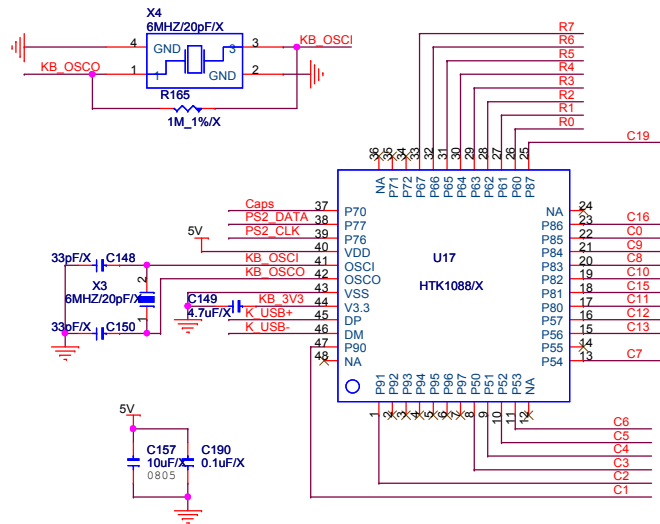
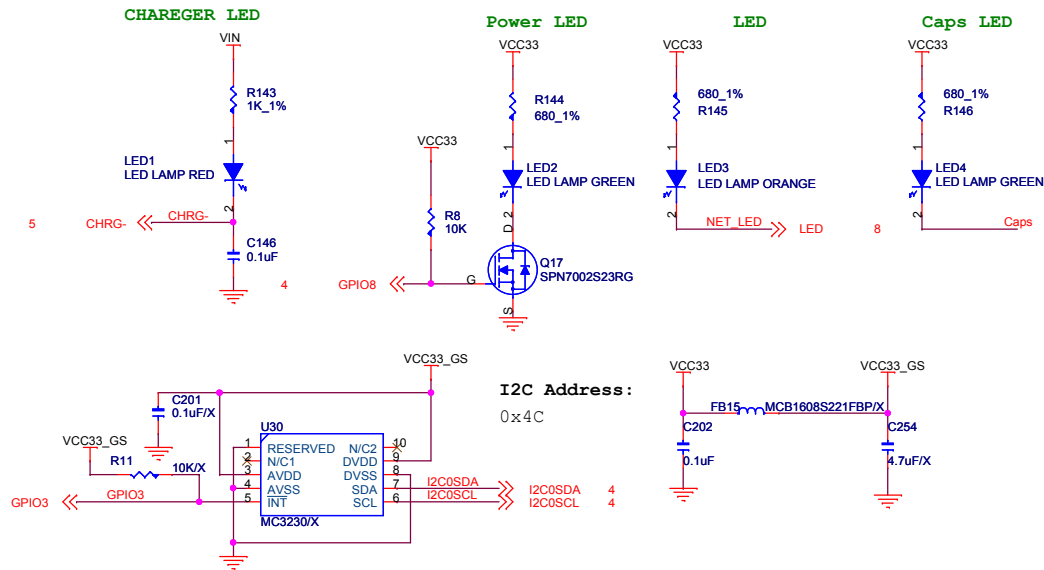
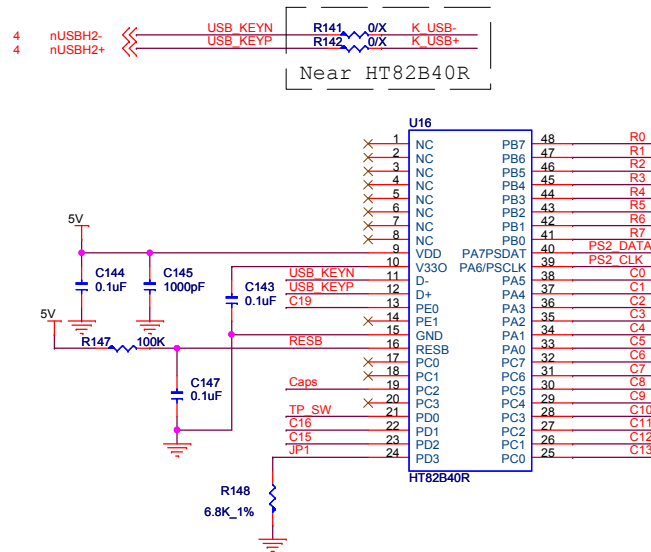
(Default: VT1603A/IF:SPI)



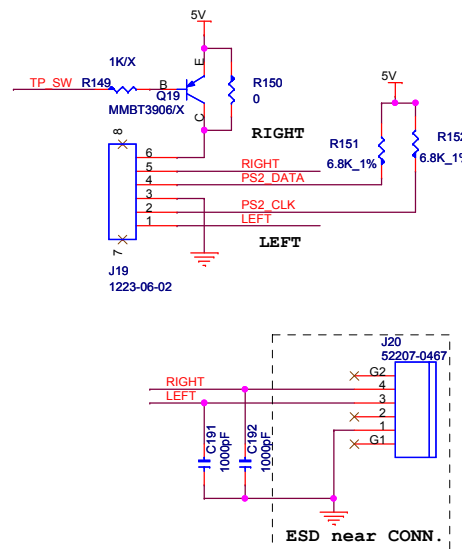
Codec Pin	VT1609(default)	VT1603
P13	Touch INT	GND
P14	Battery ADC	Touch INT
P15	RES TP Signal	Battery ADC
P16	RES TP Signal	NC



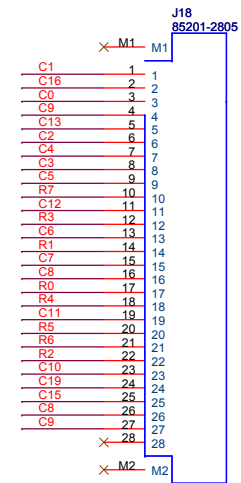
# Keyboard & Touch

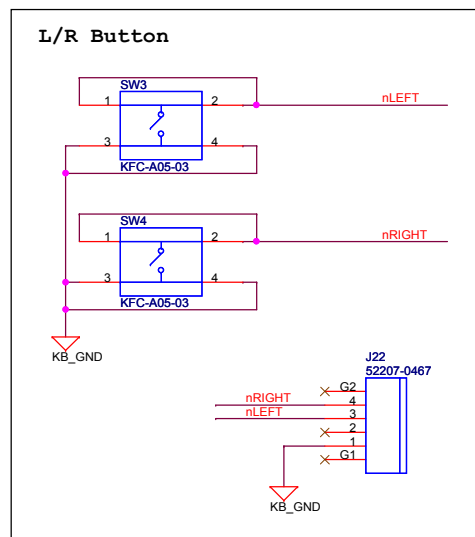
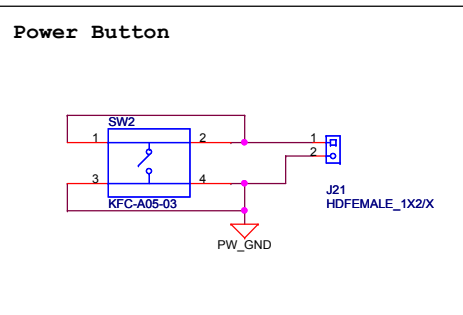


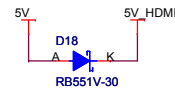
## PS2 Touchpad



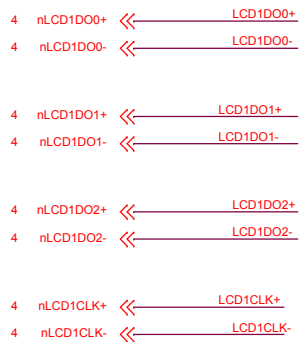
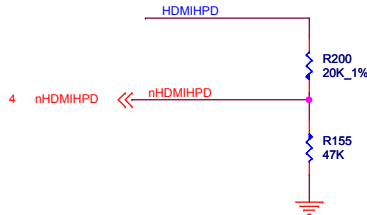
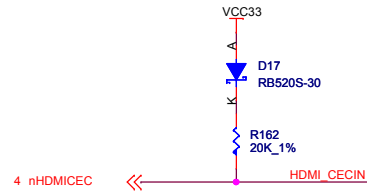
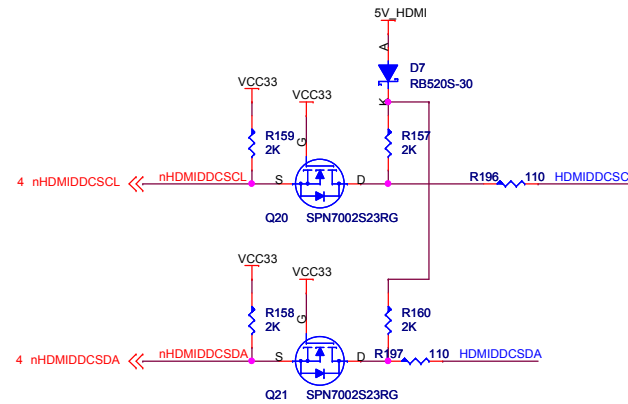
## Keyboard



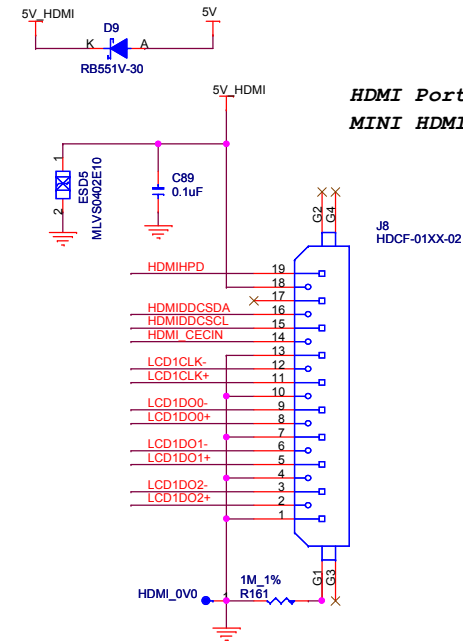




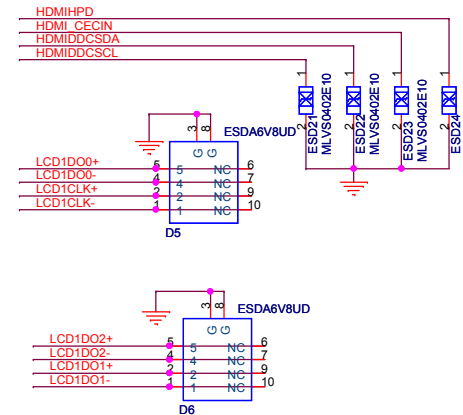
# HDMI



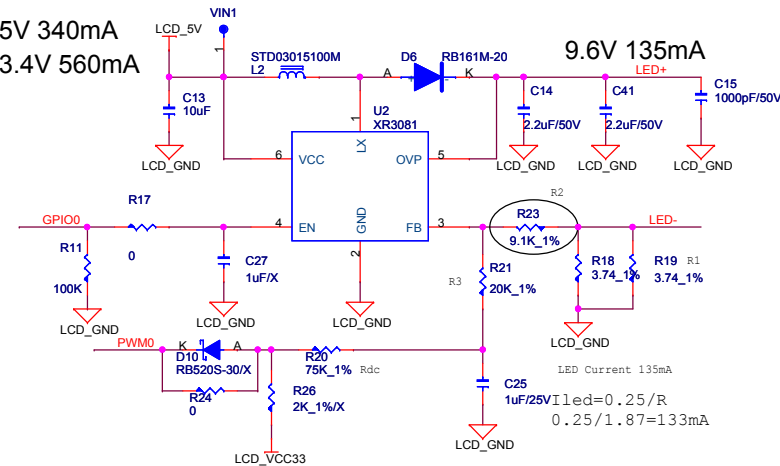
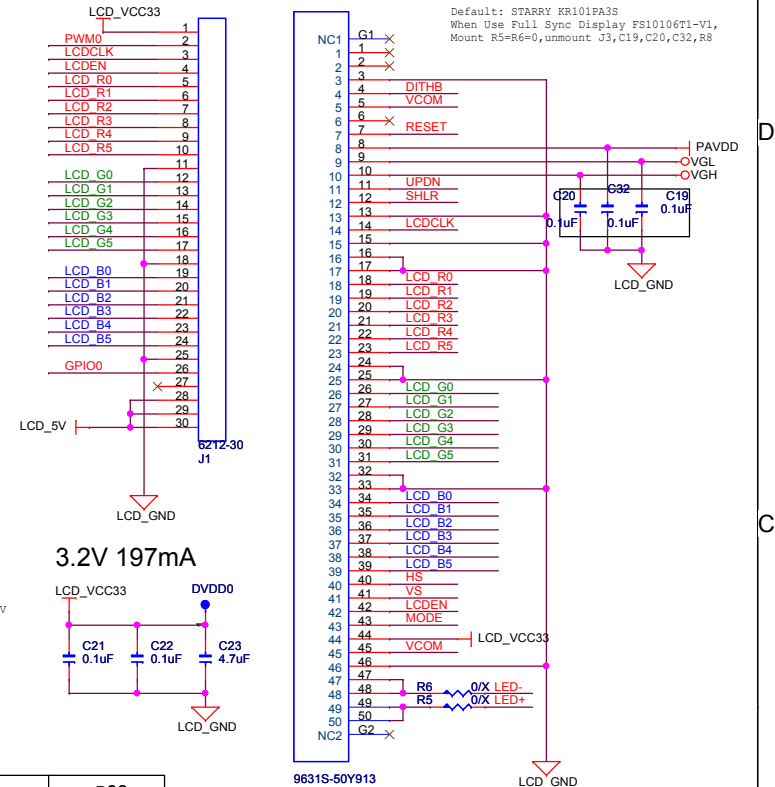
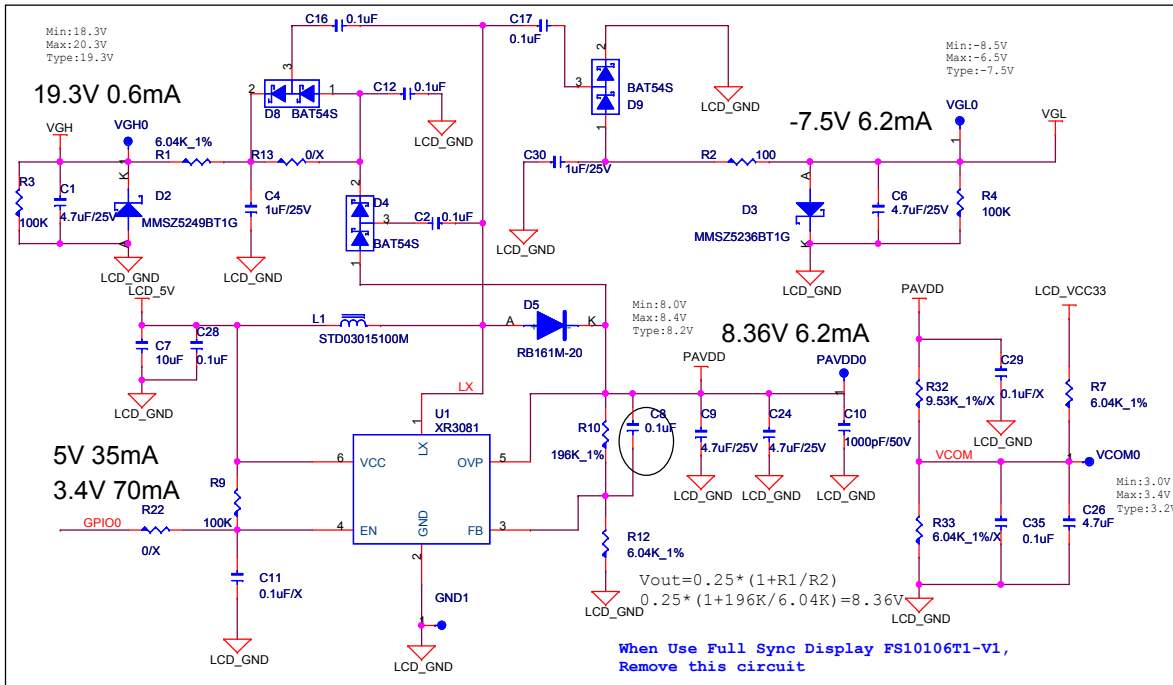
Closed to HDMI connector



Notice: ESD components place near HDMI connector.



# LCD Interface Panel



$$I_{led} = V_{fb} / R1 - R2 * (V_{pwm} * Duty - V_{fb}) / ((R3 + R_{dc}) * R1)$$

6. Full Sync Display FS10106T1-V1:  
DITHB Dithering function enable control, normally pull High.  
When DITHB="1", Disable internal dithering function,  
When DITHB="0", Enable internal dithering function,

## Replacement:

U1, U2	R10	R12	R18, R19	R23
MT9284-20J	470K_1%	6.04K_1%	1.5_1%	3.65K_1%
RT9293B-20GJ6	160K_1%	6.04K_1%	4.53_1%	11.5K_1%
uP6001AMT6	160K_1%	6.04K_1%	4.53_1%	11.5K_1%
EMD2093-00VC06NRR	196K_1%	6.04K_1%	3.74_1%	9.1K_1%
SY7200	243K_1%	6.04K_1%	3_1%	7.5K_1%
IT7903TS	243K_1%	6.04K_1%	3_1%	7.5K_1%

1. DE/SYNC mode select. Normally pull high.

When select DE mode, MODE="1", VS and HS must pull high.

When select SYNC mode, MODE="0", DE must be grounded.

2. When input 18 bits RGB data, the two low bits of R, G and B data must be grounded.

3. Selection of scanning mode

Setting of scan control input		Scanning direction
U/D	L/R	
GND	DVDD	Up to down, left to right
DVDD	GND	Down to up, right to left
GND	GND	Up to down, right to left
DVDD	DVDD	Down to up, left to right

4. Global reset. Active low to enter reset state. Suggest to connect with an RC reset circuit for stability. Normally pull high.

5. STARRY KR101PA3S: DITHB Dithering function enable control, normally pull Low.  
When DITHB="0", Disable internal dithering function,  
When DITHB="1", Enable internal dithering function,

Bottom Contact  
Place TOP Layer

