









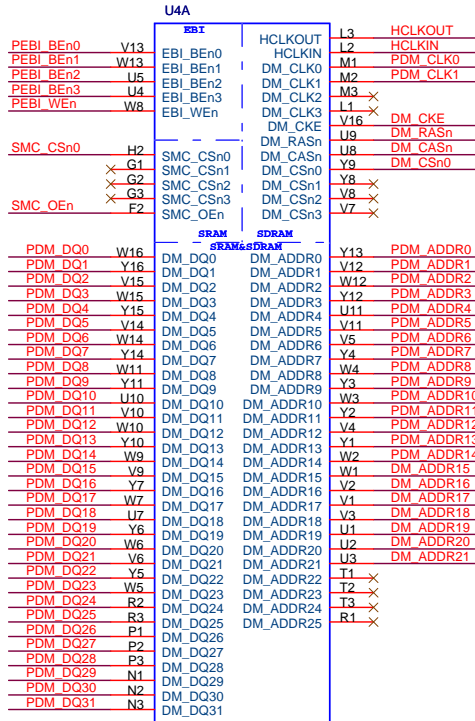


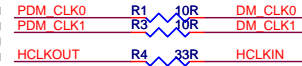


DM\_DQ[0..31]  DM\_DQ[0..31]

DM\_CLK[0..1]  DM\_CLK[0..1]  
EBI\_BEn[0..3]  EBI\_BEn[0..3]  
EBI\_WEn  EBI\_WEn  
DM\_ADDR[0..21]  DM\_ADDR[0..21]  
DM\_CSn0  DM\_CSn0  
DM\_RASn  DM\_RASn  
DM\_CASn  DM\_CASn  
DM\_CKE  DM\_CKE  
SMC\_CSn0  SMC\_CSn0  
SMC\_OEn  SMC\_OEn



Place near FIC8120



Remove the Serier Resistor

PDM_ADDR0	DM_ADDR0	PDM_DQ0	DM_DQ0	PDM_DQ16	DM_DQ16
PDM_ADDR1	DM_ADDR1	PDM_DQ1	DM_DQ1	PDM_DQ17	DM_DQ17
PDM_ADDR2	DM_ADDR2	PDM_DQ2	DM_DQ2	PDM_DQ18	DM_DQ18
PDM_ADDR3	DM_ADDR3	PDM_DQ3	DM_DQ3	PDM_DQ19	DM_DQ19
PDM_ADDR4	DM_ADDR4	PDM_DQ4	DM_DQ4	PDM_DQ20	DM_DQ20
PDM_ADDR5	DM_ADDR5	PDM_DQ5	DM_DQ5	PDM_DQ21	DM_DQ21
PDM_ADDR6	DM_ADDR6	PDM_DQ6	DM_DQ6	PDM_DQ22	DM_DQ22
		PDM_DQ7	DM_DQ7	PDM_DQ23	DM_DQ23

PDM_ADDR7	DM_ADDR7	PDM_DQ8	DM_DQ8	PDM_DQ24	DM_DQ24
PDM_ADDR8	DM_ADDR8	PDM_DQ9	DM_DQ9	PDM_DQ25	DM_DQ25
PDM_ADDR9	DM_ADDR9	PDM_DQ10	DM_DQ10	PDM_DQ26	DM_DQ26
PDM_ADDR10	DM_ADDR10	PDM_DQ11	DM_DQ11	PDM_DQ27	DM_DQ27
PDM_ADDR11	DM_ADDR11	PDM_DQ12	DM_DQ12	PDM_DQ28	DM_DQ28
PDM_ADDR12	DM_ADDR12	PDM_DQ13	DM_DQ13	PDM_DQ29	DM_DQ29
PDM_ADDR13	DM_ADDR13	PDM_DQ14	DM_DQ14	PDM_DQ30	DM_DQ30
PDM_ADDR14	DM_ADDR14	PDM_DQ15	DM_DQ15	PDM_DQ31	DM_DQ31

PSMC_OEn	SMC_OEn
PDM_RASn	DM_RASn
PDM_CASn	DM_CASn
PEBI_WEn	EBI_WEn
PDM_CKE	DM_CKE

PDM_CSn0	DM_CSn0
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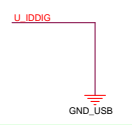
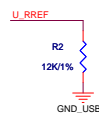
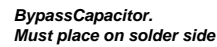
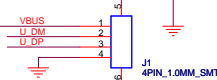
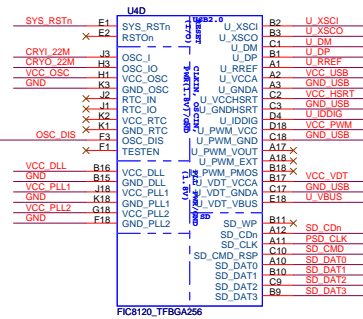
  

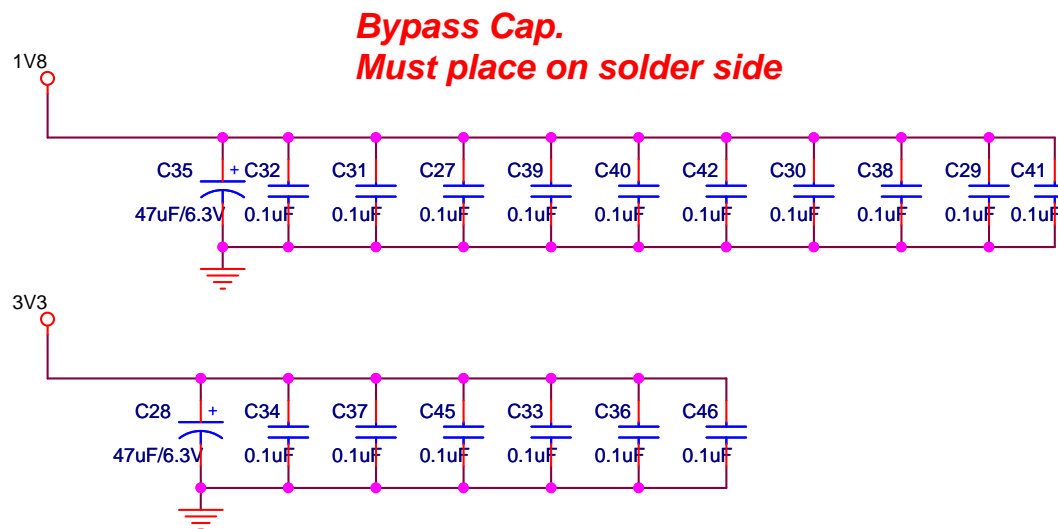
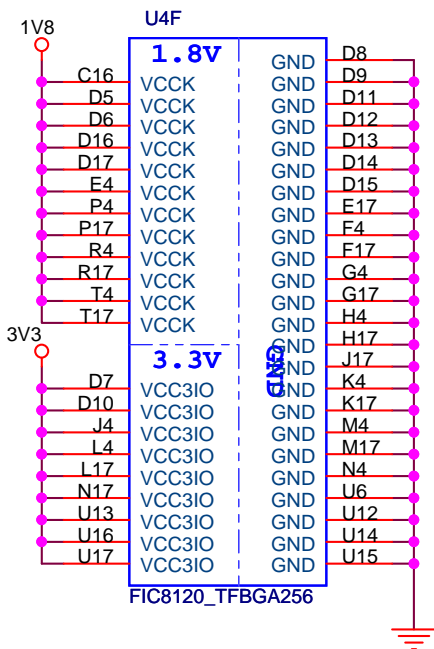
PSMC_CSn0	SMC_CSn0
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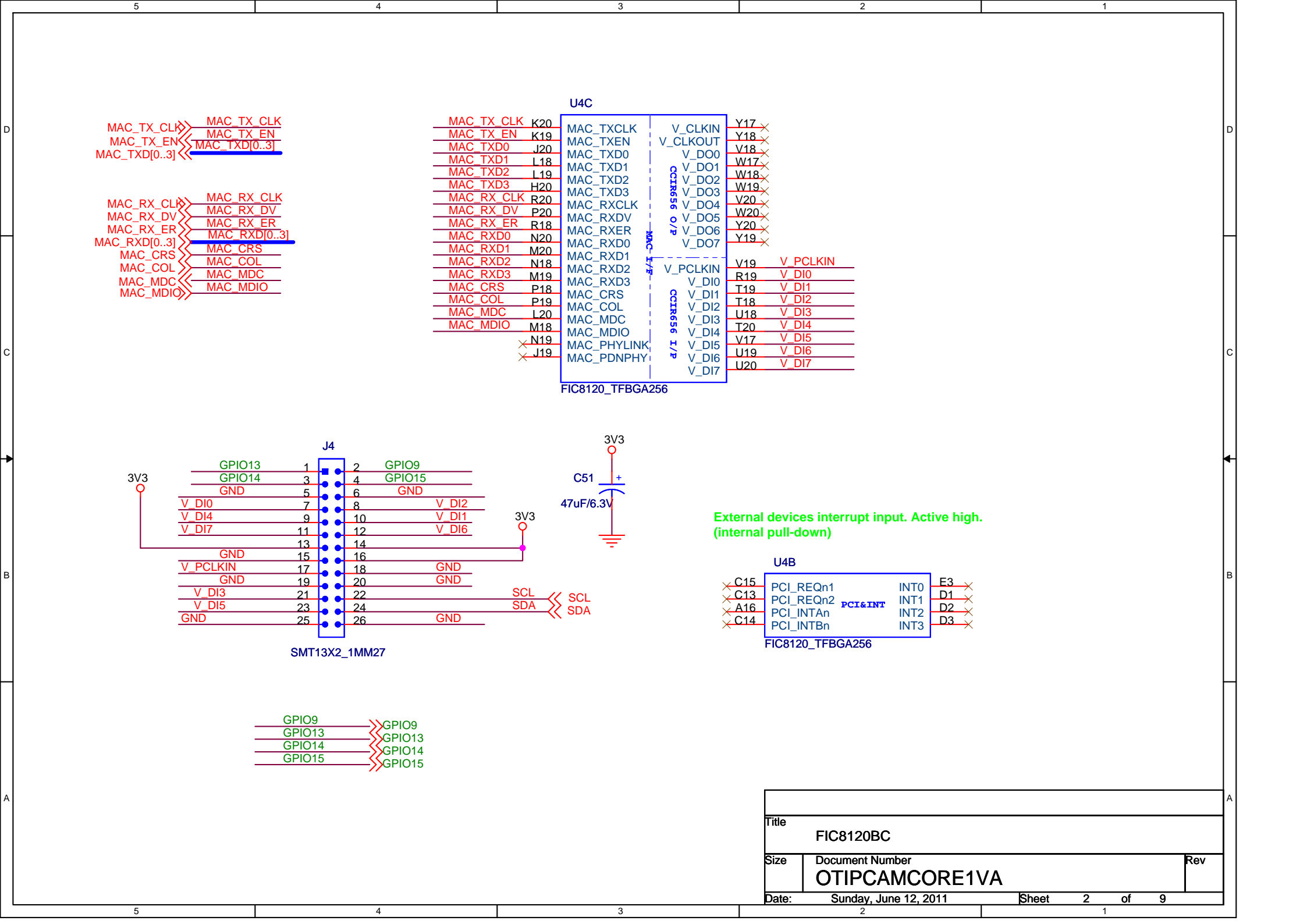
PEBI_BEn0	EBI_BEn0
PEBI_BEn1	EBI_BEn1
PEBI_BEn2	EBI_BEn2
PEBI_BEn3	EBI_BEn3

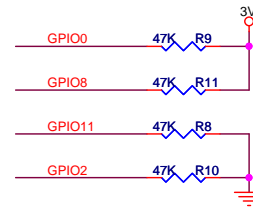
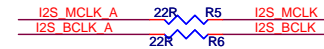
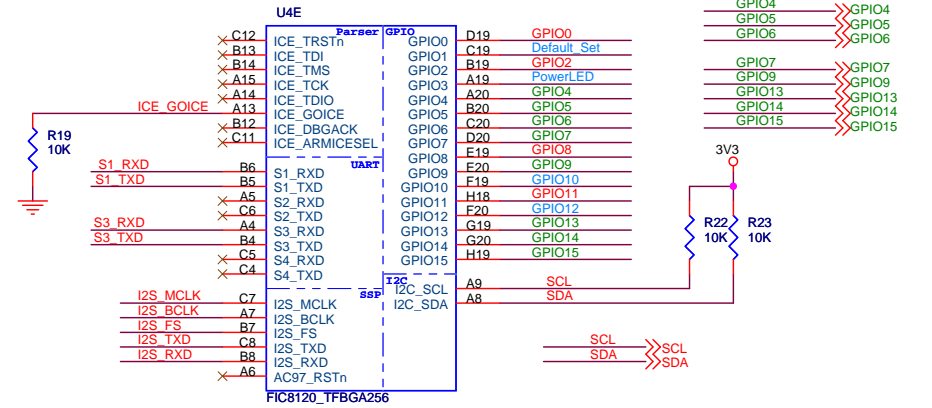
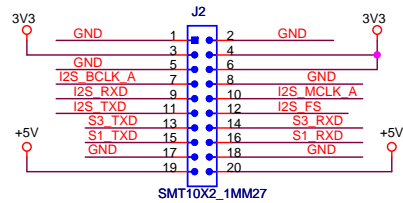
Title		
FIC8120C		
Size	Document Number	Rev
	OTIPCAMCORE1VA	
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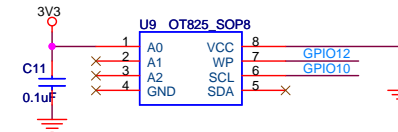
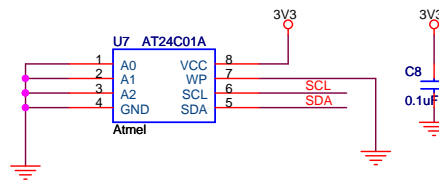
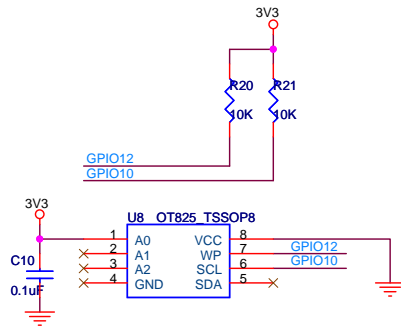


Title			
FIC8120F			
Size	Document Number		Rev
	OTIPCAMCORE1VA		
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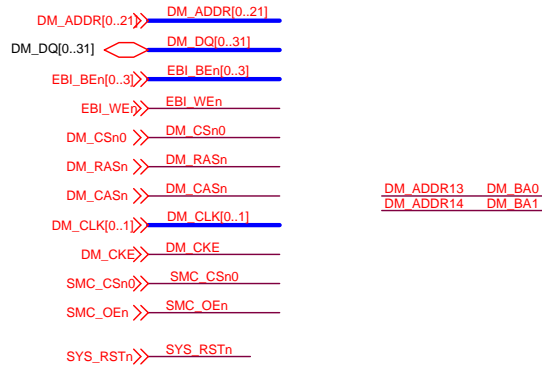




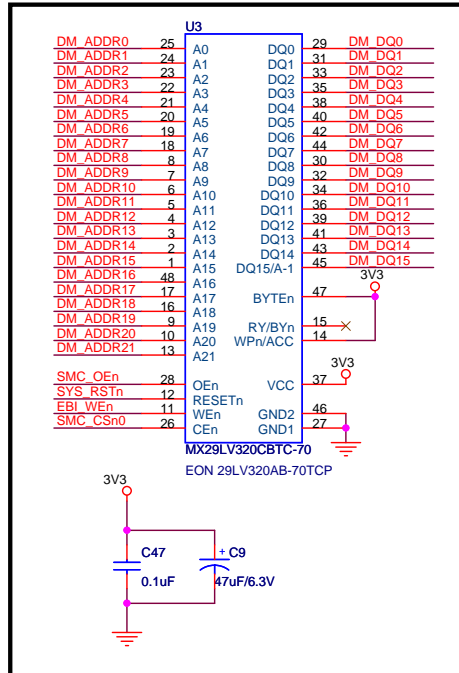
**GPIO[11] Timer test. Should be pull-low in normal use.**  
**GPIO[8] CPU operating mode. 0=sync mode, 1=asyn mode**  
**At-speed testing use sync mode, but normal operation use asyn mode.**  
**Should be pull-high in normal use.**  
**GPIO[2] Bypass PLL or not. IC Tester use this jumper setting.**  
**Should be pull-low in normal use.**  
**GPIO[0] Define 8-bit or 16-bit booting.**  
**pull-low: 8-bit booting pull-high: 16-bit booting**



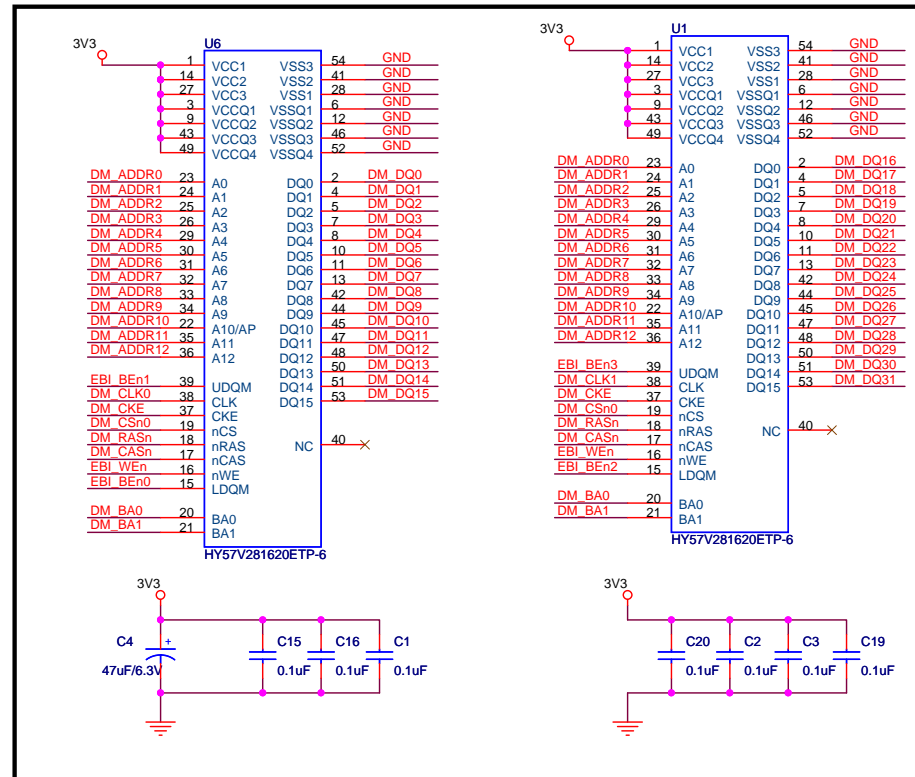
Title		
FIC8120E		
Size	Document Number	Rev
OTIPCAMCORE1VA		
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## On Board Flash (2Mx16)



## On Board SDRAM(8Mx16)x2



Title		
Memory		
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	OTIPCAMCORE1VA	
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MAC\_RXD[0..3] << MAC\_RXD[0..3]  
 MAC\_RX\_CLK << MAC\_RX\_CLK  
 MAC\_RX\_DV << MAC\_RX\_DV  
 MAC\_RX\_ER << MAC\_RX\_ER

MAC\_CRS << MAC\_CRS  
 MAC\_COL << MAC\_COL

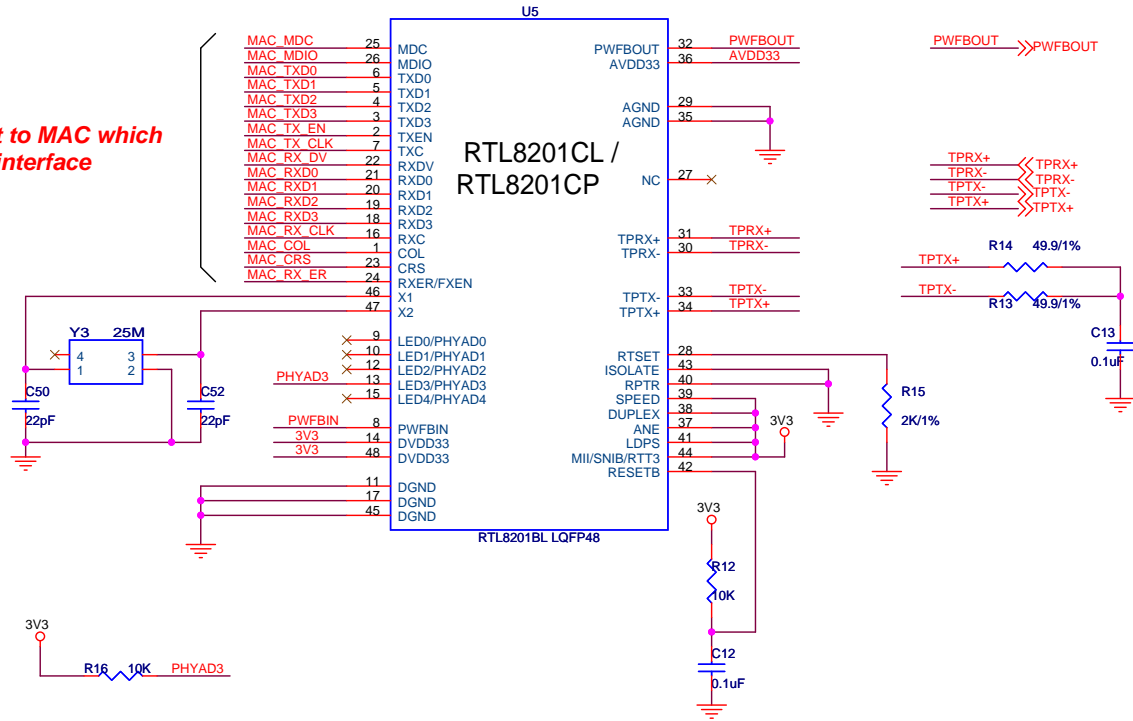
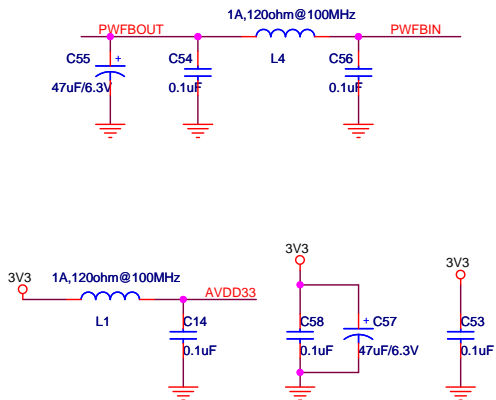
MAC\_MDIO << MAC\_MDIO  
 MAC\_MDC << MAC\_MDC

MAC\_TXD[0..3] >> MAC\_TXD[0..3]

MAC\_TX\_CLK << MAC\_TX\_CLK

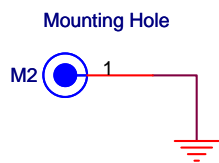
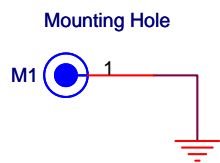
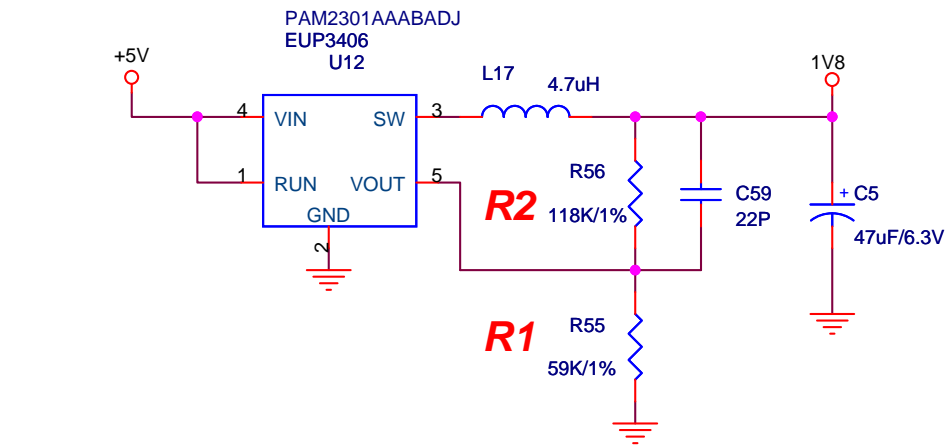
MAC\_TX\_EN >> MAC\_TX\_EN

Connect to MAC which  
 has MII interface

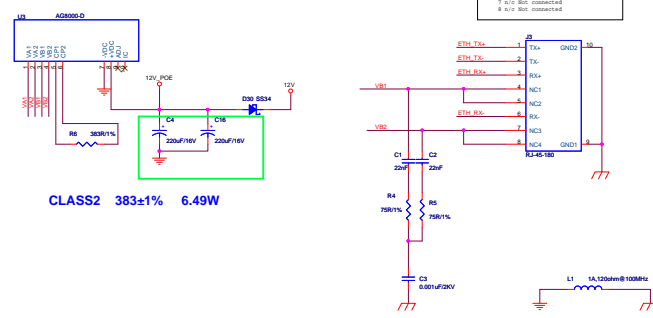
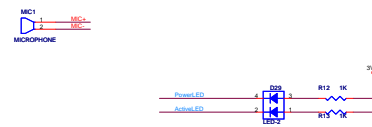
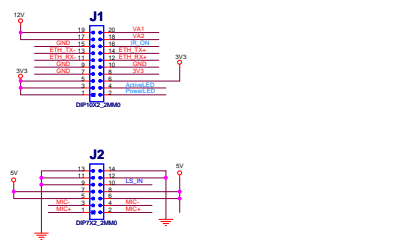
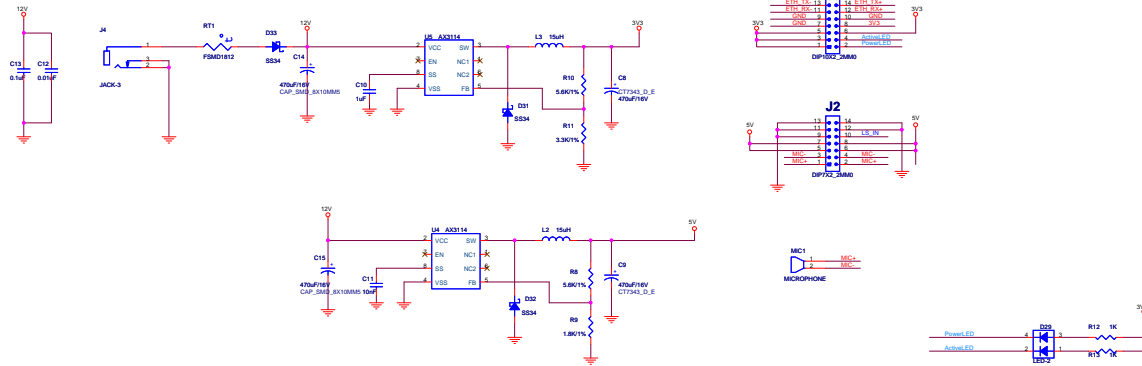
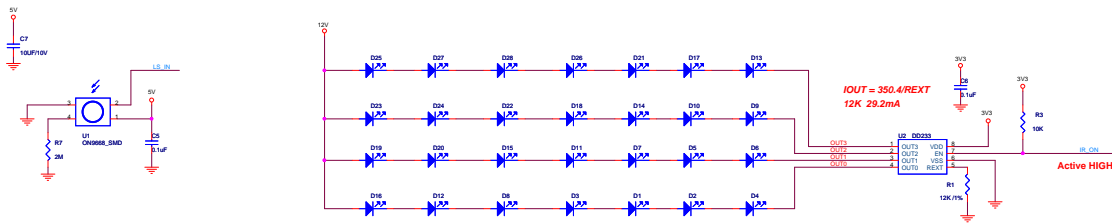


Title		
Network		
Size	Document Number	Rev
OTIPCAMCORE1VA		
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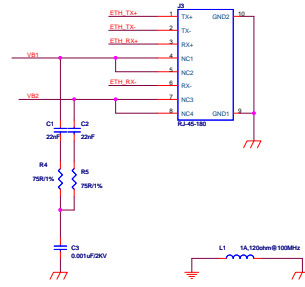


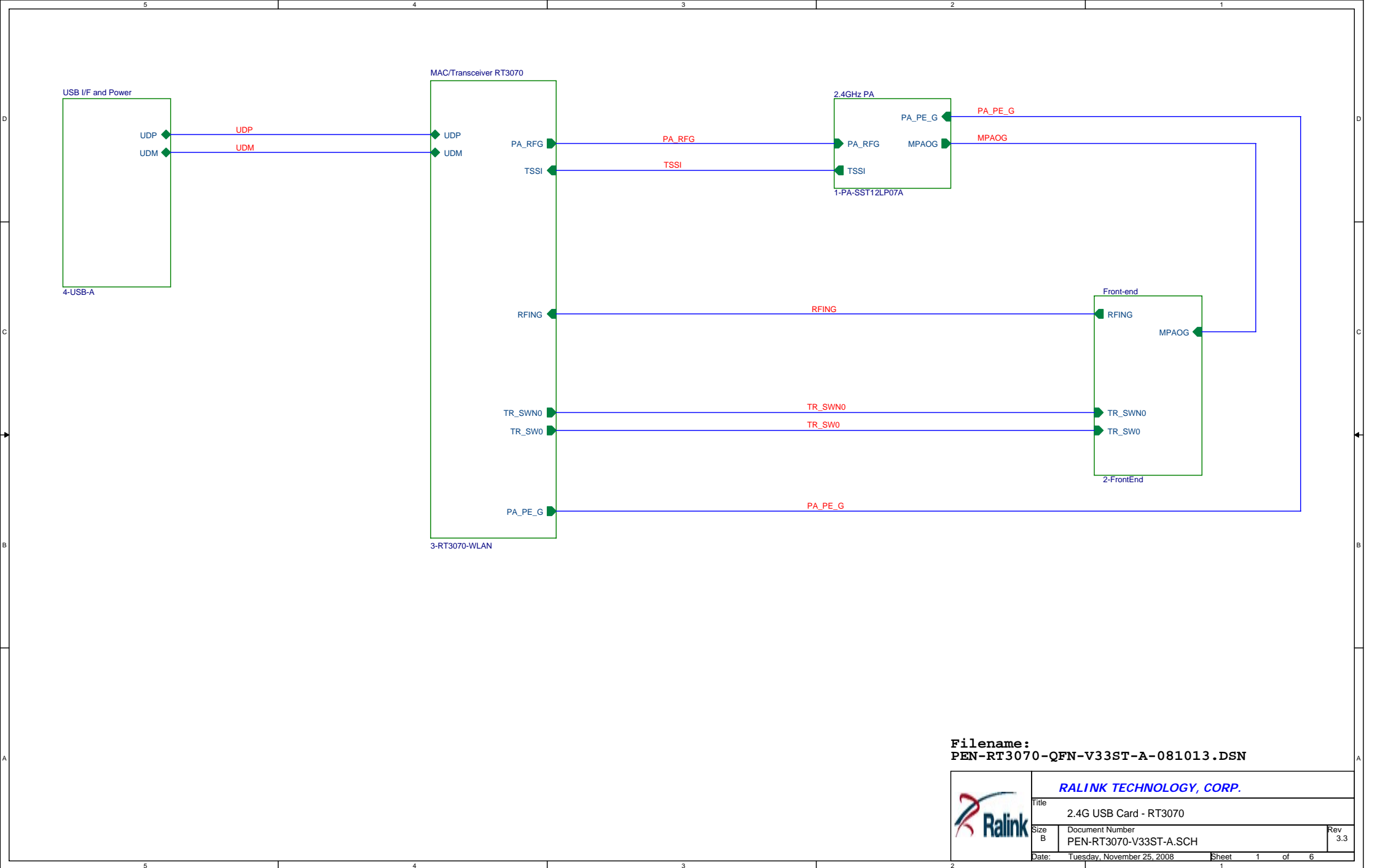
Title			
Power			
Size	Document Number		Rev
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10/10/2008-7 Pin Header Description

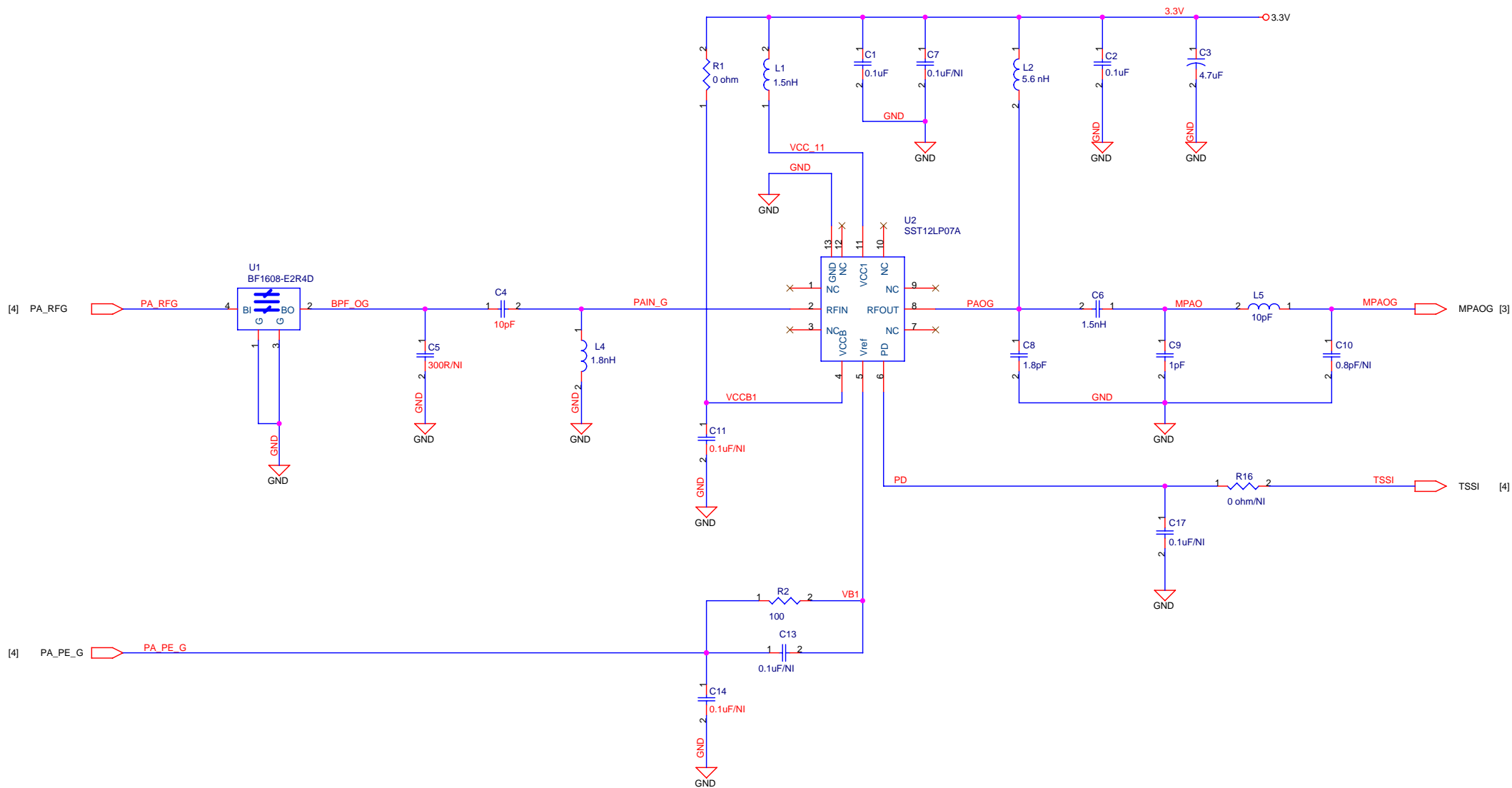
1	TX+	Transceiver Data+
2	TX-	Transceiver Data-
3	Rx+	Receiver Data+
4	Rx-	Receiver Data-
5	NC1	Not Connected
6	NC2	Not Connected
7	NC3	Not Connected
8	NC4	Not Connected
9	NC5	Not Connected
10	NC6	Not Connected



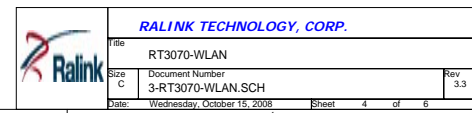


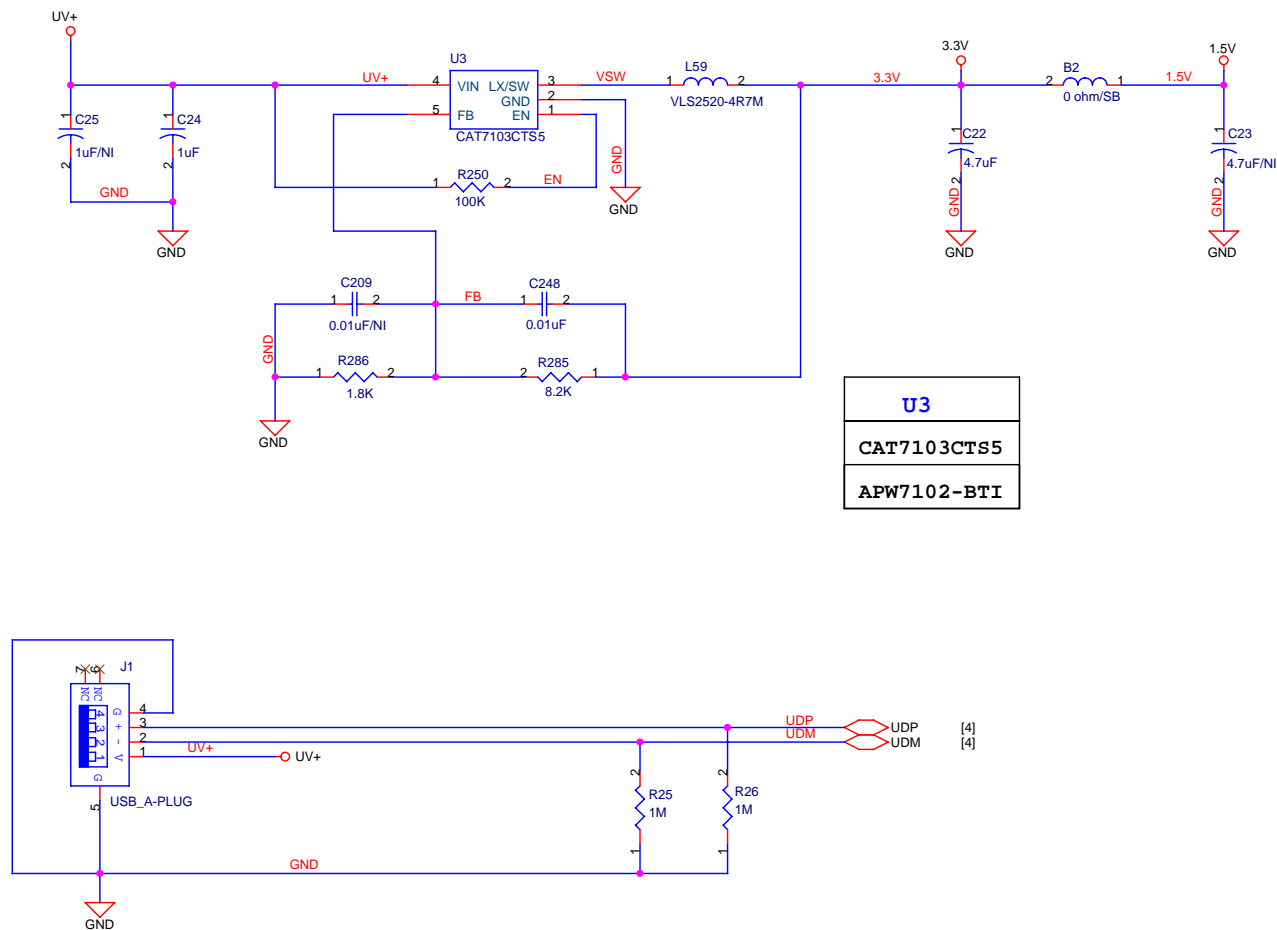
Filename:  
PEN-RT3070-QFN-V33ST-A-081013.DSN

		<i><b>RALINK TECHNOLOGY, CORP.</b></i>				
		Title		2.4G USB Card - RT3070		
		Size B	Document Number			Rev 3.3
			PEN-RT3070-V33ST-A.SCH			
Date:		Tuesday, November 25, 2008		Sheet	1 of 6	









<b>U3</b>
<b>CAT7103CTS5</b>
<b>APW7102-BTI</b>