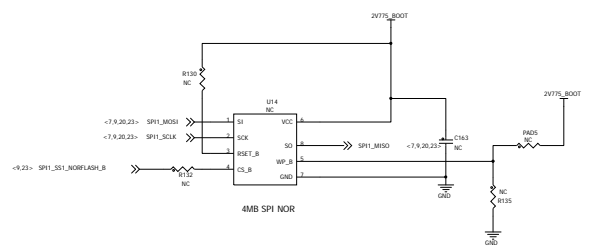
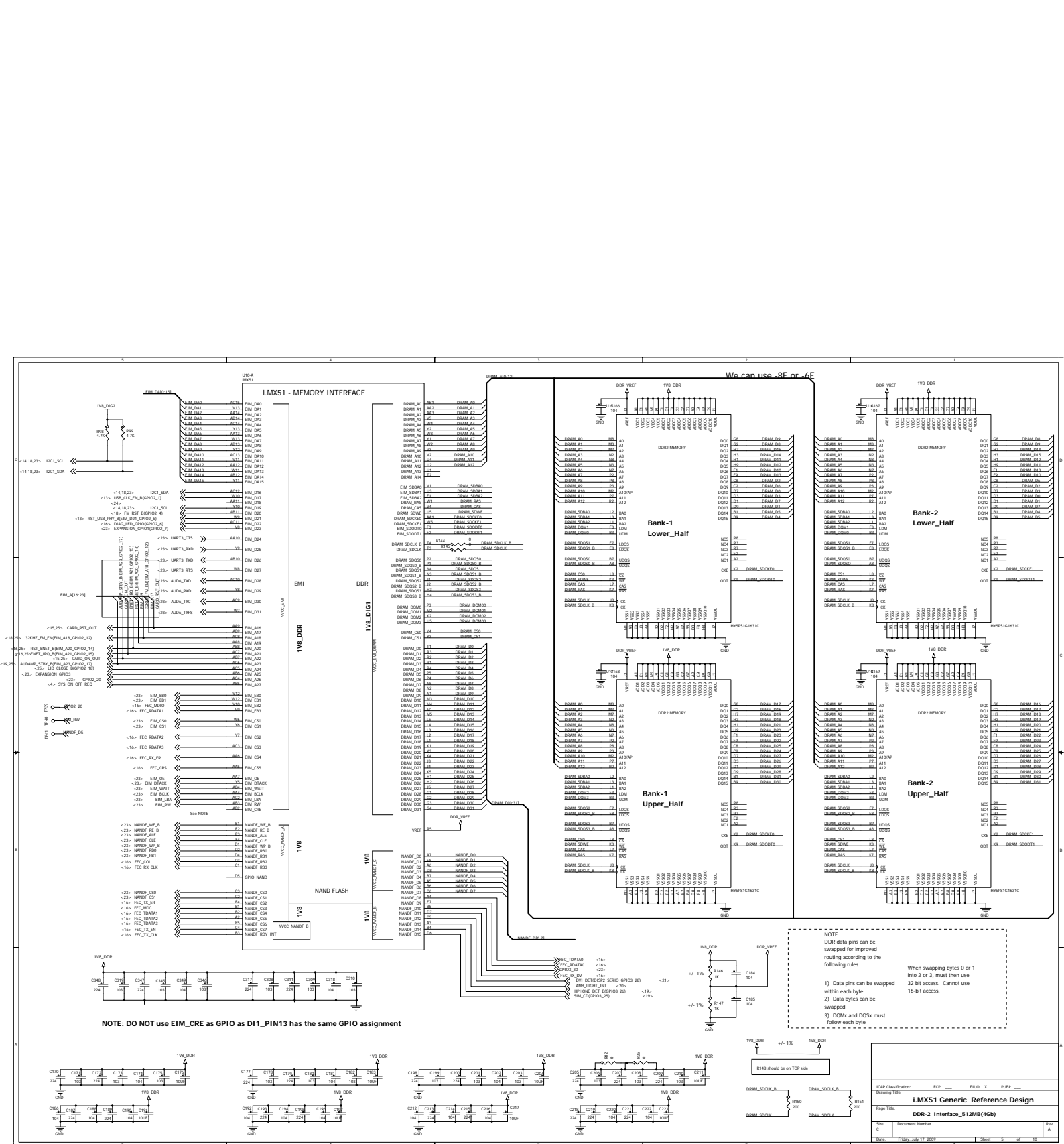
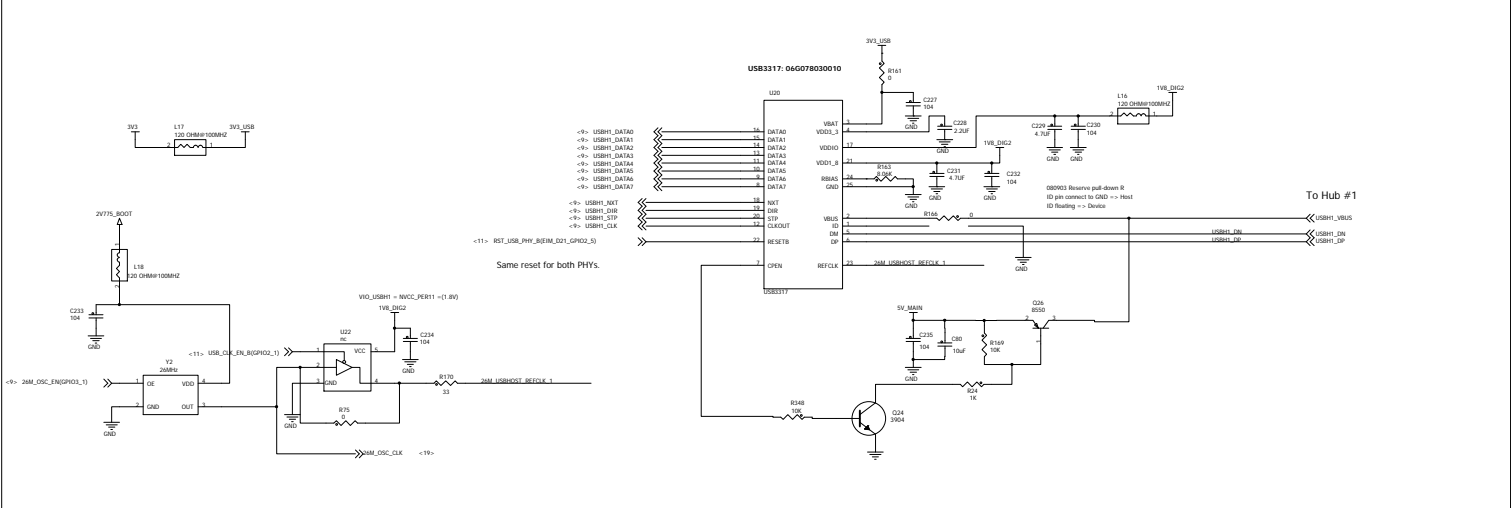


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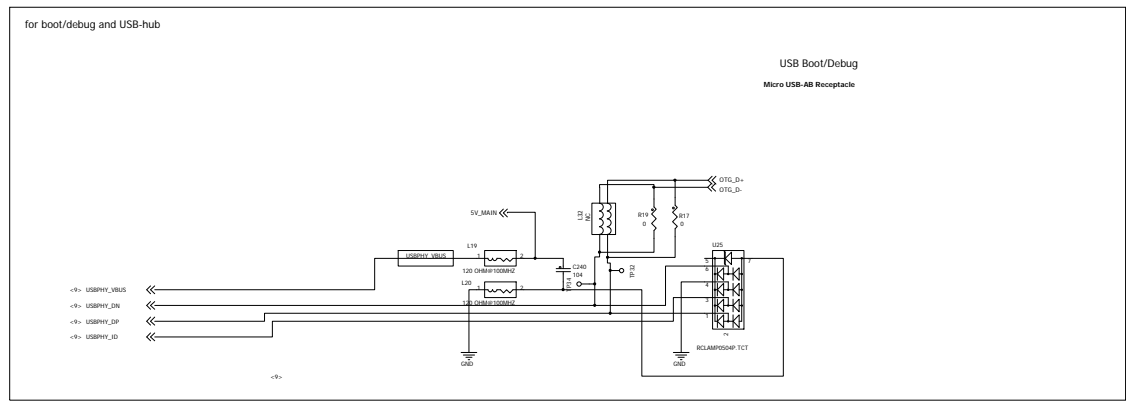
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Drawing Title: i.MX51 Generic Reference Design			
Page Title: BOOT SPI-NOR and MMC-SD			
Size C	Document Number		Rev A
Model:	110000_0001_01_2009	Model	8 of 30

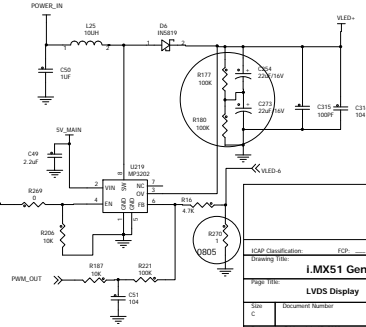
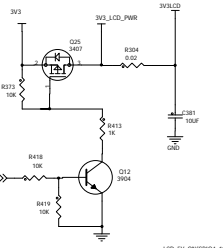
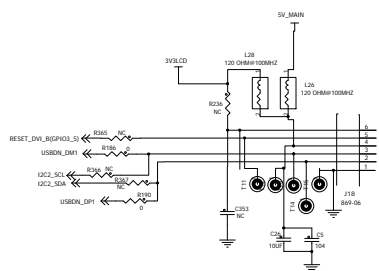
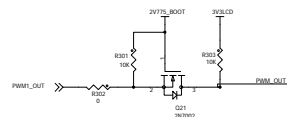
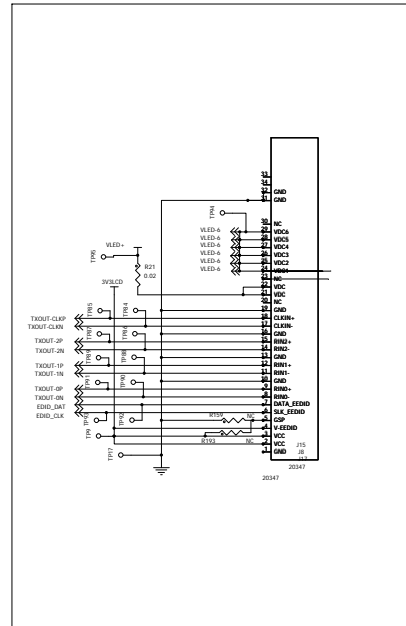
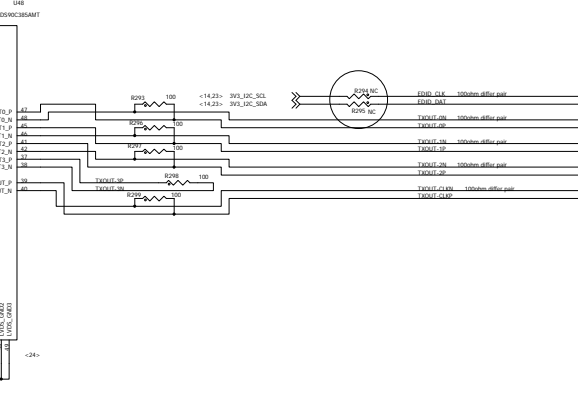
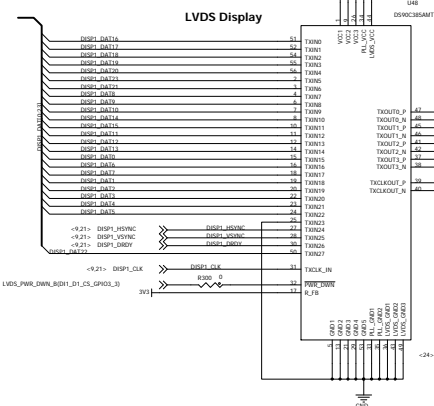
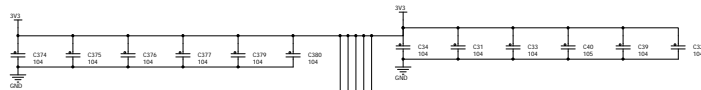


USB ULPI Host 1
to USB hub



USB on-chip OTG PHY



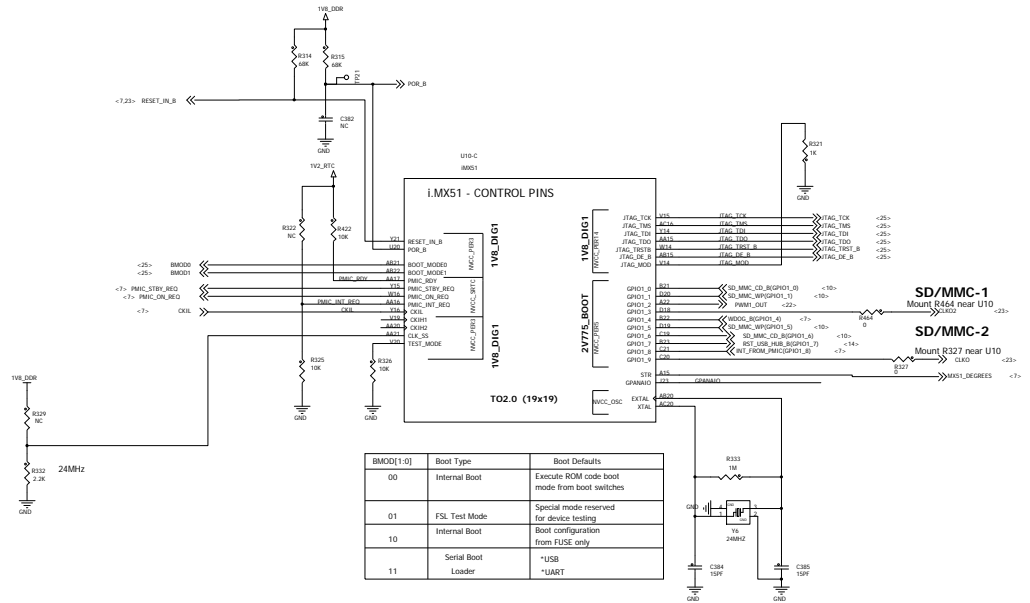


ICAP Classification: FCP F800 8 P080			
Page Title: I.MX51 Generic Reference Design			
Page Number: 1			
Rev	Doc Number	Rev	Doc Number
C		A	
Date: Friday, May 17, 2008			

i.MX Control

Reset Switch

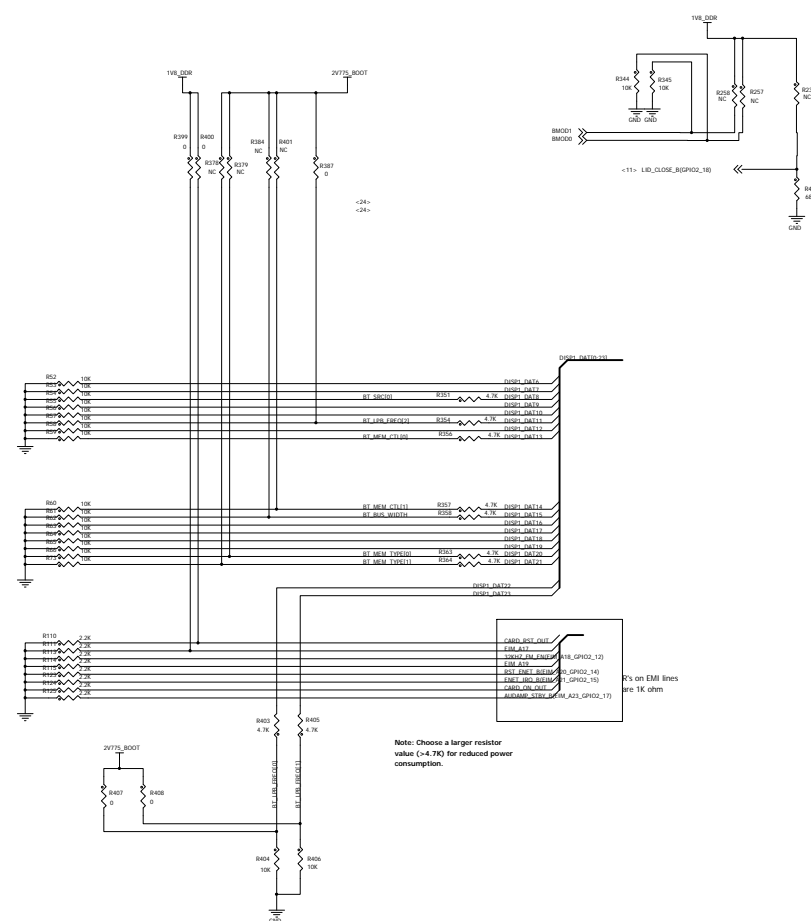
22.5792MHz needed for SPDIF
Some specific audio data rates may
require a different reference frequency



BMOD[1:0]	Boot Type	Boot Defaults
00	Internal Boot	Execute ROM code boot mode from boot switches
01	FSL Test Mode	Special mode reserved for device testing
10	Internal Boot	Boot configuration from FUSE only
11	Serial Boot Loader	*USB *UART

BOOT Mode Selection

JTAG



SMD Female DB-9 Connector

BMOD[1:0]	Boot Type	Boot Defaults
00	Internal Boot	Execute ROM code boot mode from boot switches
01	FSL Test Mode	Special mode reserved for device testing
10	Internal Boot	Boot configuration from FUSE only
11	Serial Boot Loader	*USB *UART

ICAP Classification: FCP: F300: 8: P000: 0000			
Drawing Title: I.MX51 Generic Reference Design			
Page Title: JTAG, DBG, UART, BOOT_Config			
Rev: C	Document Number:		Rev: A
Date: Friday, May 11, 2007		Sheet: 9	of 10

