## MCU:STM32F102C4T6A

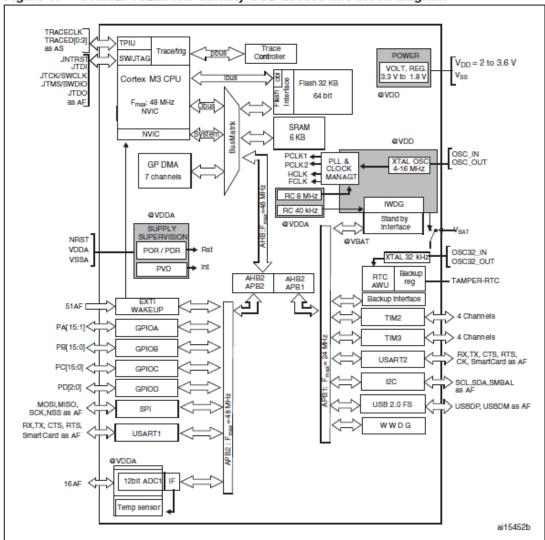


Figure 1. STM32F102xx low-density USB access line block diagram

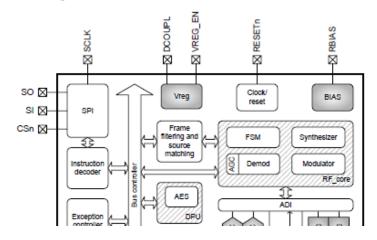
- 1. AF = alternate function on I/O port pin.
- 2.  $T_A = -40$  °C to +85 °C (junction temperature up to 105 °C).

#### MSP430F5437IPN PA PB PC PD XIN XOUT DVCC DVSS AVCC AVSS RST/NMI P1.x| P2.x| P3.x| P4.x| P5.x| P6.x| P7.x| P8 XT2IN I/O Ports Unified → ACLK Power I/O Ports I/O Ports I/O Po P1/P2 256KB P3/P4 Management P5/P6 P7/P Clock 2×8 I/Os 16KB XT2OUT ◀ 2×8 I/Os 2×8 I/Os 2×8 I/ 192KB SYS System → SMCLK Interrupt 128KB Capability LDO Watchdog RAM SVM/SVS PB PC PD Flash MCLK PA Brownout 1×16 I/Os 1×16 I/Os 1×16 L 1×16 I/Os MAB CPUXV2 and Working MDB Registers EEM (L: 8+2) ADC1 USCI0,1,2,3 12 E TA0 TA1 TB0 UCSI Ax: JTAG/ 200 K UART, SBW MPY32 RTC A CRC16 Timer\_A 5 CC Timer A Timer\_B IrDA, SPI Interface 3 CC 7 CC 16 Cha Registers Registers Registers (14 ext UCSI Bx: SPI, I2C Autos

Note: Supported memory, peripherals, and ports may vary depending upon the device.

# CC2520

### 8.3 Block Diagram



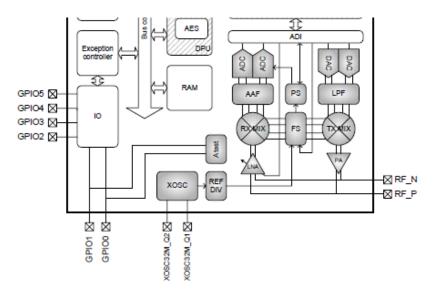
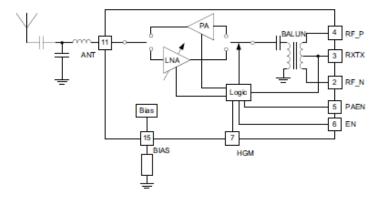


Figure 2: CC2520 block diagram

# CC2591

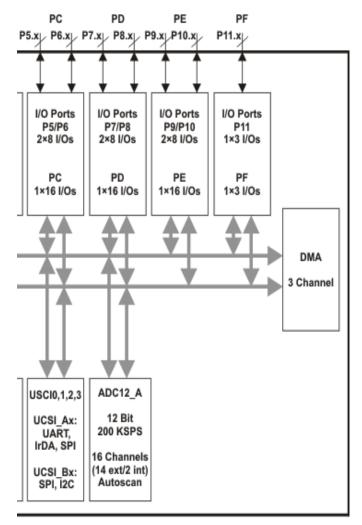
## CC2591 BLOCK DIAGRAM



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