

NAND-8bit

U5

K9G8G08 TSOP_48

VDD_3V3

ND_CLE >> 16 CLE VCC1 12

ND_ALE >> 17 ALE VCC2 37

ND_CEn0 >> 9 $\overline{\text{CE}}$ IO1 29 **ND_D0**

ND_CEn1 >> 10 NC IO2 30 **ND_D1**

ND_CEn2 >> 14 NC IO3 31 **ND_D2**

ND_CEn3 >> 15 NC IO4 32 **ND_D3**

ND_RDY_3 << 4 NC IO5 41 **ND_D4**

ND_RDY_2 << 5 NC IO6 42 **ND_D5**

ND_RDY_1 << 6 GND IO7 43 **ND_D6**

ND_RDY_0 << 7 $\overline{\text{R/B}}$ IO8 44 **ND_D7**

ND_WEn >> 18 $\overline{\text{WE}}$ NC 48

ND_OEn >> 8 $\overline{\text{RE}}$ NC 47

ND_WP >> 19 $\overline{\text{WP}}$ NC 46

1 NC NC 45

2 NC NC 40

3 NC NC 39

11 NC NC 38

20 NC NC 35

21 NC NC 34

22 NC NC 33

23 NC NC 28

24 NC VSS1 13

25 NC VSS2 36

26 NC

27 NC

VDD_3V3

ND_D[0..7]

ND_CSn0 >> R56 0R/5% R0402

ND_CSn1 >> R57 0R/5% R0402

ND_CSn2 >> NC R58 0R/5% R0402

ND_CSn3 >> NC R59 0R/5% R0402

ND_CEn0 >> R60 0R/5% R0402

ND_CEn1 >> R61 0R/5% R0402

ND_CEn2 >> NC R62 0R/5% R0402

ND_CEn3 >> NC R63 0R/5% R0402

VDD_3V3

ND_RDY_0 >> R64 47K/5% R0402

ND_RDY_1 >> R65 47K/5% R0402

ND_RDY_2 >> R66 47K/5% R0402

ND_RDY_3 >> R67 47K/5% R0402

VDD_3V3

ND_WP << R68 4.7K/5% R0402

VDD_3V3

PassWord << R69 1K2/5% R0402

U6

DS2431P DS28E10P+ TSOC-6

VDD_3V3

PassWord << 2 DATA NC1 3

1 GND NC2 4

NC3 5

NC4 6

VDD_3V3

R63 1K2/5% R0402

R64 0R/5% R0402

R65 4.7K/5% R0402

R66 47K/5% R0402

R67 47K/5% R0402

R68 4.7K/5% R0402

R69 1K2/5% R0402

R70 0R/5% R0402

R71 0R/5% R0402

R72 0R/5% R0402

R73 0R/5% R0402

R74 0R/5% R0402

R75 0R/5% R0402

R76 0R/5% R0402

R77 0R/5% R0402

R78 0R/5% R0402

R79 0R/5% R0402

R80 0R/5% R0402

R81 0R/5% R0402

R82 0R/5% R0402

R83 0R/5% R0402

R84 0R/5% R0402

R85 0R/5% R0402

R86 0R/5% R0402

R87 0R/5% R0402

R88 0R/5% R0402

R89 0R/5% R0402

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R151 0R/5% R0402

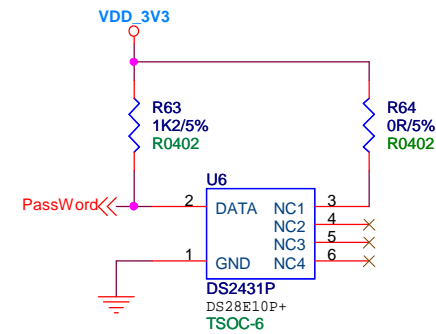
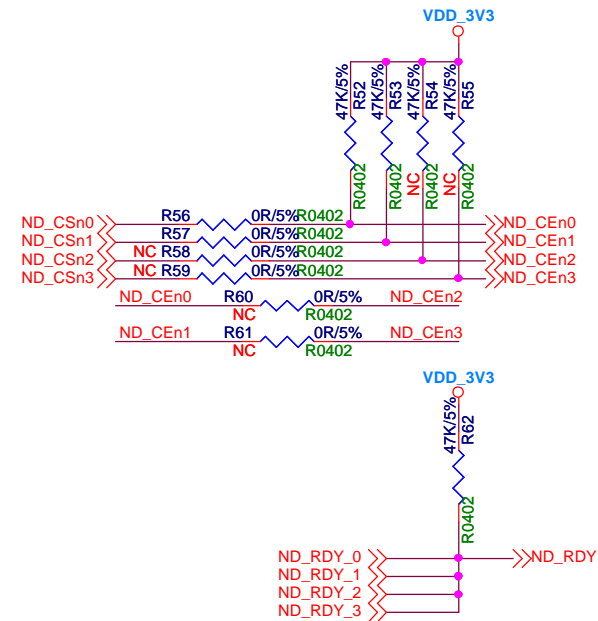
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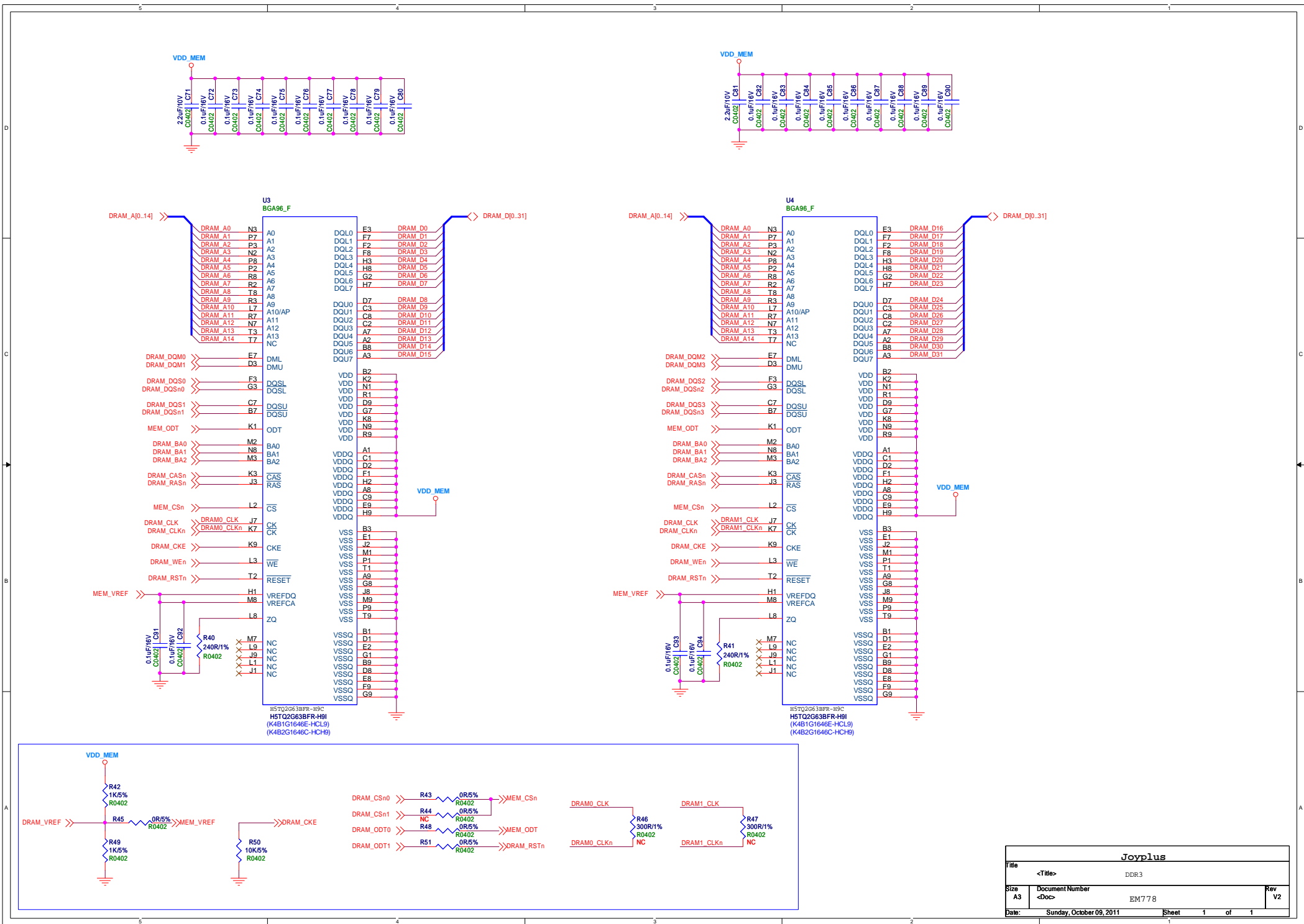
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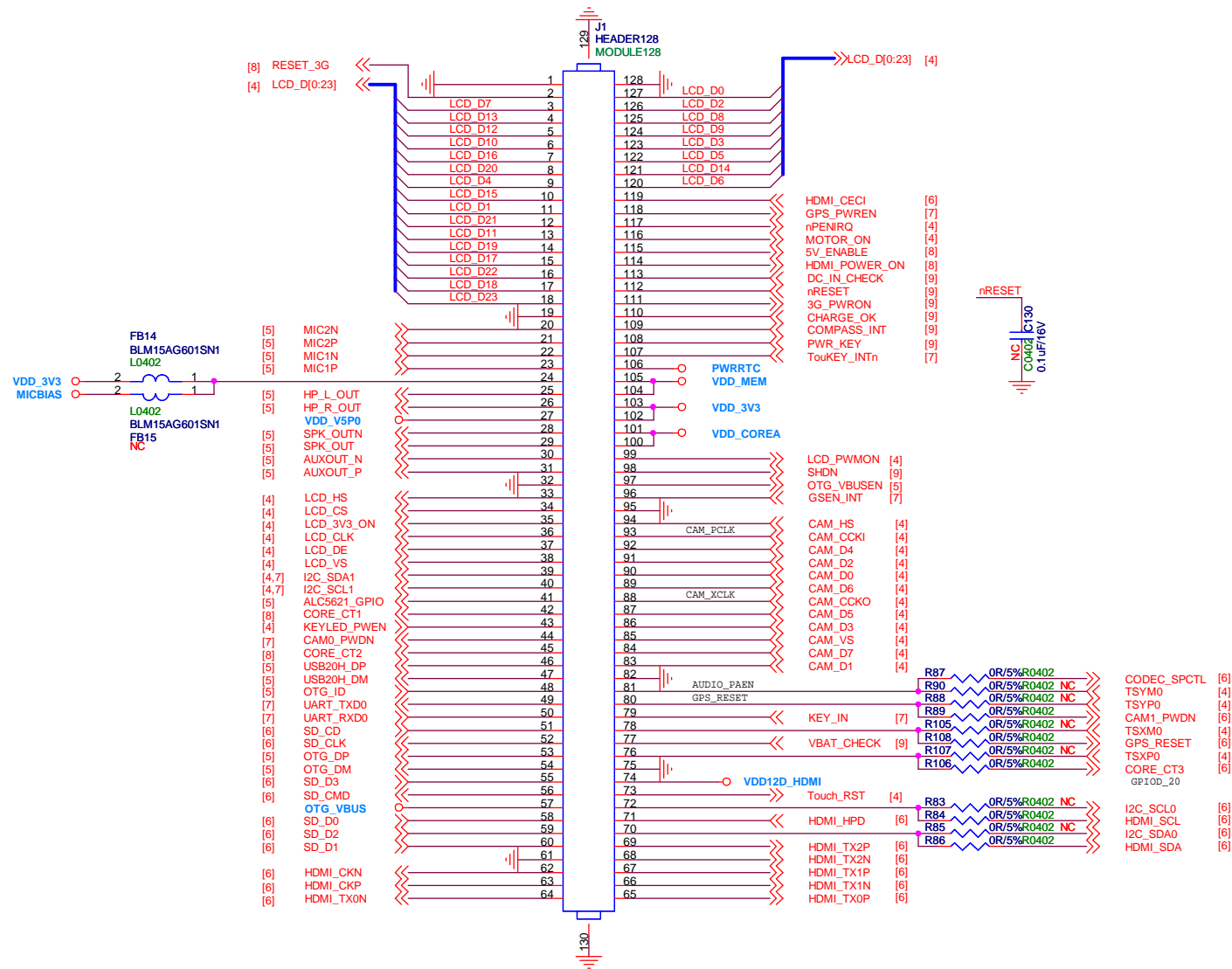
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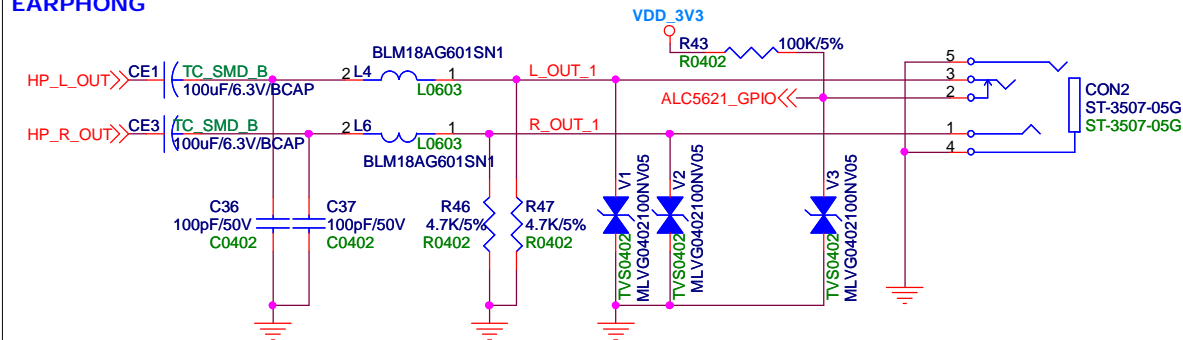


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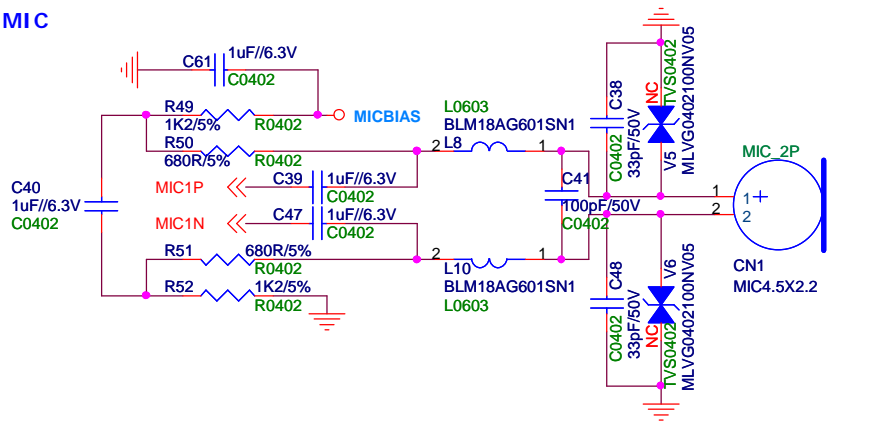


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Date:	Sunday, October 09, 2011	Sheet 1 of 1

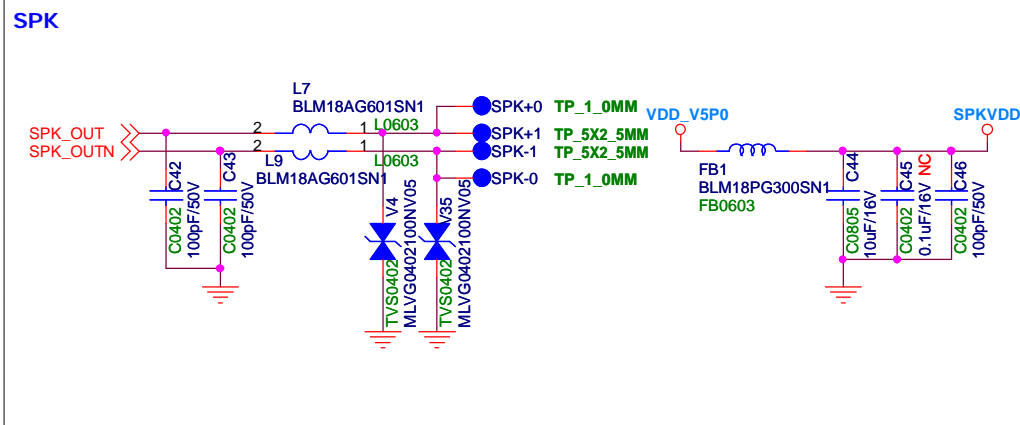
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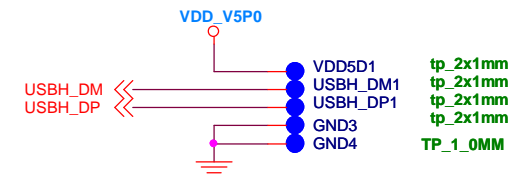
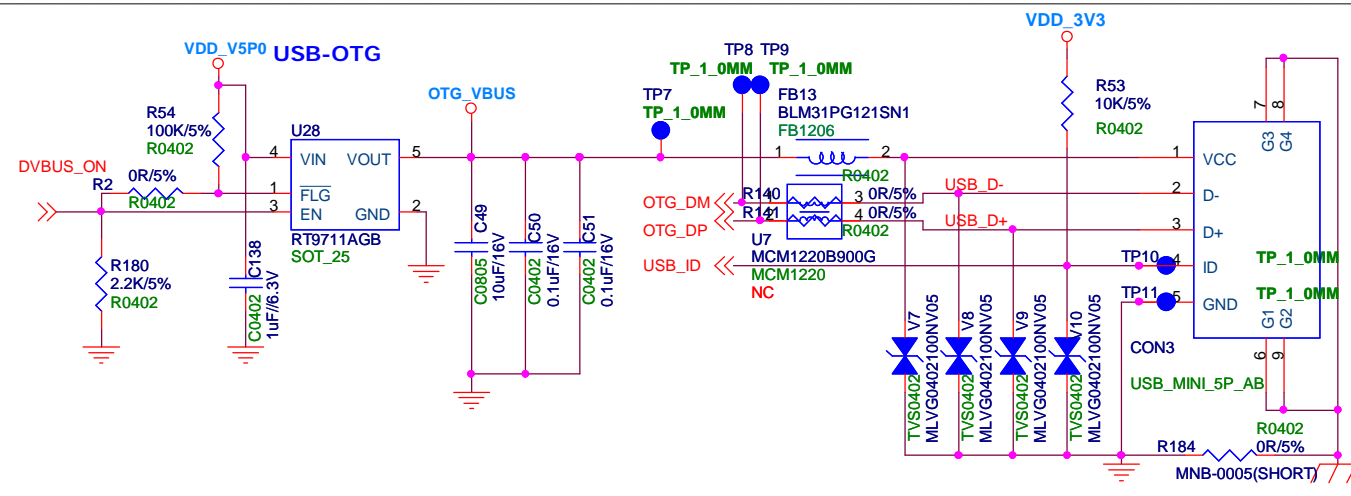
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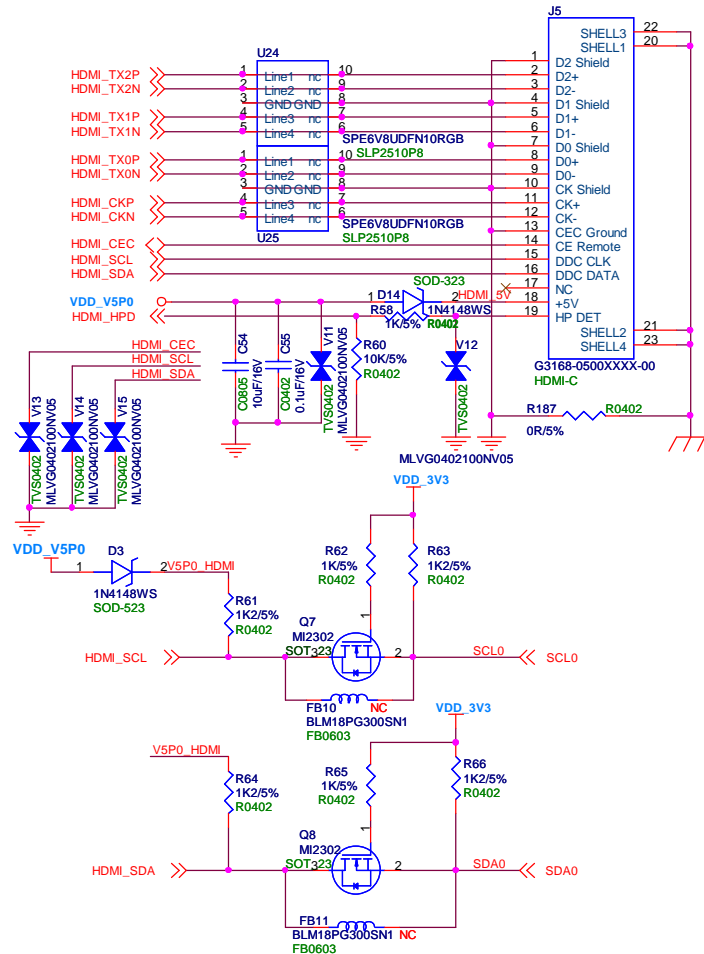
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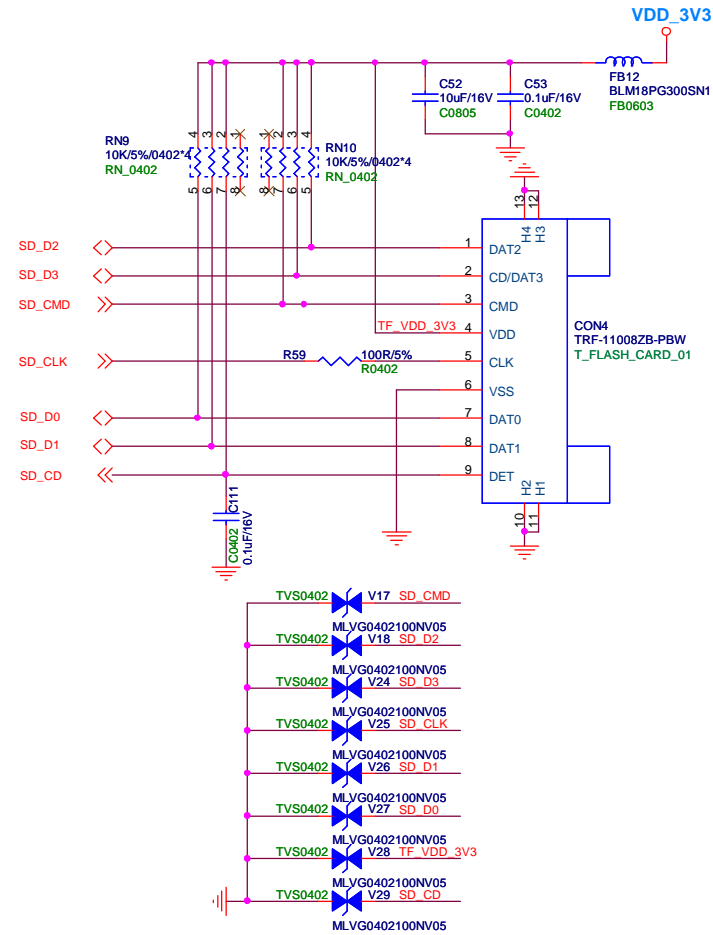
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Sheet		3 of 8
Rev		M6

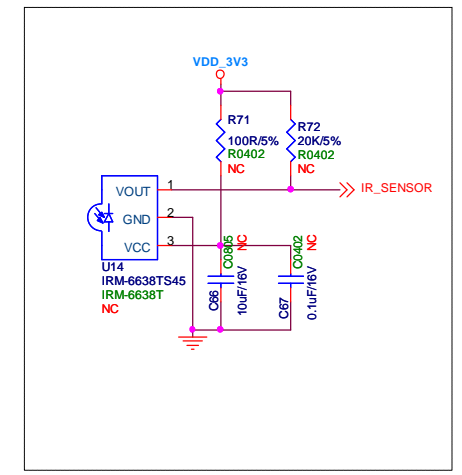
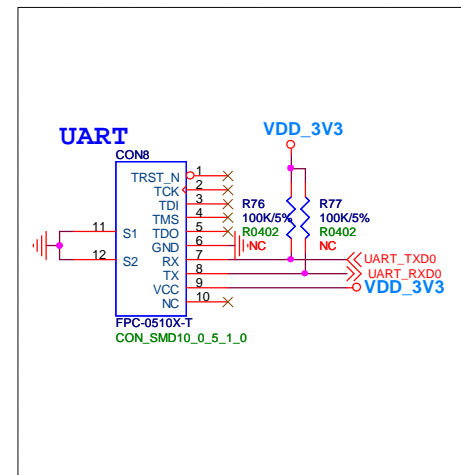
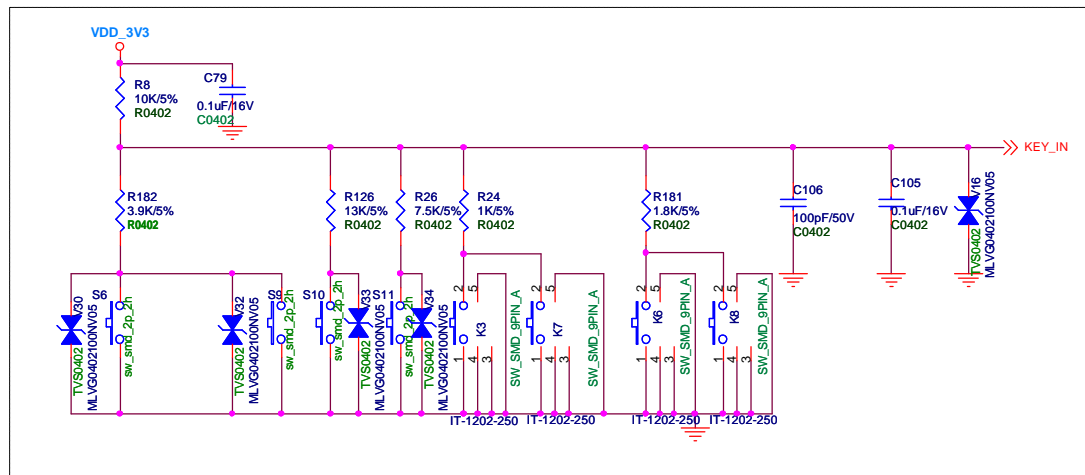
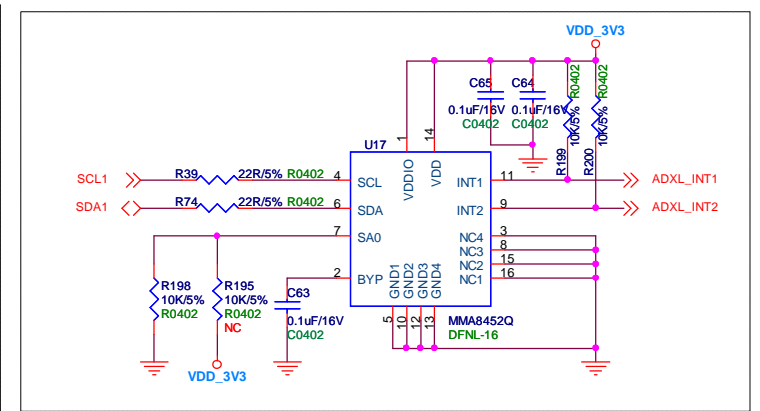
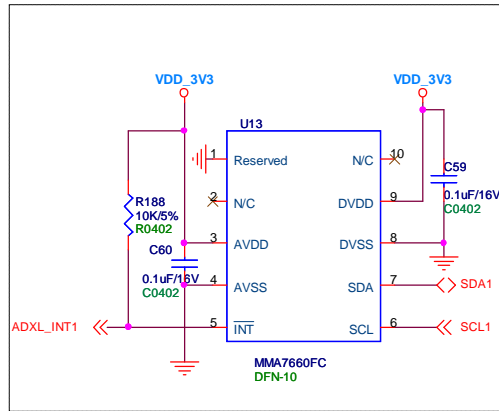
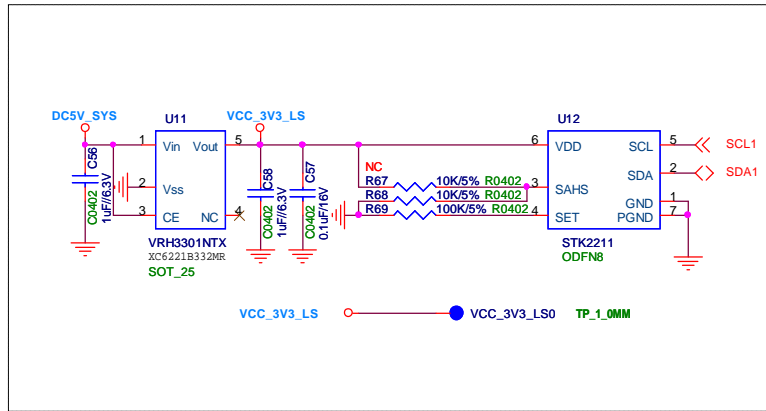
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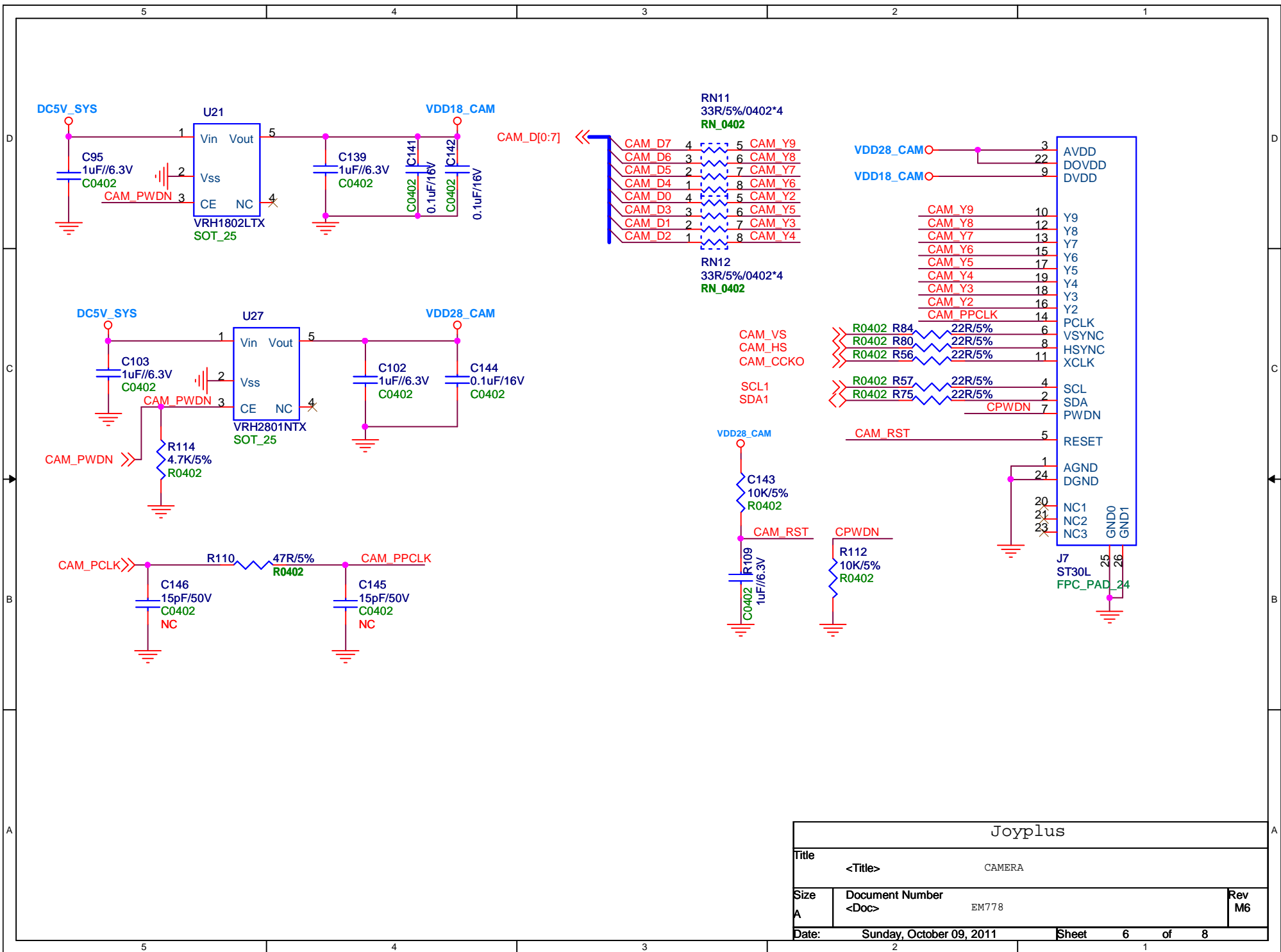
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Size	Document Number	Rev	
Custom	<Doc>	M6	
Date:	Sunday, October 09, 2011	Sheet	4 of 8

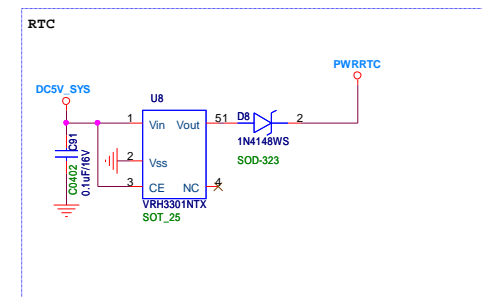
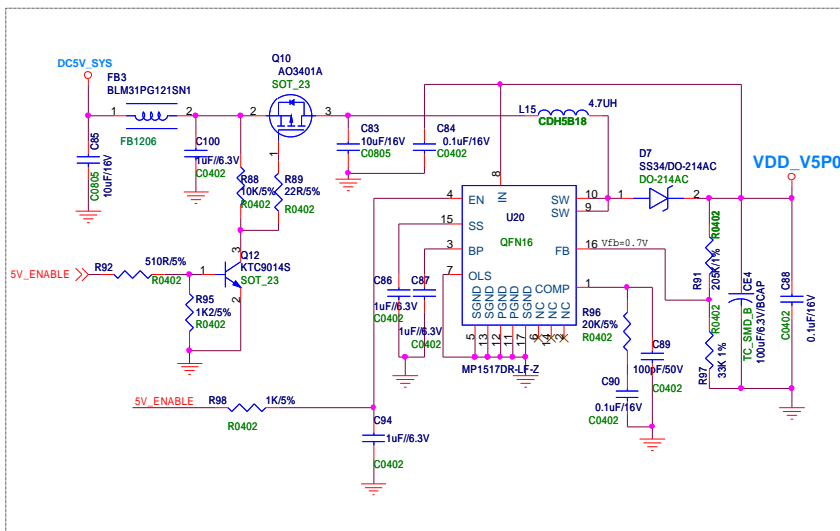
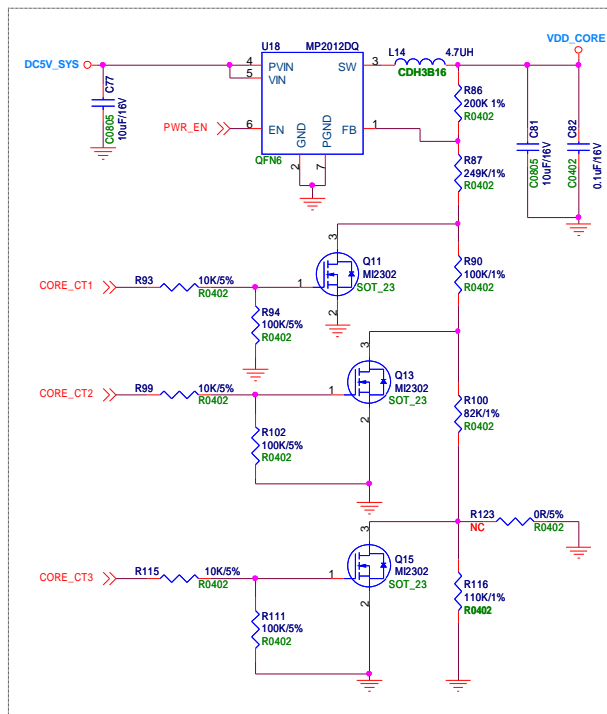
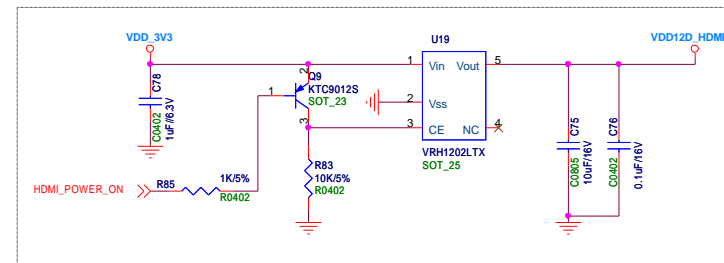
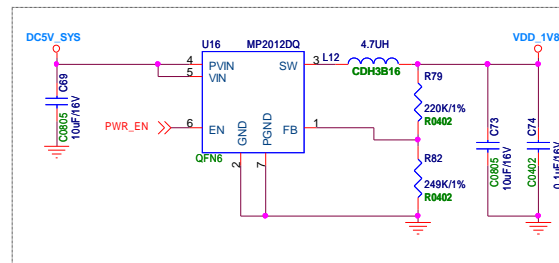
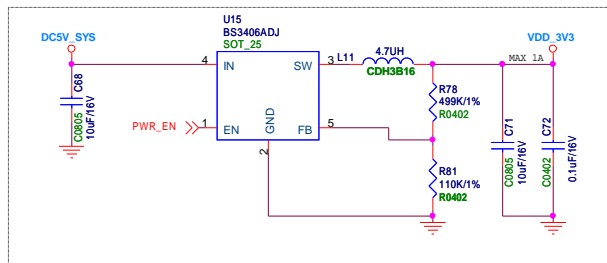


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Size	Document Number	Rev	
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Date:	Sunday, October 09, 2011	Sheet	5 of 8



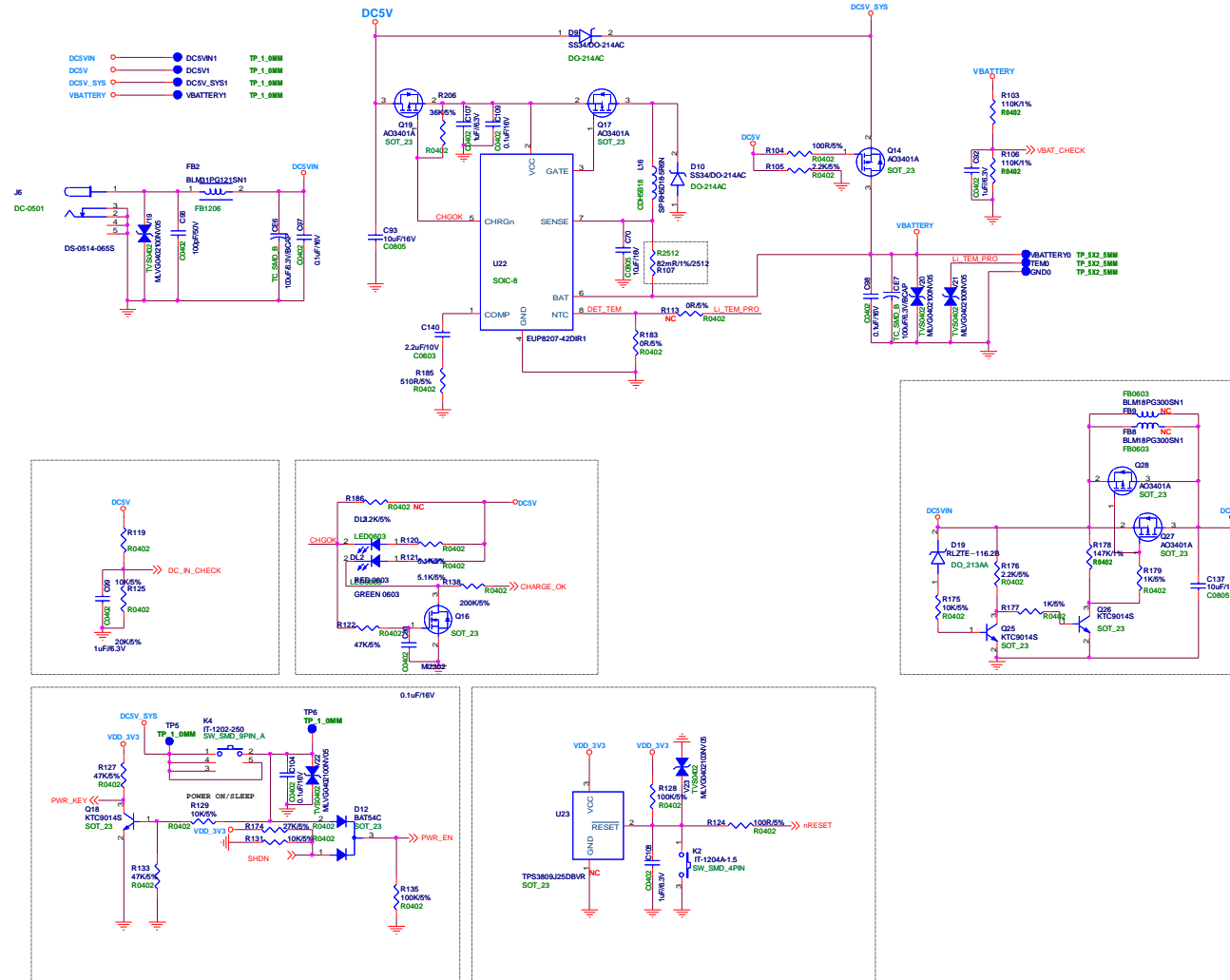
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 VDD12D_HDMI ○ ● VDD12D_HDMI1 TP_1_0MM
 VDD_CORE ○ ● VDD_CORE1 TP_1_0MM

 VDD_V5P0 ○ ● VDD_V5P0 TP_1_0MM
 PWRRTC ○ ● PWRRTC0 TP_1_0MM



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Size	Document Number	Rev	M6
A3	<Doc>	EN778	
Date:	Sunday, October 09, 2011	Sheet	7 of 8

CHARGE



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Size	Document Number	Rev	Rev
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