

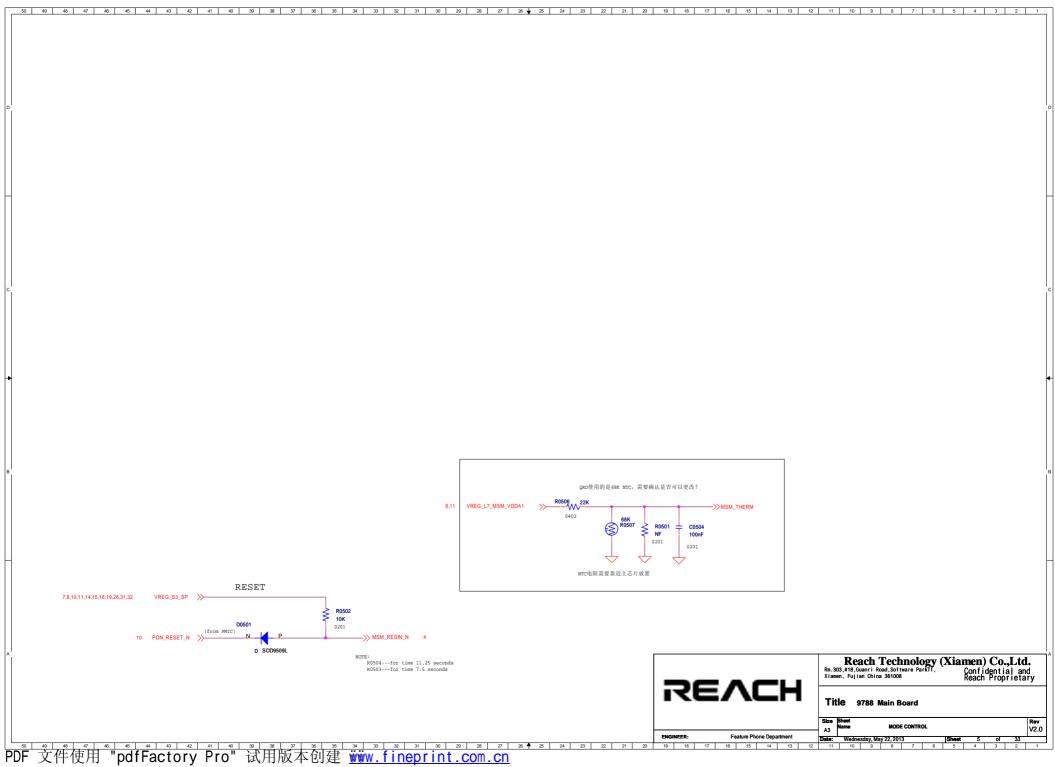
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	ENGINEEK:	rea	ture Pri	one Depa	nment		Date:	Wedn	esday,	May	/ 22, 201	3		Shee	t	2	of	33	
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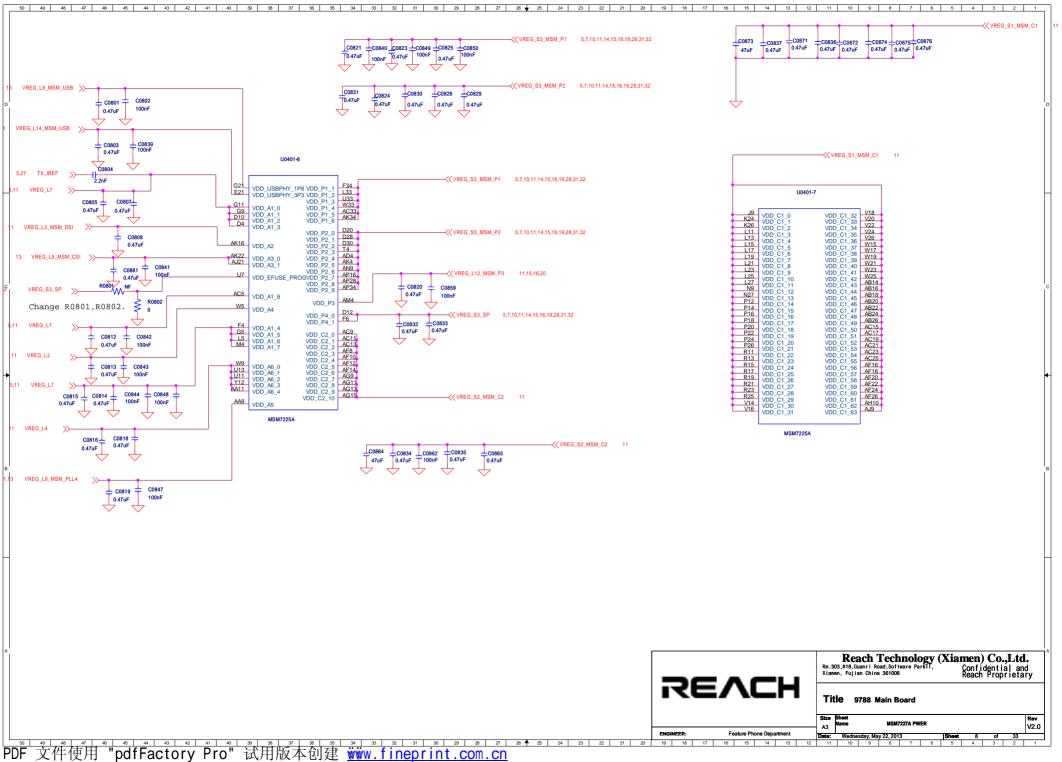
			37 36 35 34 33 32 31 30 29 28 27	26 😾 25 24 23	22 21 20 19 18	17 16 15 14 13	12 11 10 9 8 7 6 5 4 3 2
PIN	9778	QRD7227A	<b>GPIO Mapping</b>	PIN	. 9778	QRD7227A	
GPIO 00[P2] GPIO 01[P2]	PA_R1 MSM_TCXO_EN	PA_R1 MSM_TCXO_EN	11 8	GPIO 78[P2]	DTV_1V2_EN	NULL	
GPIO 02[P2] GPIO 03[P2]	PMIC_SSBI NULL	PMIC_SSBI NULL		GPIO 79[P2] GPIO 80[P2]	DTV_1V8_EN GSM_PA_EN	NULL GSM_PA_EN	
GPIO 04[P2] GPIO 05[P2]	LCDC_PCLK	KEY_BACKLIGHT_EN		GPIO 81[P2] GPIO 82[P2]	EXT_GPS_LNA_EN AUDIO_PA_SHD	EXT_GPS_LNA_EN NULL	
GPIO 06[P2]	NULL   NULL	NULL NULL		GPIO 83[P2]	FM_RDS_INT	FM_RDS_INT	
GPIO 07[P2] GPIO 08[P2]	LCDC_RED_5 LCDC_RED_4	LCDC_RED_5 LCDC_RED_4		GPIO 84[P2] GPIO 85[P2]	UIM2_MSM_CLK/TS_CLK TS_VALID	M2_MSM_CLK BT_SYS_RESET_N	
D GPIO 09[P2] GPIO 10[P2]	LCDC_RED_3 LCDC_RED_2	LCDC_RED_3 LCDC_RED_2		GPIO 86[P2] GPIO 87[P2]	I TS_TDO UIM2_MSM_DATA/TS_SYNC	HS_DETECT_N IM2_MSM_DATA	
GPIO 11[P2]	LCDC_RED_1	LCDC_RED_1		GPIO 88[P2]	SDC3_CLK	NULL	
GPIO 12[P2] GPIO 13[P2]	LCDC_RED_0   NULL	LCDC_RED_0 NULL		GPIO 89[P2] GPIO 90[P2]	SDC3_CMD SDC3_DATA3	LCD_BACKLIGHT_ON LCD_RESET_N	
GPIO 14[P2] GPIO 15[P2]	NULL CAM_MCLK	NULL CAM_MCLK		GPIO 91[P2] GPIO 92[P2]	SDC3_DATA2 SDC3_DATA1	CAM_0.3MP_PWDN WLAN_EN	
GPIO 16[P2] GPIO 17[P2]	CAM_2M_PWDN ALS_INT_N	GRYO_DRDY ALS_INT_N		GPIO 93[P2] GPIO 94[P2]	SDC3_DATA0	CAM_5MP_SHDN_N NULL	
GPIO 18[P2]	HS_DETECT_N	COMPASS_DRDY		GPIO 95[P2]	BOOT_SCUR	BOOT_SCUR	
GPIO 19[P2] GPIO 20[P2]	SDC3_DATA7   SDC3_DATA6	SPI_SDI SPI_SCLK		GPIO 96[P2] GPIO 97[P2]	I2C_DTV_SCL I2C_DTV_SDA	FLASH_EN NULL	
GPIO 21[P2] GPIO 22[P2]	SDC3_DATA5 PA_ON_1	LCD_SPI_CS PA_ON_1		GPIO 98[P2] GPIO 99[P2]	NULL	NULL NULL	
GPIO 23[P2]	CAM_2M_REST	CAM_5M_REST		GPIO 100[P2] GPIO 101[P2]	NULL NULL	NULL NULL	
GPIO 24[P2] GPIO 25[P2]	PM_INT_N PS_HOLD	PM_INT_N PS_HOLD		GPIO 102[P2]	NULL	NULL	
GPIO 26[P2] GPIO 27[P2]	CAP_TS_RESET_N SPI_SDI	CAP_TS_RESET_N GYRO_INT_N		GPIO 103[P2] GPIO 104[P2]	NULL NULL	NULL NULL	
GPIO 28[P2] GPIO 29[P2]	SPI_SCLK SPI_CS	ACC_INT CAM_0.3M_REST_N		GPIO 105[P2] GPIO 106[P2]	NULL NULL	NULL NULL	
GPIO 30[P2]	DTV_RST	COMPASS_INT		GPIO 107[P2] GPIO 108[P2]	CAM_0.3M_PWDN SDC3_DATA4	LED_BLUE_EN LED_RED_EN	
GPIO 31[P2] GPIO 32[P2]	KYPD_DRV0	KYPD_DRV0 KYPD_DRV1		GPIO 109[P2]	WLAN_EN	LED_GREEN_EN	
GPIO 33[P2] GPIO 34[P2]	LCD_RESET CAM_0.3M_REST_N	NULL NULL		GPIO 110[P2] GPIO 111[P2]	PA_ON_2 LCDC_GREEN_1	NULL LCDC_GREEN_1	
GPIO 35[P2] GPIO 36[P2]	CAM_AVDD_EN KEYSENSE_NO	EXT_2V85_EN KEYSENSE NO		GPIO 112[P2] GPIO 113[P2]	LCDC_GREEN_0	LCDC_GREEN_0 NULL	
GPIO 37[P2]	KEYSENSE_N1	KEYSENSE_N1		GPIO 114[P2]	NULL	NULL	
GPIO 38[P2] GPIO 39[P2]	DTV_INT   WAKE_ON_WIRELESS	NULL WAKE_ON_WIRELESS		GPIO 115[P2] GPIO 116[P2]	LCDC_BLUE_5 LCDC_BLUE_4	LCDC_BLUE_5 LCDC_BLUE_4	
GPIO 40[P2] GPIO 41[P2]	HOOK_DET_MSM OVP_FLAG_N	NULL OVP_FLAG_N		GPIO 117[P2] GPIO 118[P2]	LCDC_BLUE_3 LCDC_BLUE_2	LCDC_BLUE_3 LCDC_BLUE_2	
GPIO 42[P2]	BT_SYS_RESET_N	SDC1_CARD_DET		GPIO 119[P2] GPIO 120[P2]	LCDC_GREEN_4 LCDC GREEN 3	LCDC_GREEN_4 LCDC_GREEN_3	
GPIO 43[P2] GPIO 44[P2]	MSM_UART1DM_RFR_N MSM_UART1DM_CTS_N	MSM_UART1_RFR_N MSM_UART1_CTS_N		GPIO 121[P2]	LCDC_GREEN_2	LCDC_GREEN_2	
GPIO 45[P2] GPIO 46[P2]	MSM_UART1DM_RXD   MSM_UART1DM_TXD	MSM_UART1_RXD MSM_UART1_TXD		GPIO 122[P2] GPIO 123[P2]	UART1_RX UART1_TX	NULL NULL	
GPIO 47[P2] GPIO 48[P2]	UIM1_MSM_CLK CAP_TS_INT_N	UIM1_MSM_CLK CAP_TS_INT_N		GPIO 124[P2] GPIO 125[P2]	WIFI_CHIP_PWD_L LCDC_BLUE_1	WIFI_CHIP_PWD_L LCDC_BLUE_1	
GPIO 49[P2] GPIO 50[P2]	LCD_BACKLIGHT_EN UIM1_MSM_DATA	NULL UIM1_MSM_DATA		GPIO 126[P2] GPIO 127[P2]	LCDC_BLUE_0 LCDC_VSYNC	LCDC_BLUE_0 LCDC_VSYNC	
GPIO 51[P3]	SDC1_CARD_D3	SDC1_CARD_D3		GPIO 128[P2] GPIO 129[P2]	LCDC_HSYNC	LCDC_HSYNC	
GPIO 52[P3] GPIO 53[P3]	SDC1_CARD_D2 SDC1_CARD_D1	SDC1_CARD_D2 SDC1_CARD_D1		GPIO 130[P2]	LCDC_DEN LCDC_GREEN_5	LCDC_DEN LCDC_GREEN_5	
GPIO 54[P3] GPIO 55[P3]	SDC1_CARD_D0 SDC1_CARD_CMD	SDC1_CARD_D0 SDC1_CARD_CMD		GPIO 131[P2] GPIO 132[P2]	12C1_SCL 12C1_SDA	I2C1_SCL I2C1_SDA	
GPIO 56[P3] GPIO 57[P2]	SDC1_CARD_CLK NULL	SDC1_CARD_CLK NULL			 		
GPIO 58[P2]	NULL	NULL			I I		
GPIO 59[P2] GPIO 60[P2]	SSBI_RTR6285A   I2C0_SCL	SSBI_RTR6285A I2C0_SCL			I I		
GPIO 61[P2] GPIO 62[P2]	I2C0_SDA SDC2_CLK	I2C0_SDA SDC2_CLK			I I		
GPIO 63[P2] GPIO 64[P2]	SDC2_CMD SDC2_DATA3	SDC2_CMD SDC2_DATA3			I I		
GPIO 65[P2]	SDC2_DATA2	SDC2_DATA2			I I		
GPIO 66[P2] GPIO 67[P2]	SDC2_DATA1 SDC2_DATA0	SDC2_DATA1 SDC2_DATA0			I I		
GPIO 68[P2] GPIO 69[P2]	AUX_PCM_DOUT AUX_PCM_DIN	AUX_PCM_DOUT AUX_PCM_DIN			I I		
GPIO 70[P2] GPIO 71[P2]	AUX_PCM_SYNC AUX_PCM_CLK	AUX_PCM_SYNC AUX_PCM_CLK		NOTE:			
GPIO 72[P2]	ANT_SEL3	NULL		P1 Pad gr	oup 1; tied to VDD		
GPIO 73[P2] GPIO 74[P2]	ANT_SEL2 ANT_SEL1	ANT_SEL2 ANT_SEL1			oup 2; tied to VDD		
GPIO 75[P2] GPIO 76[P2]	ANT_SEL0 DTV_LNA_ON	ANT_SEL0 NULL			oup 3; tied to VDD oup 4 (internal ci		VDD P4 (1 8 V)
GPIO 77[P2]	DTV_LNA_BYPASS	APT_BUCK_ENABLE		ra rau gr	oup i (incernar Ci	TOUTUBIL CIEU CO	VDD_11 (1.0 V)
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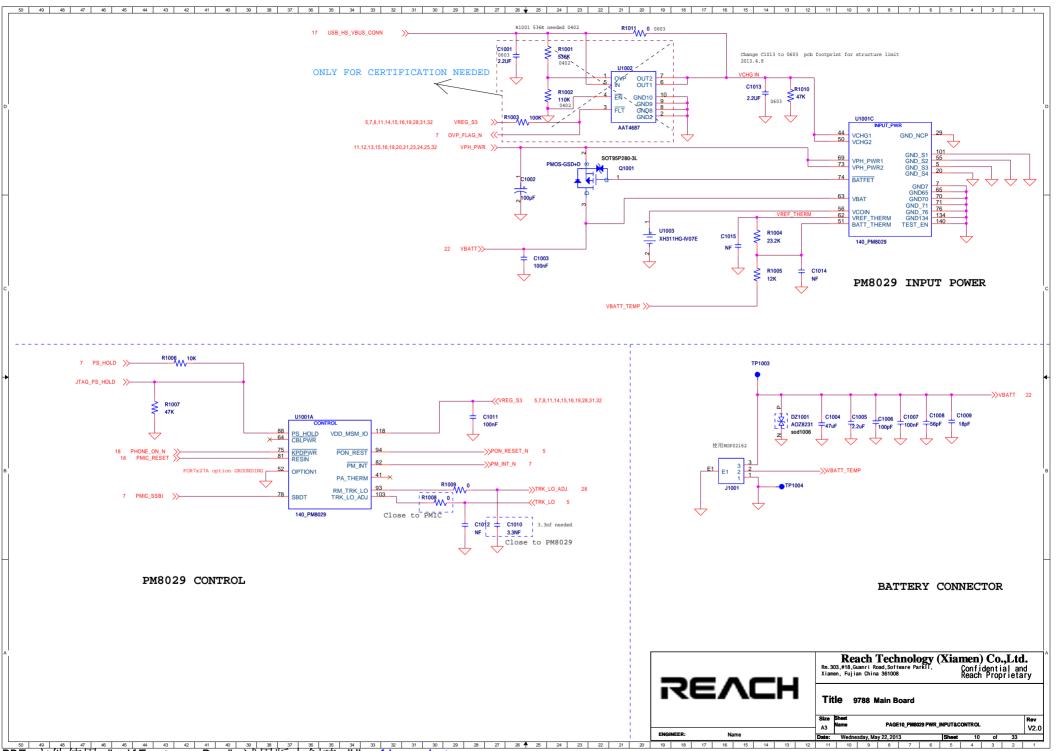


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	NGINEER:         Feature Phone Department           9         18         17         16         15         14         13         12	A3         Name         EBI1&EBI2         V2.0           Date:         Wednesday, May 22, 2013         Sheet         6         of         33
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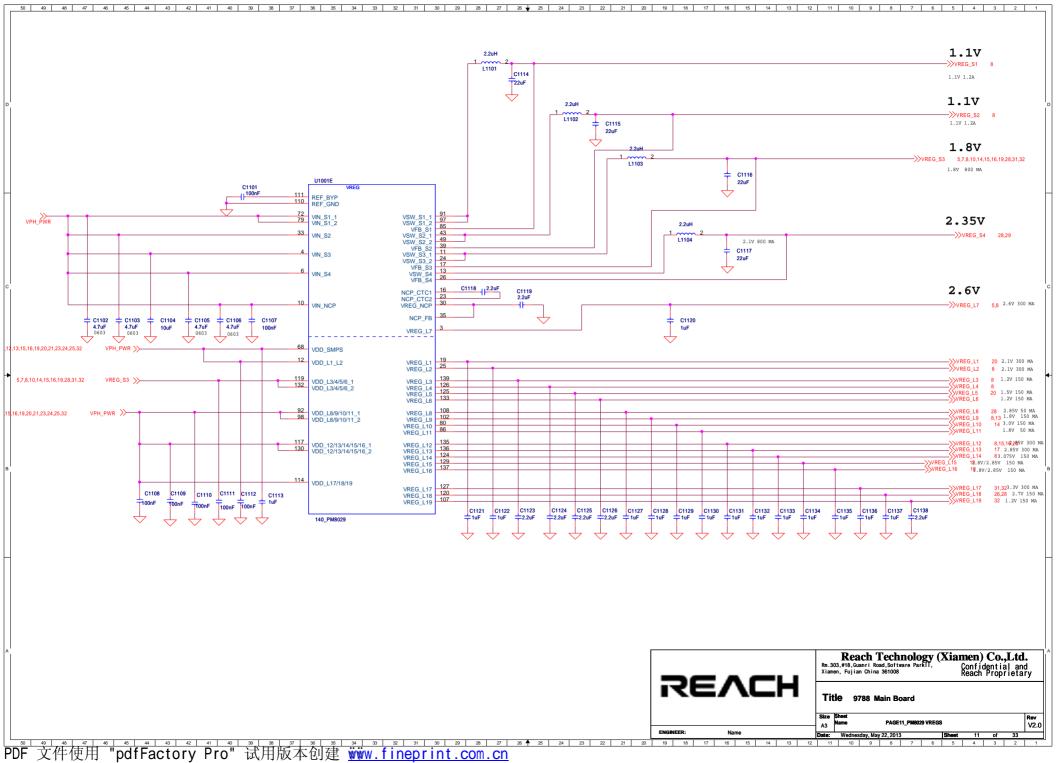
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	ENGINEER:         Feature Phone Department           19         18         17         16         15         14         13         12	Name   GPIO   V2.0     Date: Wednesday, May 22, 2013   Sheet 7 of 33
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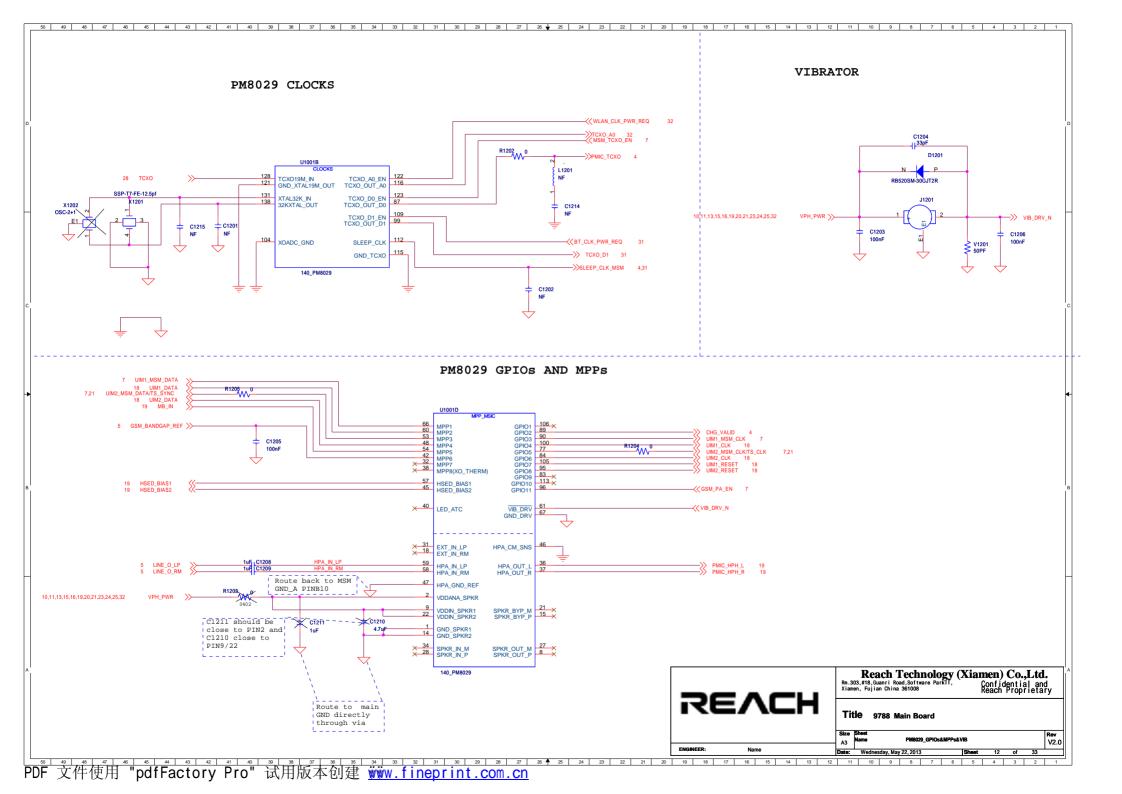


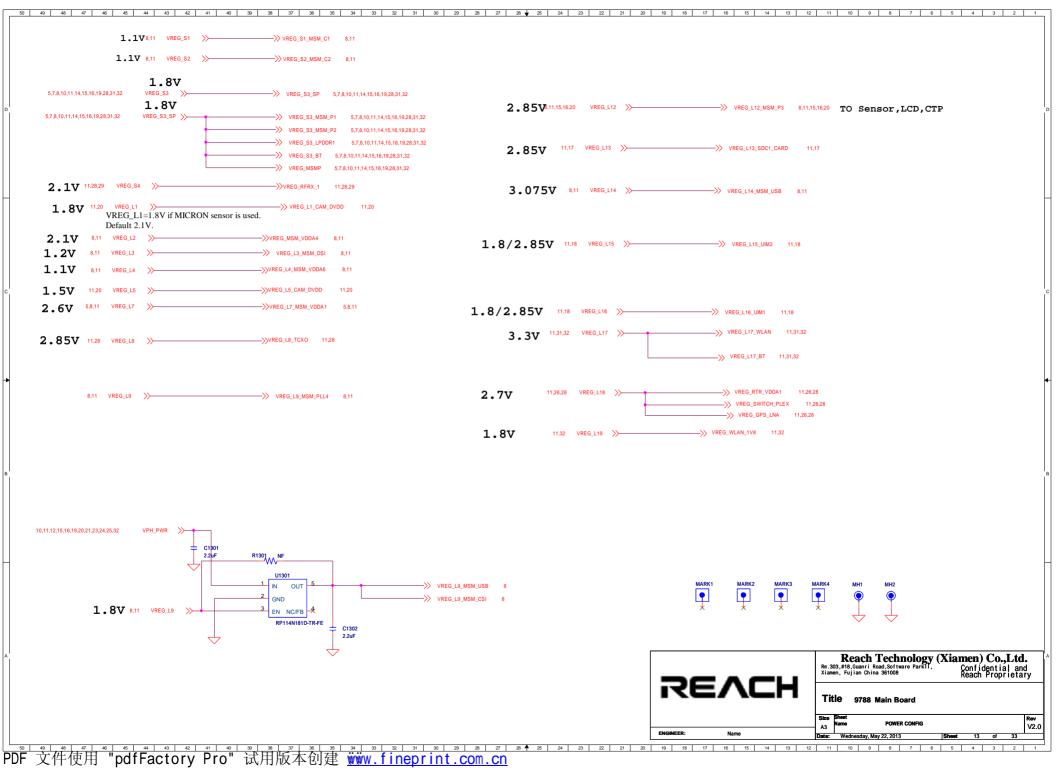
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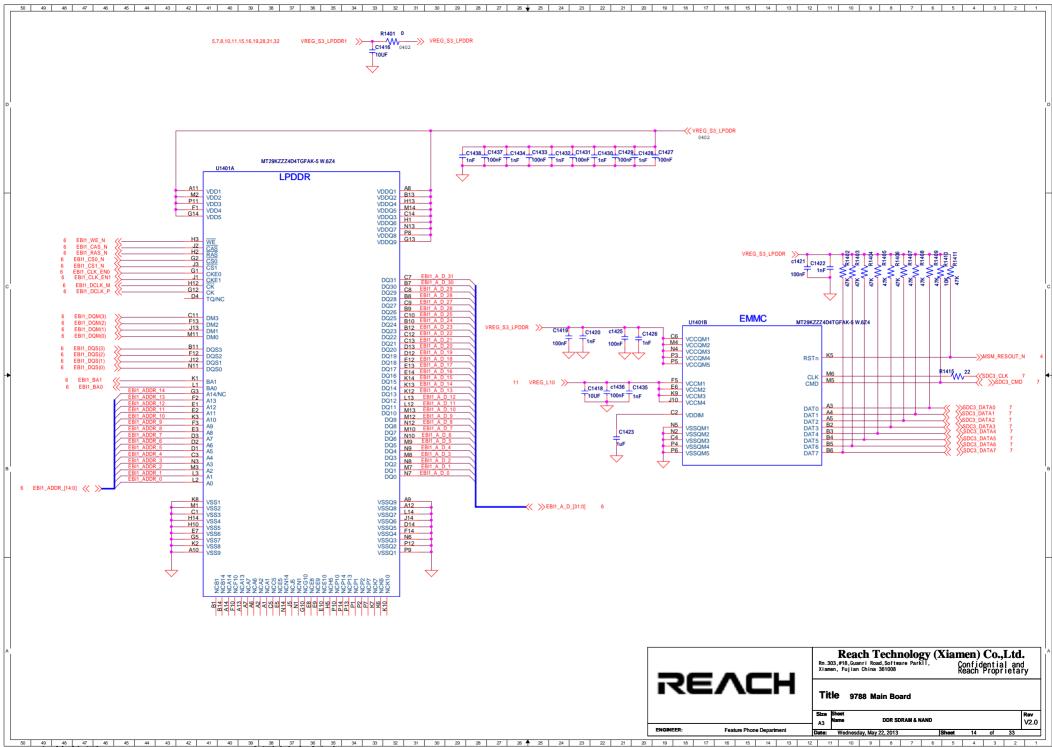


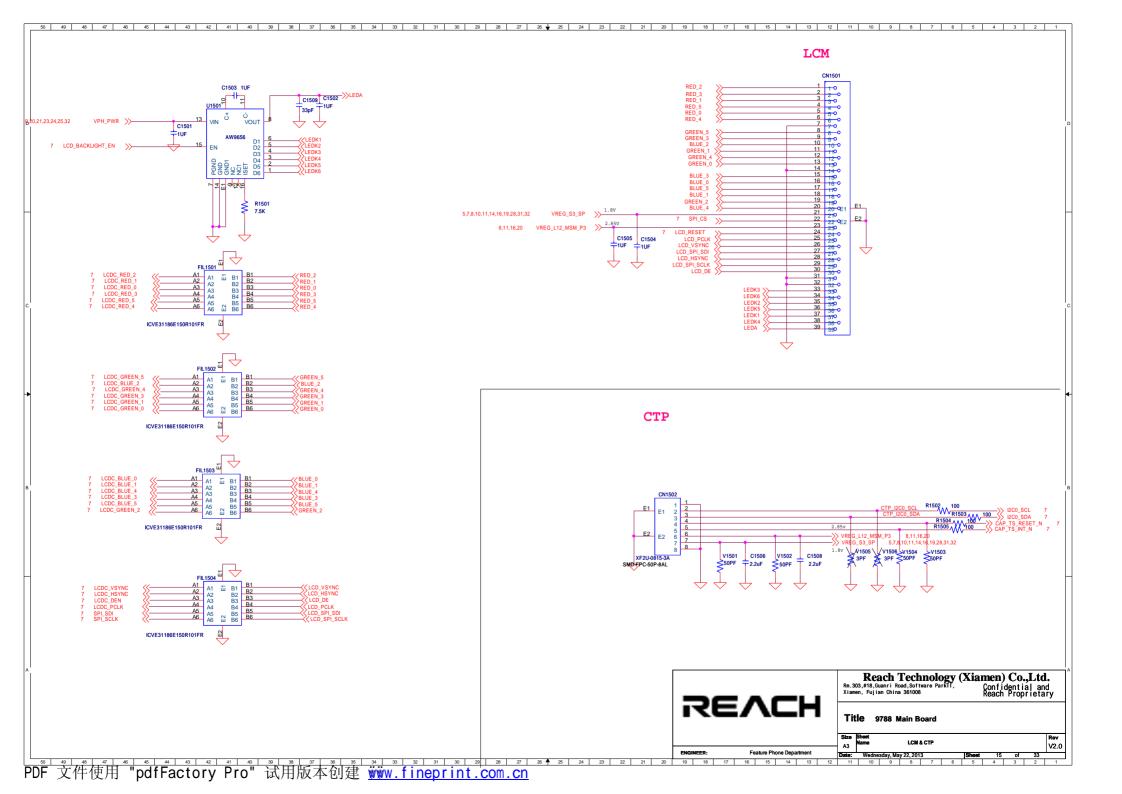
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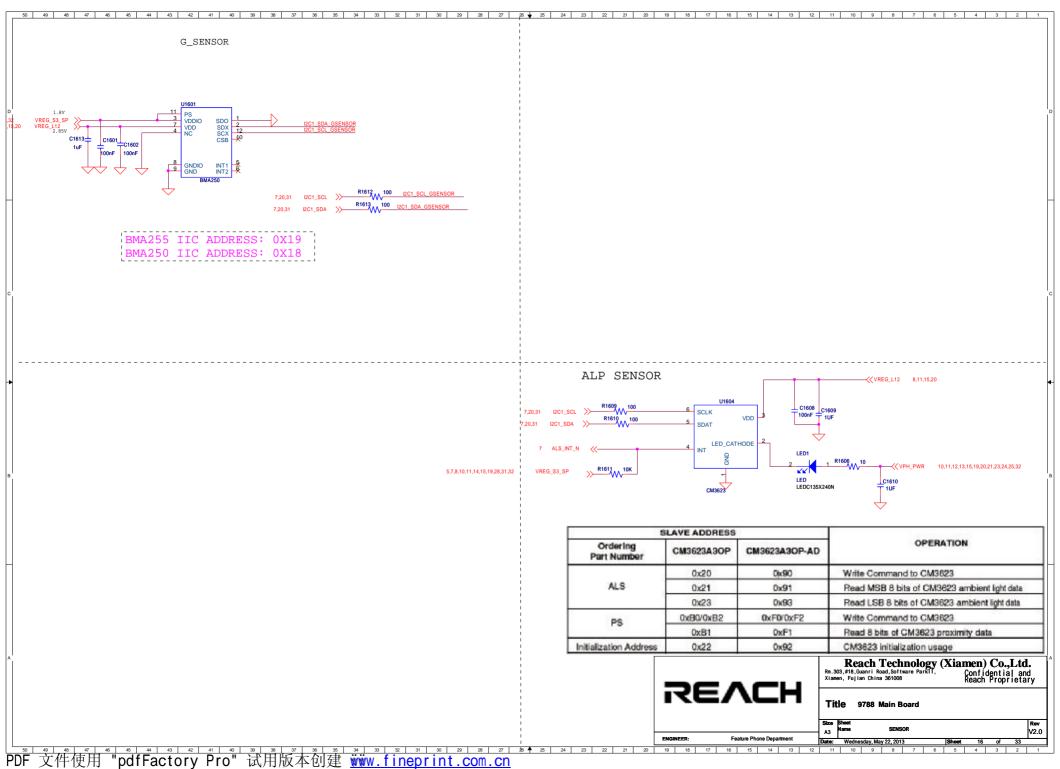


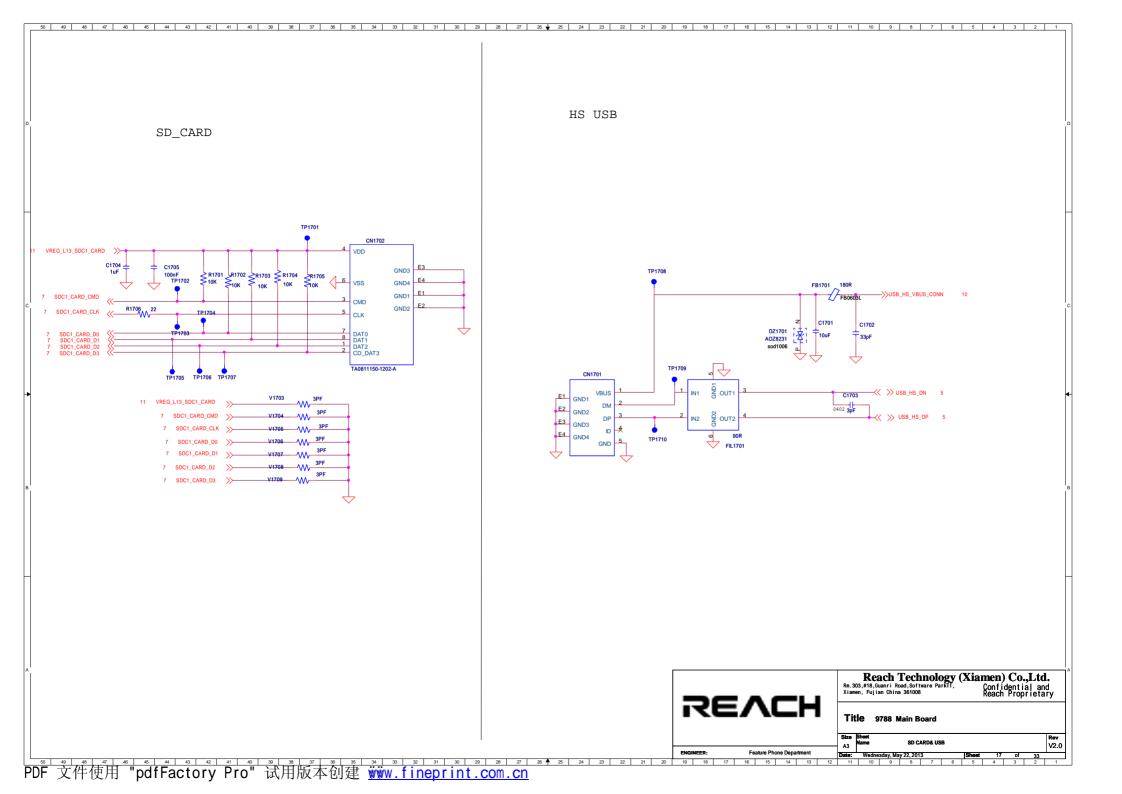


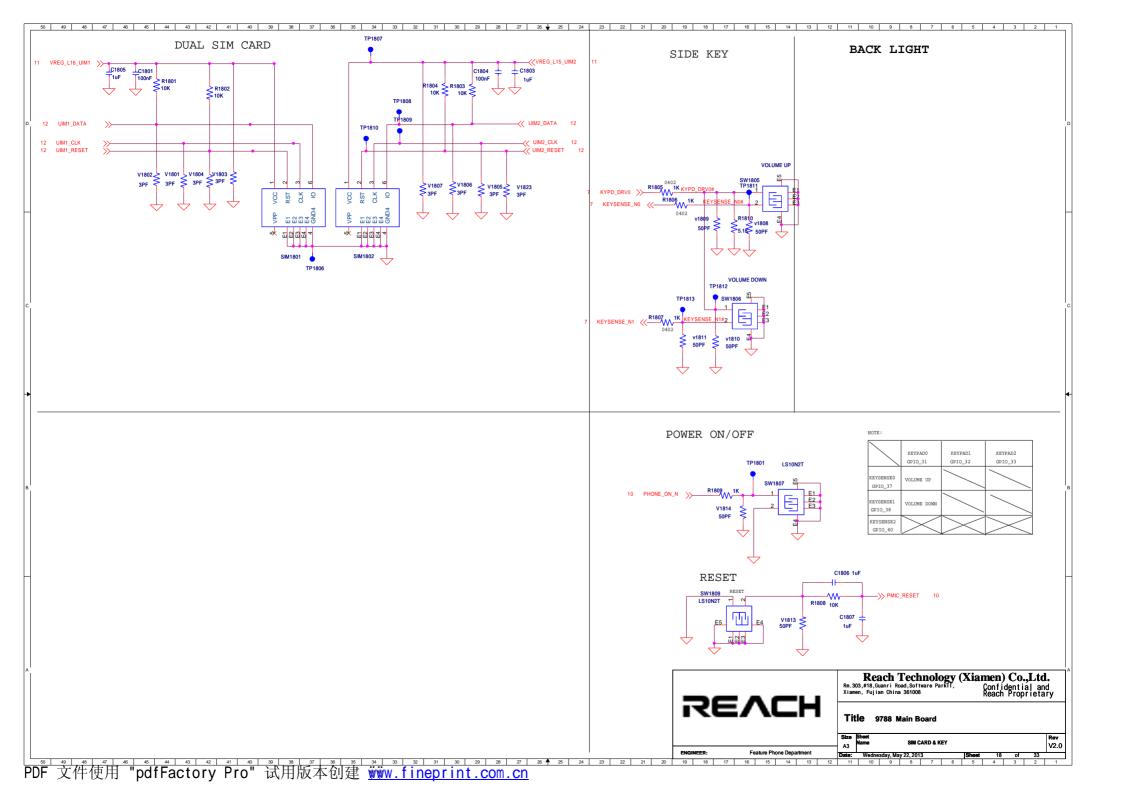


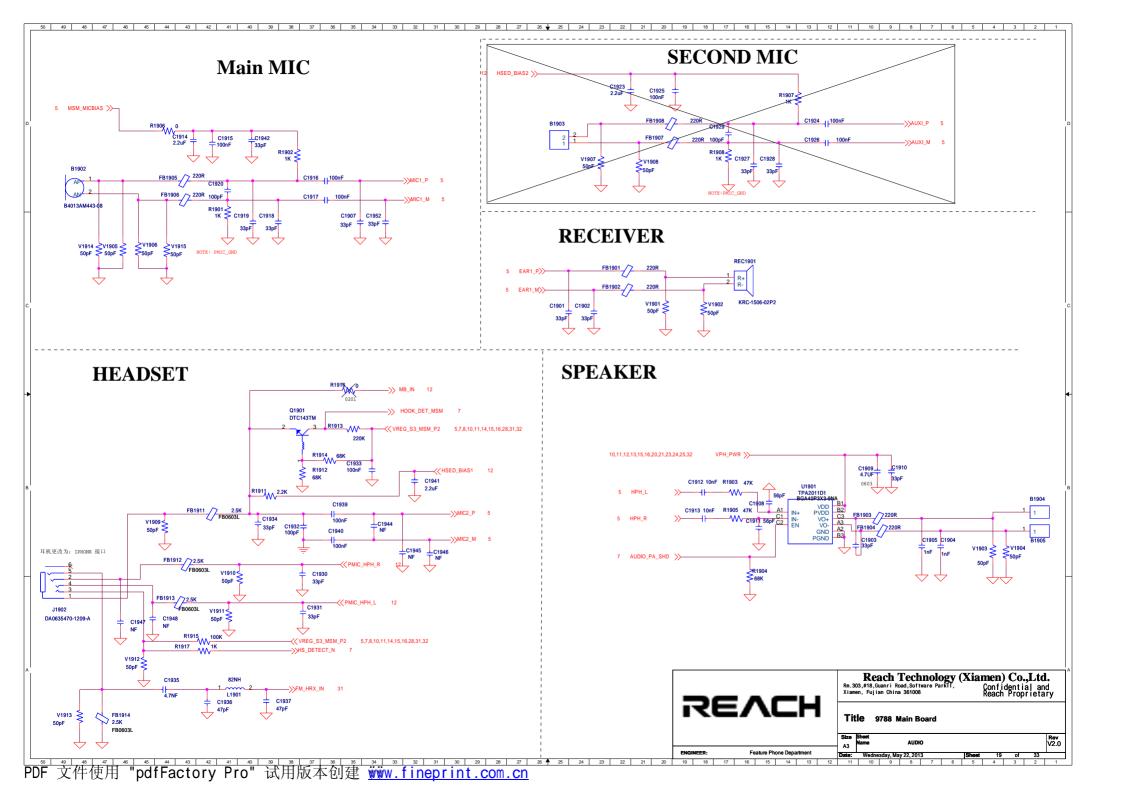


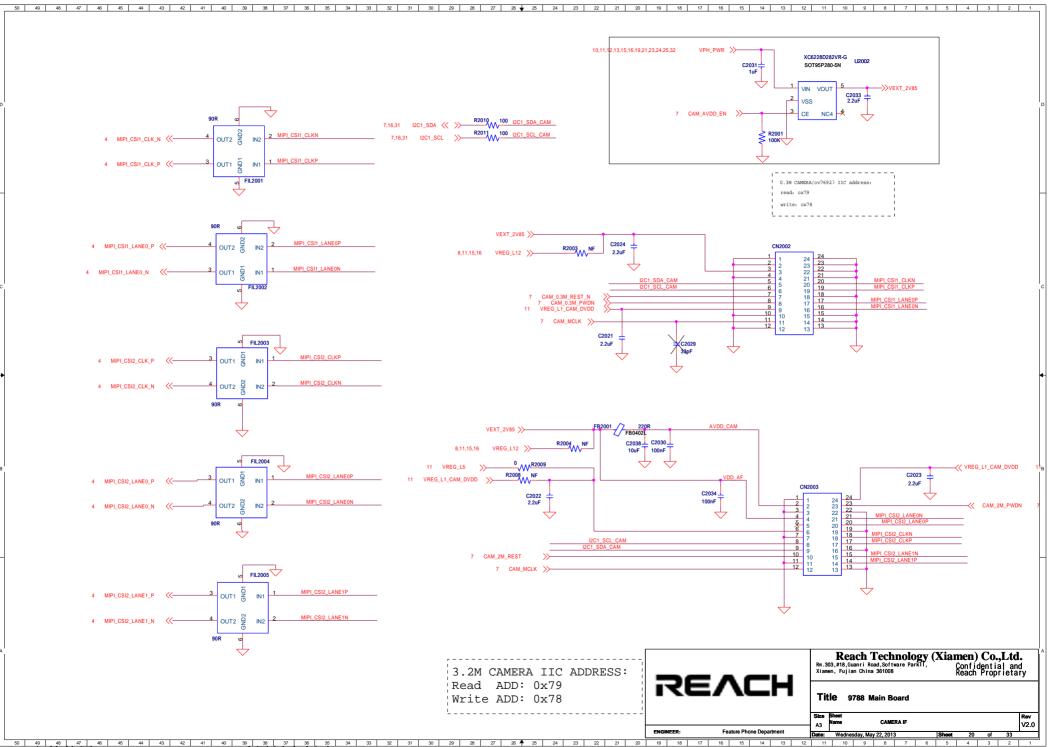


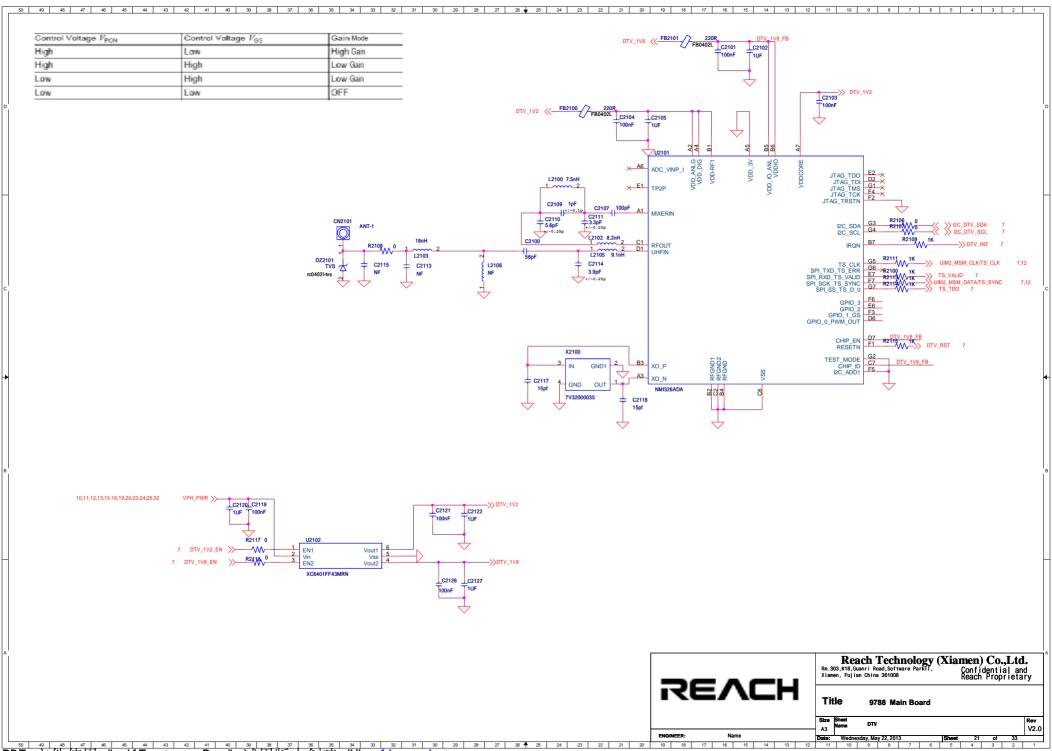


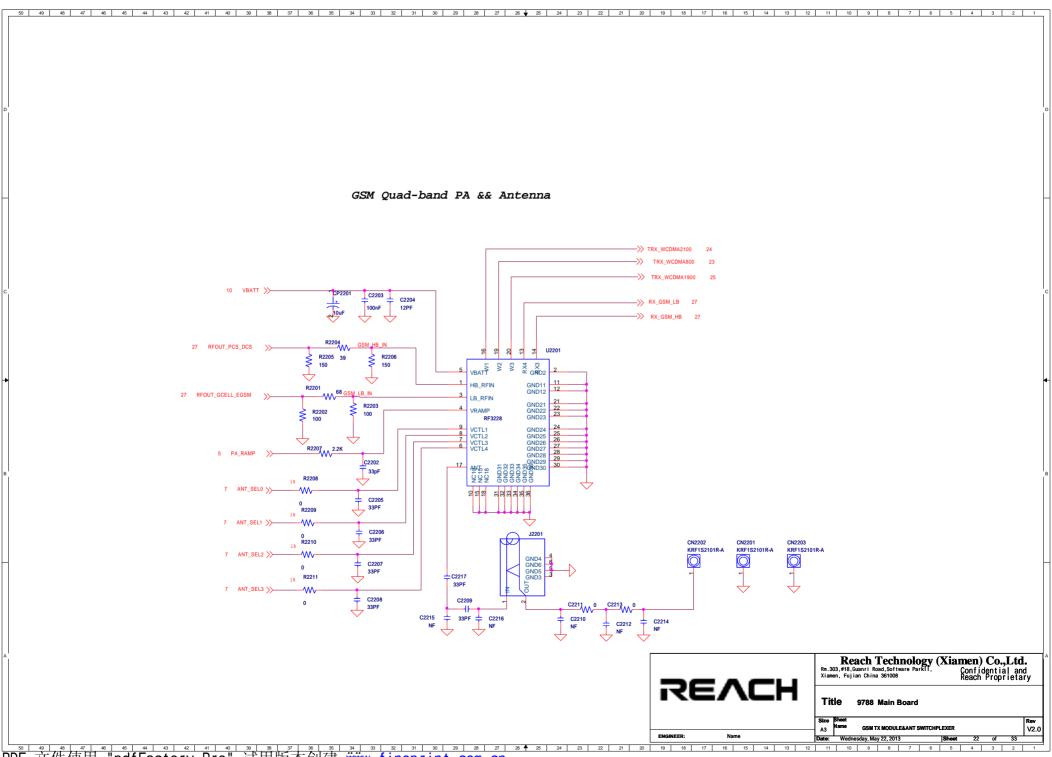




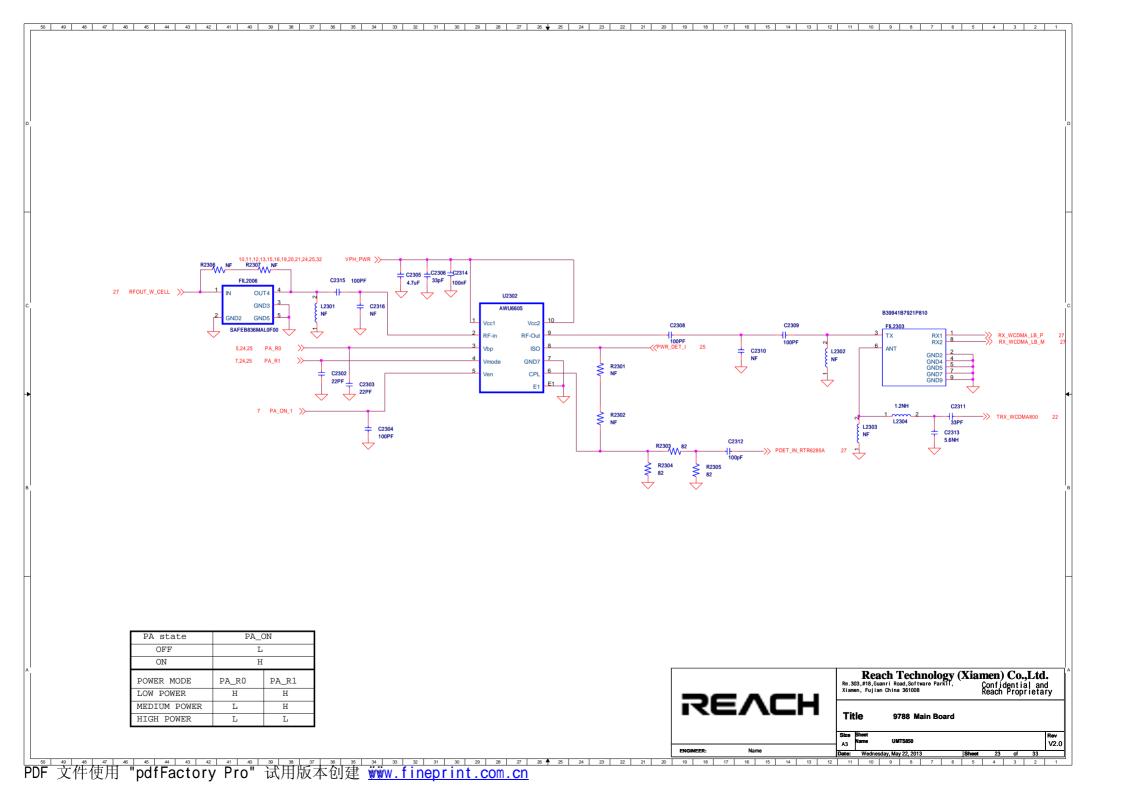


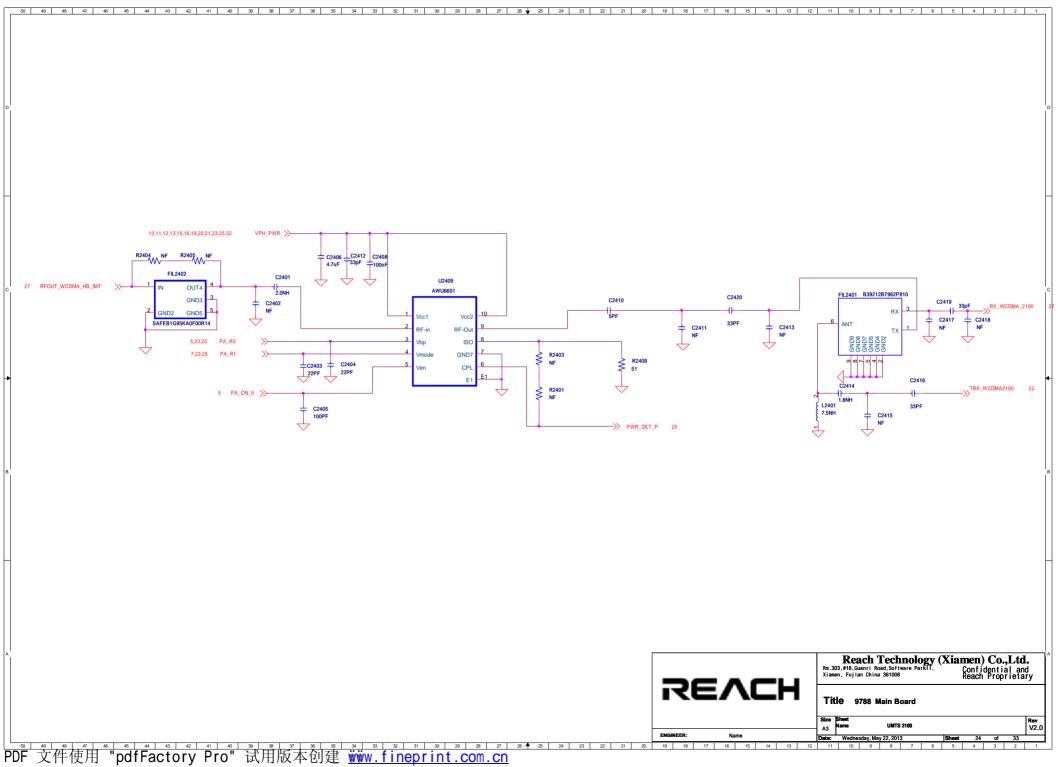


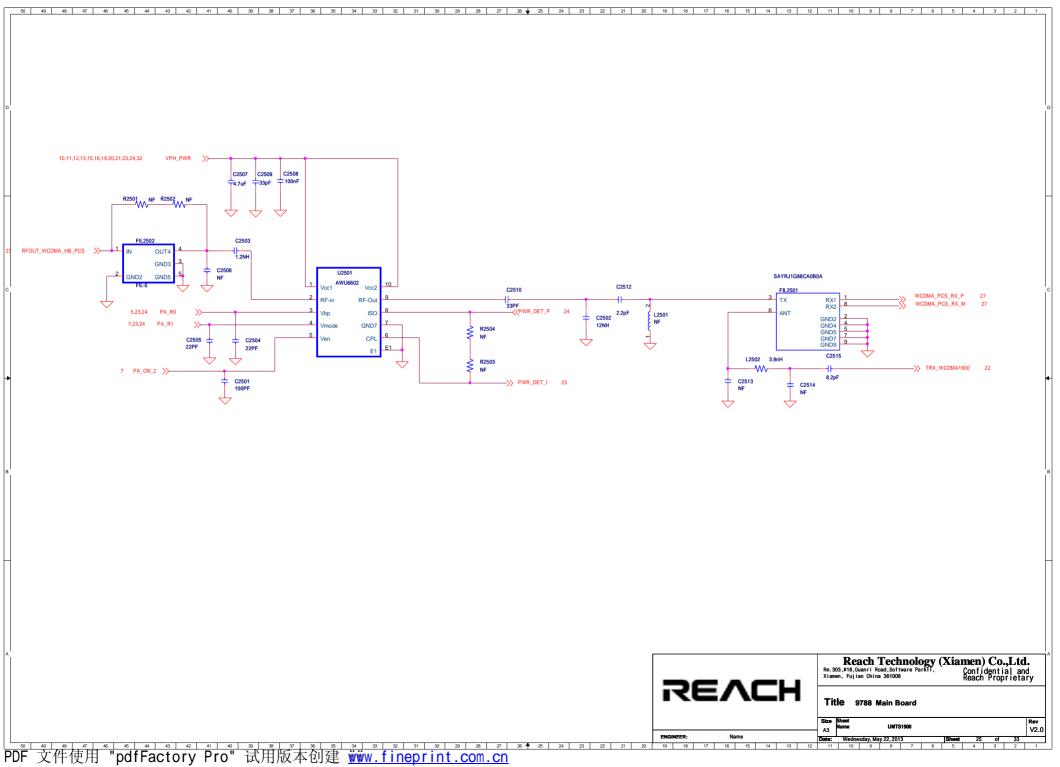


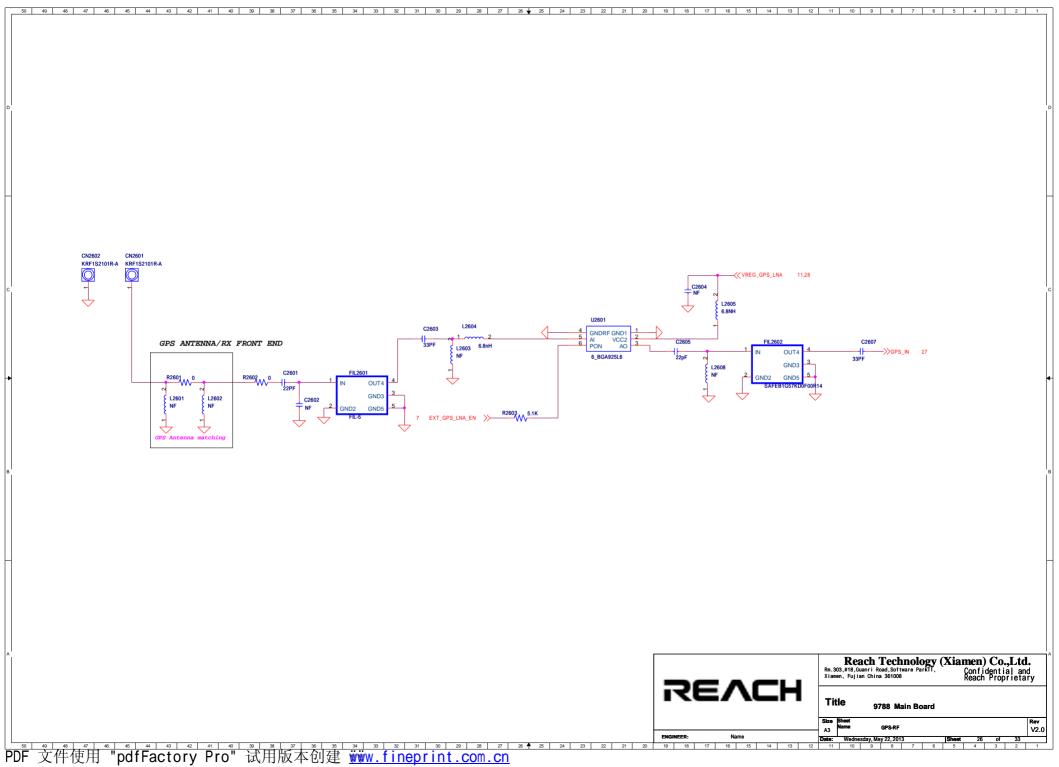


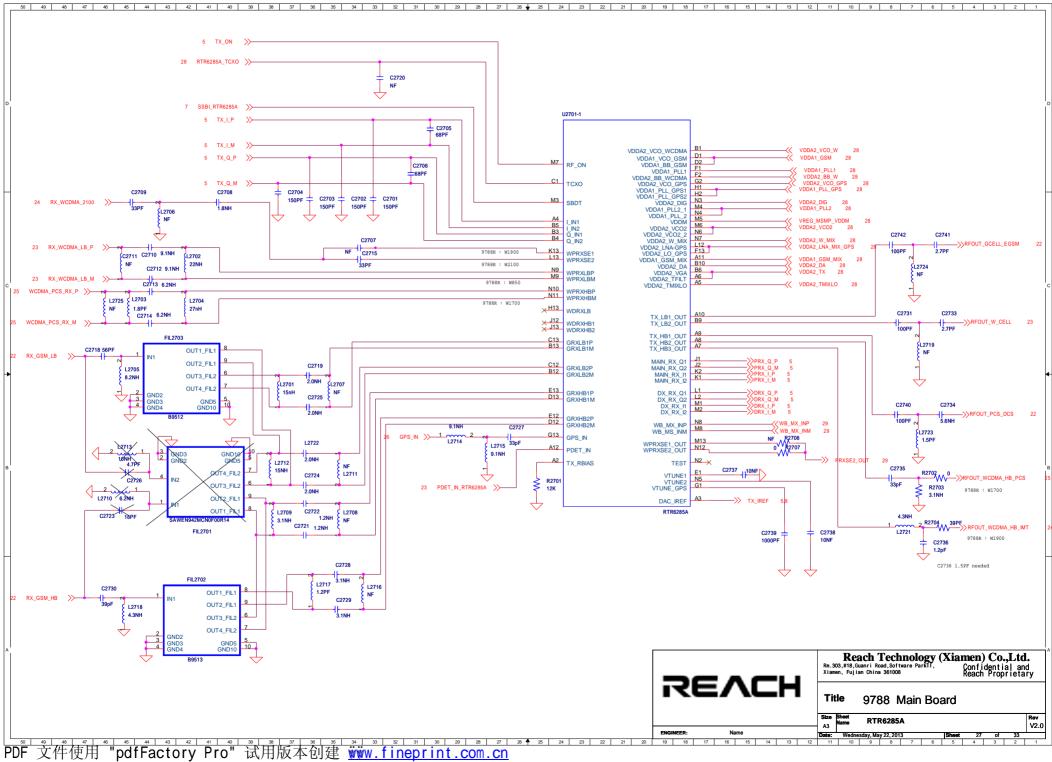
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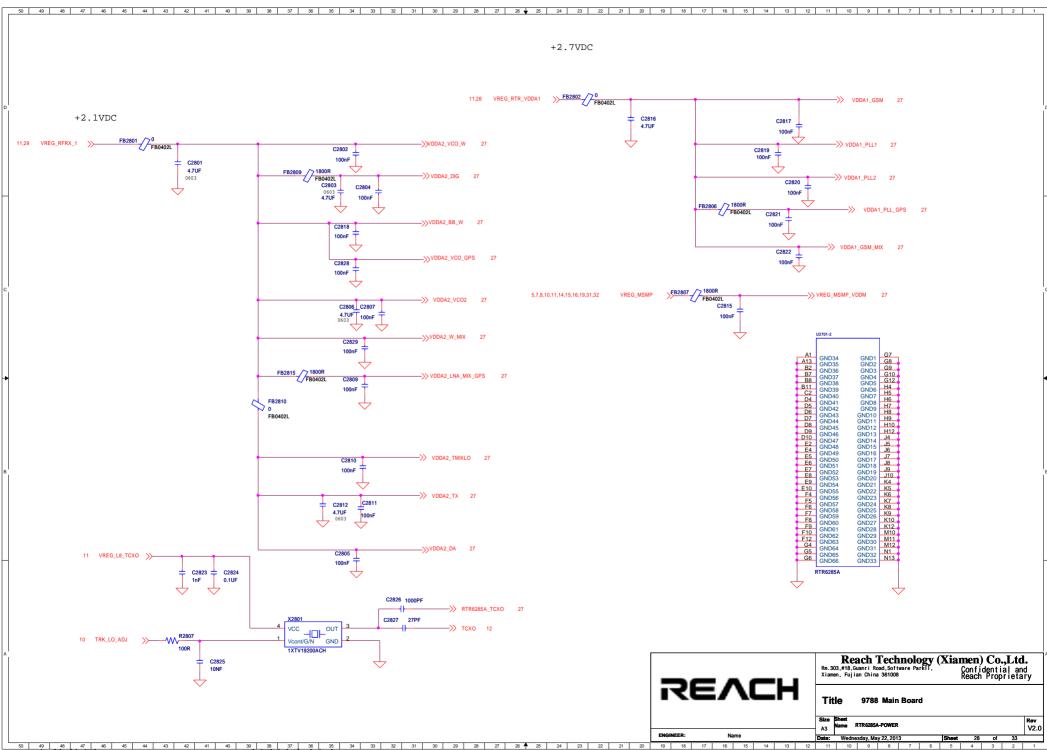


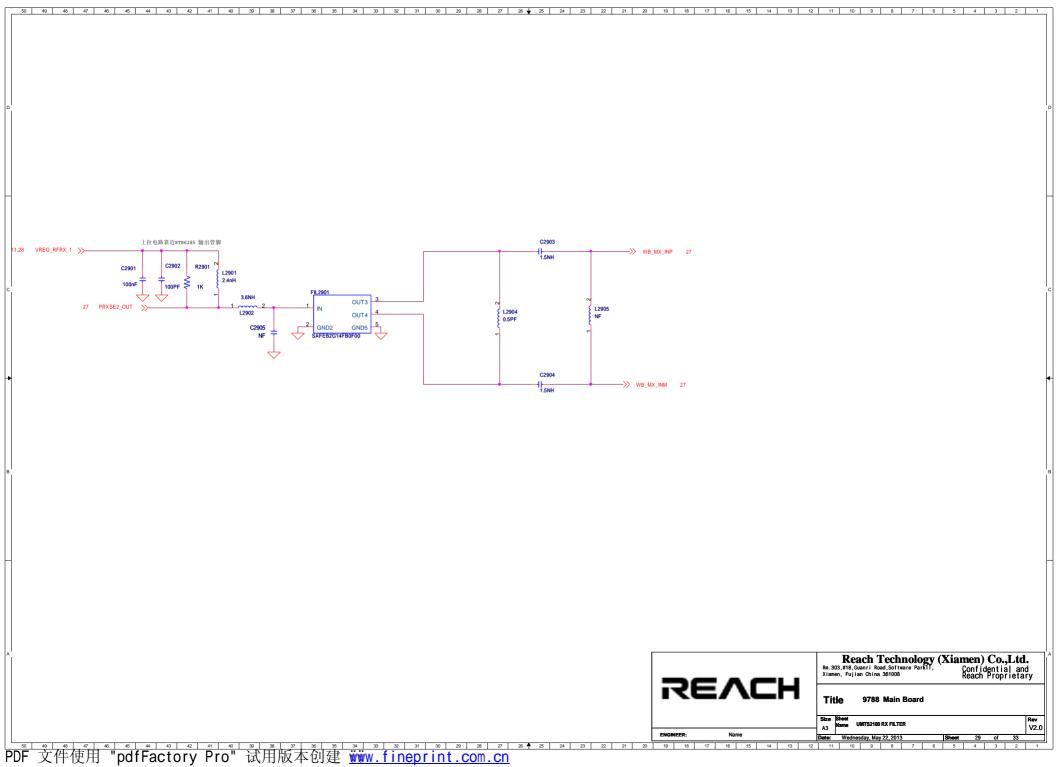




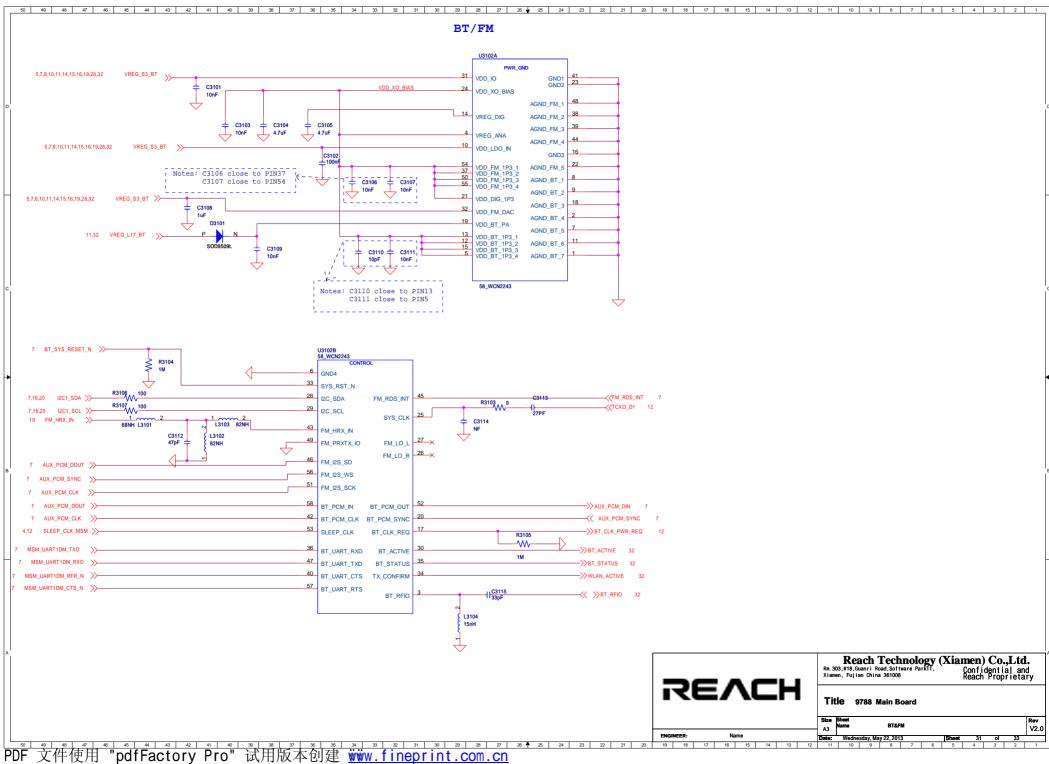


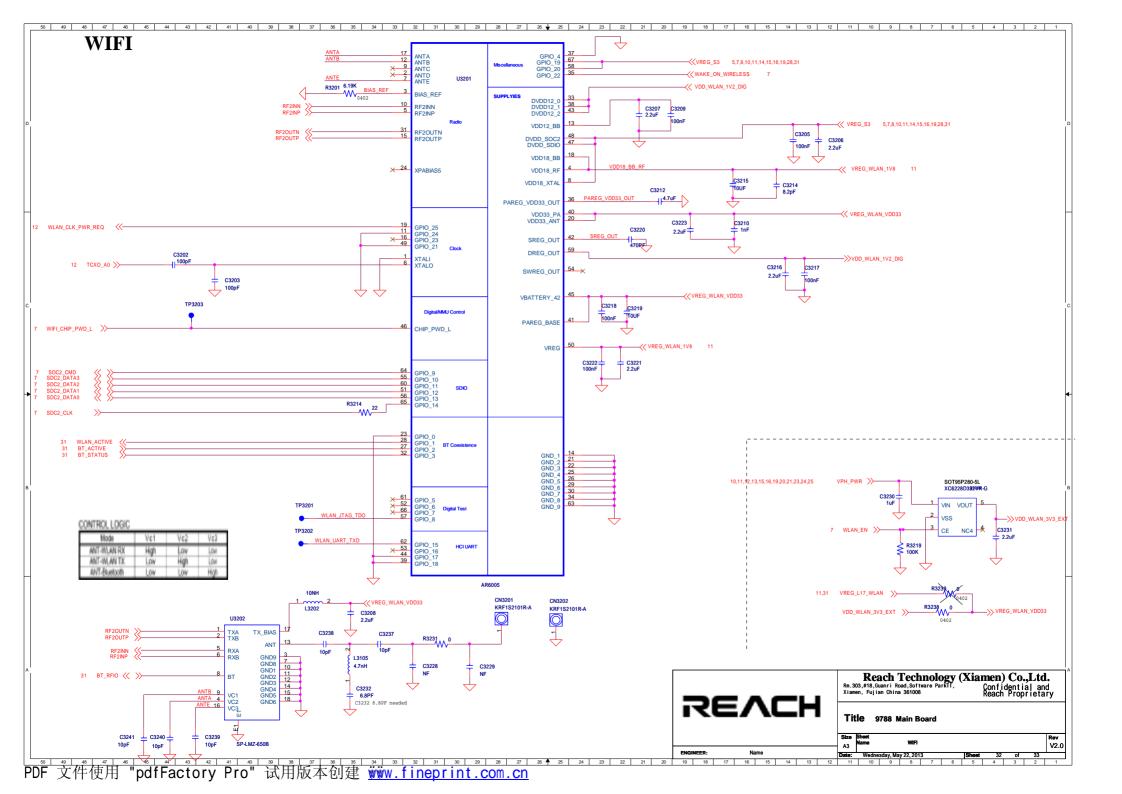






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Revi	sion Hi	istory						
	T	DDOIECT		DATE	VERSION	PROJECT STAGE	DESCRIPTION	
2012.02.23	V1.0		1.初始原理图	2012.04.19	V2.0		添加C1206为100NF; 添加R1809为0 欧0402;	1
2012.02.23	V1.0		添加R1807\R1805\R1806		V2.0		添加R1616, R1617为NF 添加R1619, R1618, R2010, R2011, R1503,	
2012.02.24	V1.0		R1004改为MRLC00812; R1005设为MRLC00811; FIL2303设为M0P02408; FIL2501设为M0P02215				R1502为100 ohm 添加U1602为MOPD2326;	
2012.02.27	V1.0		CP2201改为MRLC00474 (10UF)	-				
2012.02.29	V1.0		修改page01框图: COMPASS_INT改为GP1038; HOOK_DET_MSUK_为GP1040; AUD10_PA_SHD改为GP1088; 删除HOOK_DET_MMICMS, TP0701; C2605改为MRLC00542(22pF); SM1809改为MDP00315; B1905\B1904\Z)COM00645					
012.03.02	V1.0		纠正I2C1_SCL_GSENSOR与I2C1_SDA_GSENSOR反接	1				
012.03.05	V1.0		L2304 1.2NH LQPO3TNINZB00  C2313 5.6NH LQPO3TNSNH00  C2712 9.1NH LQPO3TNSNH100  C2712 9.1NH LQPO3TNSNH100  C2712 9.1NH LQPO3TNSNH200  C2719 2.0NH LQPO3TNSNH200  C2719 2.0NH LQPO3TNSND800  C2725 2.0NH LQPO3TNSND800  L2701 15NH LQPO3TNS1N5H00  L2707 NF  L2712 15NH LQPO3TNSND800  C2724 2.0NH LQPO3TNSND800  C2724 2.0NH LQPO3TNSND800  C2724 2.0NH LQPO3TNSND800  C2724 2.0NH LQPO3TNSND800  L2711 NF  C2730 39FF MRLC00544  L2718 4.3NH LQPO3TNSNB300  L2717 1.2FF MRLC00536					
			(2729 3.1NH LQP03TN3N1B00 L2716 NF L2716 NF L2716 NF L2709 3.1NH LQP03TN1N1B00 C2721 1.2NH LQP03TN1N2B00 C2721 1.2NH LQP03TN1N2B00 L2708 NF F52801 BLM15HG601SN1 F52802 BLM15HG601SN1 F52810 0 MELC00277 C2720 NF R1004改为6.98K					
012.03.15	V2.0		REC1901改为1506封装 R1912改为68k	-i 				
012.04.05	V2.0		1.2721 4.3NH R2704 39PF C2401 2NH F28201 FP2802 0様 C2410 5.0PF L2401 7.5NH L2401 7.5NH L2401 7.5NH L2904 0.5PF C2903 1.5NH C2904 1.5NH C2904 1.5NH C2904 1.8NH L2904 0.5PF F1L2501找分 MOP02483 L2706 NF F1L2501找入 MOP02483 L2502 3.9NH C2515 8.2PF C2502 1.2NH L2501 1.2NH C2512 2.2PF C2502 1.2NH L2501 NF C2513 1.2NH C2513 1.2NH C2513 3.7PF					Reach Technology (Xiamen) Co.,L
			C2/35 35FF L2/703 3.1NH L2/704 2.7NH C2/713 6.2NH C2/714 6.2NH L2/703 1.5PF L2/703 1.5PF C2/714 5.6NH					Title 9788 Main Board
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