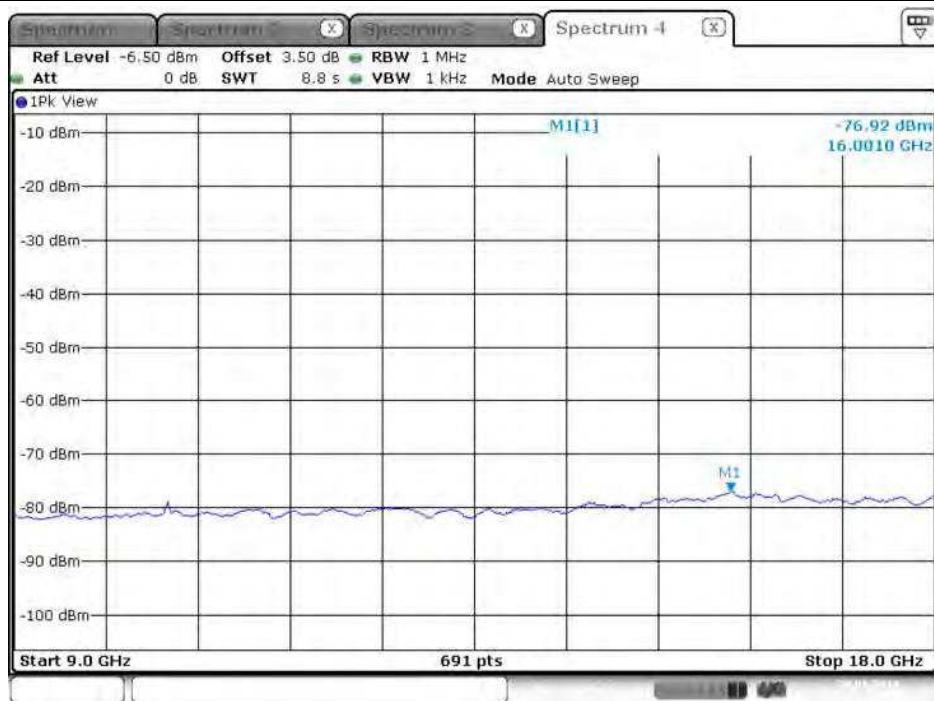
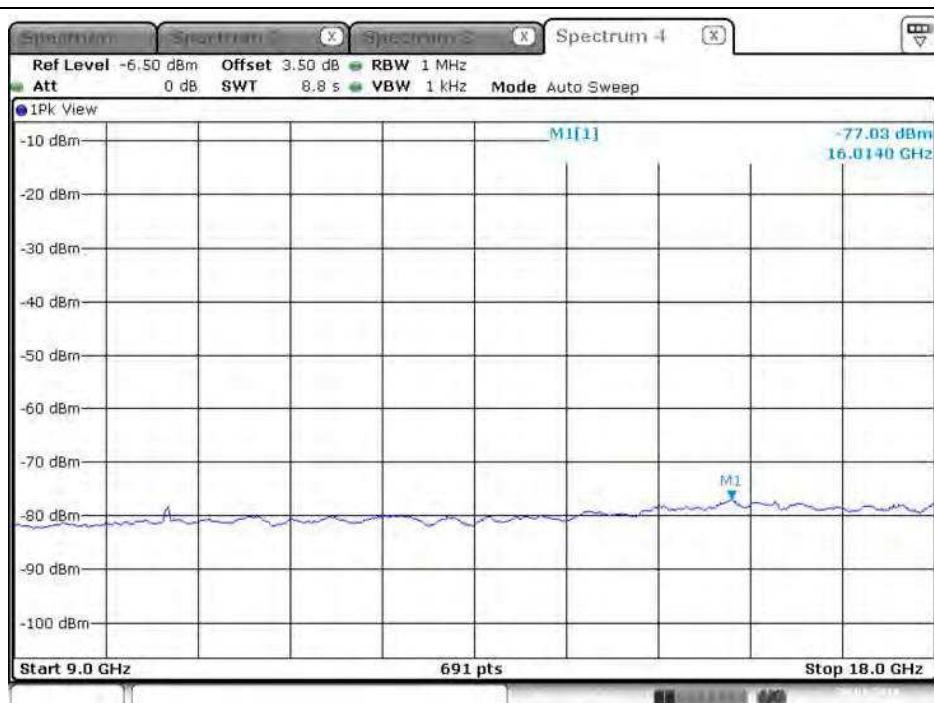
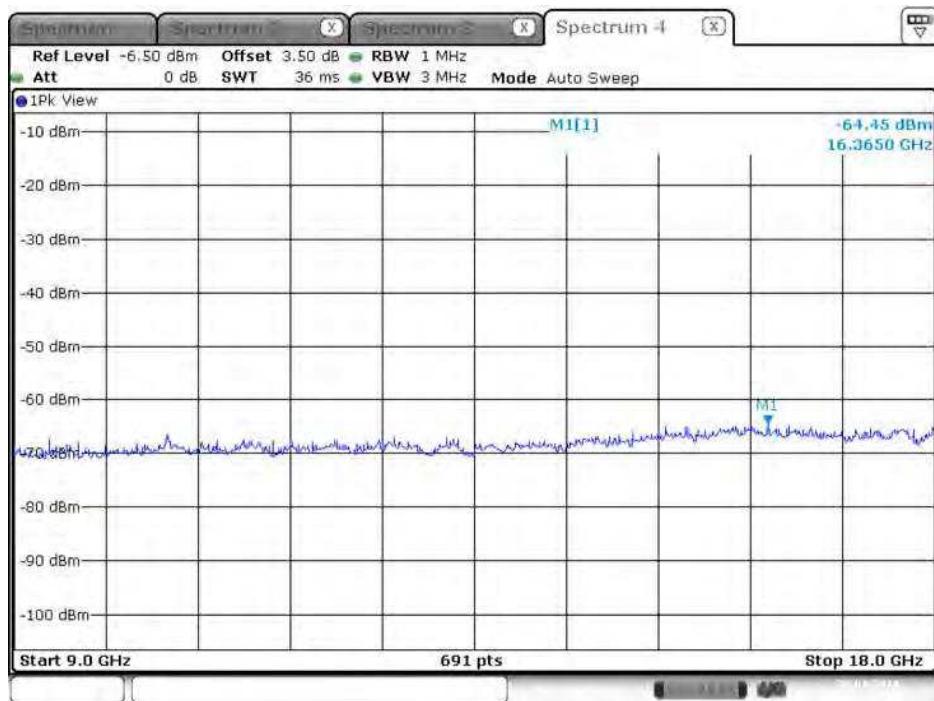
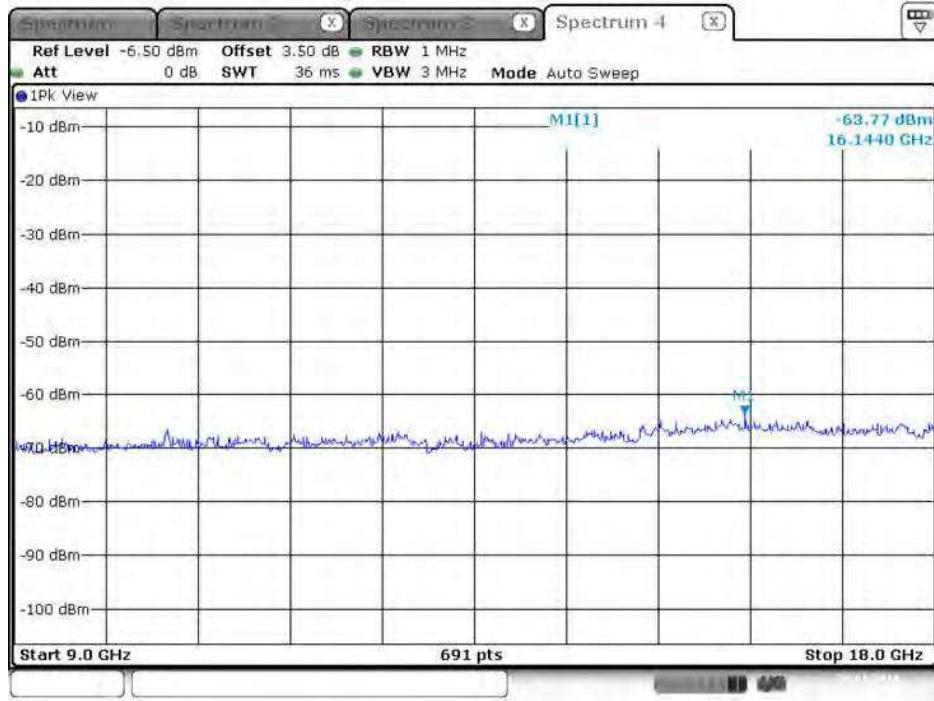


**Plot on Configuration QPSK, 40M / 5250 MHz (Straddle Channel) / Average / Port 1 / 9GHz~18GHz**

**Plot on Configuration QPSK, 40M / 5250 MHz (Straddle Channel) / Average / Port 2 / 9GHz~18GHz**


**Plot on Configuration QPSK, 40M / 5250 MHz (Straddle Channel) / Peak / Port 1 / 9GHz~18GHz**


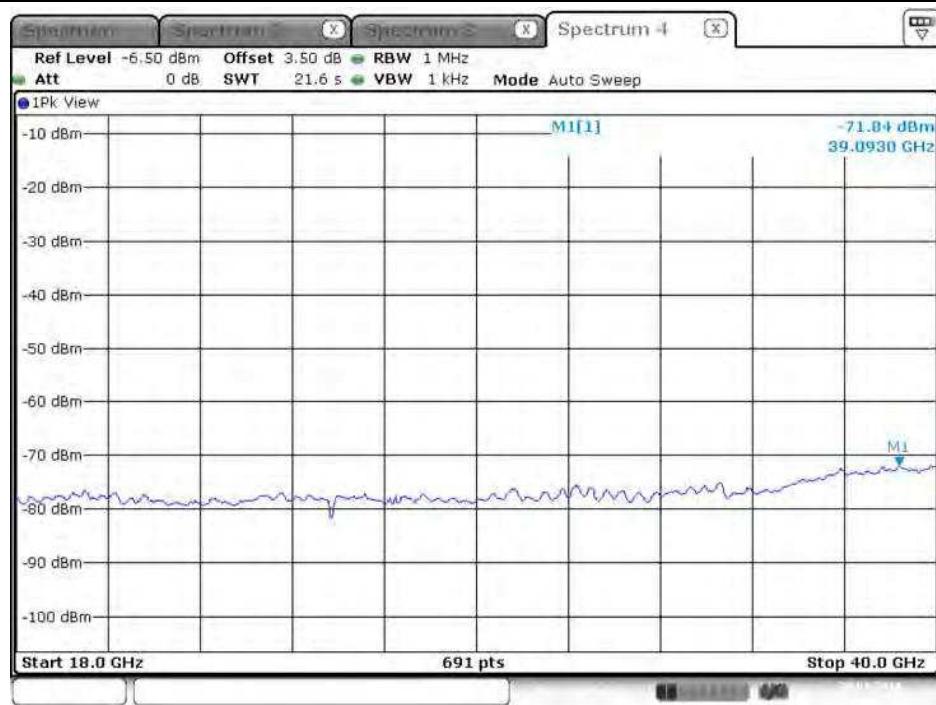
Date: 24.JAN.2018 21:34:32

**Plot on Configuration QPSK, 40M / 5250 MHz (Straddle Channel) / Peak / Port 2 / 9GHz~18GHz**


Date: 24.JAN.2018 21:42:16

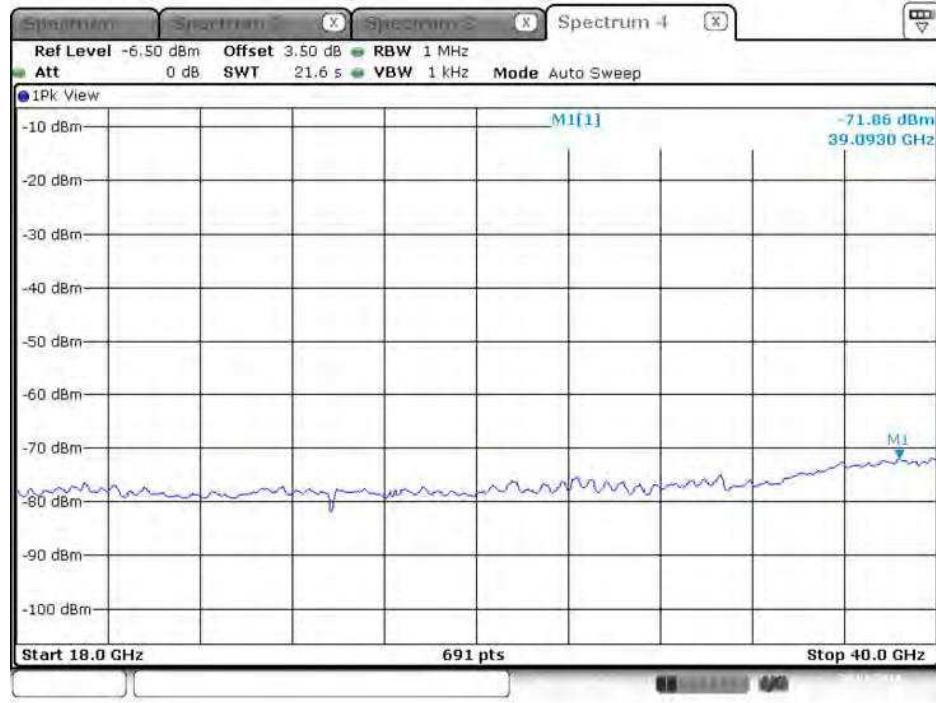


## Plot on Configuration QPSK, 10M / 5255 MHz / Average / Port 1 / 18GHz~40GHz

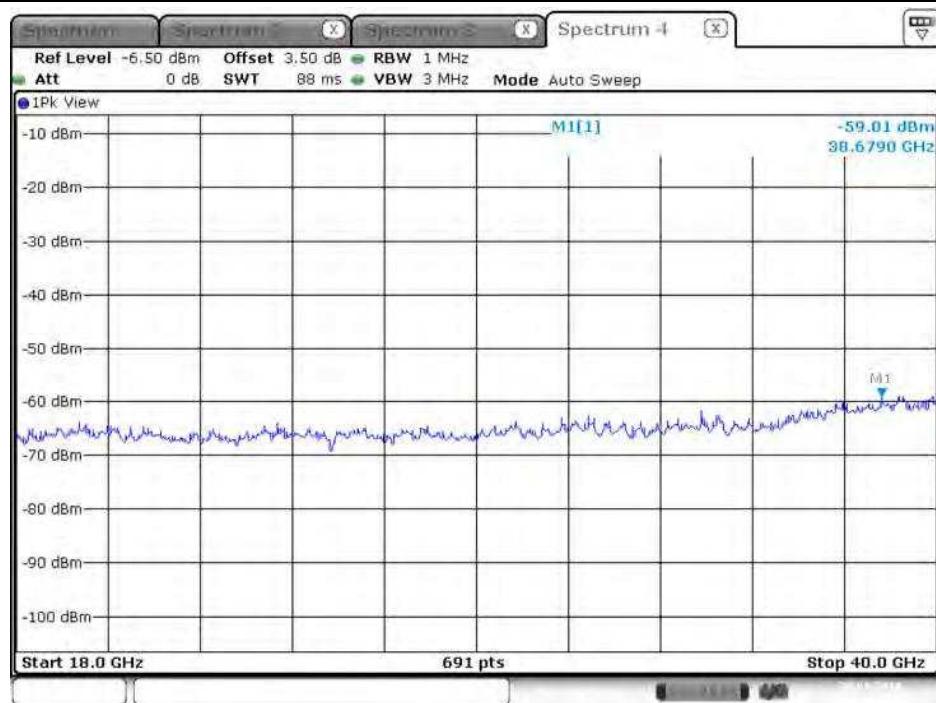


Date: 24.JAN.2018 16:59:52

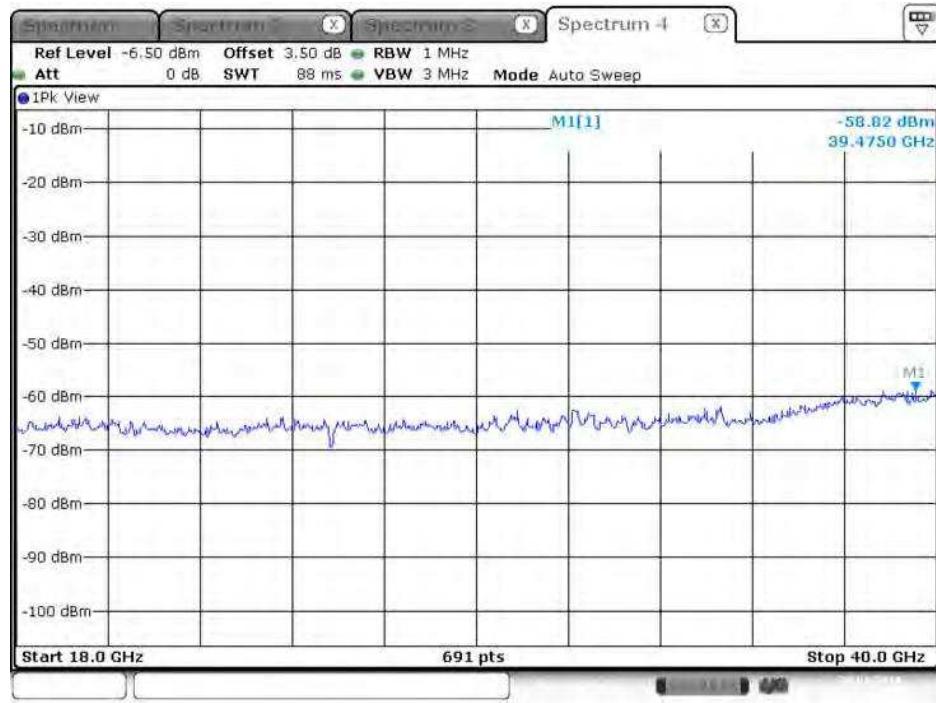
## Plot on Configuration QPSK, 10M / 5255 MHz / Average / Port 2 / 18GHz~40GHz



Date: 24.JAN.2018 17:16:38

**Plot on Configuration QPSK, 10M / 5255 MHz / Peak / Port 1 / 18GHz~40GHz**


Date: 24.JAN.2018 17:01:13

**Plot on Configuration QPSK, 10M / 5255 MHz / Peak / Port 2 / 18GHz~40GHz**


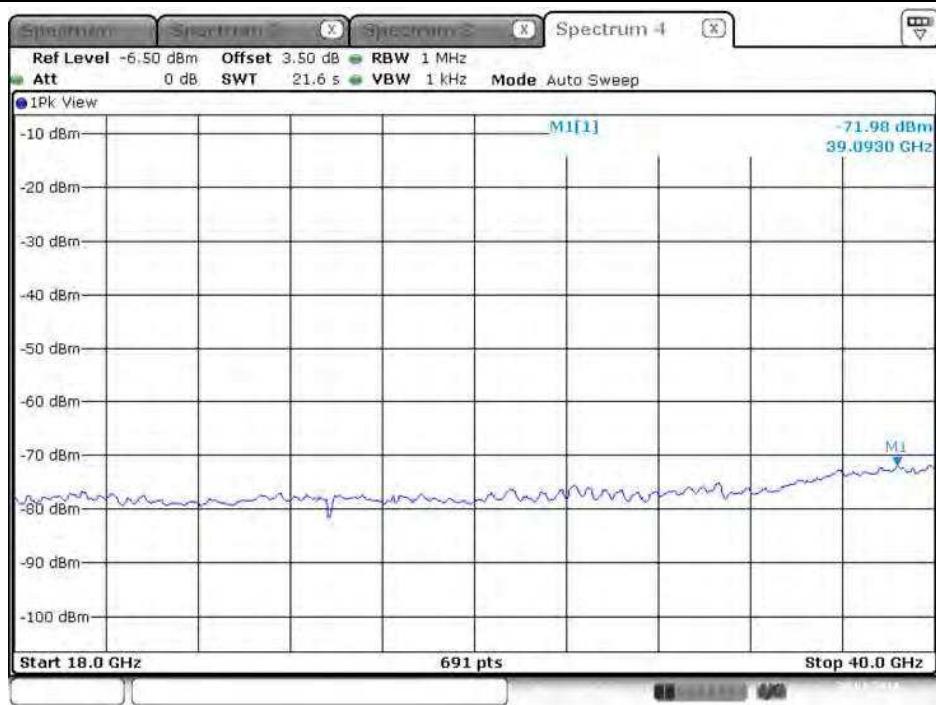
Date: 24.JAN.2018 17:18:35



## CSE TX above 1GHz Result

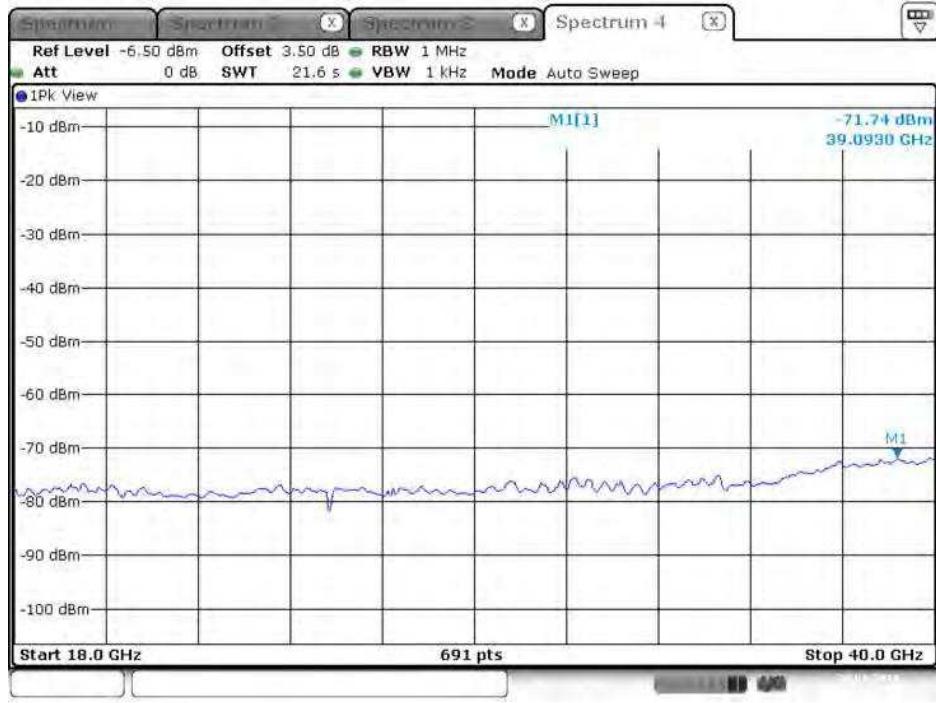
Appendix D.1

### Plot on Configuration QPSK, 10M / 5295 MHz / Average / Port 1 / 18GHz~40GHz

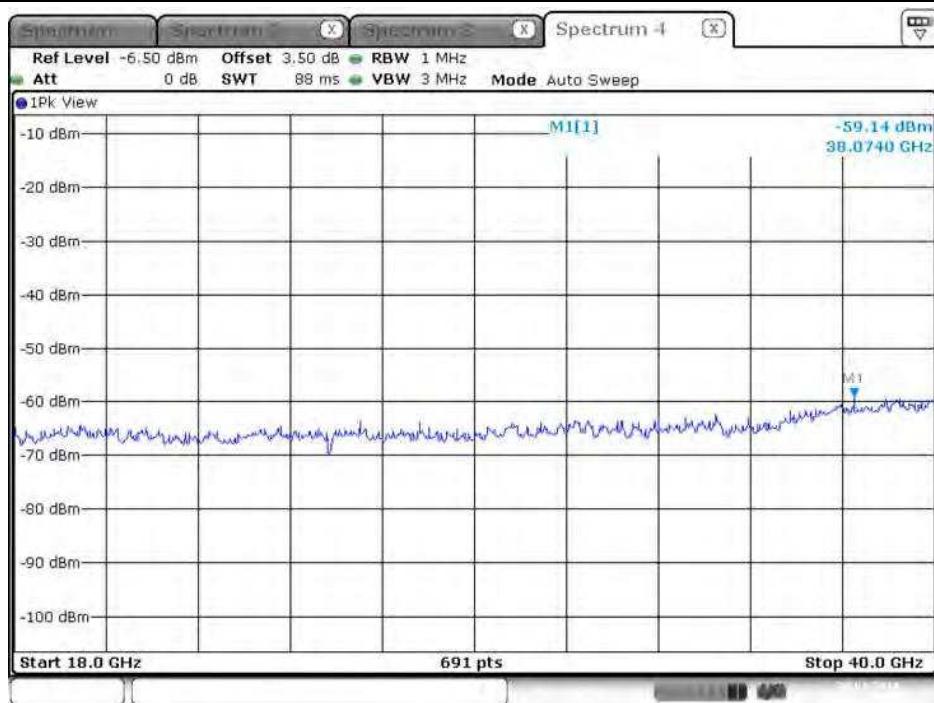


Date: 24.JAN.2018 17:51:31

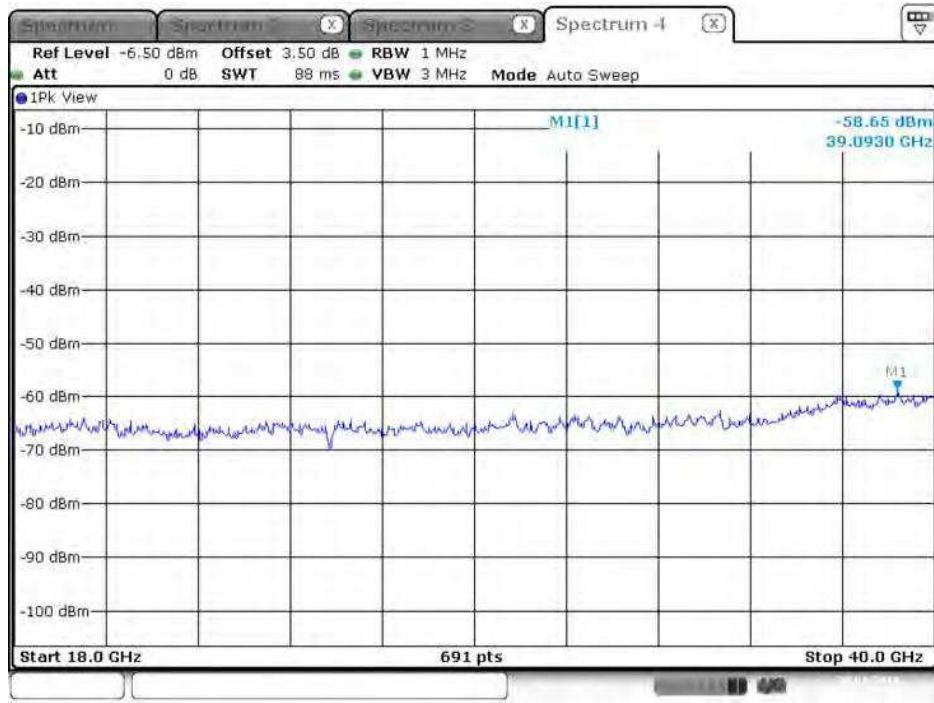
### Plot on Configuration QPSK, 10M / 5295 MHz / Average / Port 2 / 18GHz~40GHz



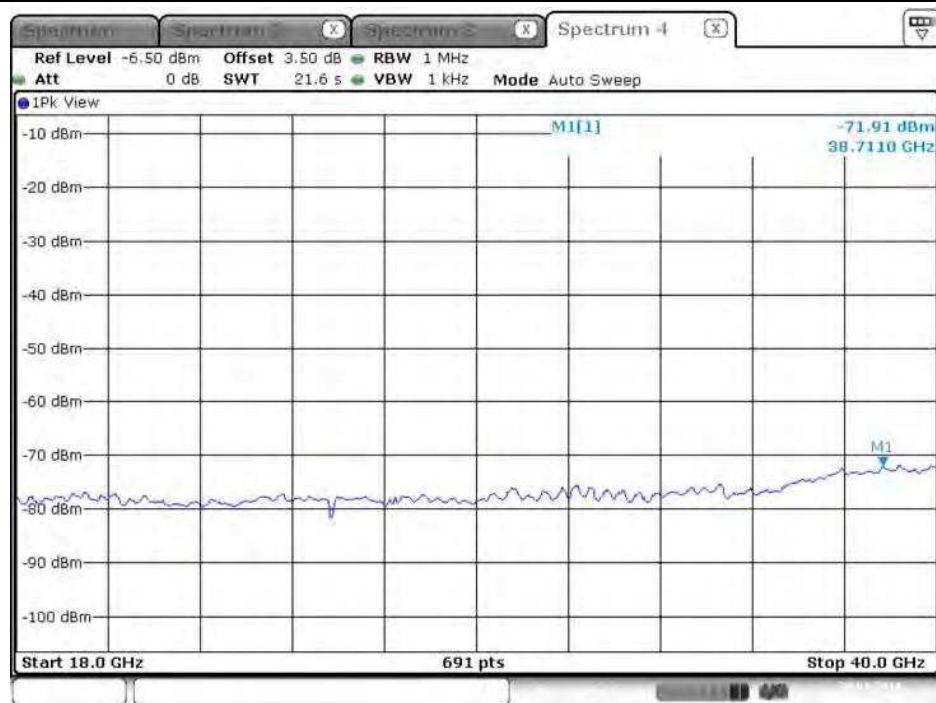
Date: 24.JAN.2018 17:56:40

**Plot on Configuration QPSK, 10M / 5295 MHz / Peak / Port 1 / 18GHz~40GHz**


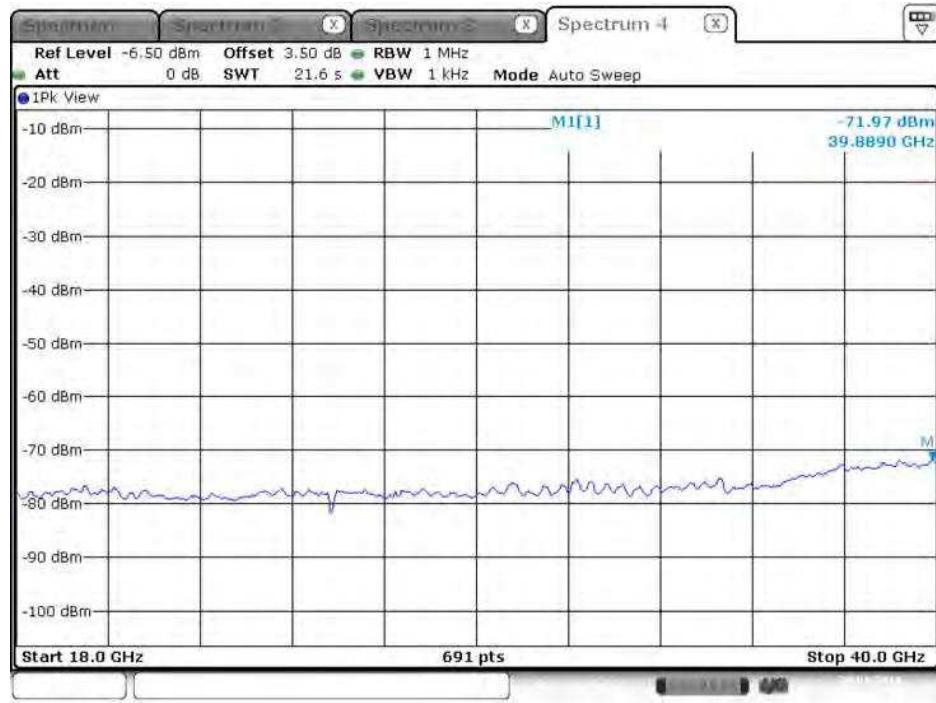
Date: 24.JAN.2018 17:52:26

**Plot on Configuration QPSK, 10M / 5295 MHz / Peak / Port 2 / 18GHz~40GHz**


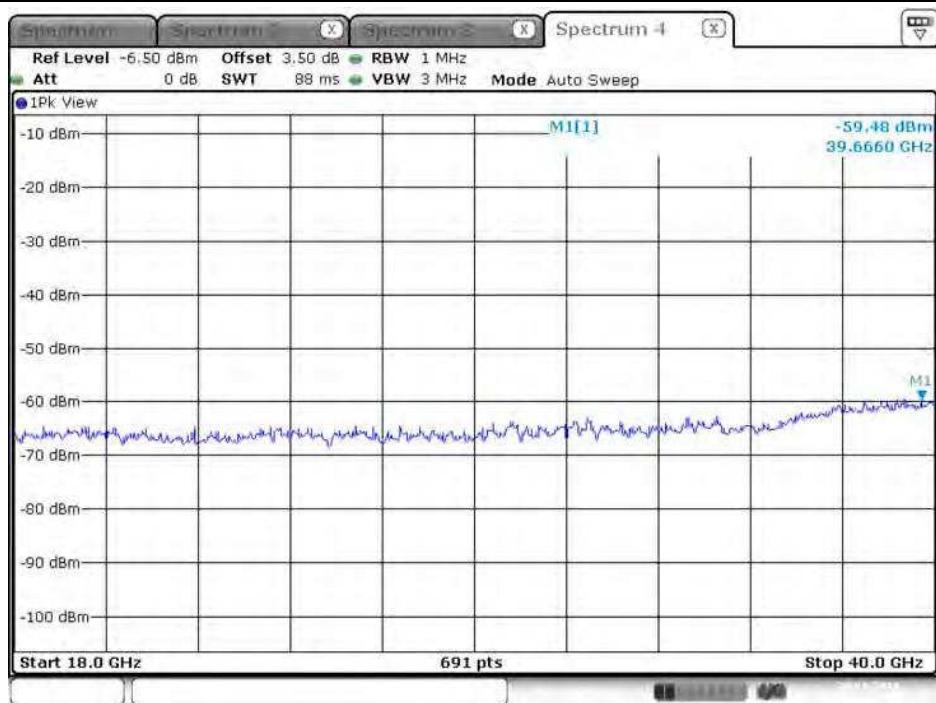
Date: 24.JAN.2018 17:57:27

**Plot on Configuration QPSK, 10M / 5340 MHz / Average / Port 1 / 18GHz~40GHz**


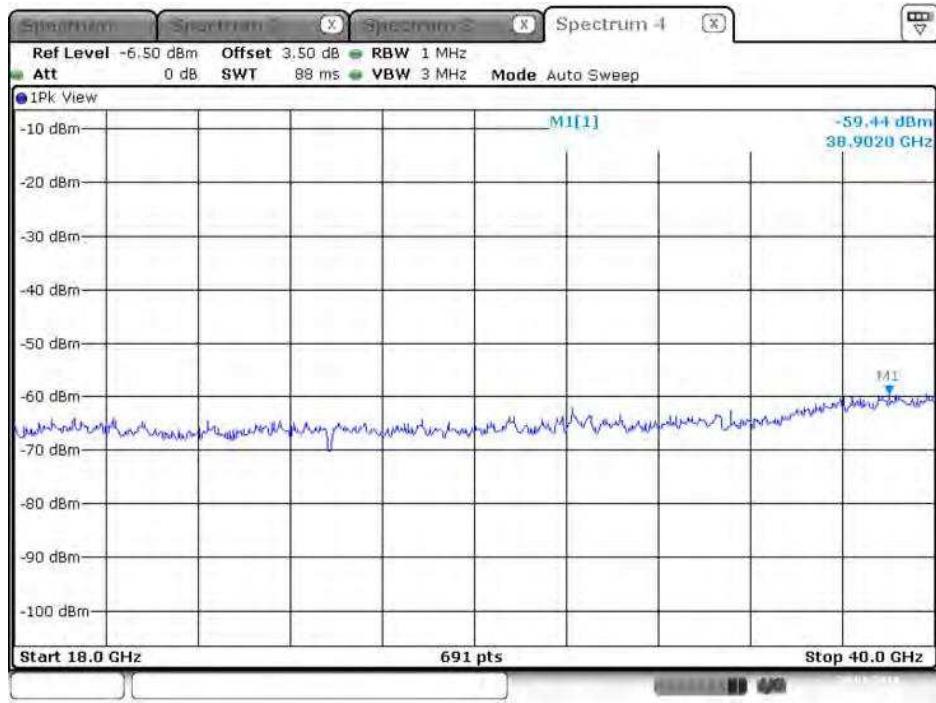
Date: 24.JAN.2018 19:05:48

**Plot on Configuration QPSK, 10M / 5340 MHz / Average / Port 2 / 18GHz~40GHz**


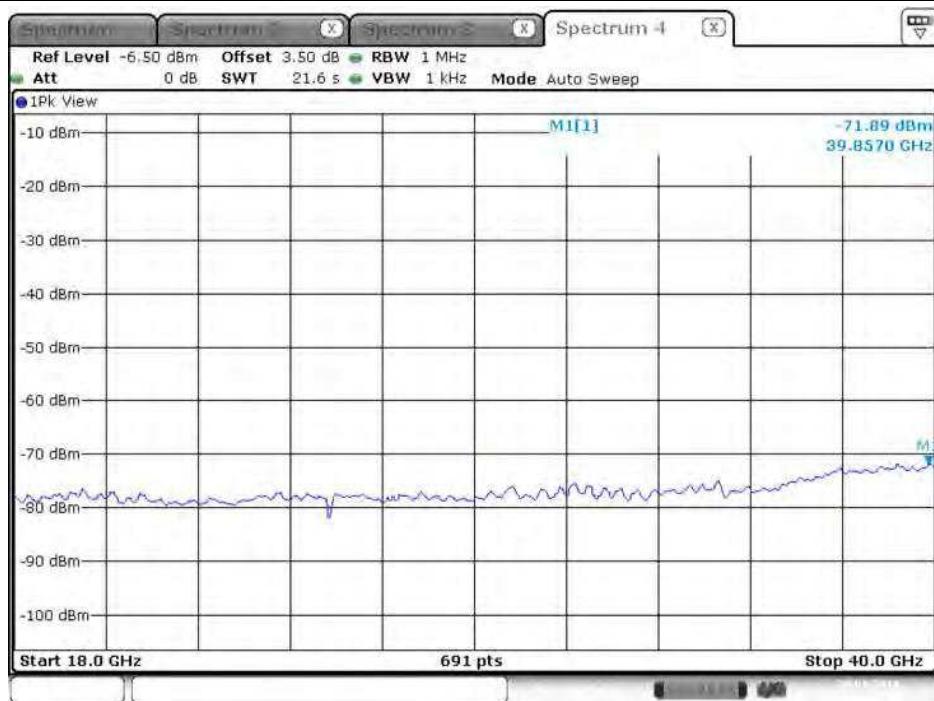
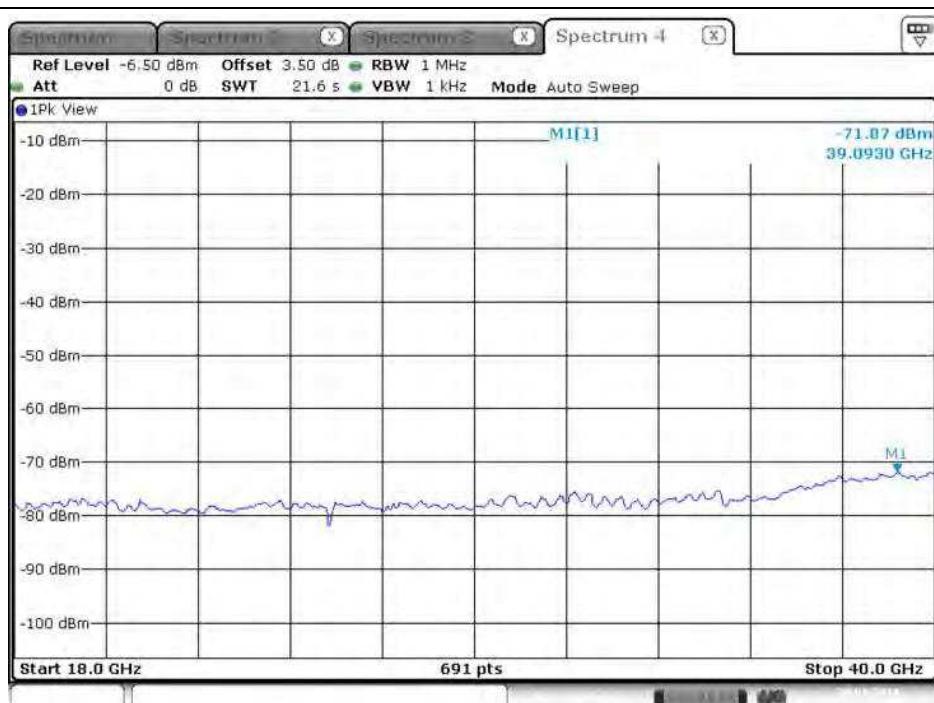
Date: 24.JAN.2018 19:08:42

**Plot on Configuration QPSK, 10M / 5340 MHz / Peak / Port 1 / 18GHz~40GHz**


Date: 24.JAN.2018 19:06:40

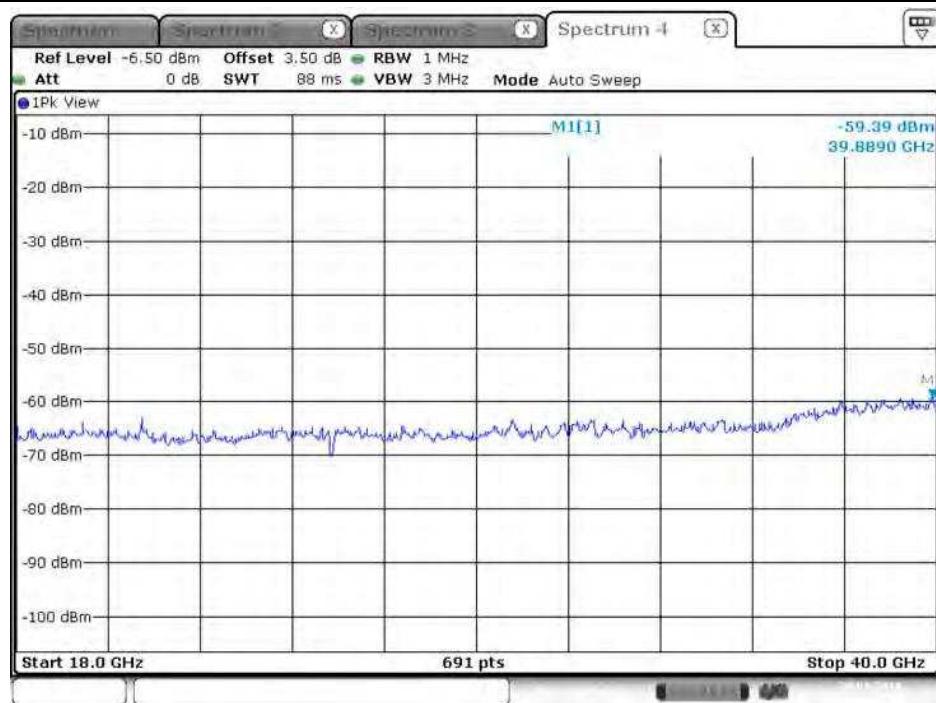
**Plot on Configuration QPSK, 10M / 5340 MHz / Peak / Port 2 / 18GHz~40GHz**


Date: 24.JAN.2018 19:09:17

**Plot on Configuration QPSK, 10M / 5250 MHz (Straddle Channel) / Average / Port 1 / 18GHz~40GHz**

**Plot on Configuration QPSK, 10M / 5250 MHz (Straddle Channel) / Average / Port 2 / 18GHz~40GHz**


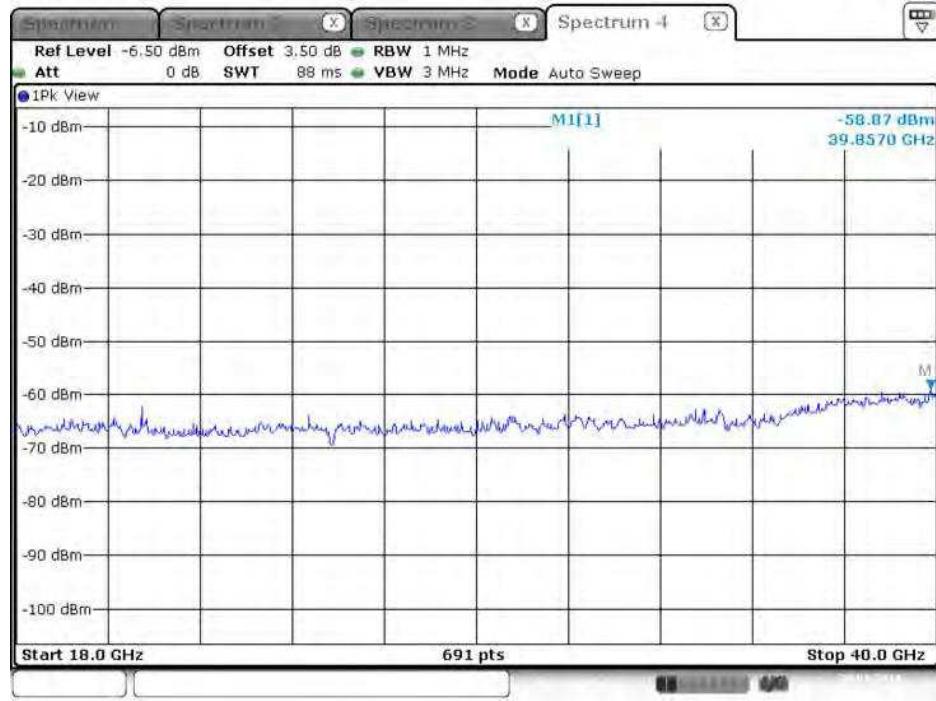


## Plot on Configuration QPSK, 10M / 5250 MHz (Straddle Channel) / Peak / Port 1 / 18GHz~40GHz



Date: 24.JAN.2018 19:33:53

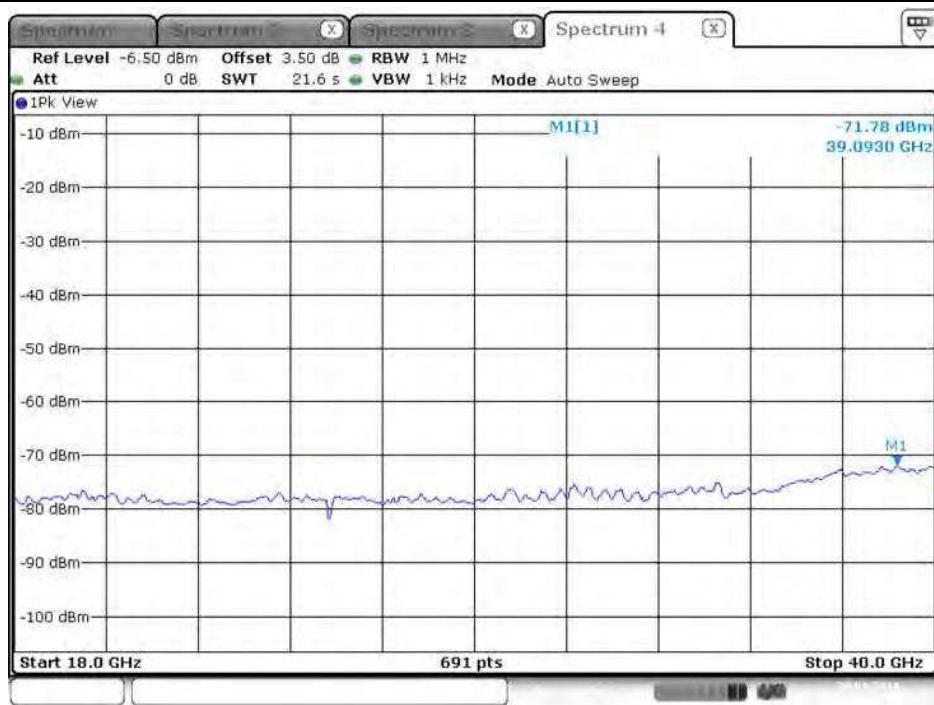
## Plot on Configuration QPSK, 10M / 5250 MHz (Straddle Channel) / Peak / Port 2 / 18GHz~40GHz



Date: 24.JAN.2018 19:36:52

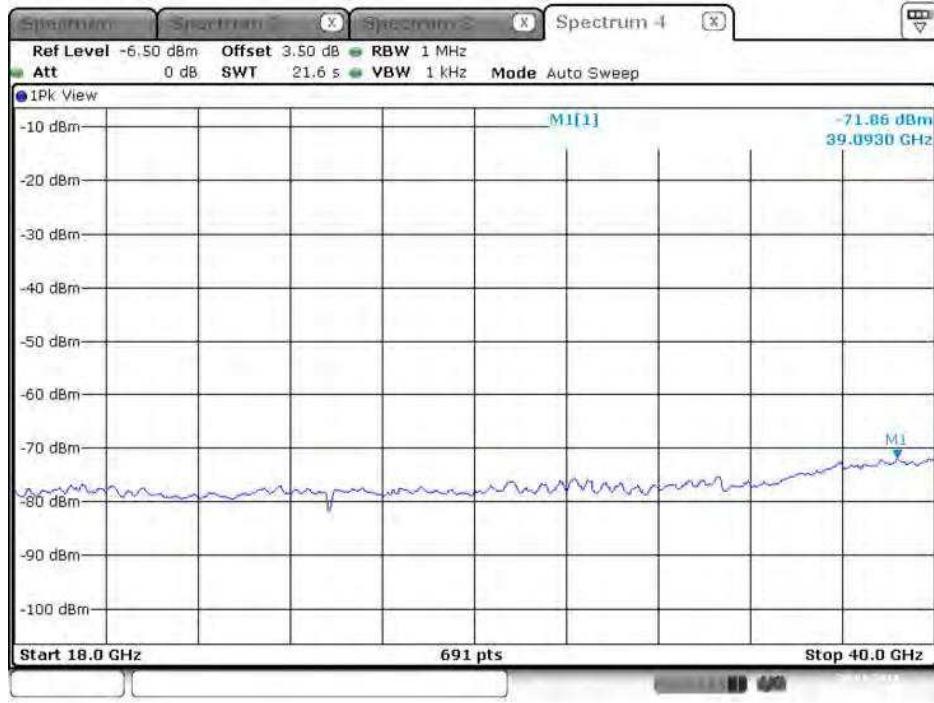


## Plot on Configuration QPSK, 40M / 5270 MHz / Average / Port 1 / 18GHz~40GHz



Date: 24.JAN.2018 20:01:04

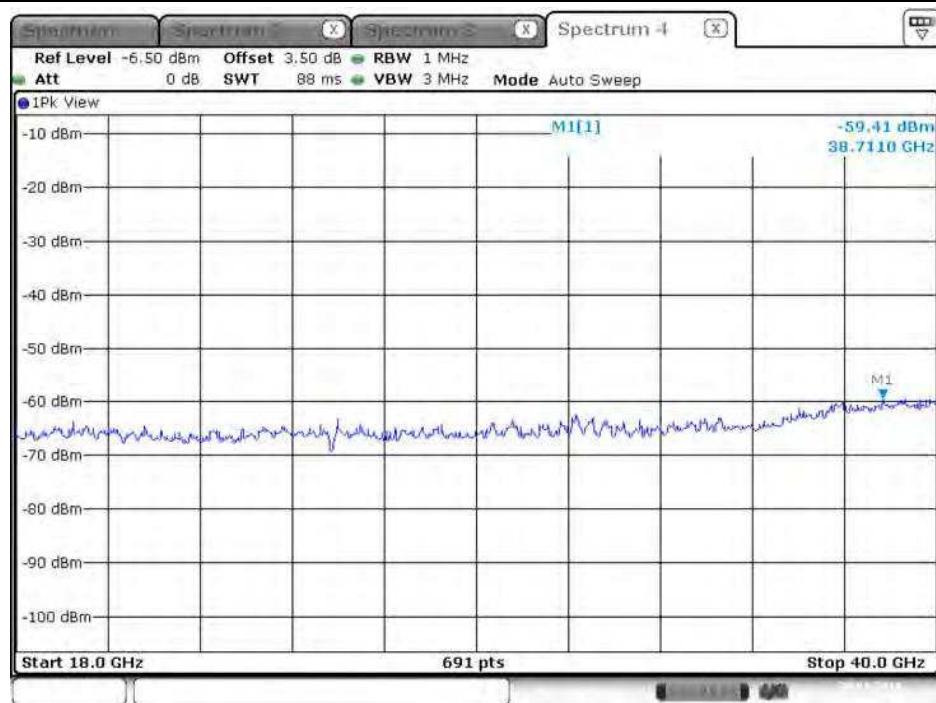
## Plot on Configuration QPSK, 40M / 5270 MHz / Average / Port 2 / 18GHz~40GHz



Date: 24.JAN.2018 20:04:05

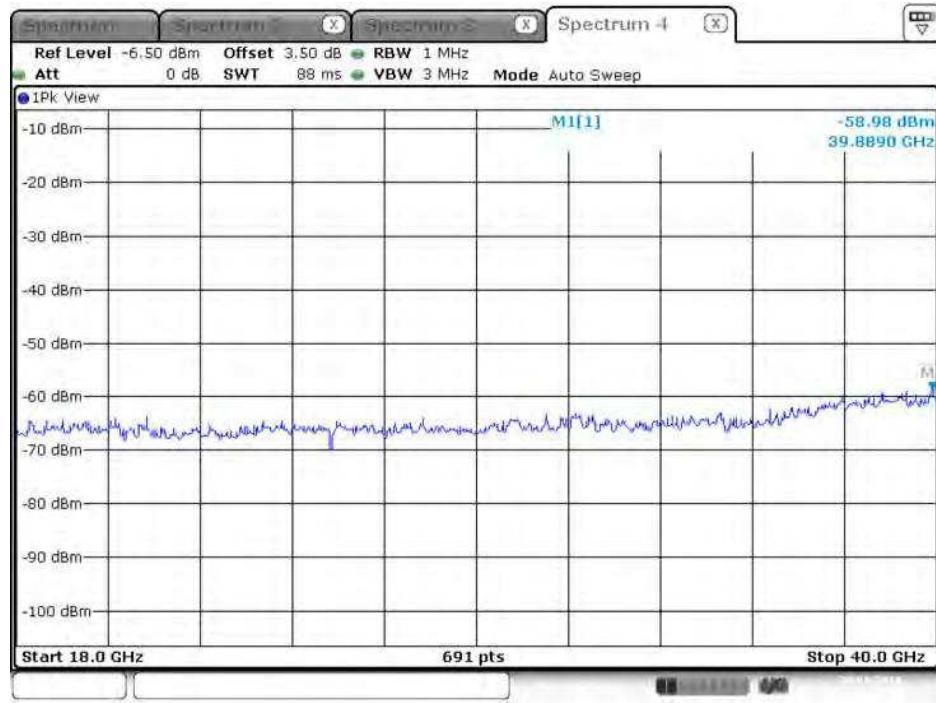


## Plot on Configuration QPSK, 40M / 5270 MHz / Peak / Port 1 / 18GHz~40GHz



Date: 24.JAN.2018 20:01:53

## Plot on Configuration QPSK, 40M / 5270 MHz / Peak / Port 2 / 18GHz~40GHz



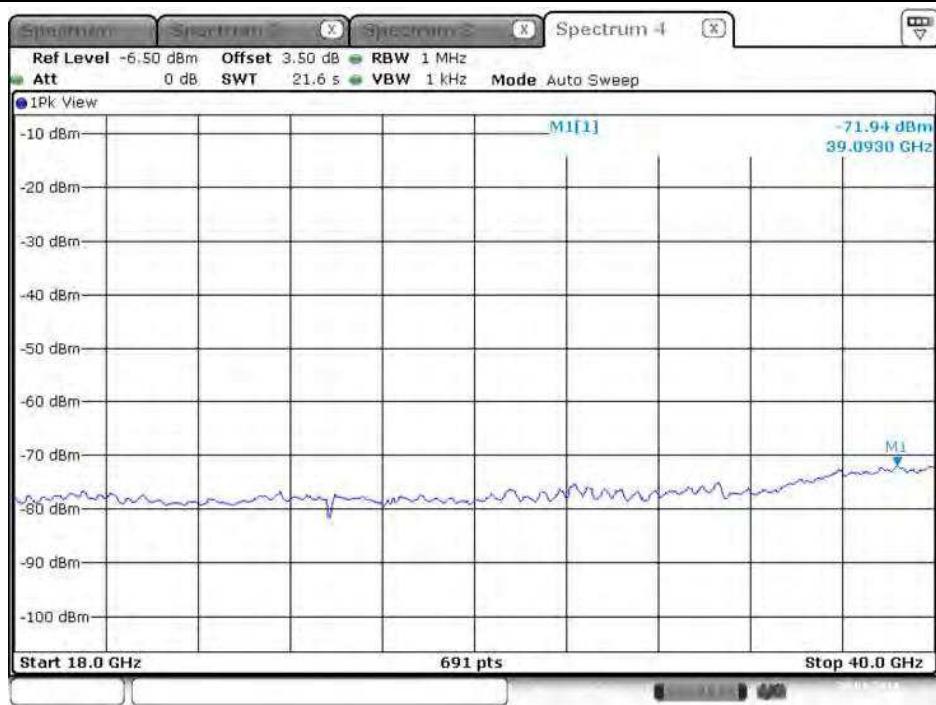
Date: 24.JAN.2018 20:05:00



## CSE TX above 1GHz Result

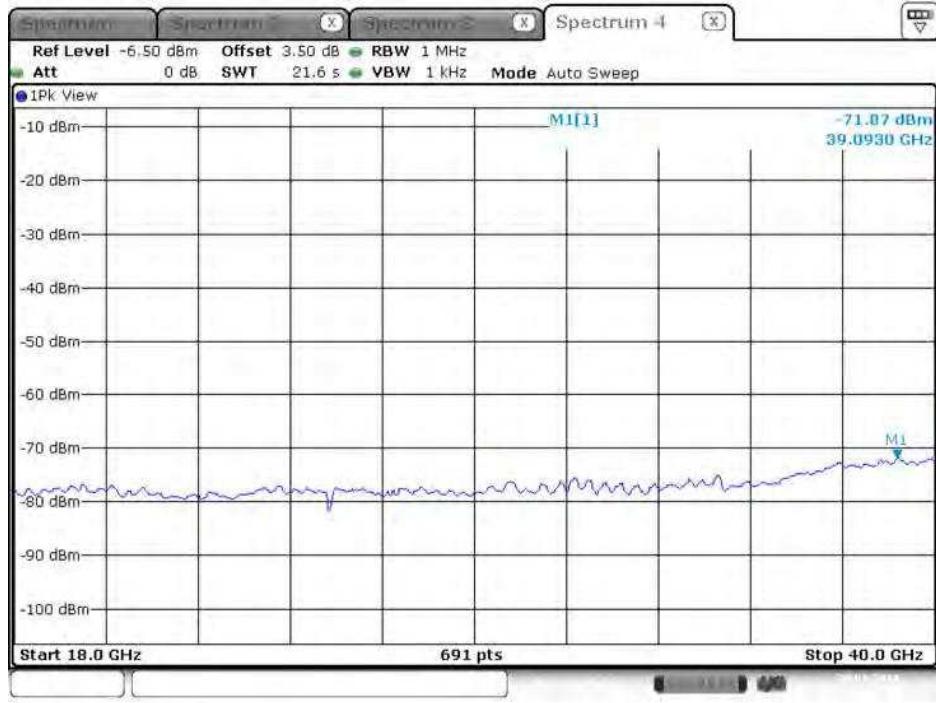
Appendix D.1

### Plot on Configuration QPSK, 40M / 5300 MHz / Average / Port 1 / 18GHz~40GHz

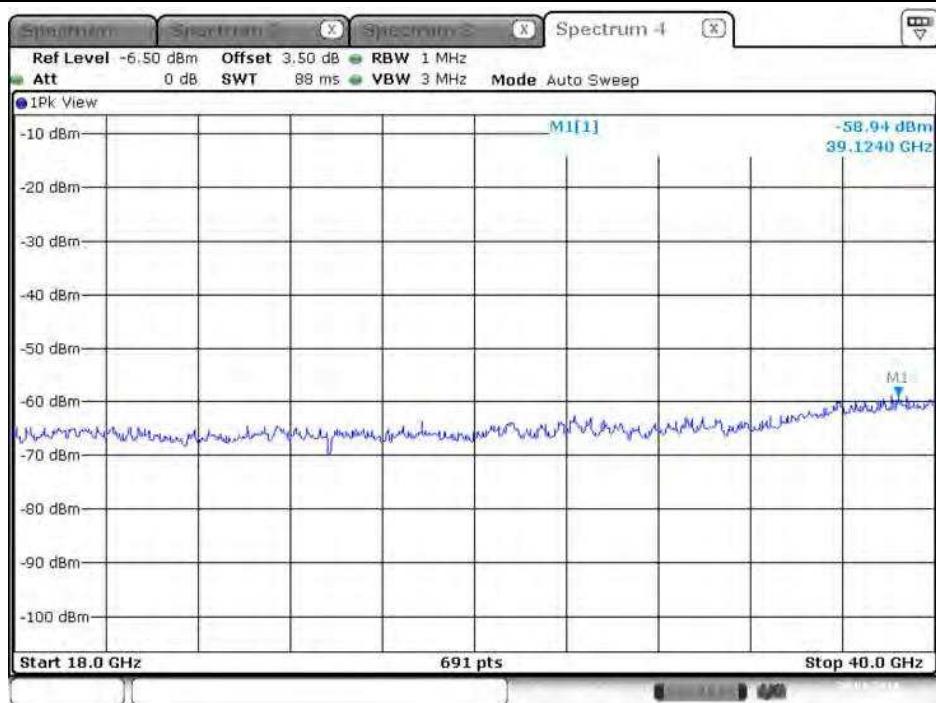


Date: 24.JAN.2018 20:39:06

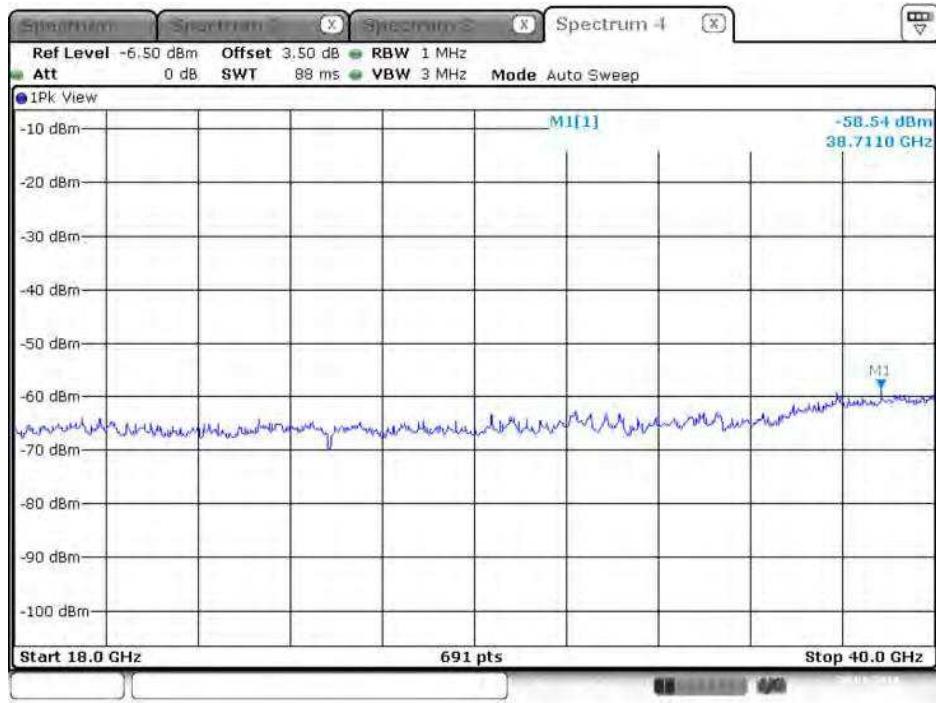
### Plot on Configuration QPSK, 40M / 5300 MHz / Average / Port 2 / 18GHz~40GHz



Date: 24.JAN.2018 20:42:10

**Plot on Configuration QPSK, 40M / 5300 MHz / Peak / Port 1 /18GHz~40GHz**


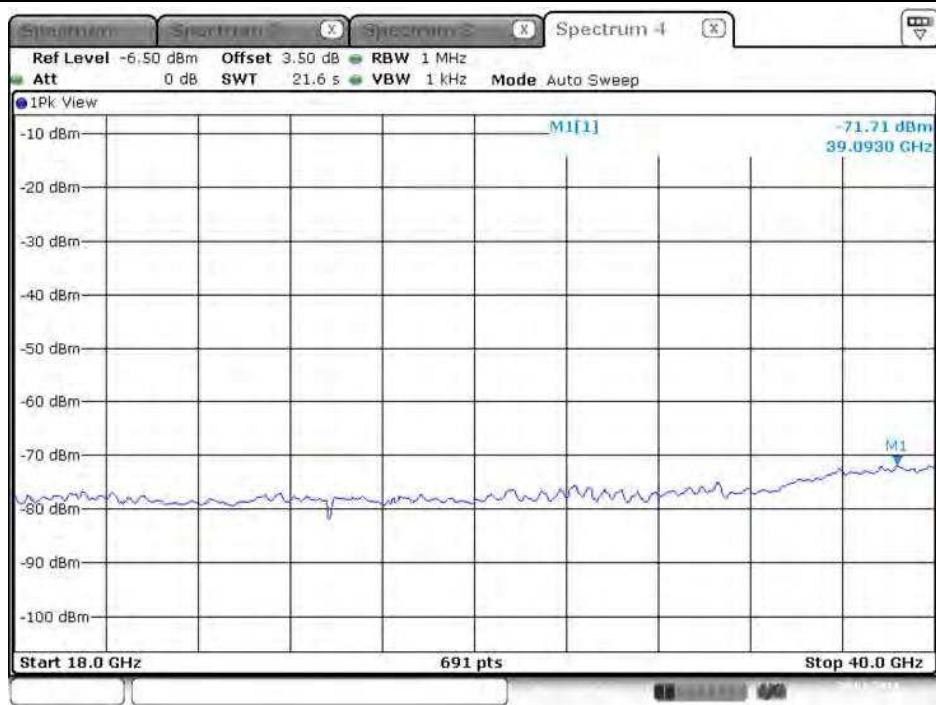
Date: 24.JAN.2018 20:39:51

**Plot on Configuration QPSK, 40M / 5300 MHz / Peak / Port 2 / 18GHz~40GHz**


Date: 24.JAN.2018 20:42:48

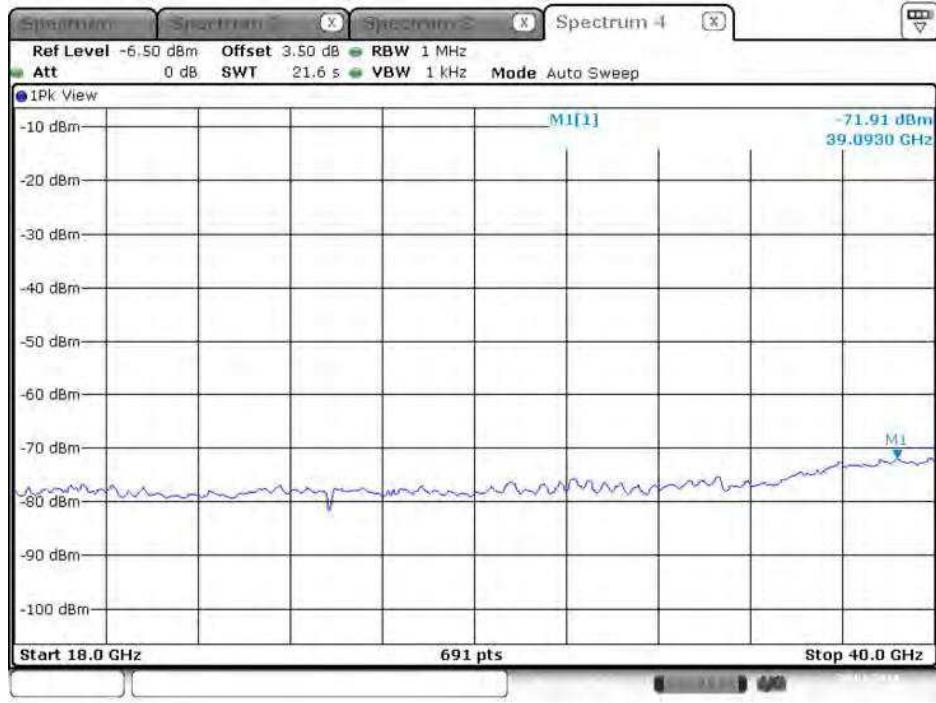


## Plot on Configuration QPSK, 40M / 5325 MHz / Average / Port 1 / 18GHz~40GHz

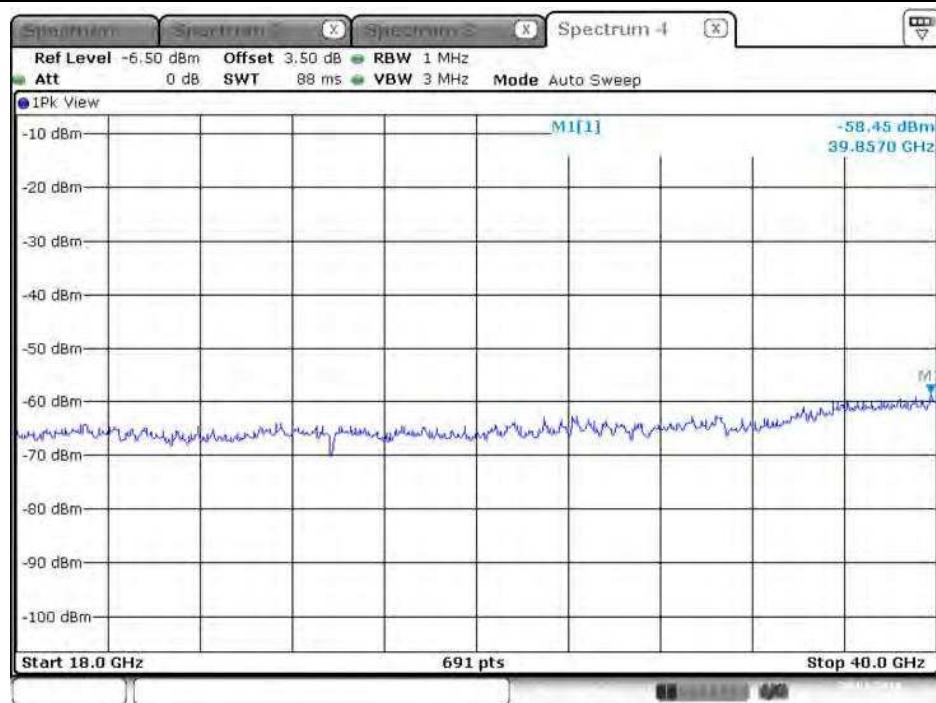


Date: 24.JAN.2018 21:07:40

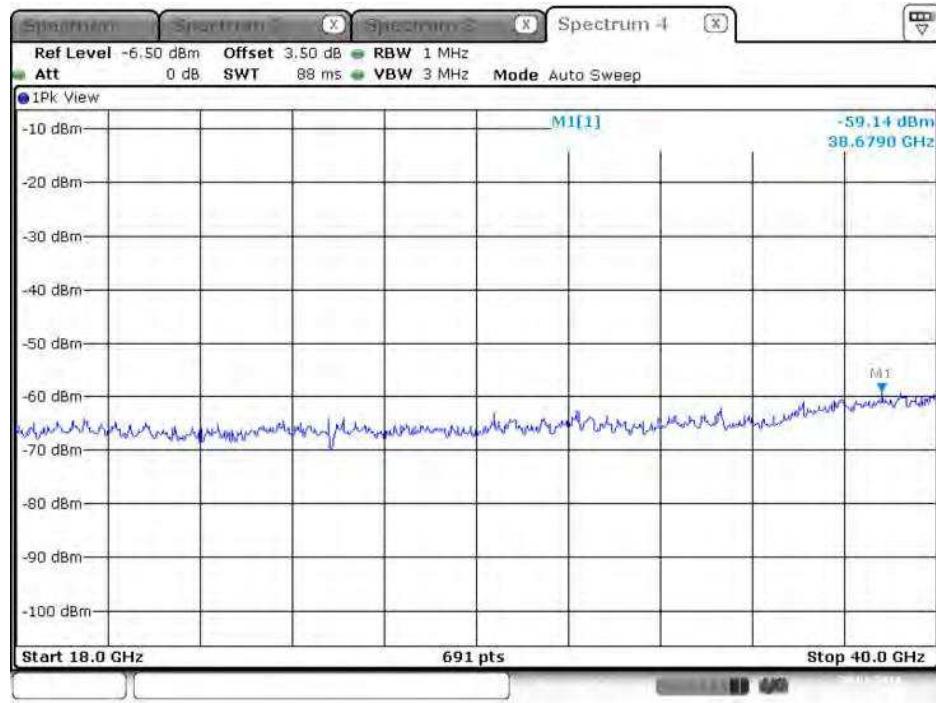
## Plot on Configuration QPSK, 40M / 5325 MHz / Average / Port 2 / 18GHz~40GHz



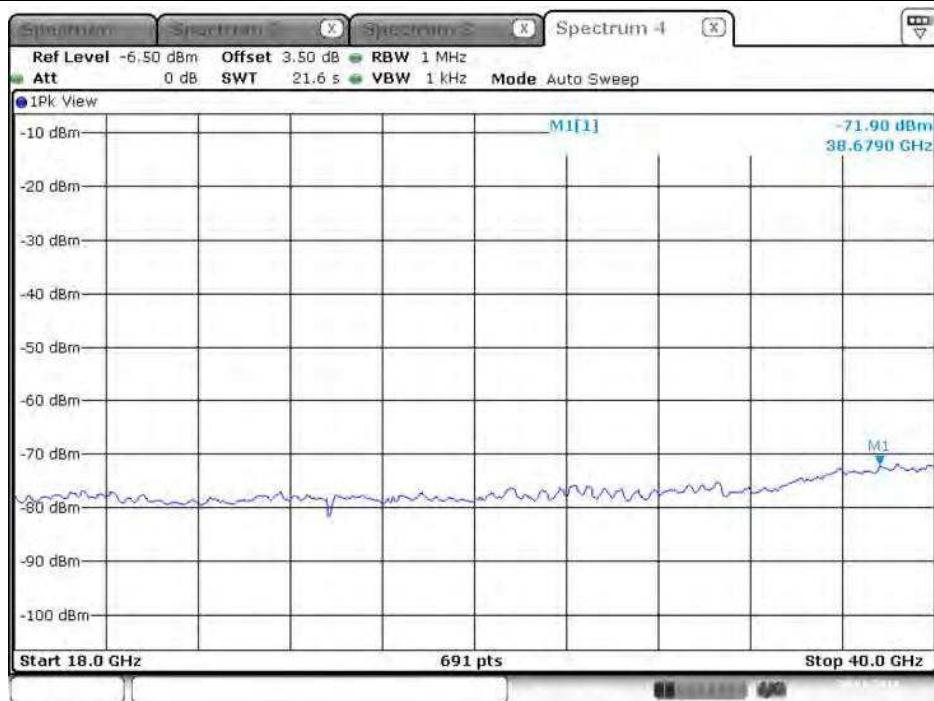
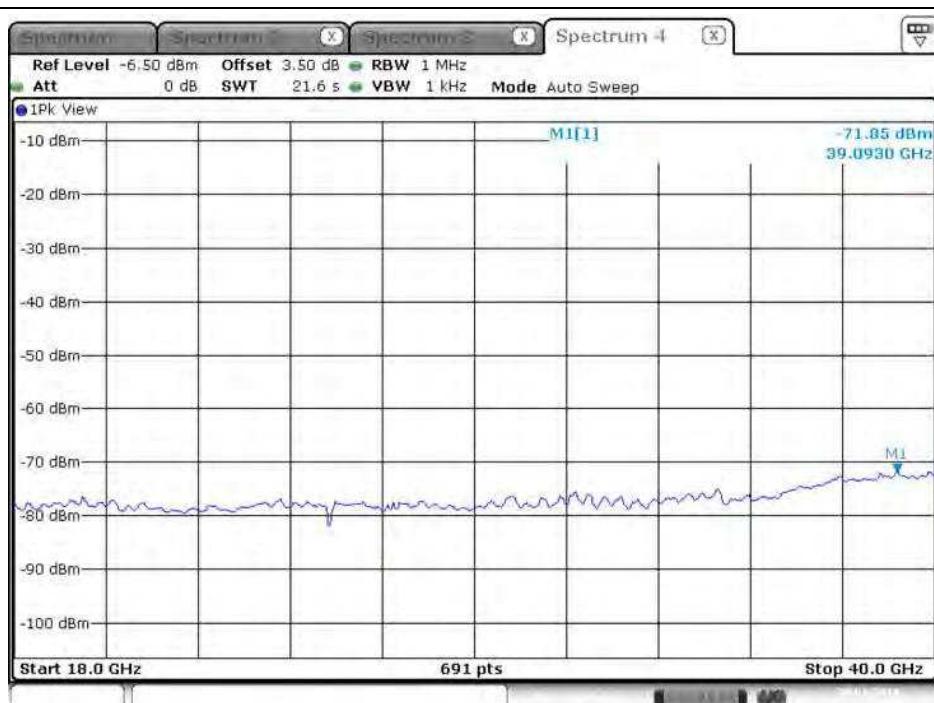
Date: 24.JAN.2018 21:10:32

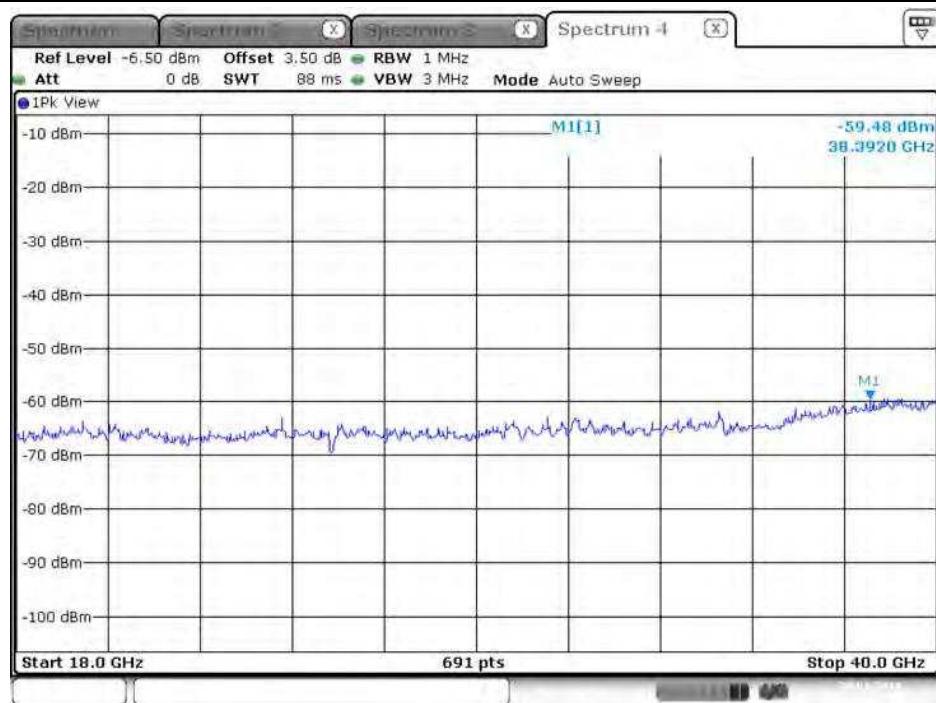
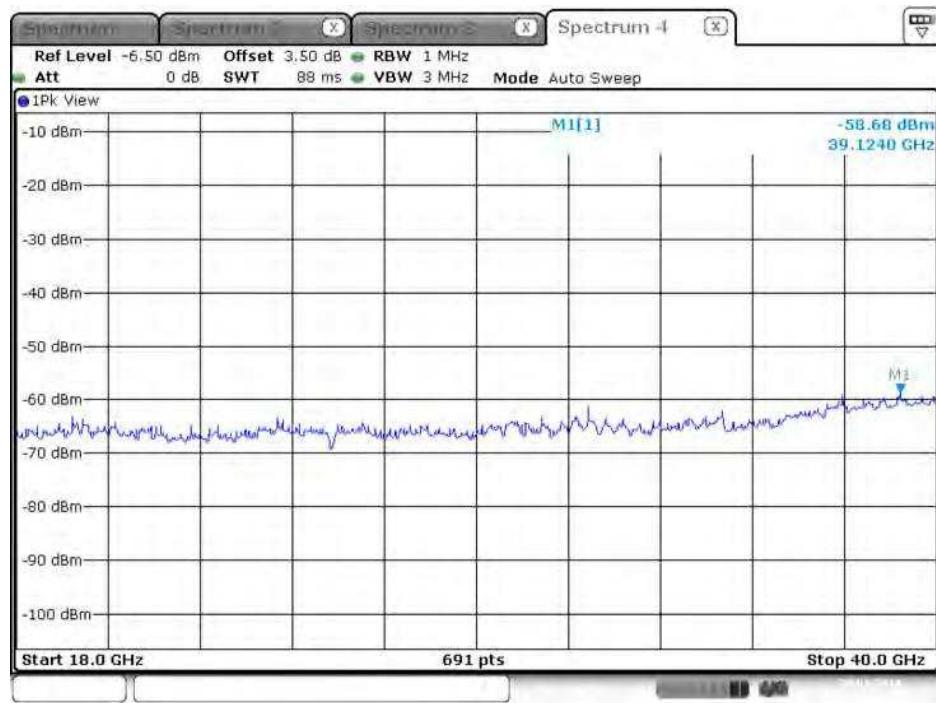
**Plot on Configuration QPSK, 40M / 5325 MHz / Peak / Port 1 / 18GHz~40GHz**


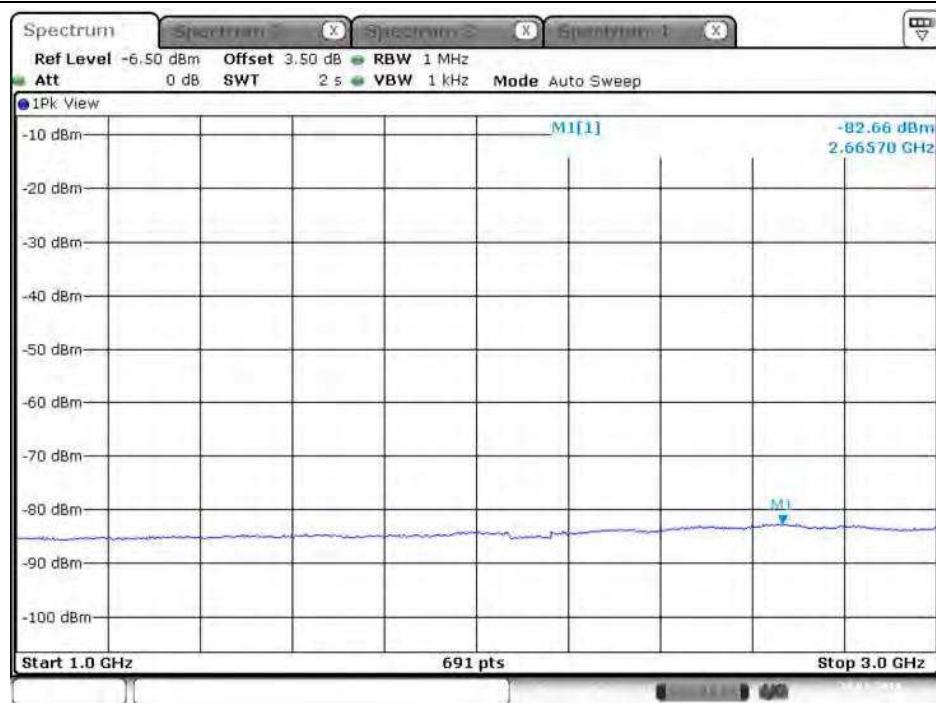
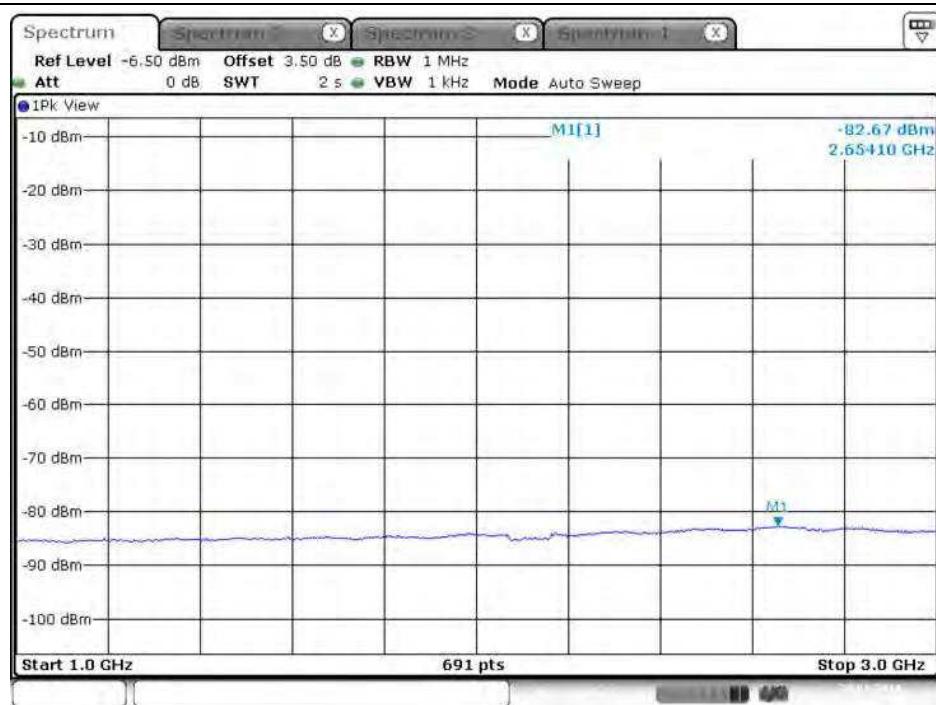
Date: 24.JAN.2018 21:08:53

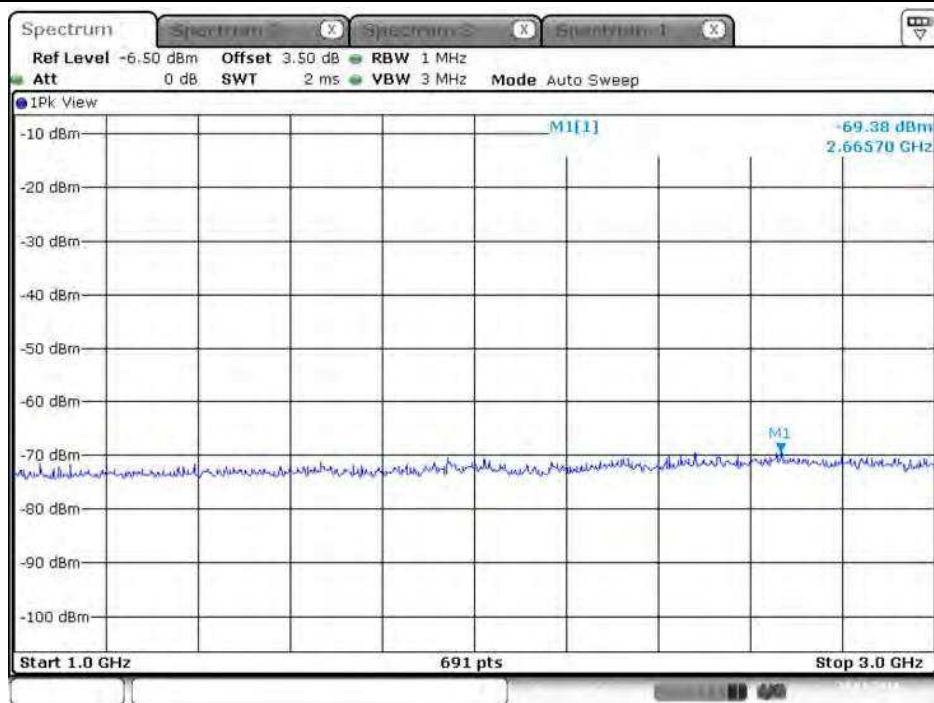
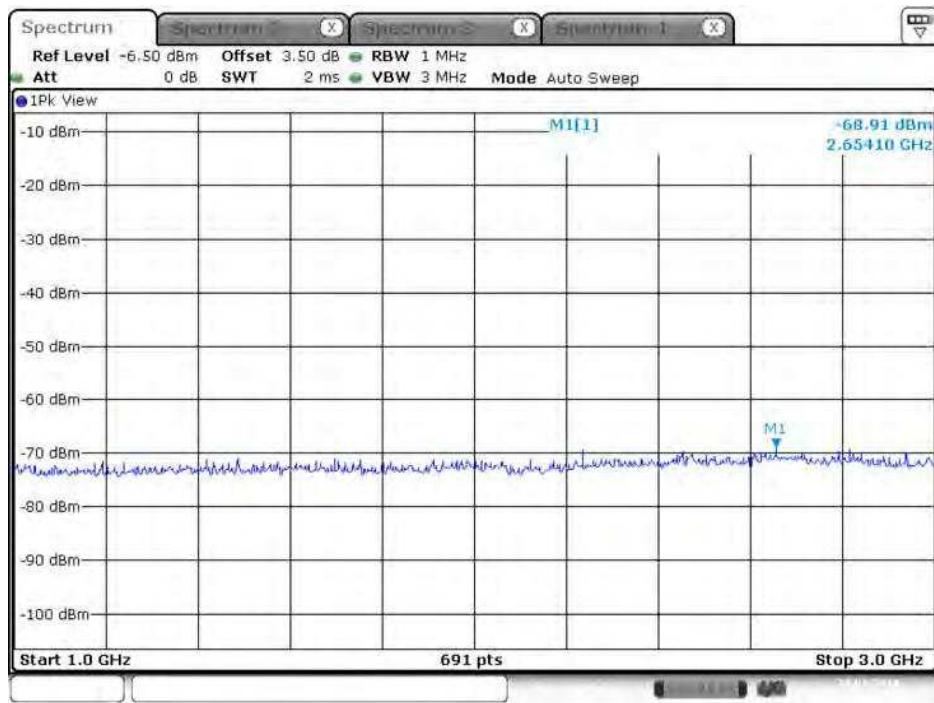
**Plot on Configuration QPSK, 40M / 5325 MHz / Peak / Port 2 / 18GHz~40GHz**


Date: 24.JAN.2018 21:11:19

**Plot on Configuration QPSK, 40M / 5250 MHz (Straddle Channel) / Average / Port 1 / 18GHz~40GHz**

**Plot on Configuration QPSK, 40M / 5250 MHz (Straddle Channel) / Average / Port 2 / 18GHz~40GHz**


**Plot on Configuration QPSK, 40M / 5250 MHz (Straddle Channel) / Peak / Port 1 / 18GHz~40GHz**

**Plot on Configuration QPSK, 40M / 5250 MHz (Straddle Channel) / Peak / Port 2 / 18GHz~40GHz**


**Plot on Configuration QPSK, 10M / 5480 MHz / Average / Port 1 / 1GHz~3GHz**

**Plot on Configuration QPSK, 10M / 5480 MHz / Average / Port 2 / 1GHz~3GHz**


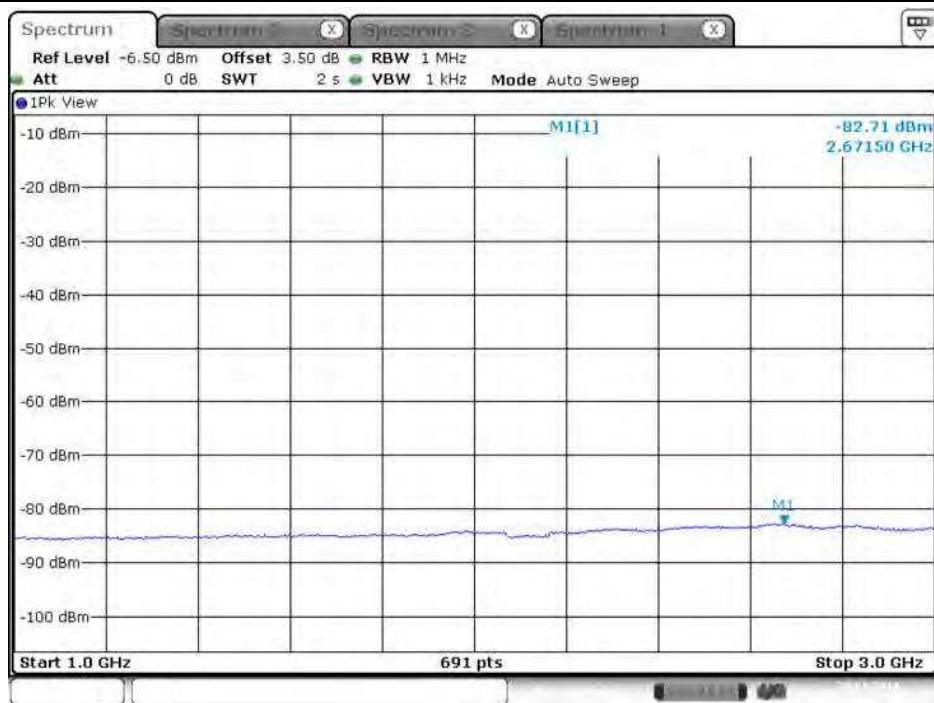
**Plot on Configuration QPSK, 10M / 5480 MHz / Peak / Port 1 / 1GHz~3GHz**

**Plot on Configuration QPSK, 10M / 5480 MHz / Peak / Port 2 / 1GHz~3GHz**




## CSE TX above 1GHz Result

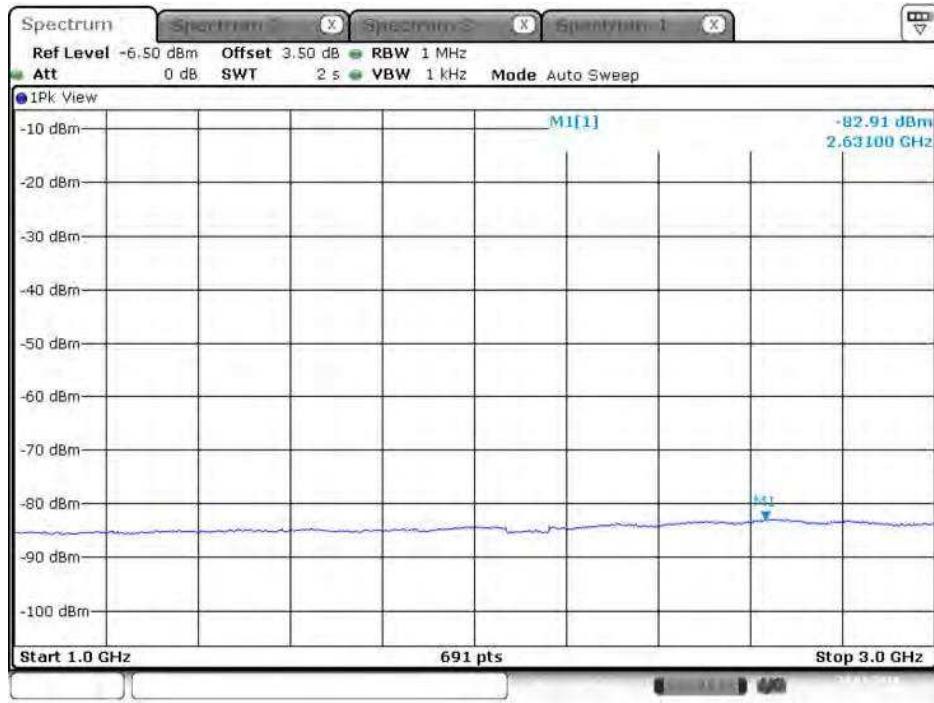
Appendix D.1

### Plot on Configuration QPSK, 10M / 5575 MHz / Average / Port 1 / 1GHz~3GHz



Date: 24.JAN.2018 02:27:29

### Plot on Configuration QPSK, 10M / 5575 MHz / Average / Port 2 / 1GHz~3GHz



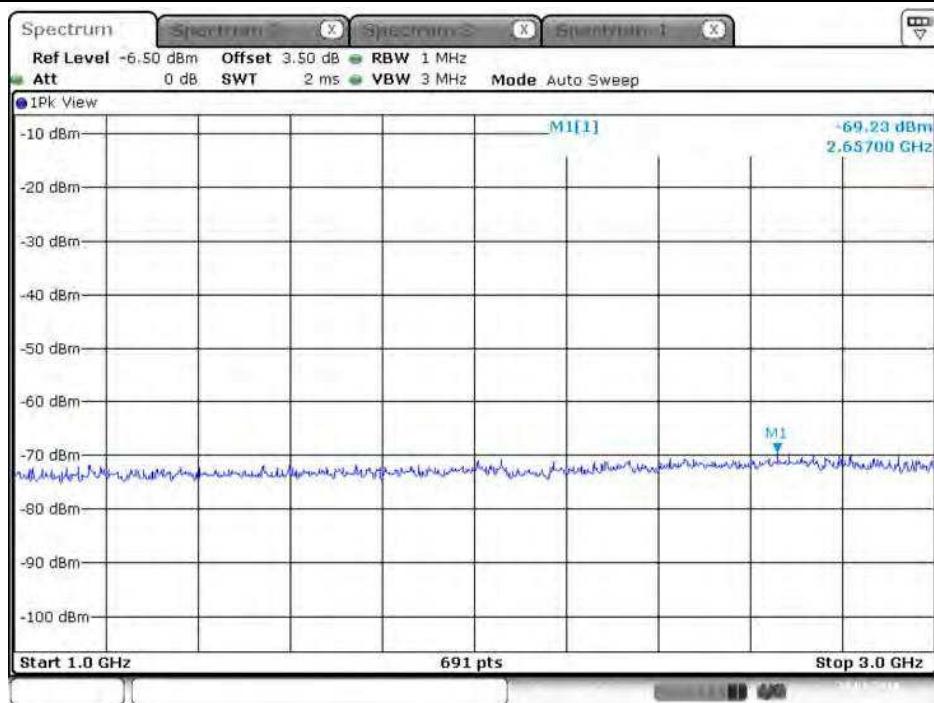
Date: 24.JAN.2018 02:21:51



## CSE TX above 1GHz Result

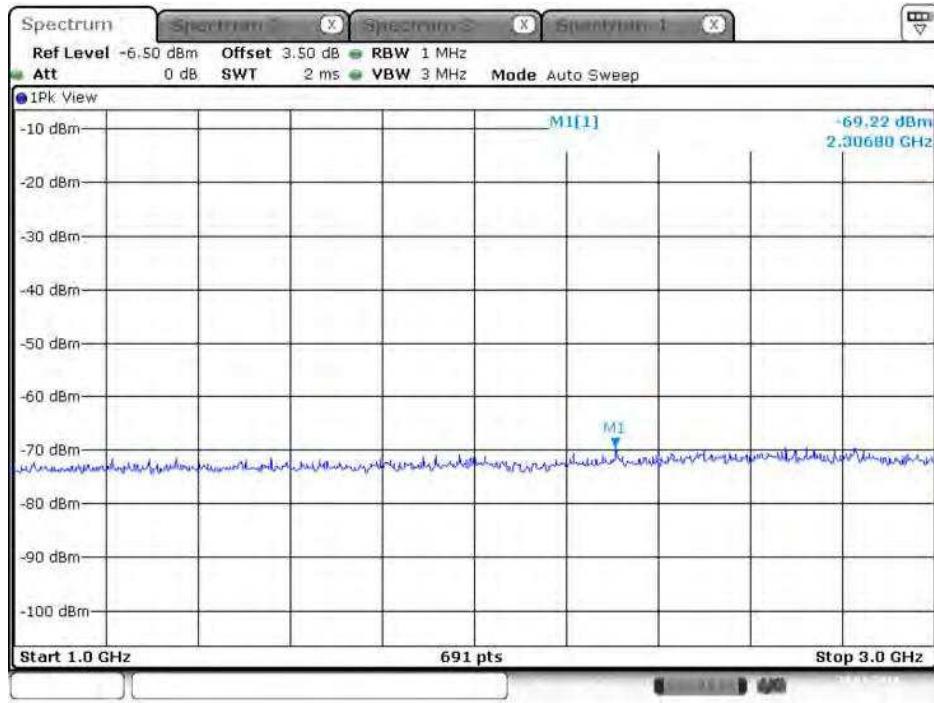
Appendix D.1

### Plot on Configuration QPSK, 10M / 5575 MHz / Peak / Port 1 / 1GHz~3GHz



Date: 24.JAN.2018 02:27:17

### Plot on Configuration QPSK, 10M / 5575 MHz / Peak / Port 2 / 1GHz~3GHz



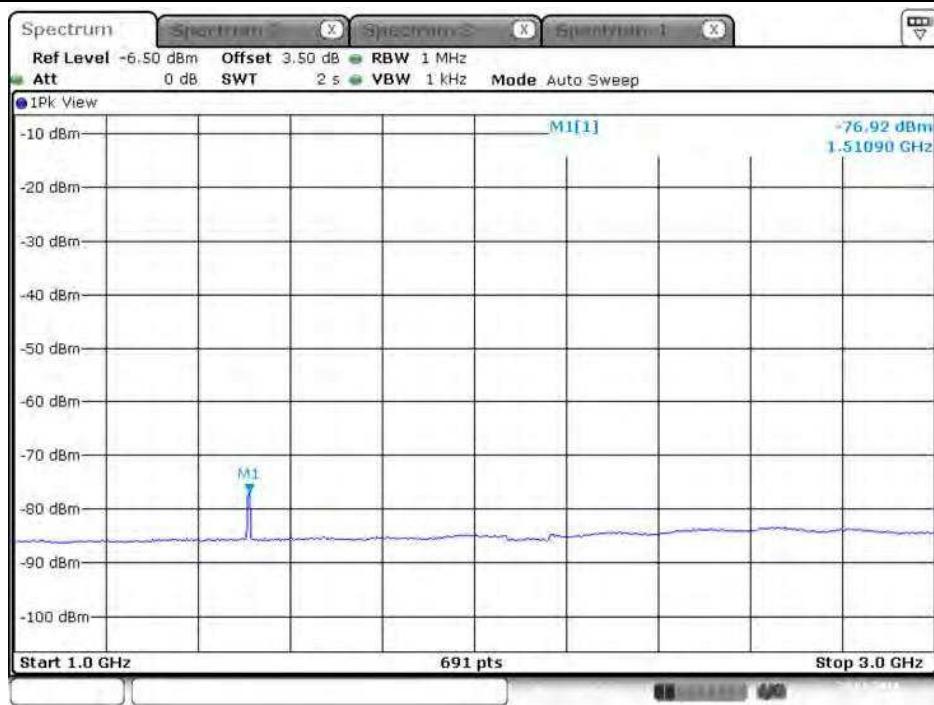
Date: 24.JAN.2018 02:22:08



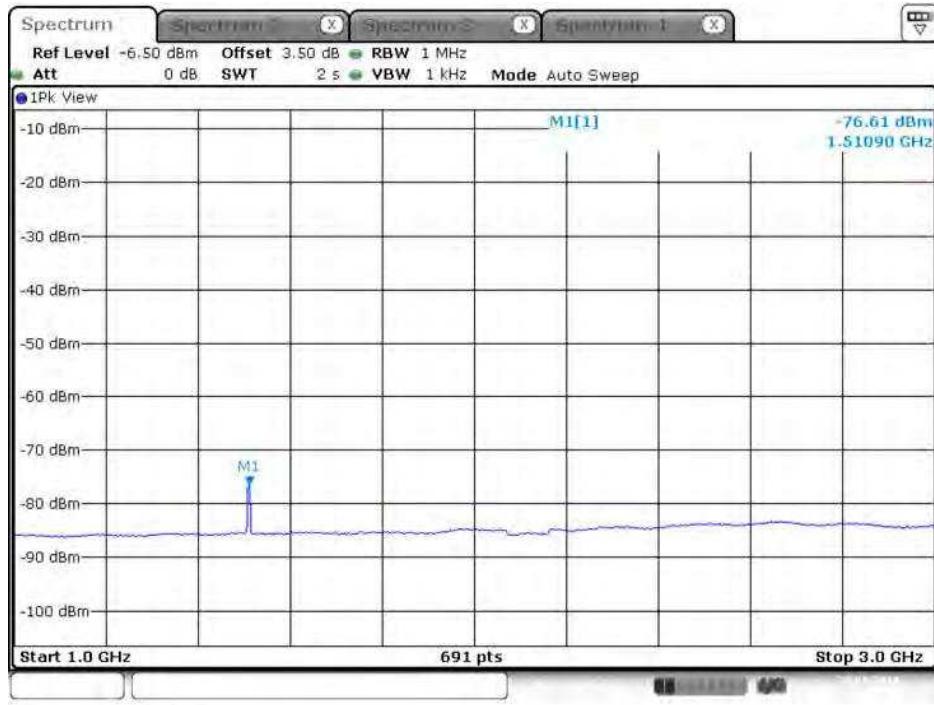
## CSE TX above 1GHz Result

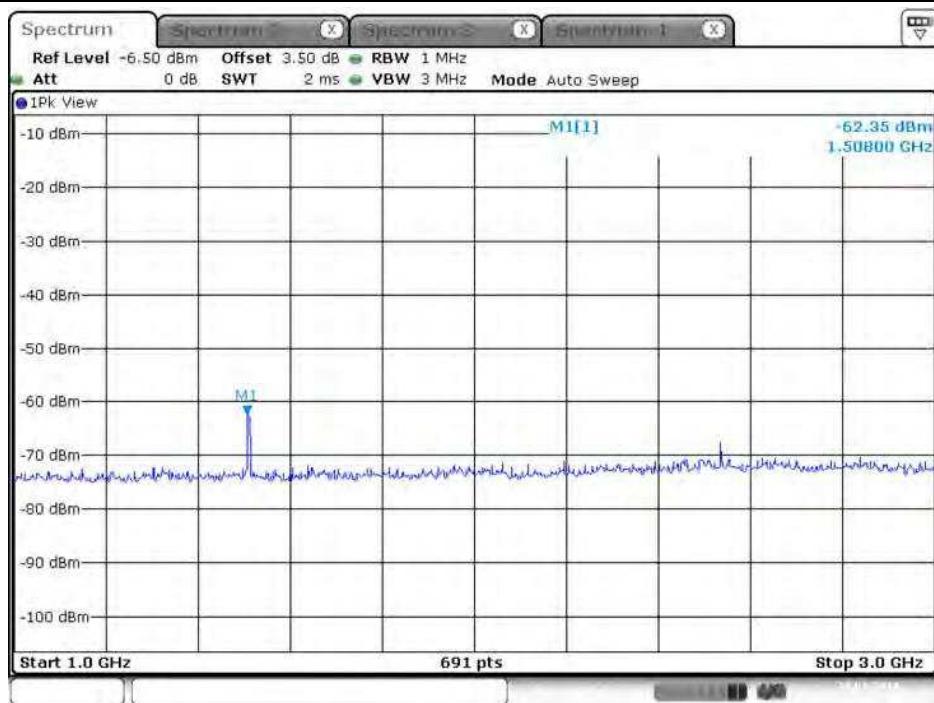
Appendix D.1

### Plot on Configuration QPSK, 10M / 5715 MHz / Average / Port 1 / 1GHz~3GHz

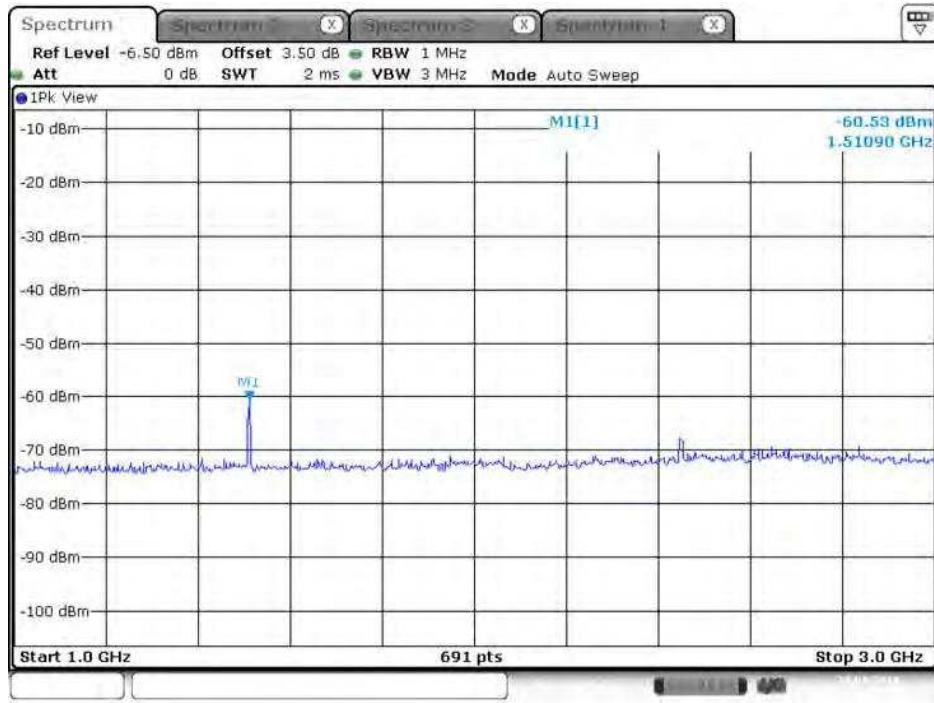


### Plot on Configuration QPSK, 10M / 5715 MHz / Average / Port 2 / 1GHz~3GHz

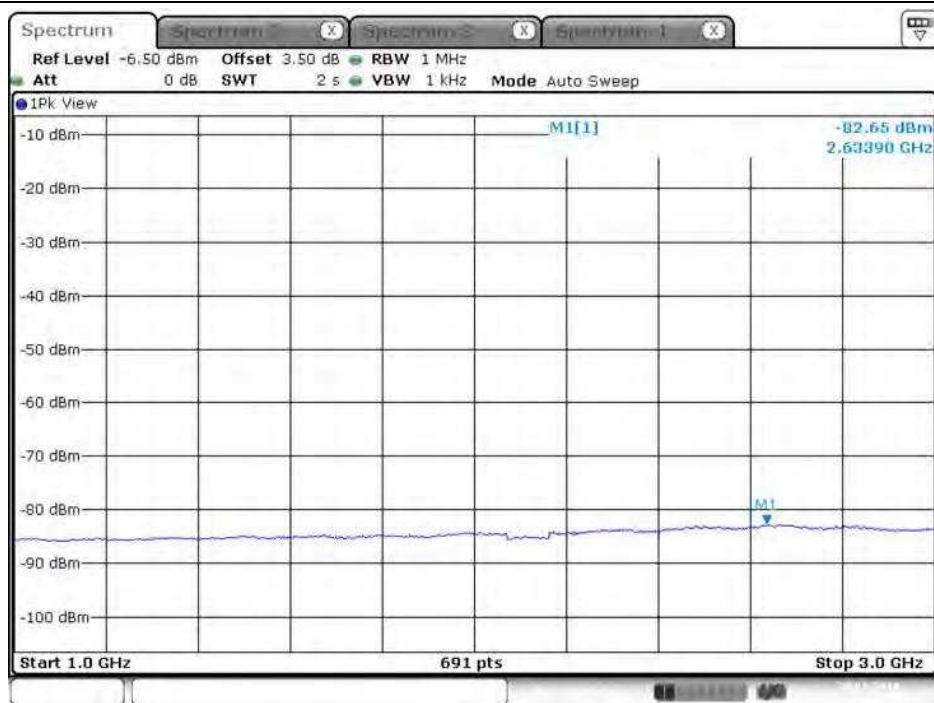


**Plot on Configuration QPSK, 10M / 5715 MHz / Peak / Port 1 / 1GHz~3GHz**


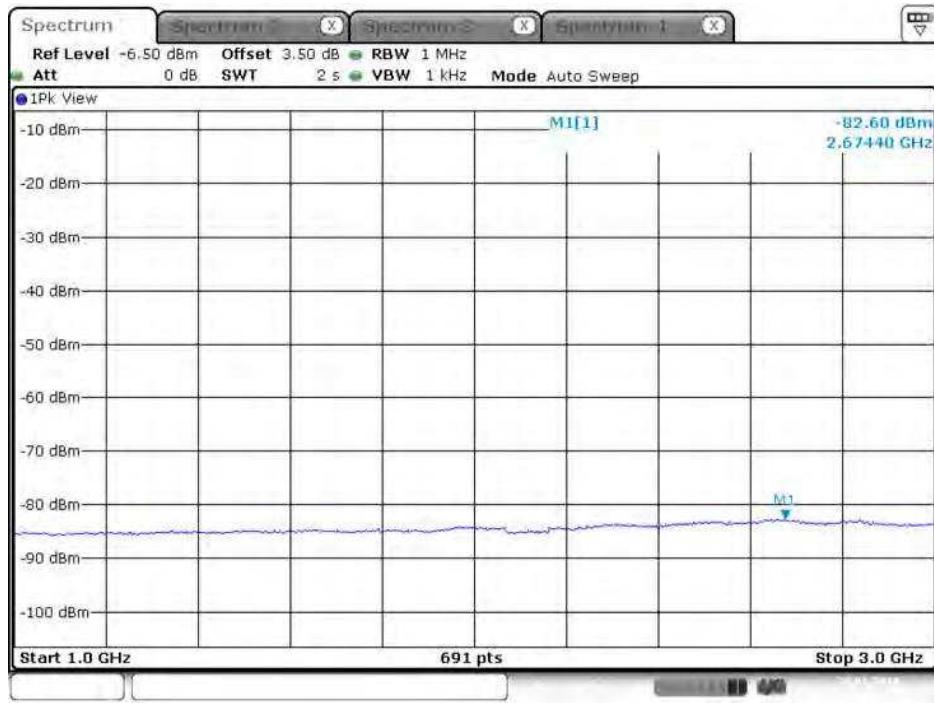
Date: 24.JAN.2018 10:23:26

**Plot on Configuration QPSK, 10M / 5715 MHz / Peak / Port 2 / 1GHz~3GHz**


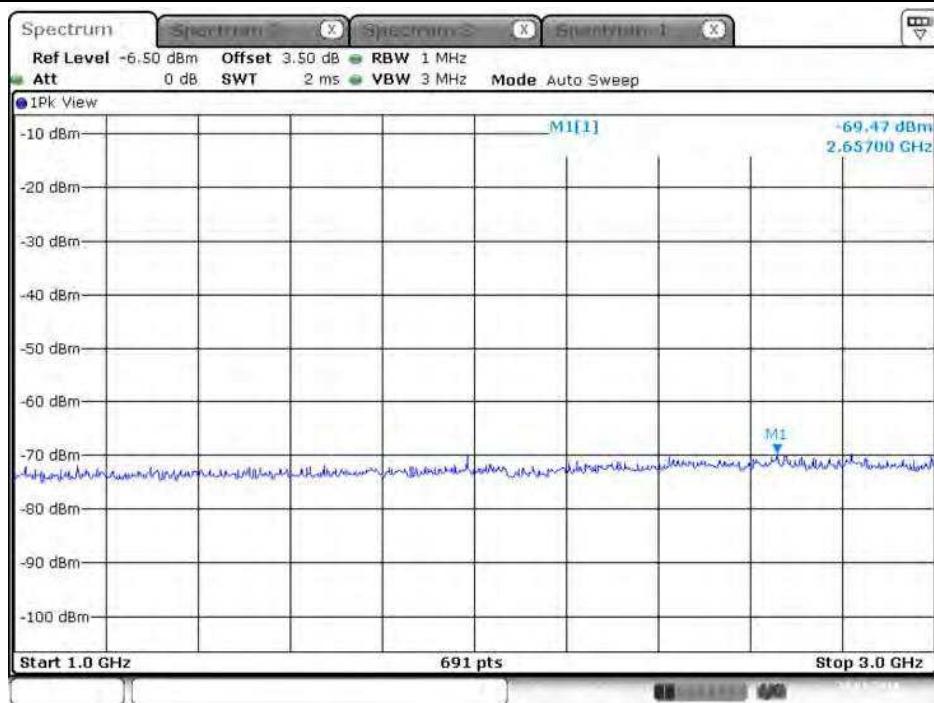
Date: 24.JAN.2018 10:53:55

**Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Average / Port 1 / 1GHz~3GHz**


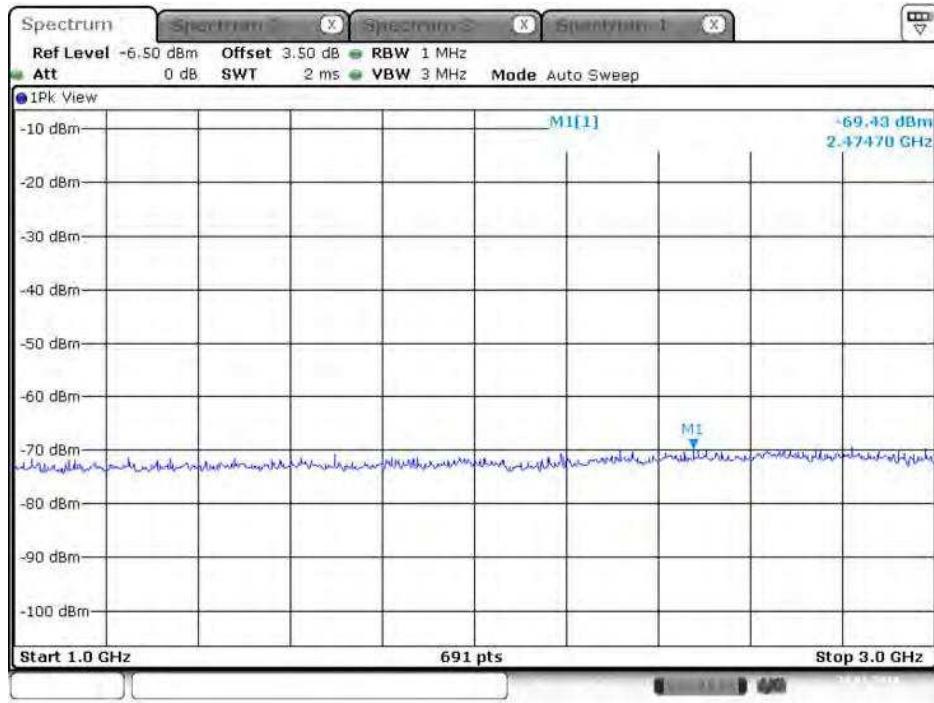
Date: 24.JAN.2018 02:07:42

**Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Average / Port 2 / 1GHz~3GHz**


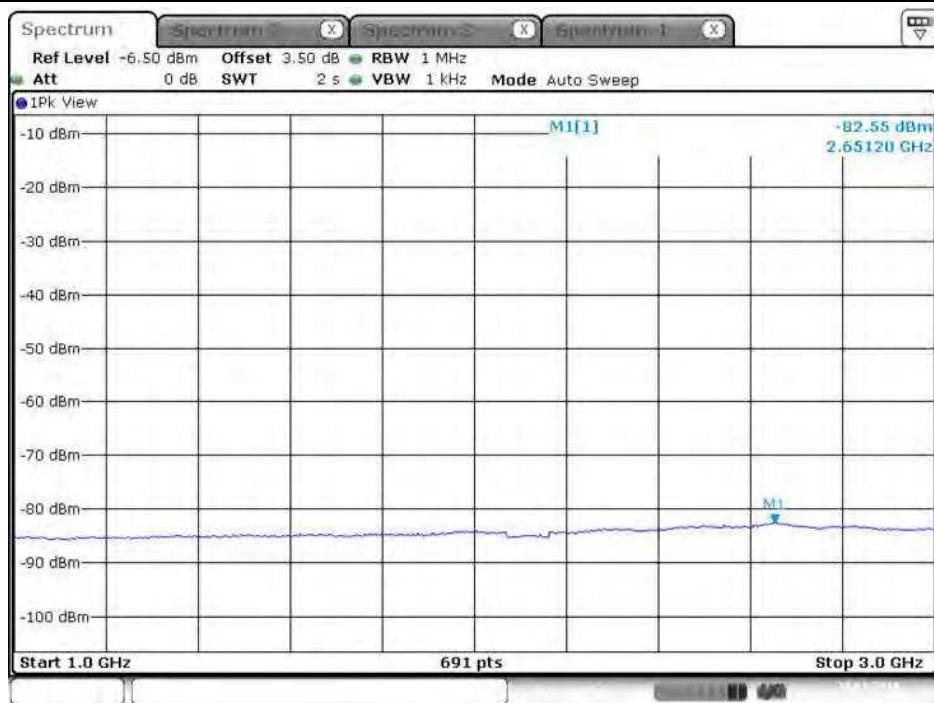
Date: 24.JAN.2018 01:54:45

**Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Peak / Port 1 / 1GHz~3GHz**


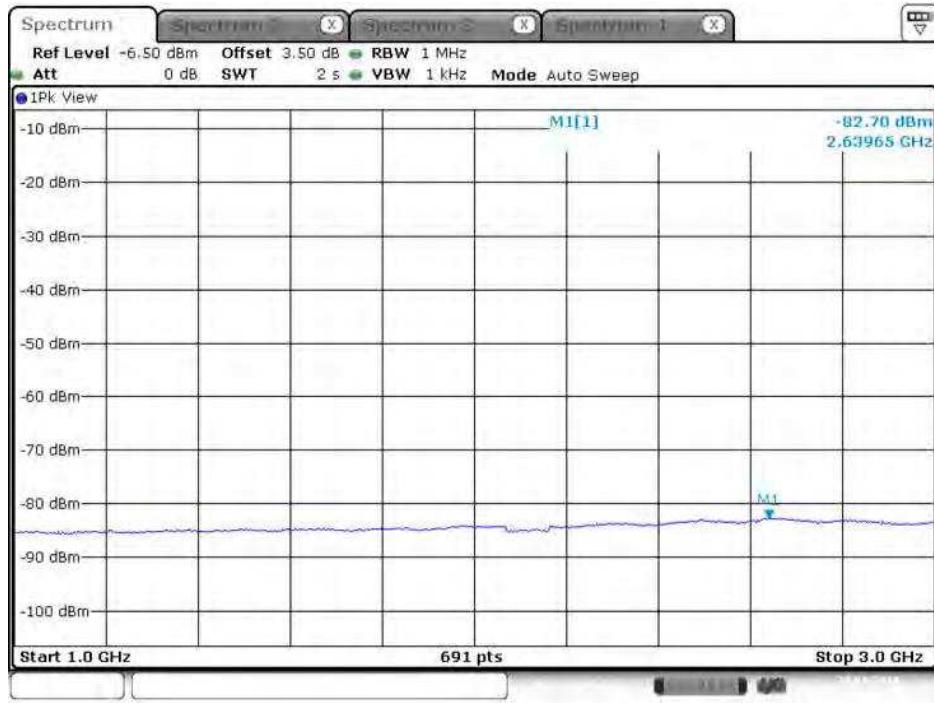
Date: 24.JAN.2018 02:07:31

**Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Peak / Port 2 / 1GHz~3GHz**


Date: 24.JAN.2018 01:55:13

**Plot on Configuration QPSK, 40M / 5495 MHz / Average / Port 1 / 1GHz~3GHz**


Date: 24.JAN.2018 03:11:11

**Plot on Configuration QPSK, 40M / 5495 MHz / Average / Port 2 / 1GHz~3GHz**


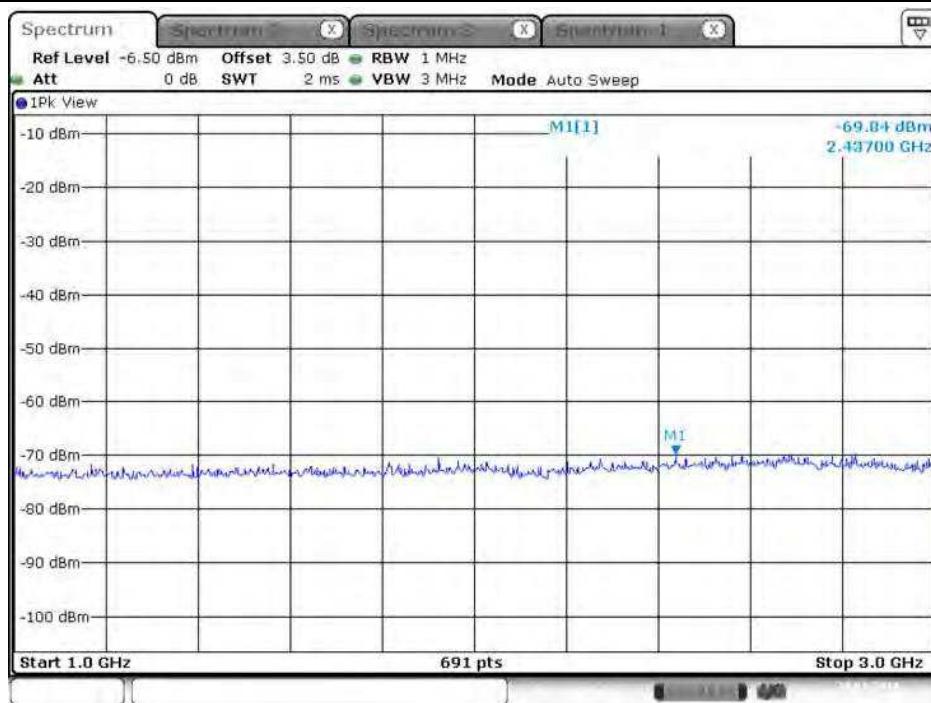
Date: 24.JAN.2018 03:04:11



## CSE TX above 1GHz Result

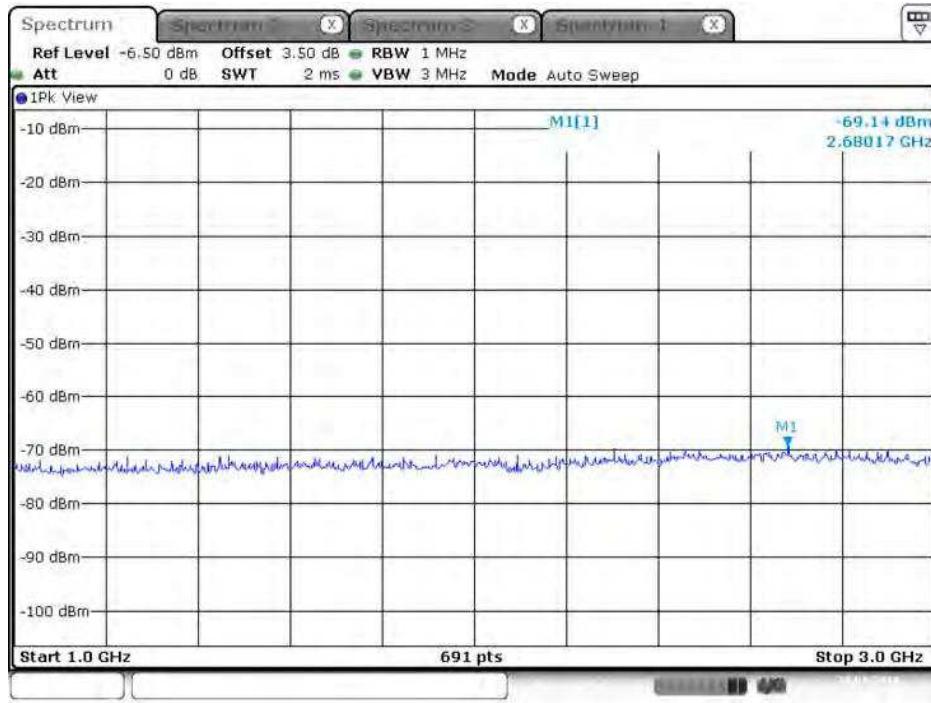
Appendix D.1

### Plot on Configuration QPSK, 40M / 5495 MHz / Peak / Port 1 / 1GHz~3GHz



Date: 24.JAN.2018 03:10:48

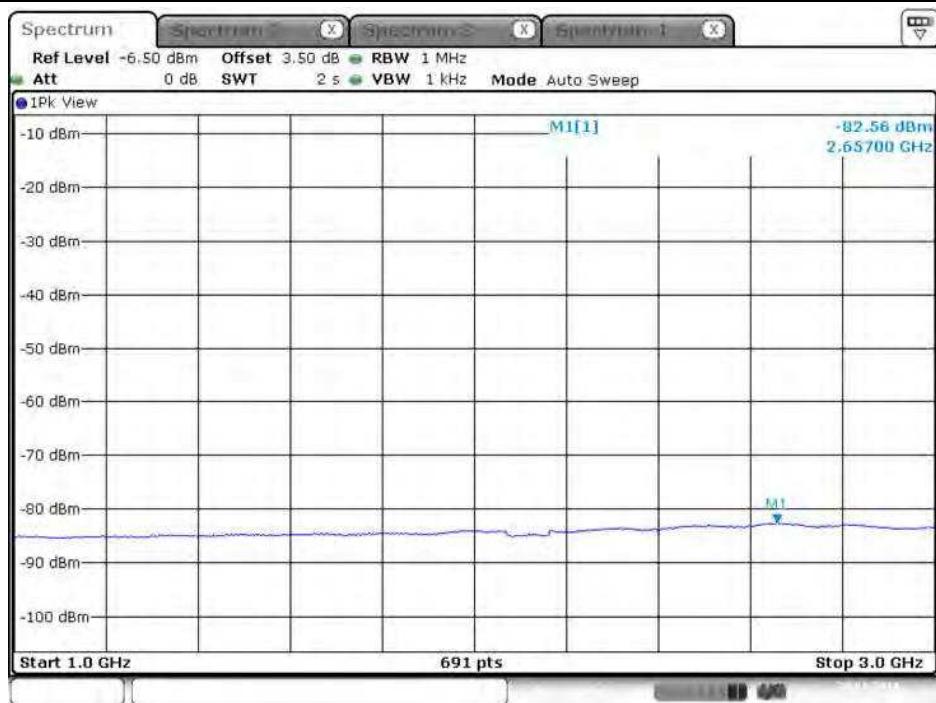
### Plot on Configuration QPSK, 40M / 5495 MHz / Peak / Port 2 / 1GHz~3GHz



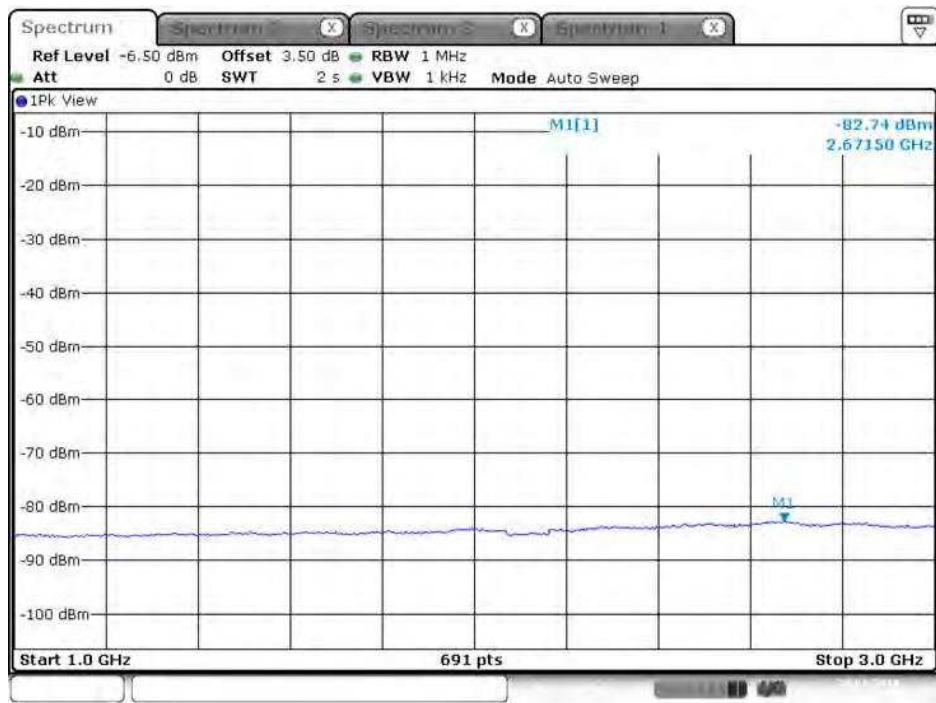
Date: 24.JAN.2018 03:04:29



## Plot on Configuration QPSK, 40M / 5550 MHz / Average / Port 1 / 1GHz~3GHz



## Plot on Configuration QPSK, 40M / 5550 MHz / Average / Port 2 / 1GHz~3GHz

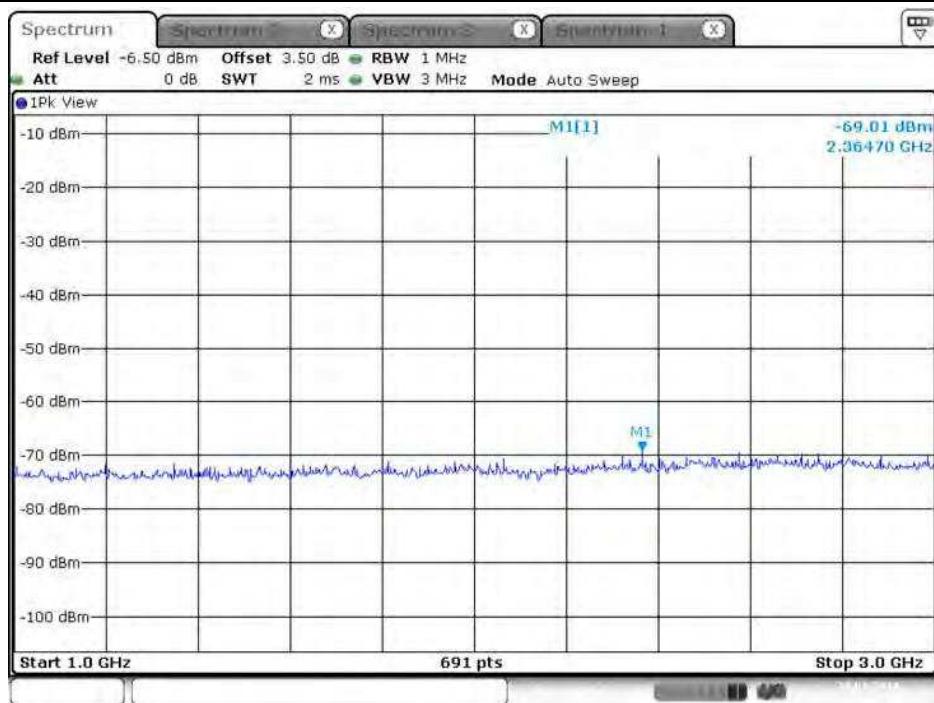




## CSE TX above 1GHz Result

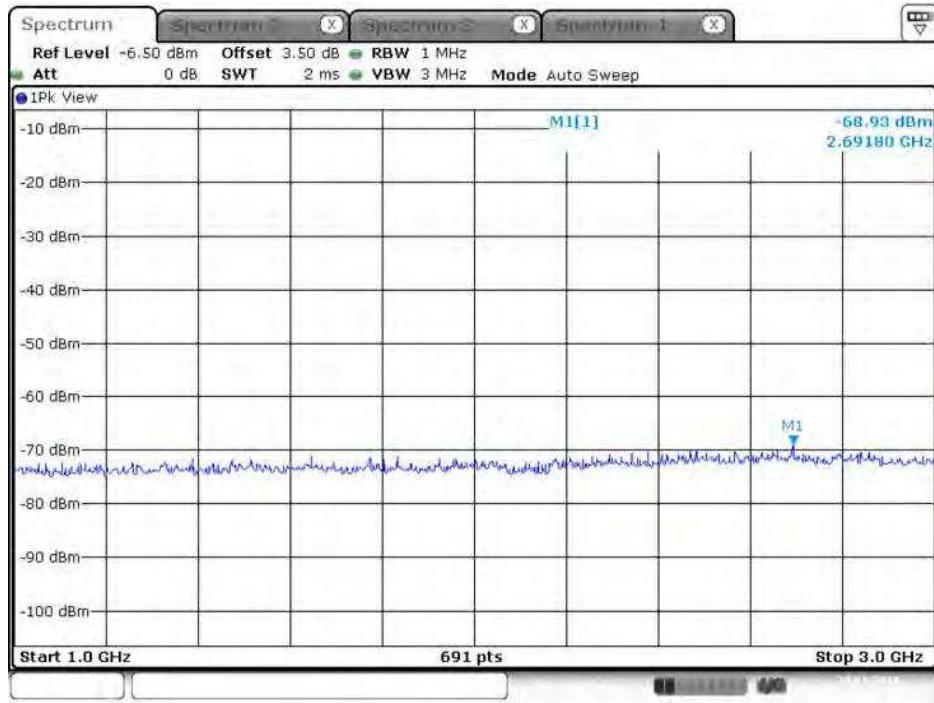
Appendix D.1

### Plot on Configuration QPSK, 40M / 5550 MHz / Peak / Port 1 / 1GHz~3GHz



Date: 24.JAN.2018 03:27:56

### Plot on Configuration QPSK, 40M / 5550 MHz / Peak / Port 2 / 1GHz~3GHz



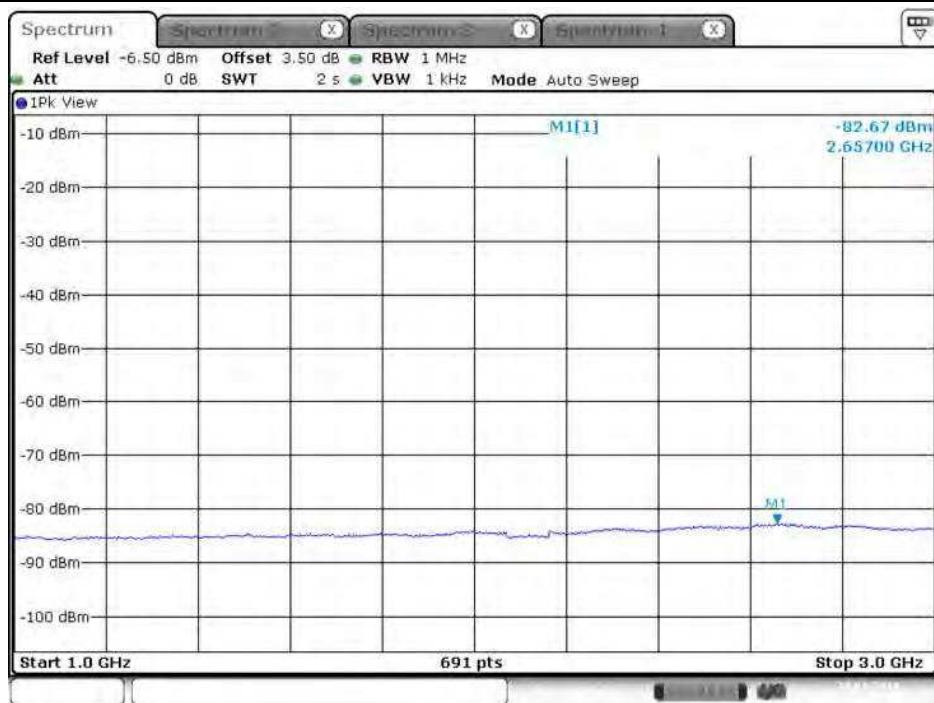
Date: 24.JAN.2018 03:35:29



## CSE TX above 1GHz Result

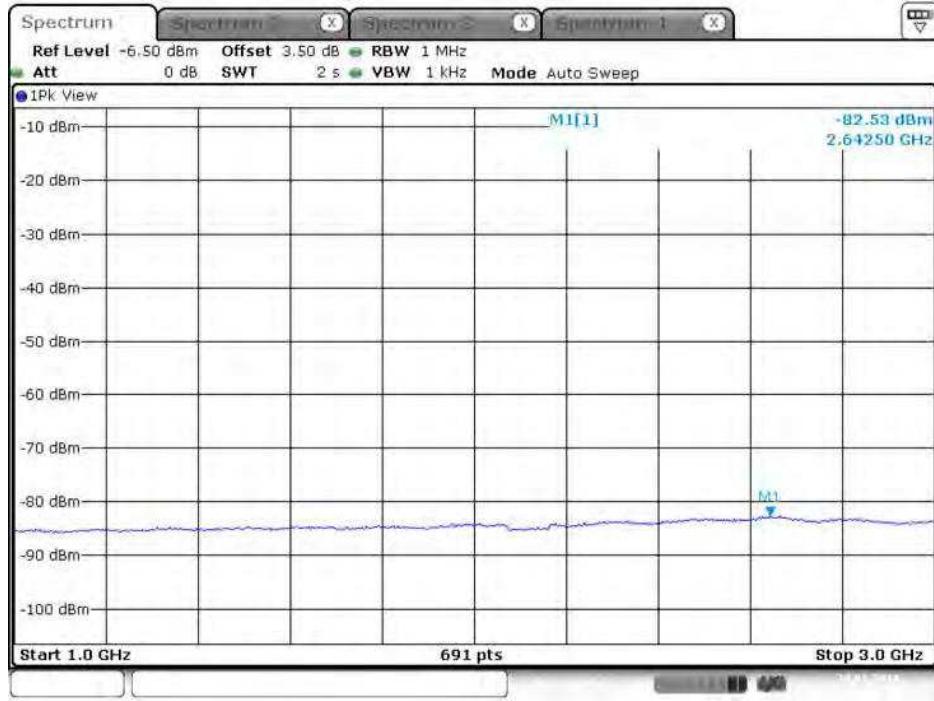
Appendix D.1

### Plot on Configuration QPSK, 40M / 5700 MHz / Average / Port 1 / 1GHz~3GHz



Date: 24.JAN.2018 03:55:48

### Plot on Configuration QPSK, 40M / 5700 MHz / Average / Port 2 / 1GHz~3GHz



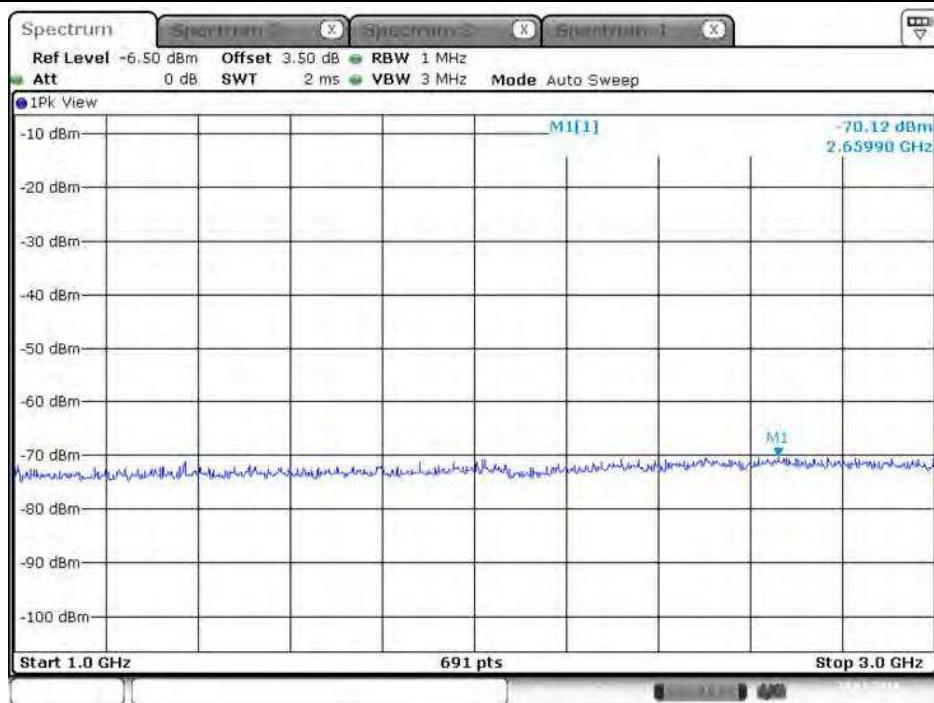
Date: 24.JAN.2018 03:49:17



## CSE TX above 1GHz Result

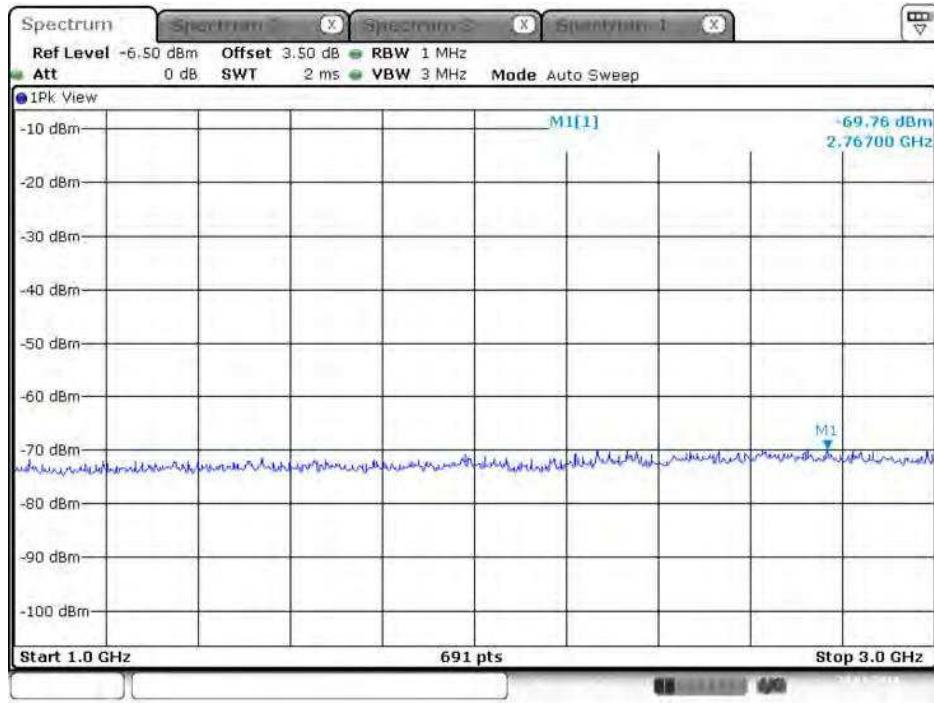
Appendix D.1

### Plot on Configuration QPSK, 40M / 5700 MHz / Peak / Port 1 / 1GHz~3GHz

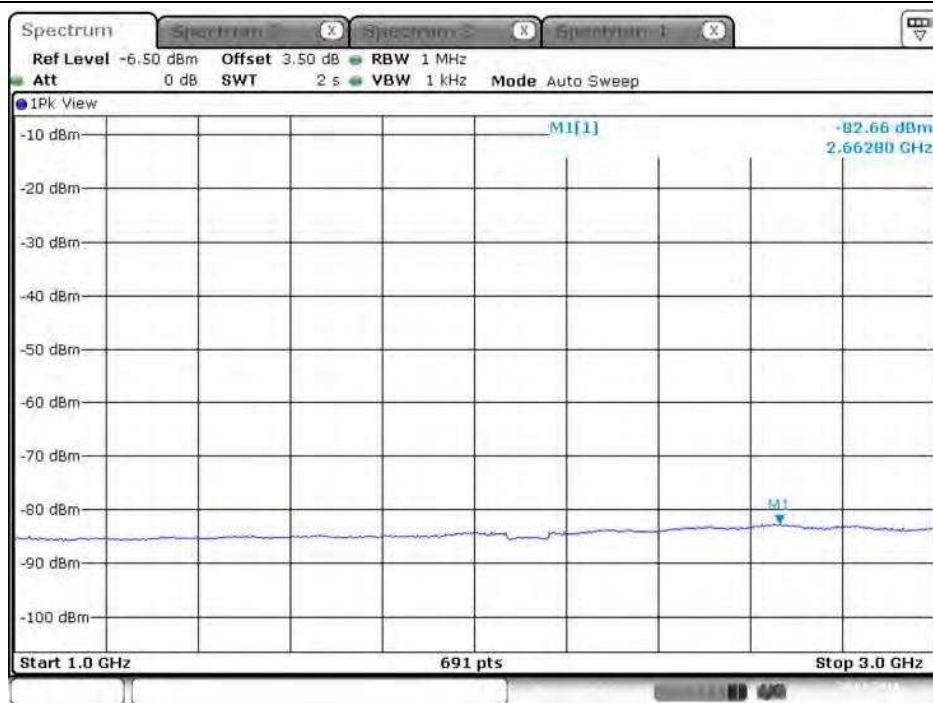
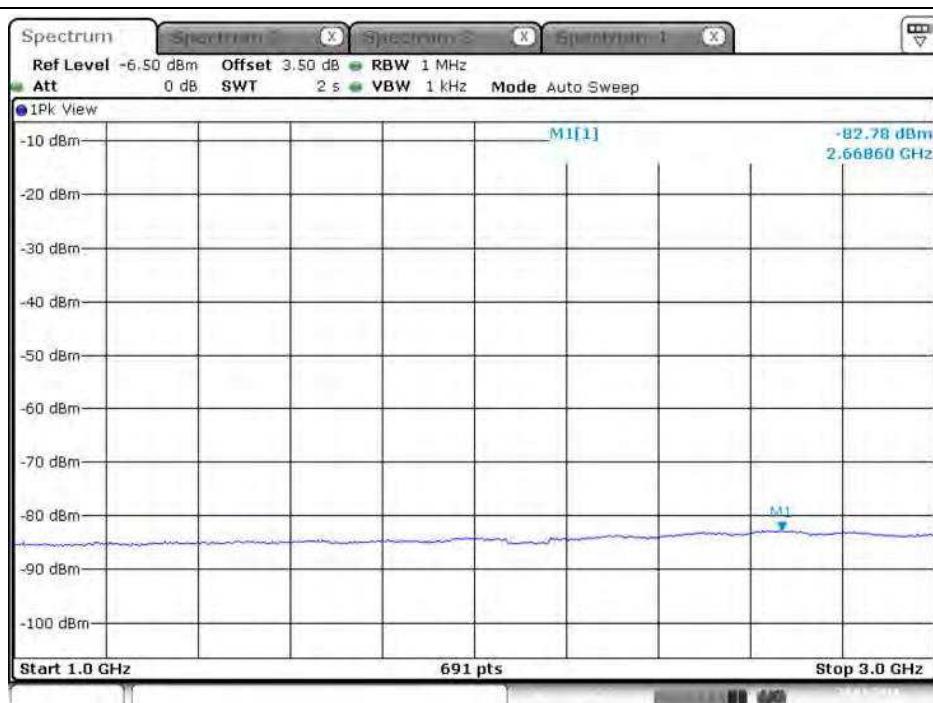


Date: 24.JAN.2018 03:55:38

### Plot on Configuration QPSK, 40M / 5700 MHz / Peak / Port 2 / 1GHz~3GHz

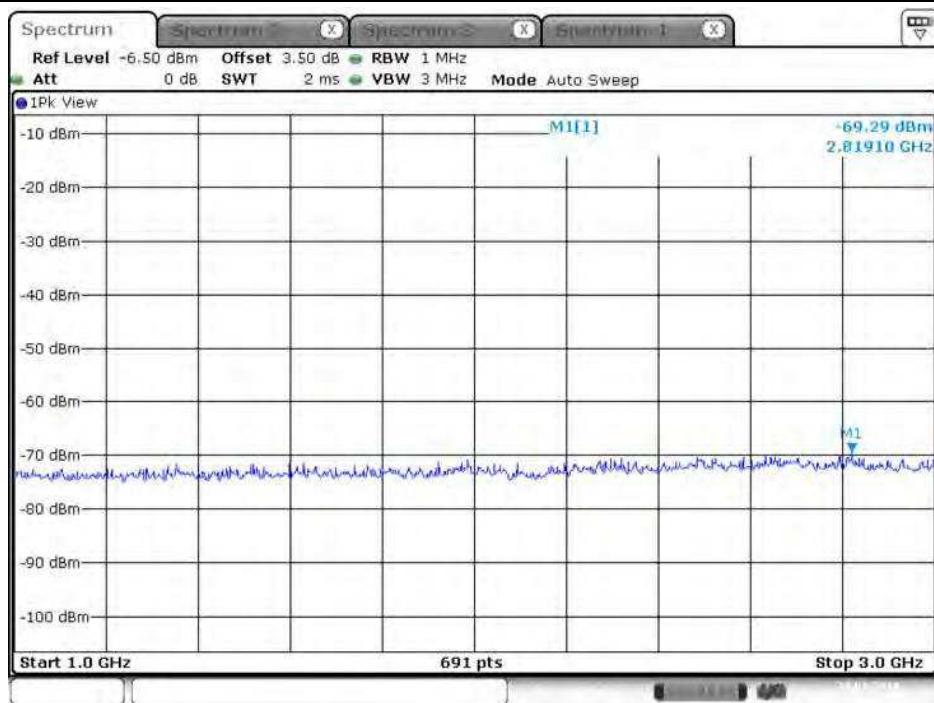


Date: 24.JAN.2018 03:49:36

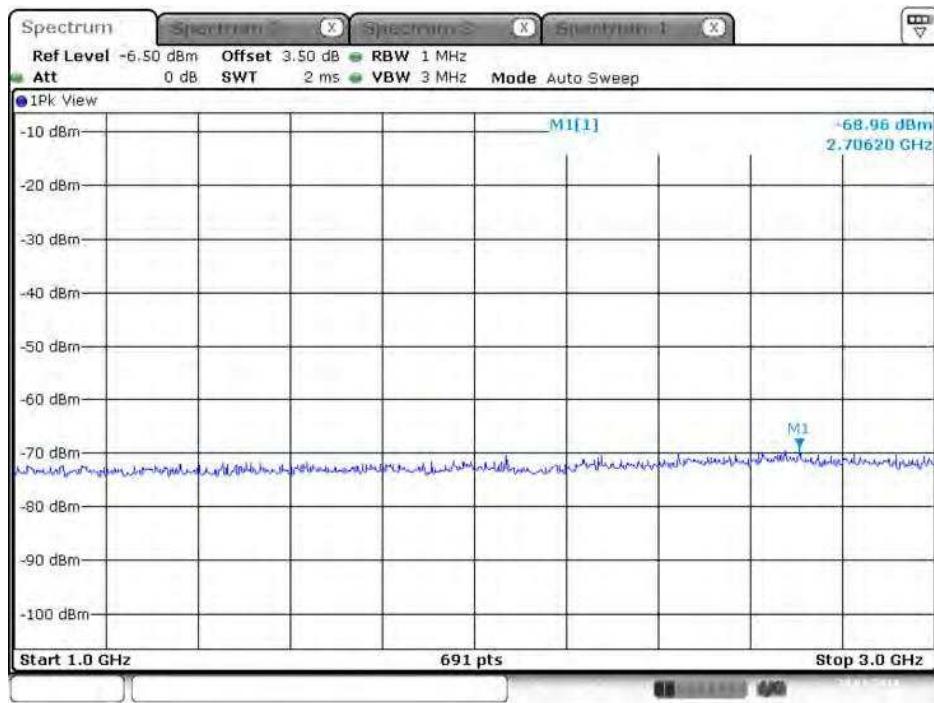
**Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Average / Port 1 / 1GHz~3GHz**

**Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Average / Port 2 / 1GHz~3GHz**




## Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Peak / Port 1 / 1GHz~3GHz



## Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Peak / Port 2 / 1GHz~3GHz



**Plot on Configuration QPSK, 10M / 5480 MHz / Average / Port 1 / 3GHz~6GHz**


Date: 24.JAN.2018 02:33:17

**Plot on Configuration QPSK, 10M / 5480 MHz / Average / Port 2 / 3GHz~6GHz**

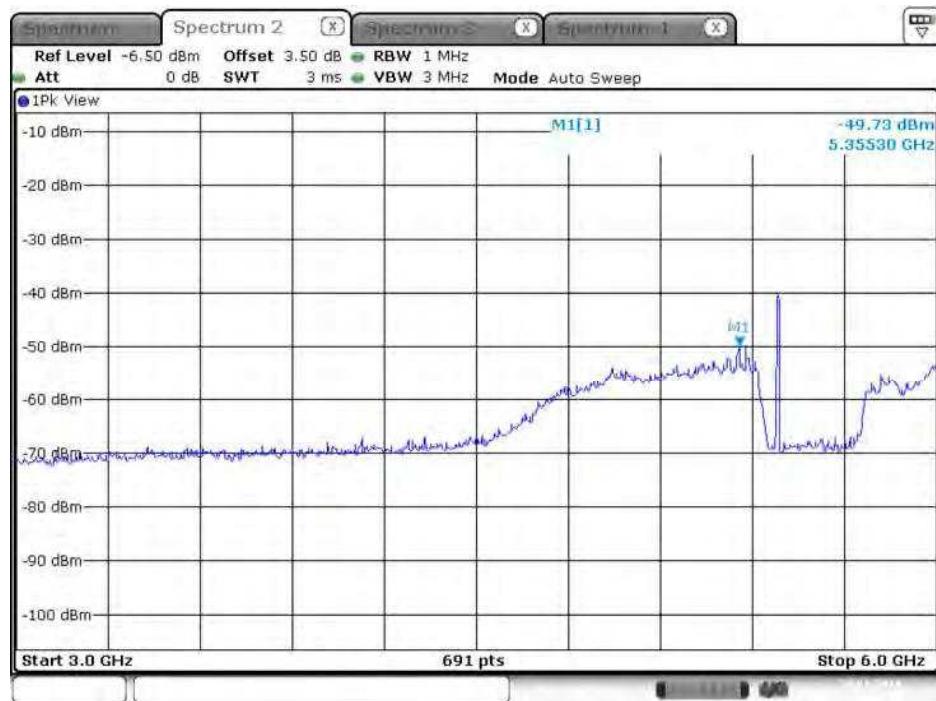

Date: 24.JAN.2018 02:35:57



## CSE TX above 1GHz Result

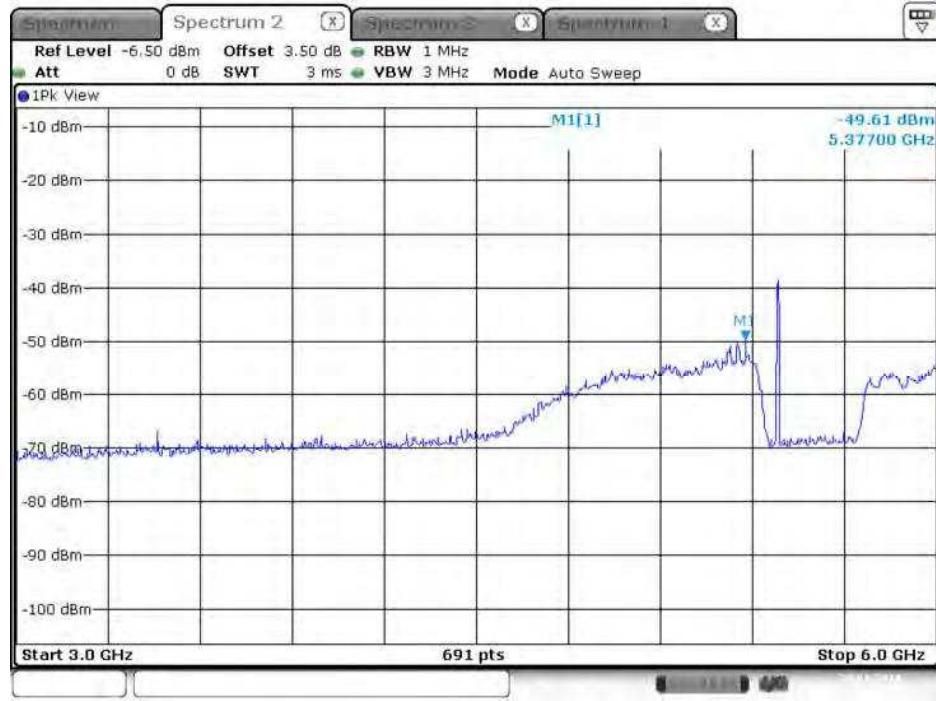
Appendix D.1

### Plot on Configuration QPSK, 10M / 5480 MHz / Peak / Port 1 / 3GHz~6GHz



Date: 24.JAN.2018 02:33:46

### Plot on Configuration QPSK, 10M / 5480 MHz / Peak / Port 2 / 3GHz~6GHz



Date: 24.JAN.2018 02:35:41

**Plot on Configuration QPSK, 10M / 5575 MHz / Average / Port 1 / 3GHz~6GHz**


Date: 24.JAN.2018 02:26:29

**Plot on Configuration QPSK, 10M / 5575 MHz / Average / Port 2 / 3GHz~6GHz**

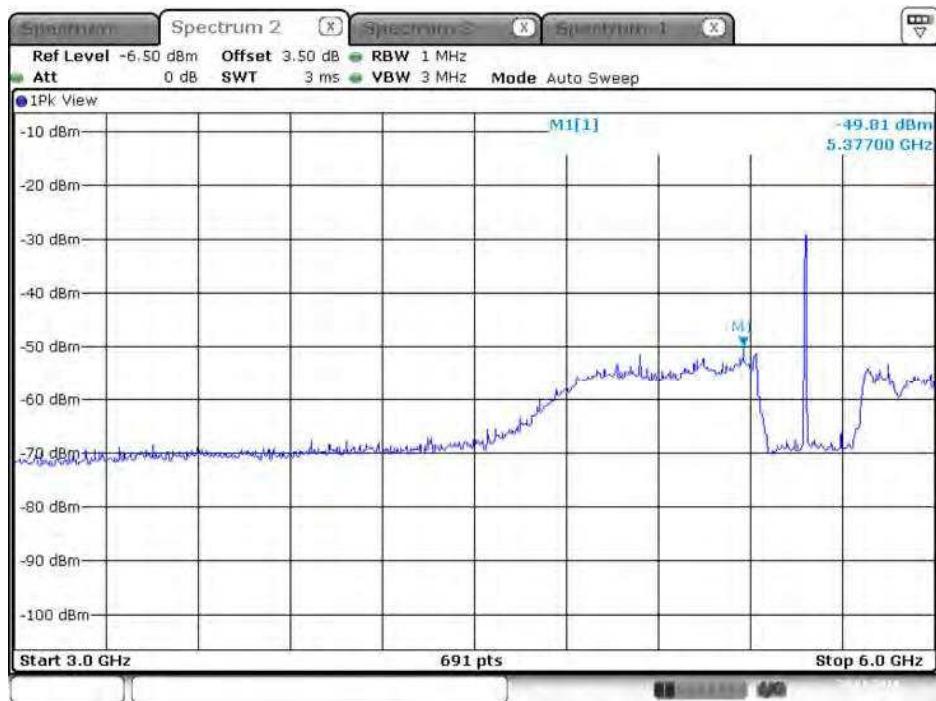

Date: 24.JAN.2018 02:23:28



## CSE TX above 1GHz Result

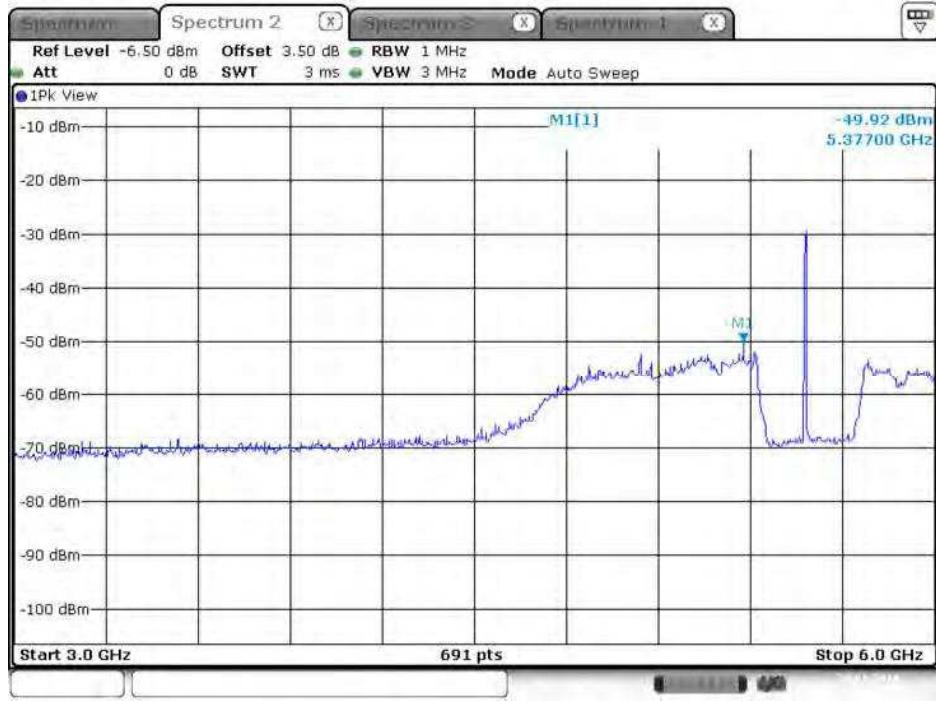
Appendix D.1

### Plot on Configuration QPSK, 10M / 5575 MHz / Peak / Port 1 / 3GHz~6GHz

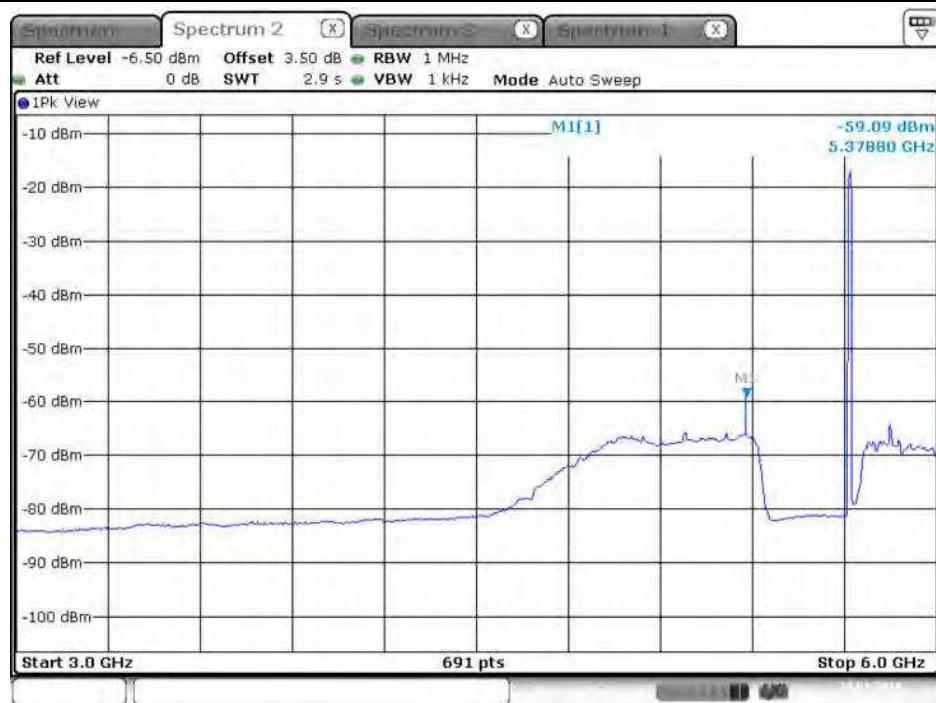


Date: 24.JAN.2018 02:26:09

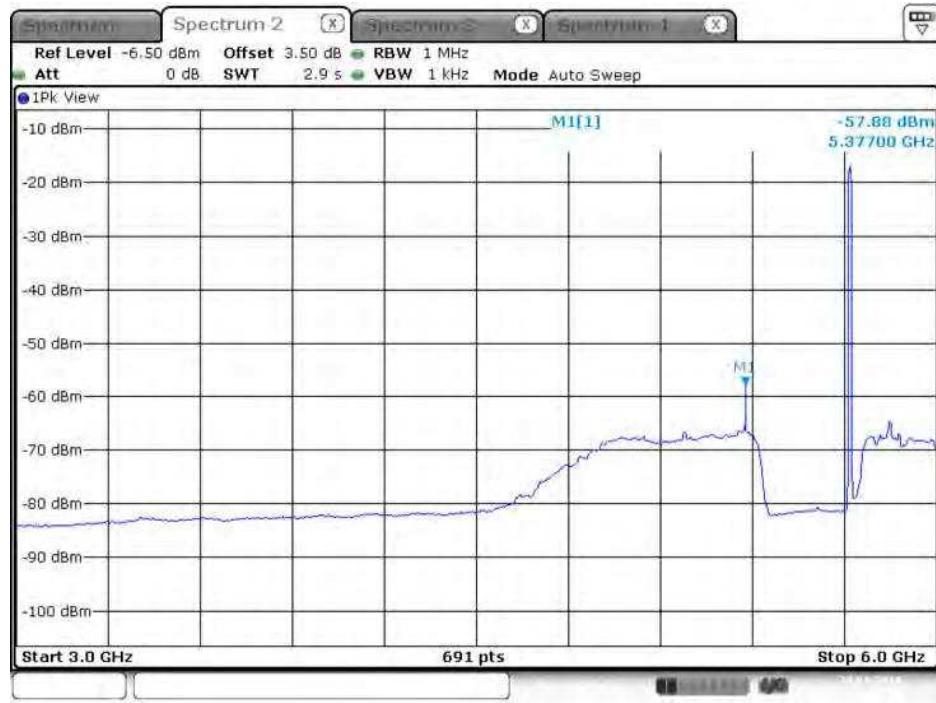
### Plot on Configuration QPSK, 10M / 5575 MHz / Peak / Port 2 / 3GHz~6GHz



Date: 24.JAN.2018 02:23:55

**Plot on Configuration QPSK, 10M / 5715 MHz / Average / Port 1 / 3GHz~6GHz**


Date: 24.JAN.2018 10:24:53

**Plot on Configuration QPSK, 10M / 5715 MHz / Average / Port 2 / 3GHz~6GHz**


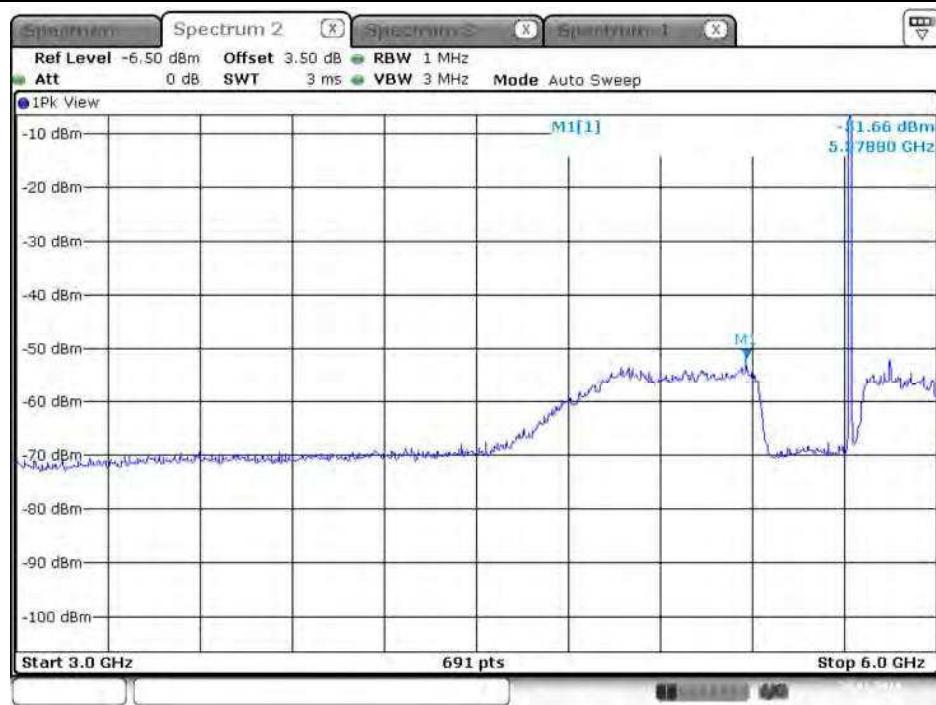
Date: 24.JAN.2018 10:50:03



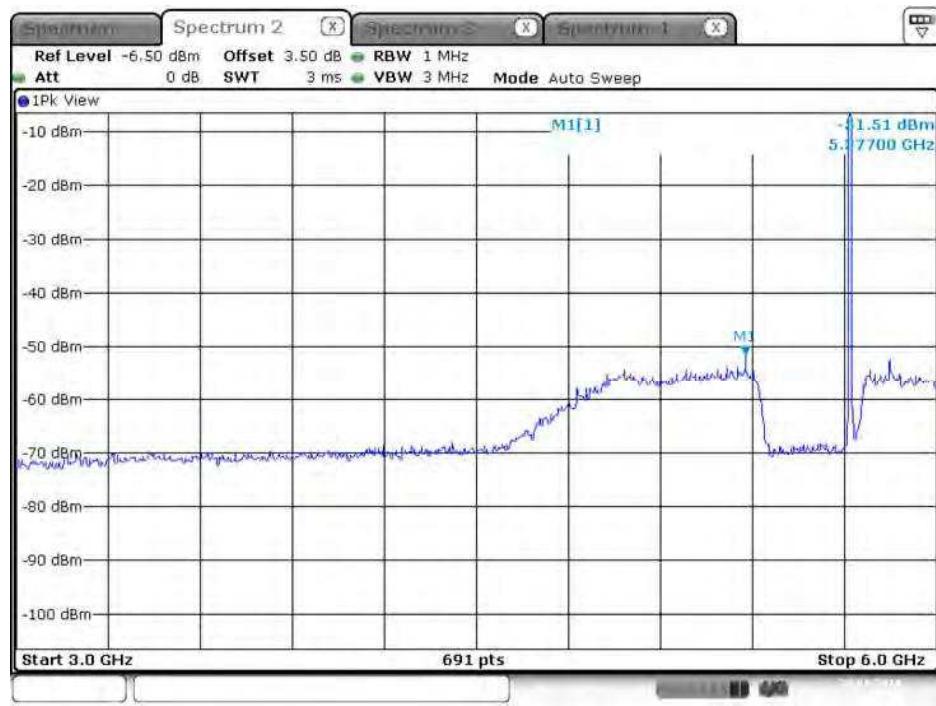
## CSE TX above 1GHz Result

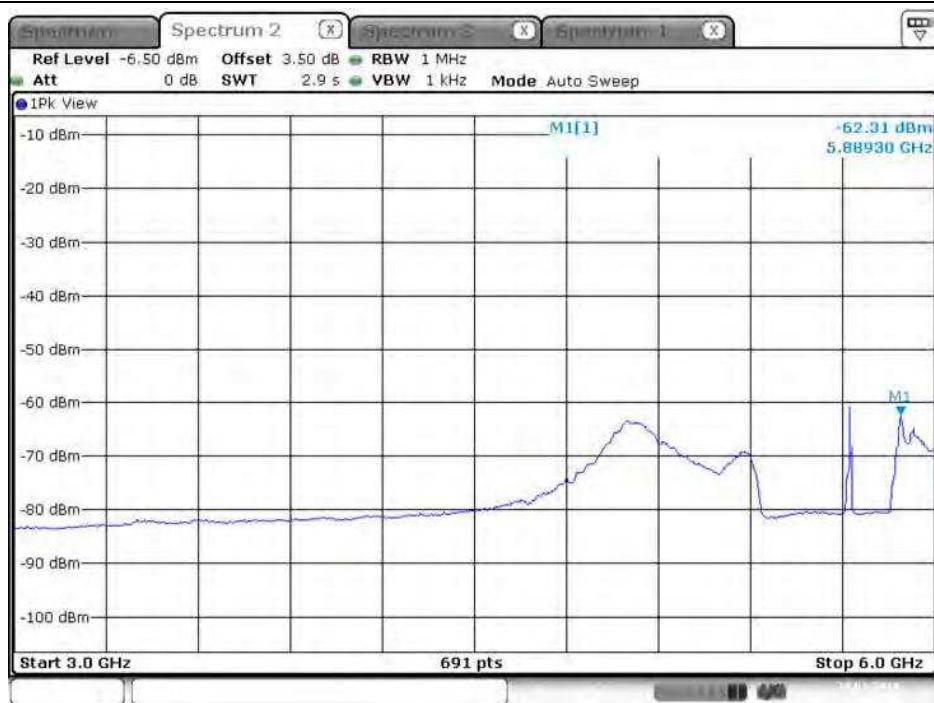
Appendix D.1

### Plot on Configuration QPSK, 10M / 5715 MHz / Peak / Port 1 / 3GHz~6GHz

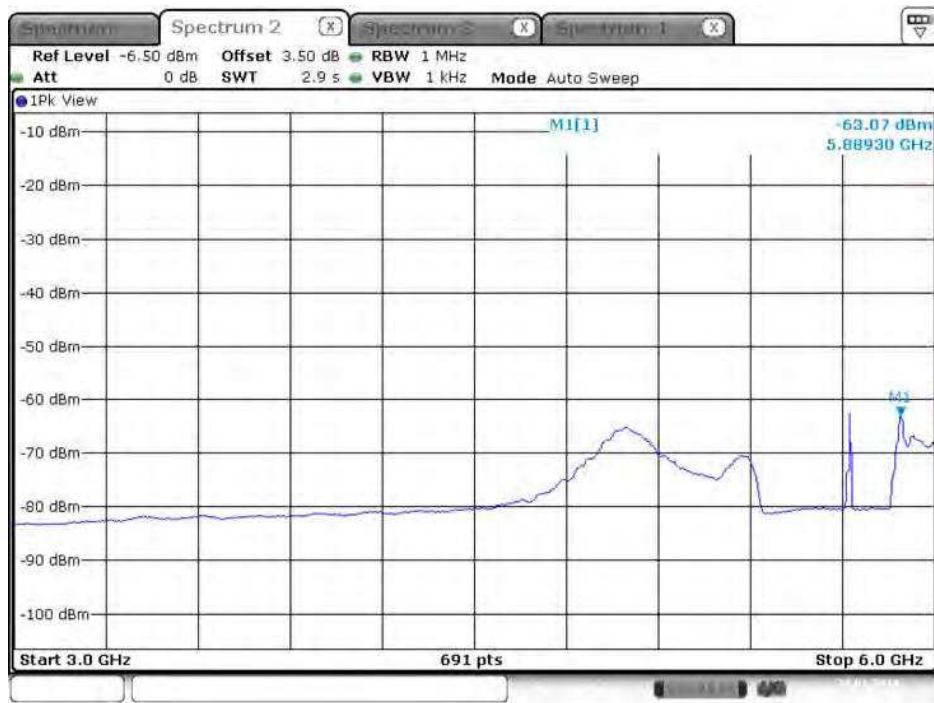


### Plot on Configuration QPSK, 10M / 5715 MHz / Peak / Port 2 / 3GHz~6GHz

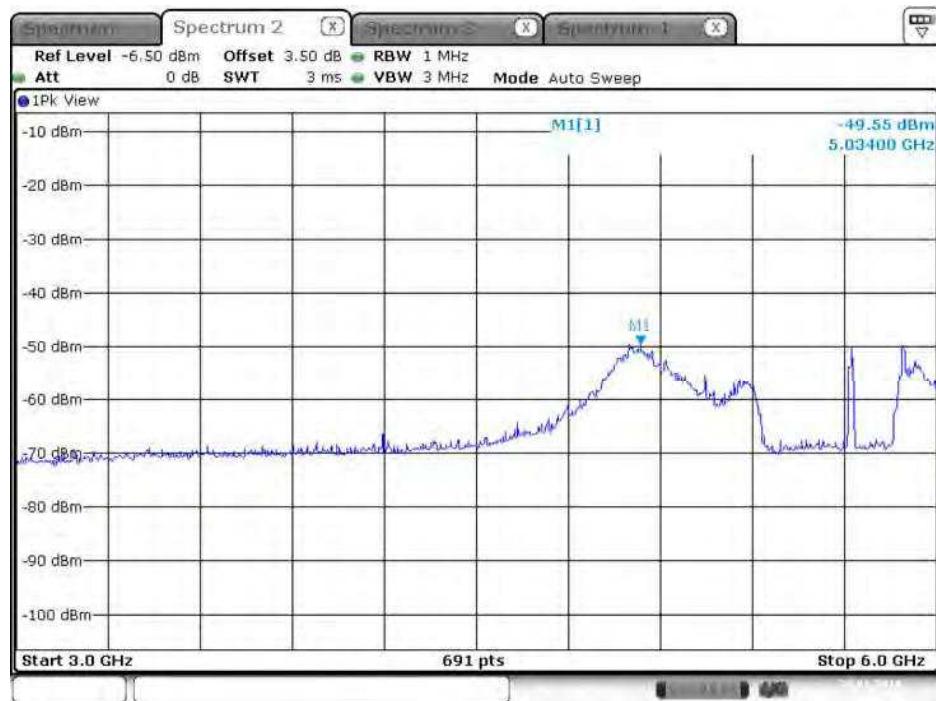


**Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Average / Port 1 / 3GHz~6GHz**


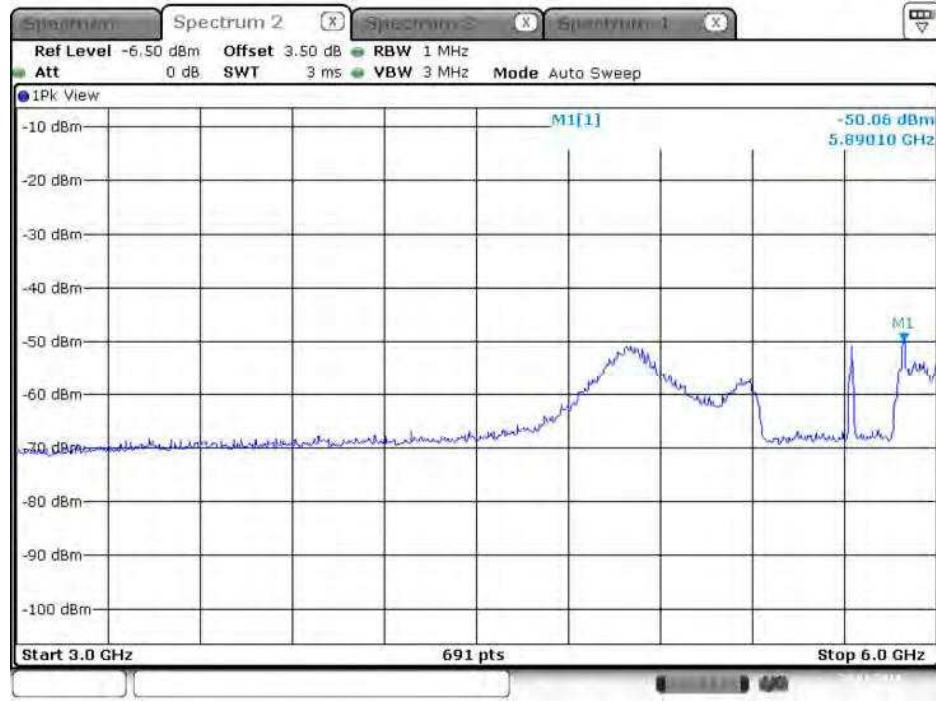
Date: 24.JAN.2018 02:05:56

**Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Average / Port 2 / 3GHz~6GHz**


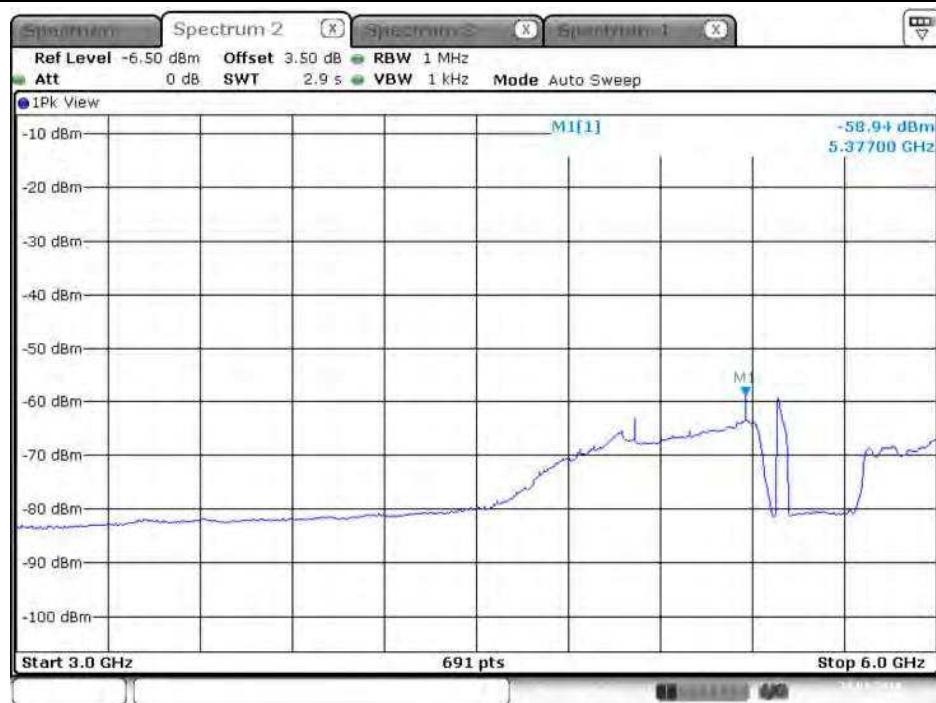
Date: 24.JAN.2018 02:01:28

**Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Peak / Port 1 / 3GHz~6GHz**


Date: 24.JAN.2018 02:07:06

**Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Peak / Port 2 / 3GHz~6GHz**


Date: 24.JAN.2018 01:57:01

**Plot on Configuration QPSK, 40M / 5495 MHz / Average / Port 1 / 3GHz~6GHz**


Date: 24.JAN.2018 03:11:57

**Plot on Configuration QPSK, 40M / 5495 MHz / Average / Port 2 / 3GHz~6GHz**

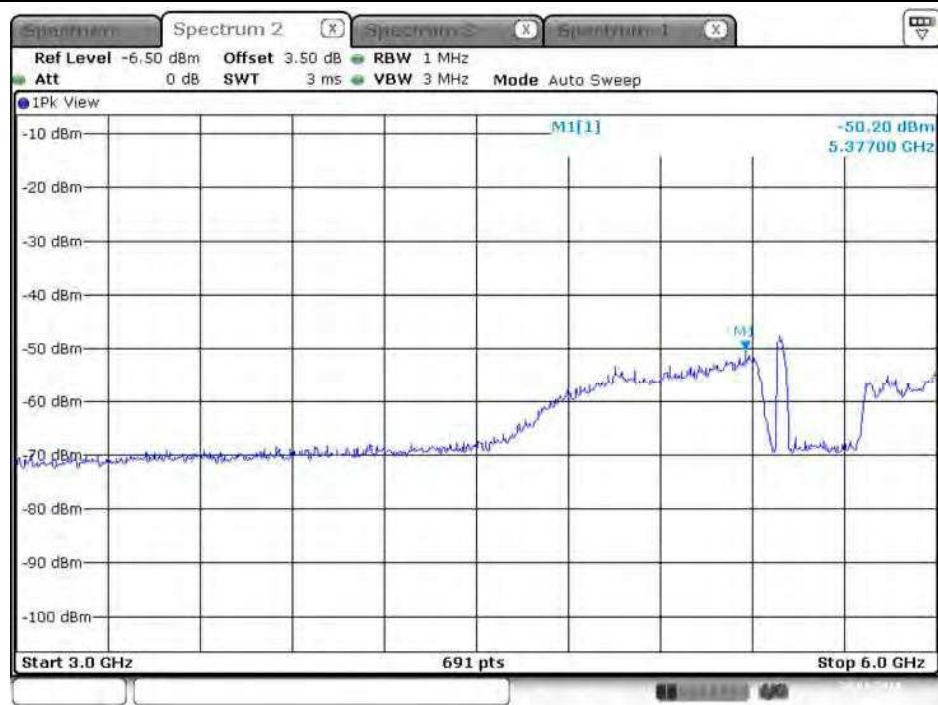

Date: 24.JAN.2018 03:05:09



## CSE TX above 1GHz Result

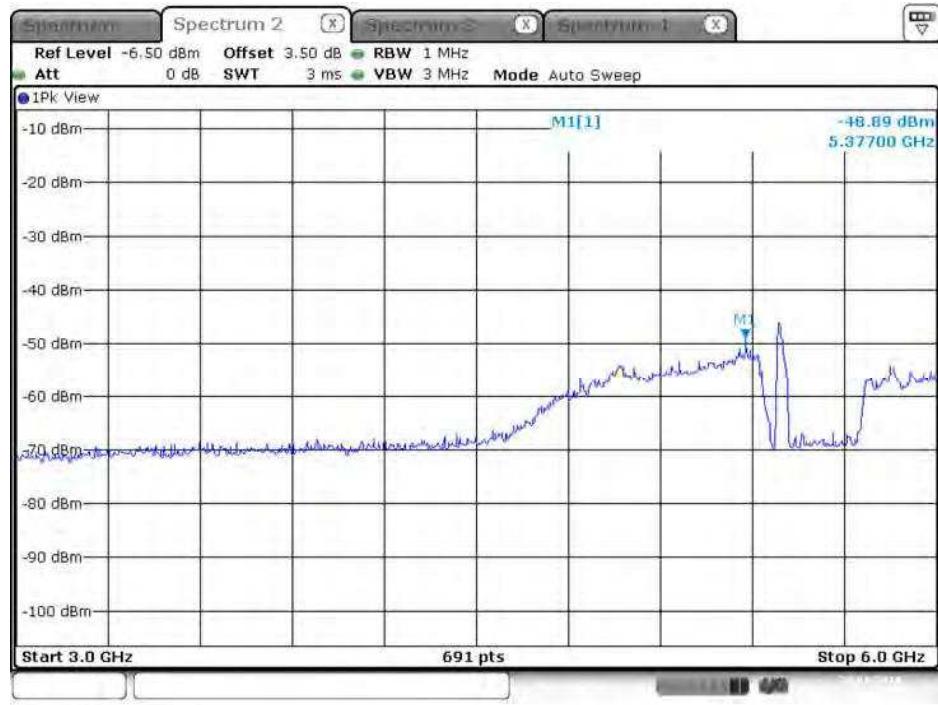
Appendix D.1

### Plot on Configuration QPSK, 40M / 5495 MHz / Peak / Port 1 / 3GHz~6GHz



Date: 24.JAN.2018 03:11:34

### Plot on Configuration QPSK, 40M / 5495 MHz / Peak / Port 2 / 3GHz~6GHz



Date: 24.JAN.2018 03:05:47

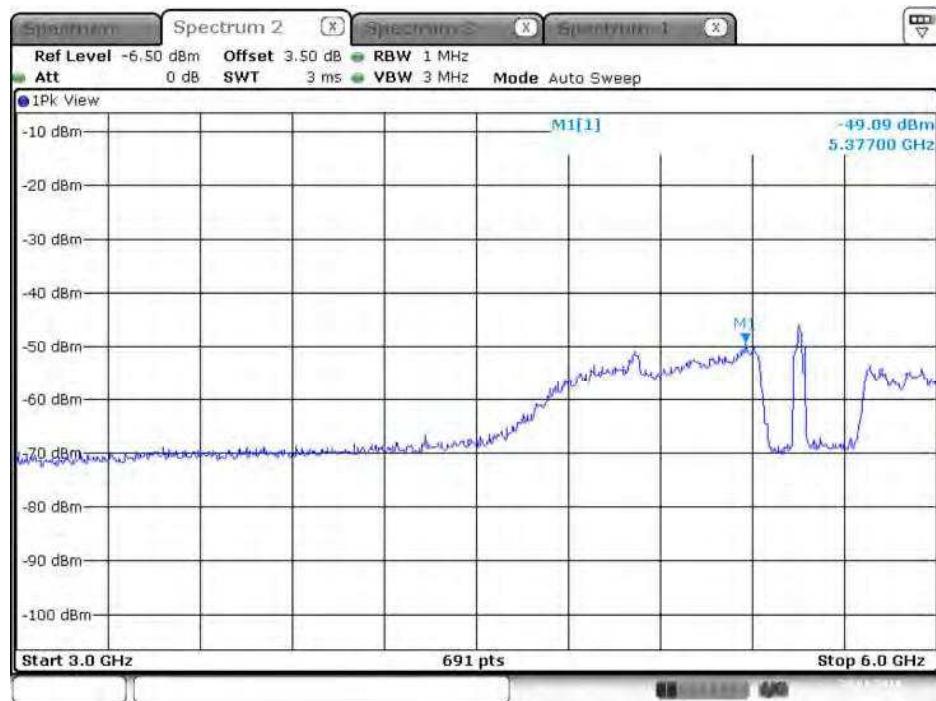


## Plot on Configuration QPSK, 40M / 5550 MHz / Average / Port 1 / 3GHz~6GHz

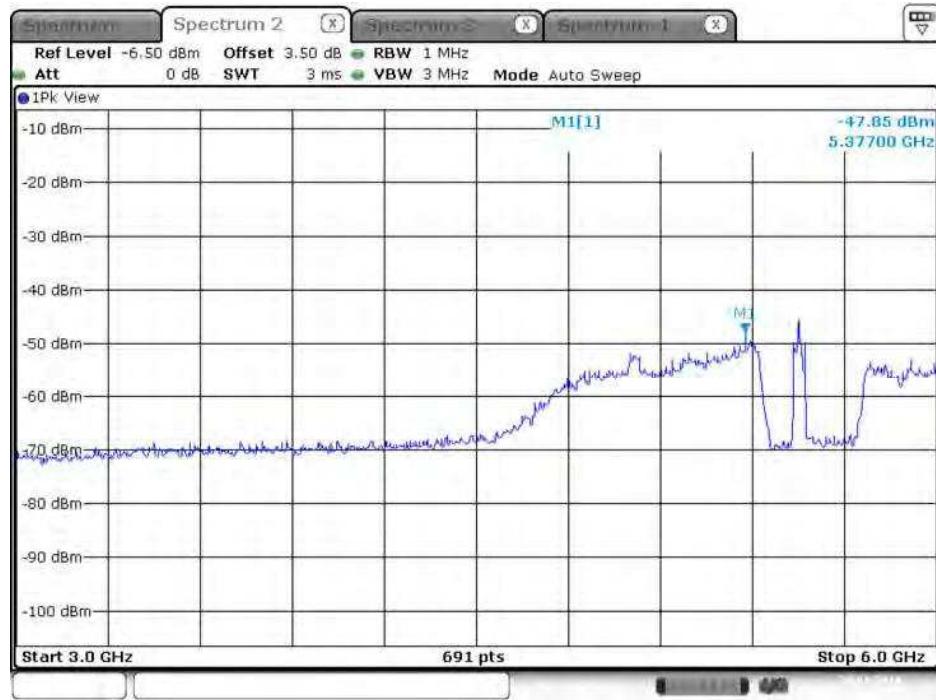


## Plot on Configuration QPSK, 40M / 5550 MHz / Average / Port 2 / 3GHz~6GHz

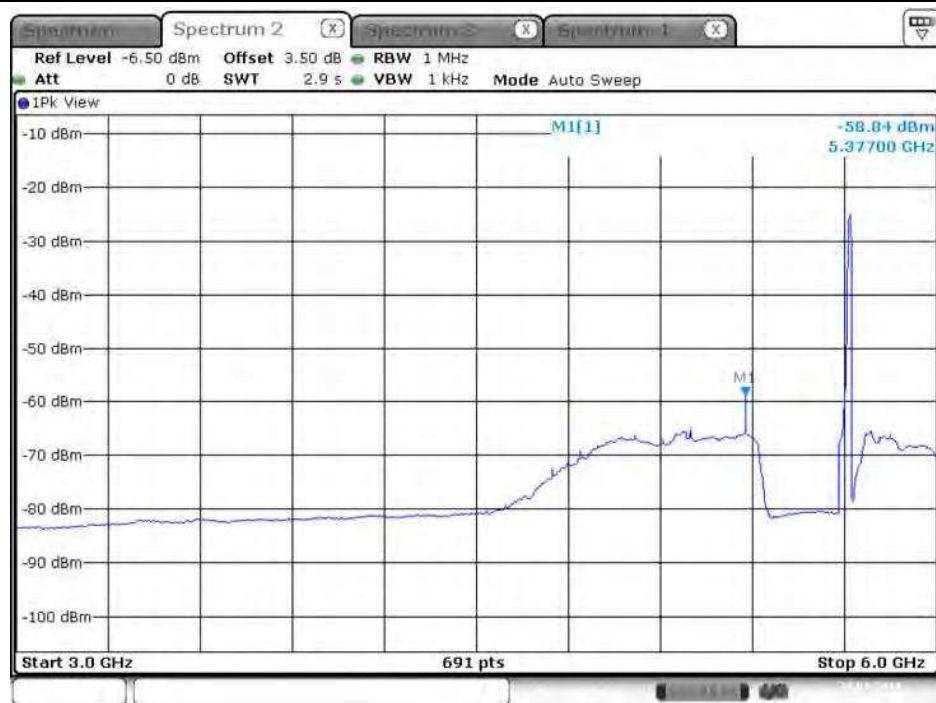


**Plot on Configuration QPSK, 40M / 5550 MHz / Peak / Port 1 / 3GHz~6GHz**


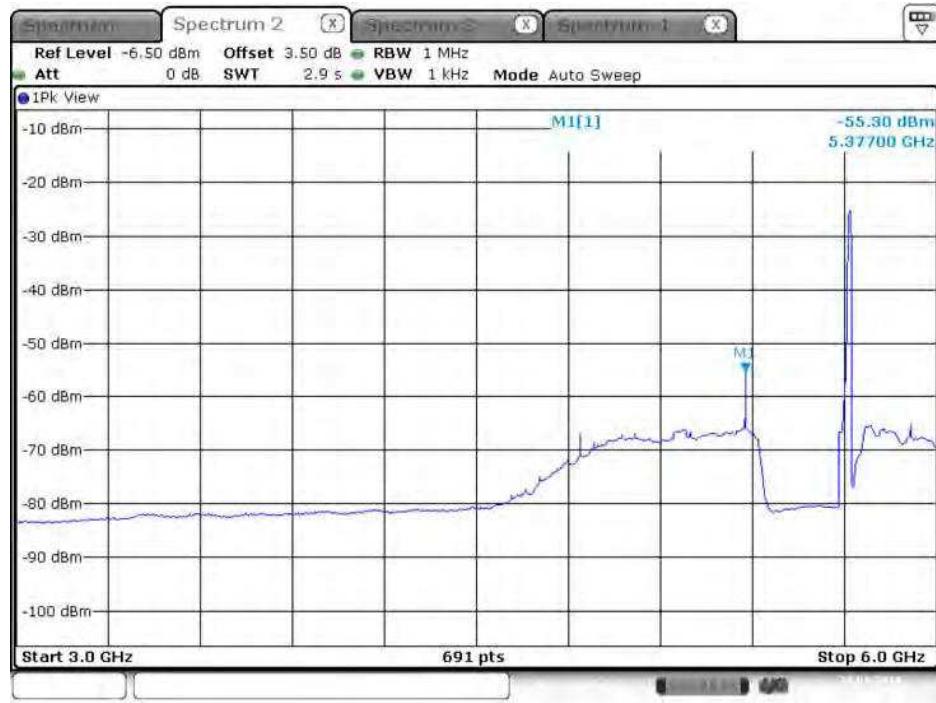
Date: 24.JAN.2018 03:29:43

**Plot on Configuration QPSK, 40M / 5550 MHz / Peak / Port 2 / 3GHz~6GHz**


Date: 24.JAN.2018 03:34:24

**Plot on Configuration QPSK, 40M / 5700 MHz / Average / Port 1 / 3GHz~6GHz**


Date: 24.JAN.2018 03:54:46

**Plot on Configuration QPSK, 40M / 5700 MHz / Average / Port 2 / 3GHz~6GHz**


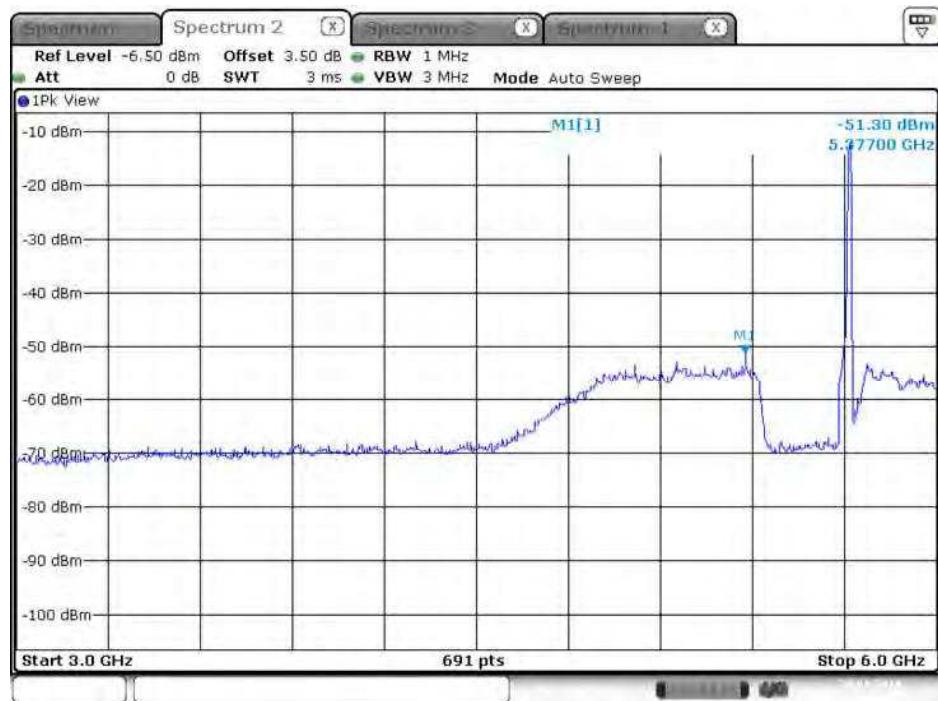
Date: 24.JAN.2018 03:50:39



## CSE TX above 1GHz Result

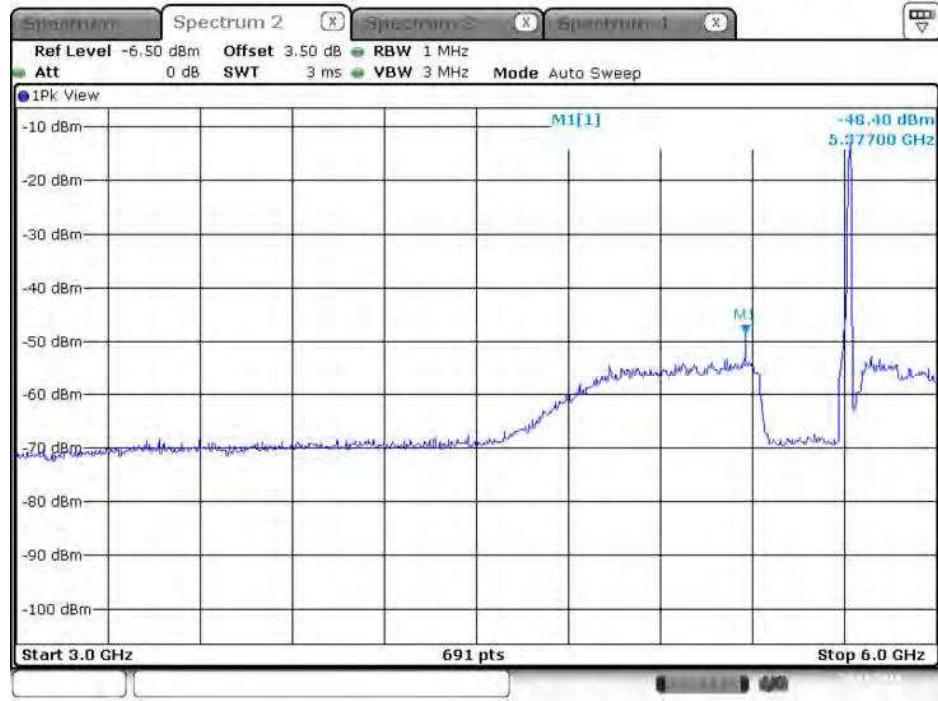
Appendix D.1

### Plot on Configuration QPSK, 40M / 5700 MHz / Peak / Port 1 / 3GHz~6GHz

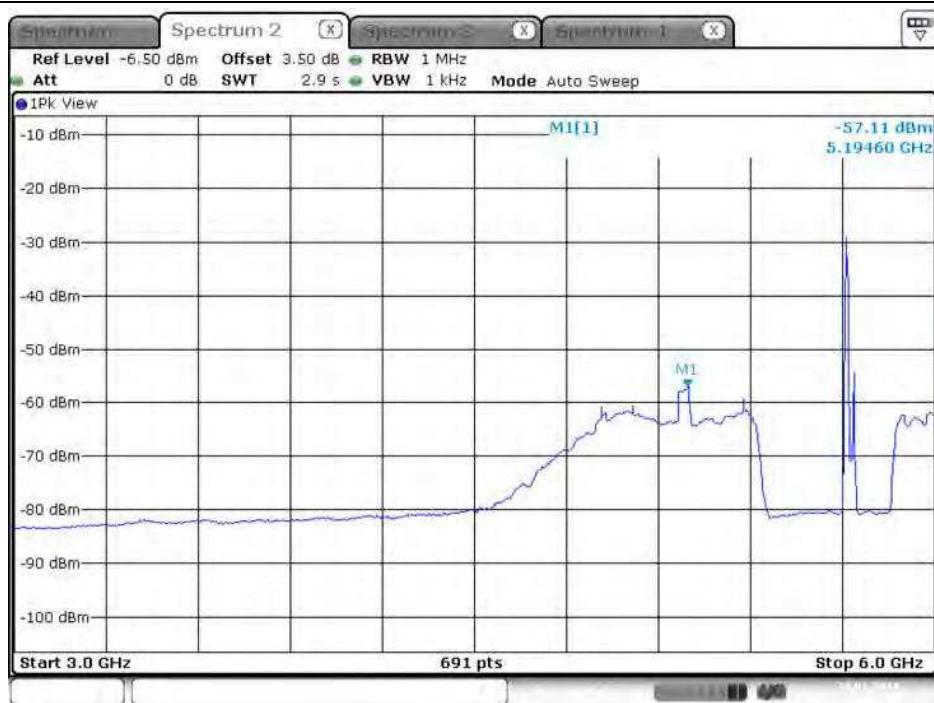


Date: 24.JAN.2018 03:54:15

### Plot on Configuration QPSK, 40M / 5700MHz / Peak / Port 2 / 3GHz~6GHz



Date: 24.JAN.2018 03:51:07

**Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Average / Port 1 / 3GHz~6GHz**


Date: 24.JAN.2018 01:44:48

**Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Average / Port 2 / 3GHz~6GHz**

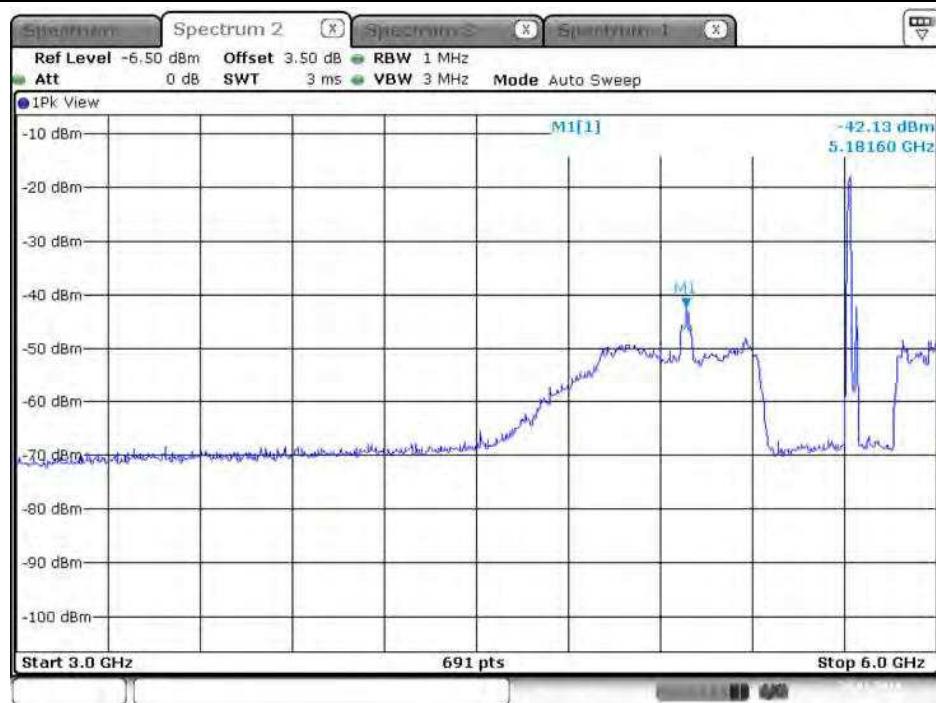

Date: 24.JAN.2018 01:47:55



## CSE TX above 1GHz Result

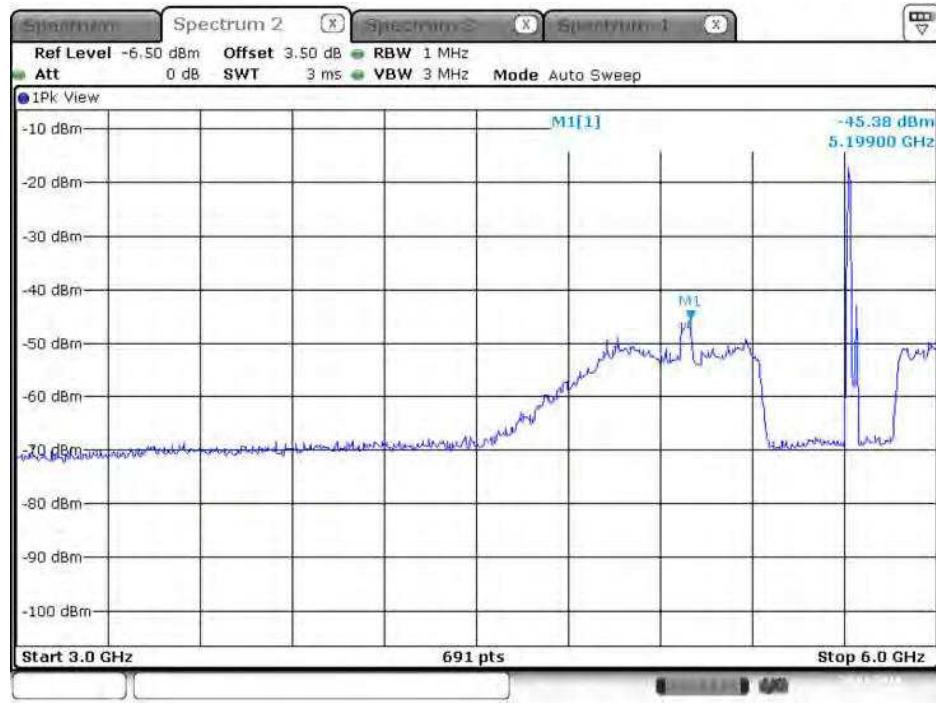
Appendix D.1

### Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Peak / Port 1 / 3GHz~6GHz



Date: 24.JAN.2018 01:43:07

### Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Peak / Port 2 / 3GHz~6GHz



Date: 24.JAN.2018 01:48:27