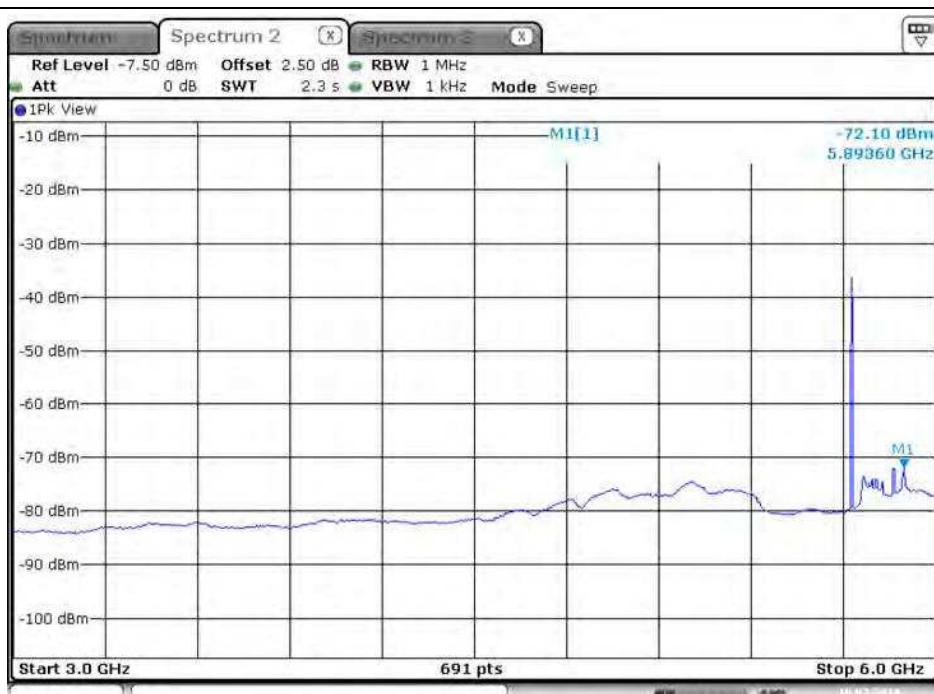


**Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Average / Port 1 / 3GHz~6GHz**

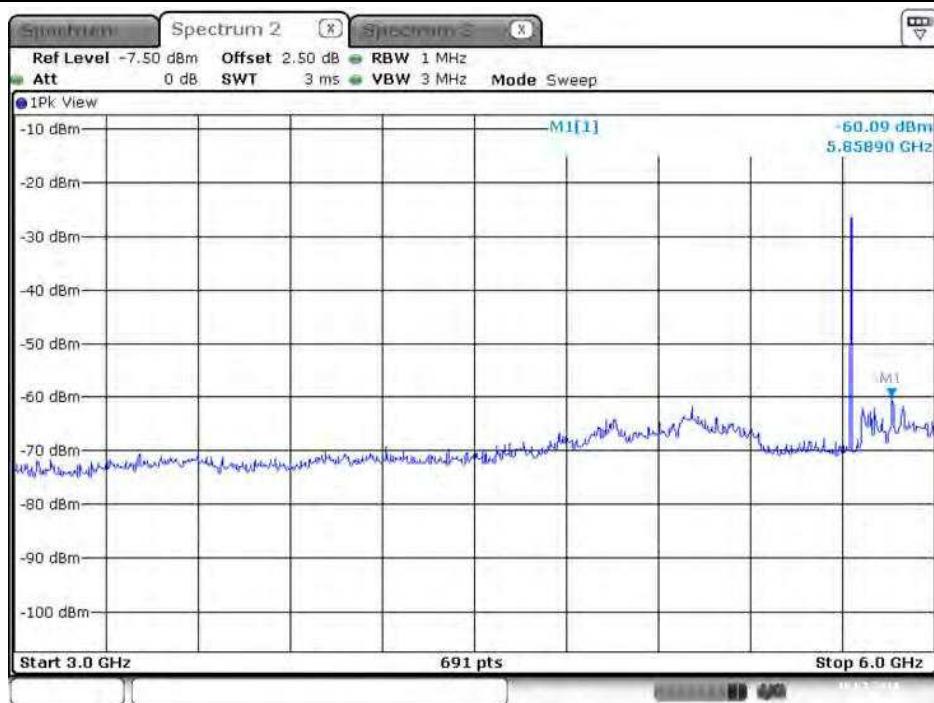
**Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Average / Port 2 / 3GHz~6GHz**




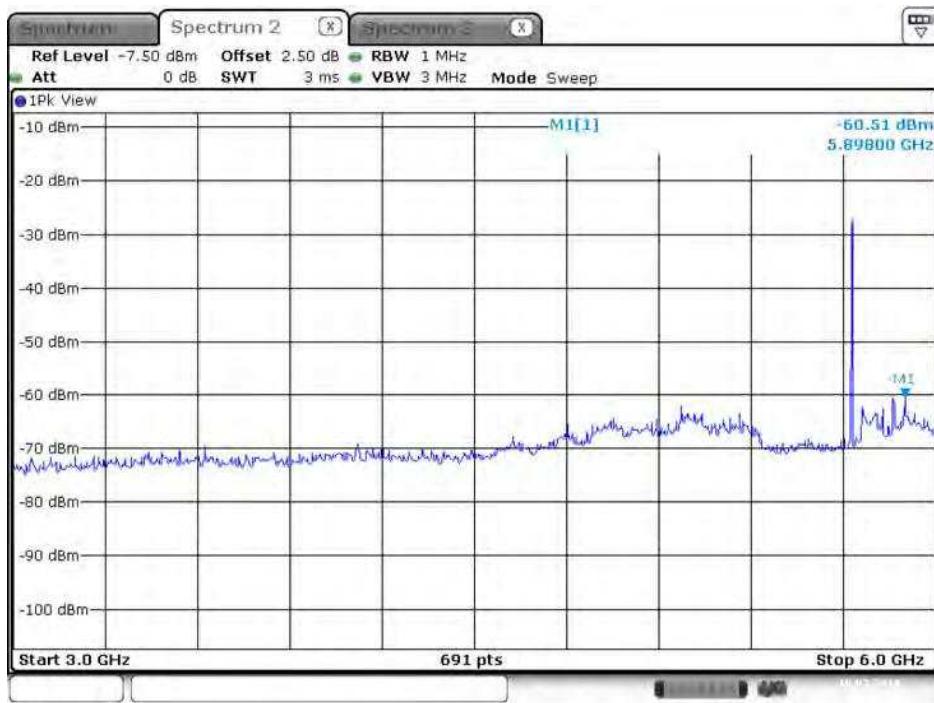
## CSE TX above 1GHz Result

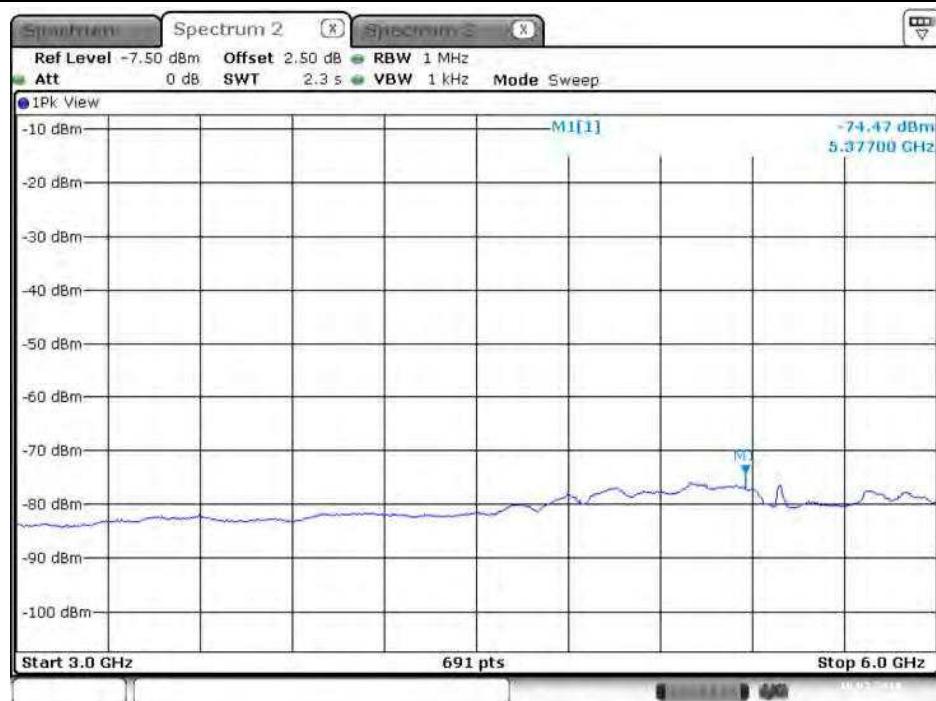
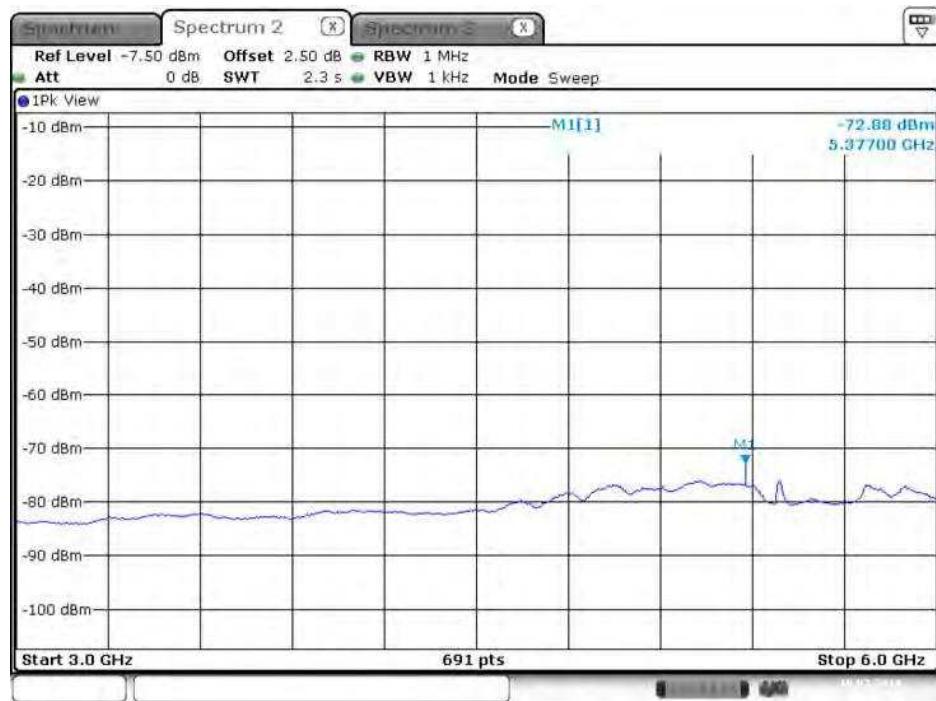
Appendix D.1

### Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Peak / Port 1 / 3GHz~6GHz



### Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Peak / Port 2 / 3GHz~6GHz



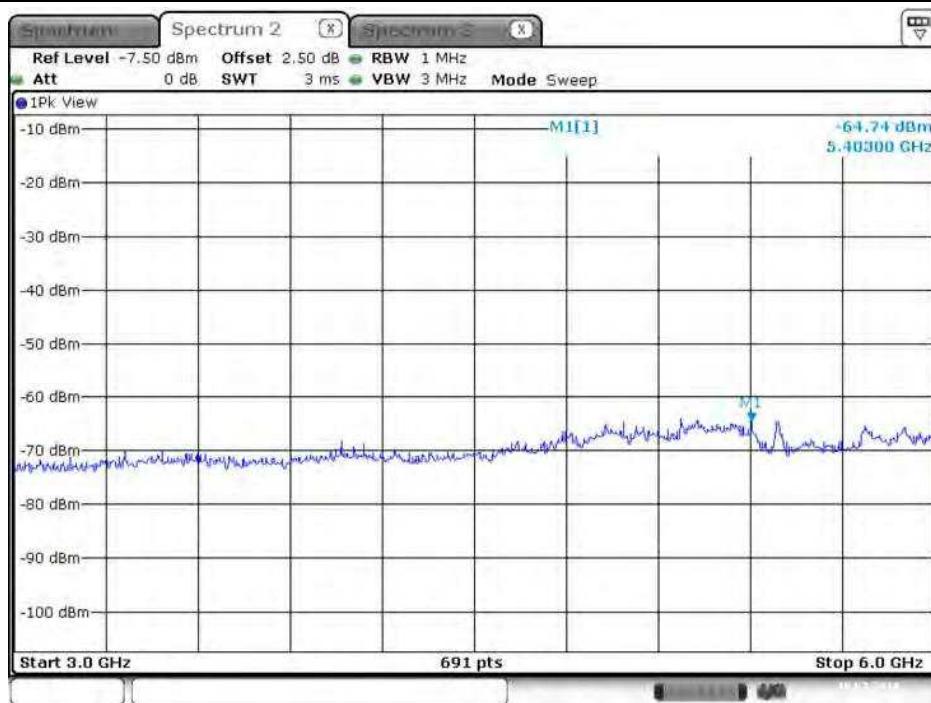
**Plot on Configuration QPSK, 40M / 5495 MHz / Average / Port 1 / 3GHz~6GHz**

**Plot on Configuration QPSK, 40M / 5495 MHz / Average / Port 2 / 3GHz~6GHz**




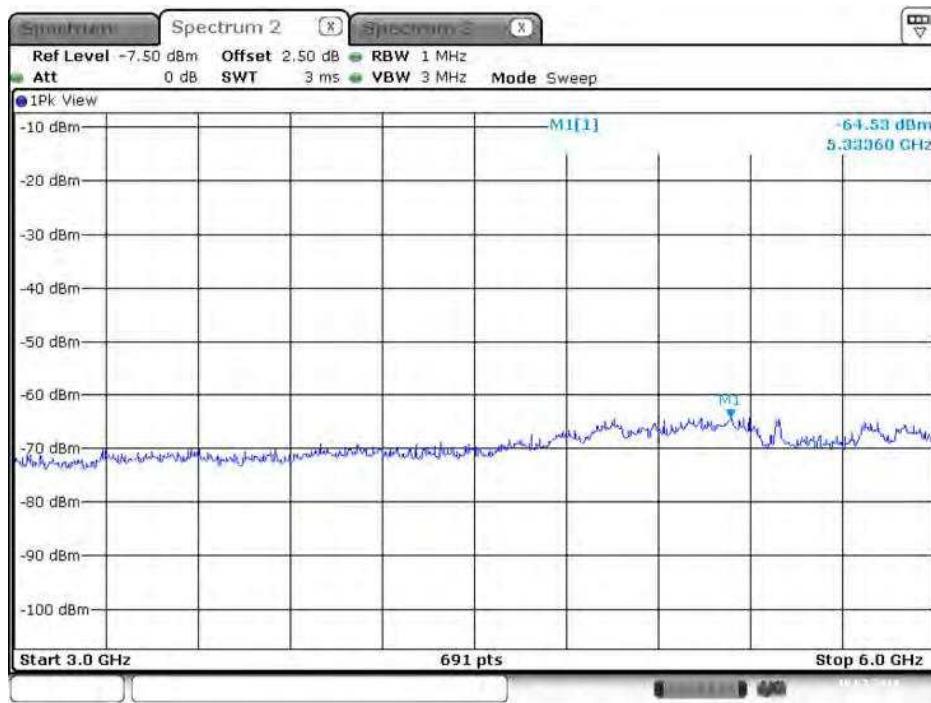
## CSE TX above 1GHz Result

Appendix D.1

### Plot on Configuration QPSK, 40M / 5495 MHz / Peak / Port 1 / 3GHz~6GHz



### Plot on Configuration QPSK, 40M / 5495 MHz / Peak / Port 2 / 3GHz~6GHz





## CSE TX above 1GHz Result

Appendix D.1

### Plot on Configuration QPSK, 40M / 5550 MHz / Average / Port 1 / 3GHz~6GHz

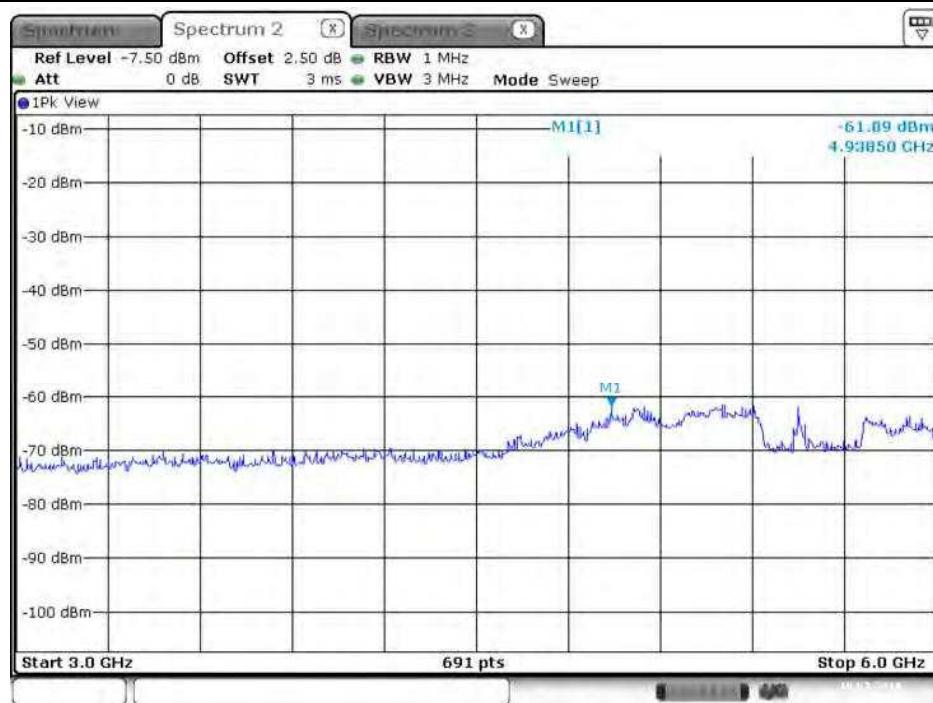


### Plot on Configuration QPSK, 40M / 5550 MHz / Average / Port 2 / 3GHz~6GHz

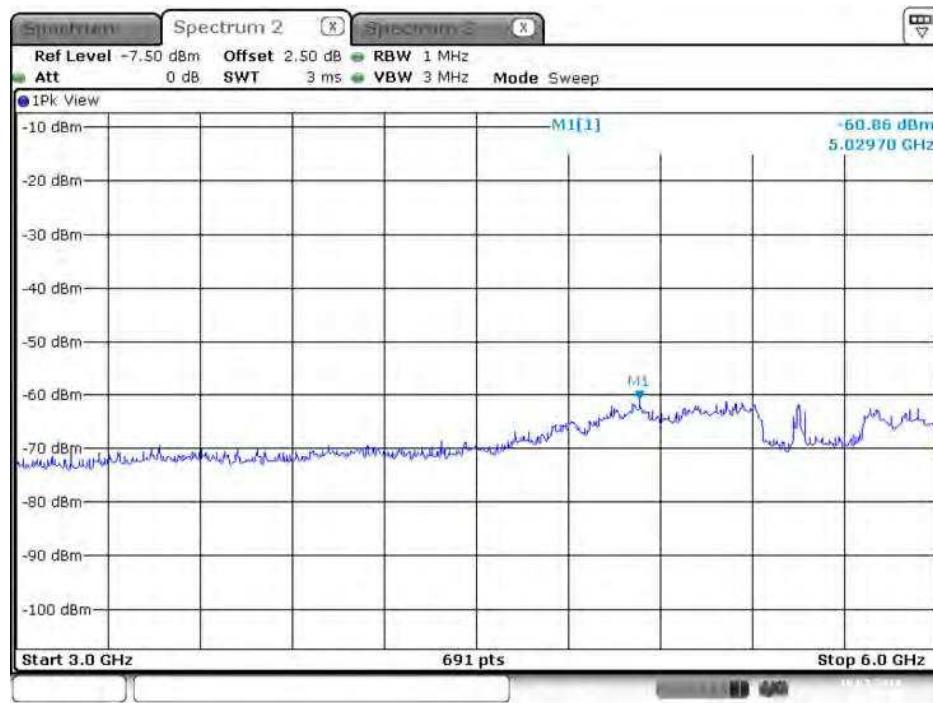




## Plot on Configuration QPSK, 40M / 5550 MHz / Peak / Port 1 / 3GHz~6GHz



## Plot on Configuration QPSK, 40M / 5550 MHz / Peak / Port 2 / 3GHz~6GHz





## Plot on Configuration QPSK, 40M / 5700 MHz / Average / Port 1 / 3GHz~6GHz

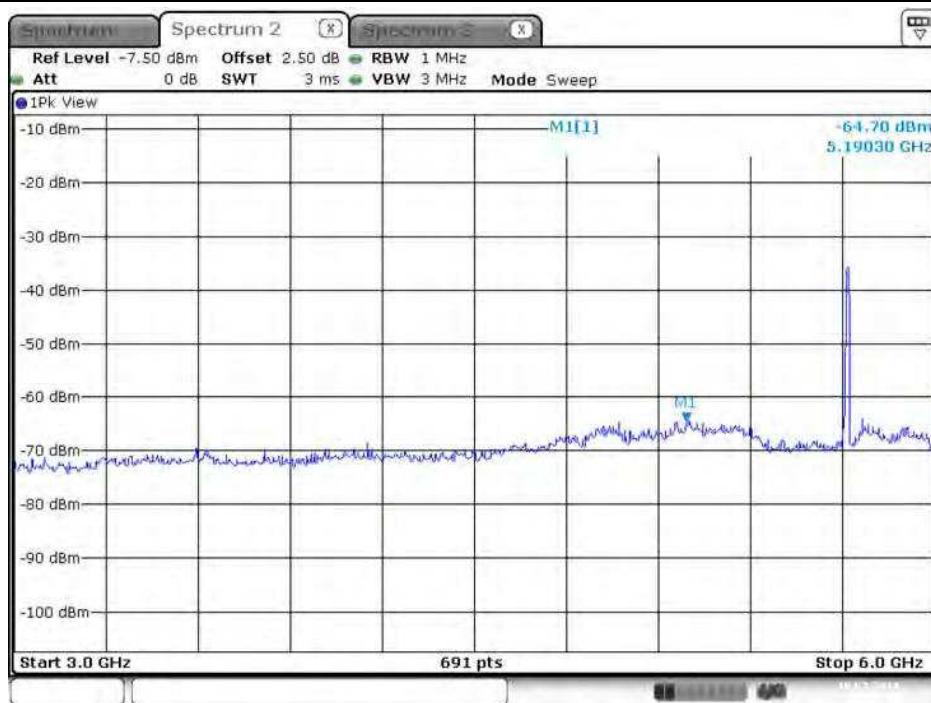
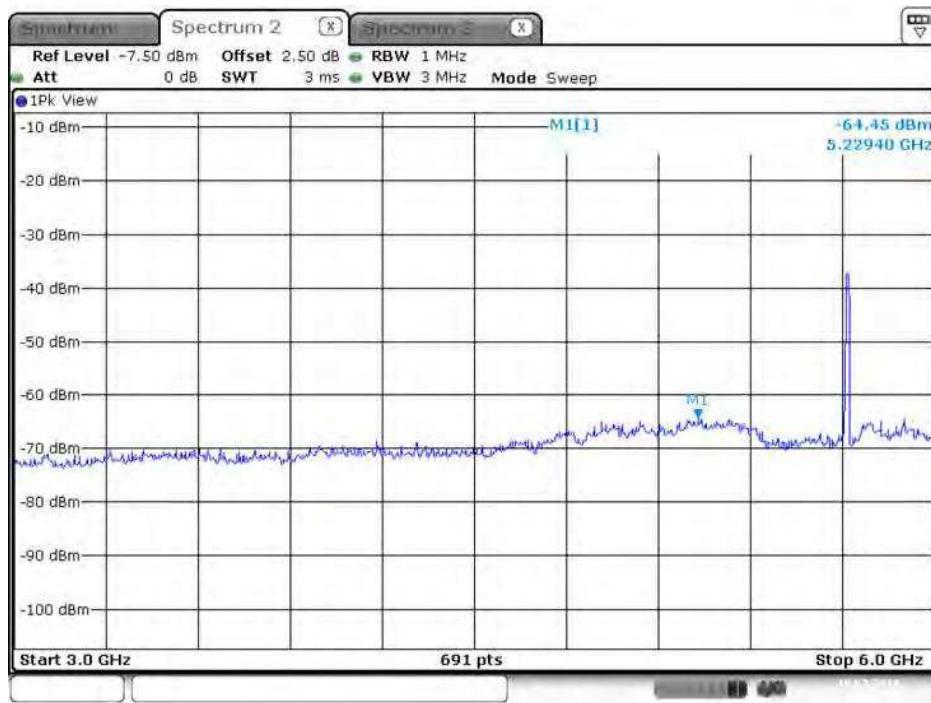


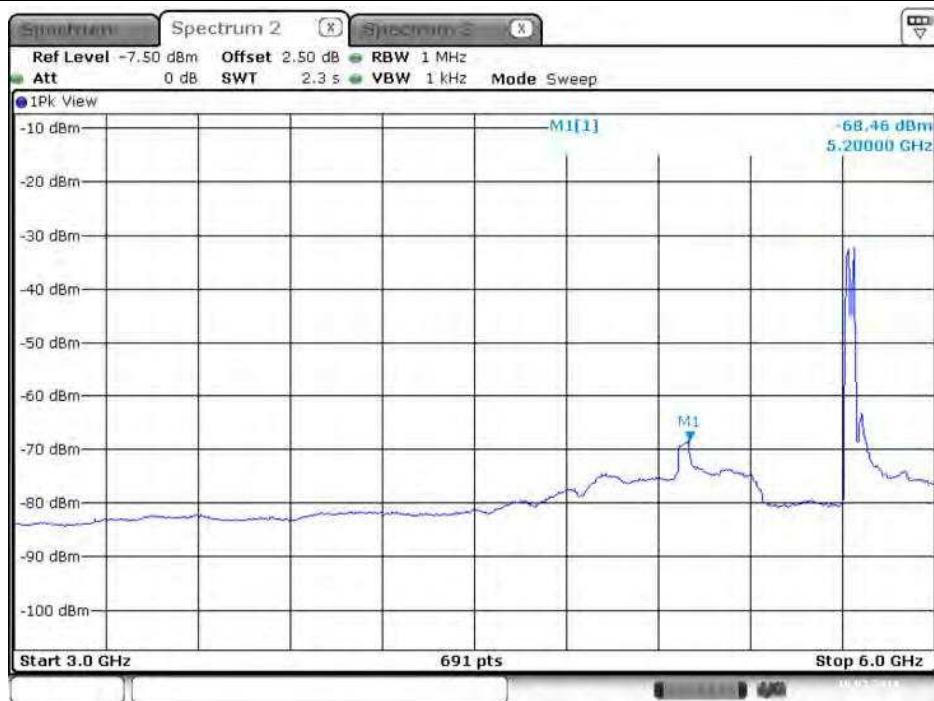
Date: 10.FEB.2018 12:31:41

## Plot on Configuration QPSK, 40M / 5700 MHz / Average / Port 2 / 3GHz~6GHz



Date: 10.FEB.2018 12:29:48

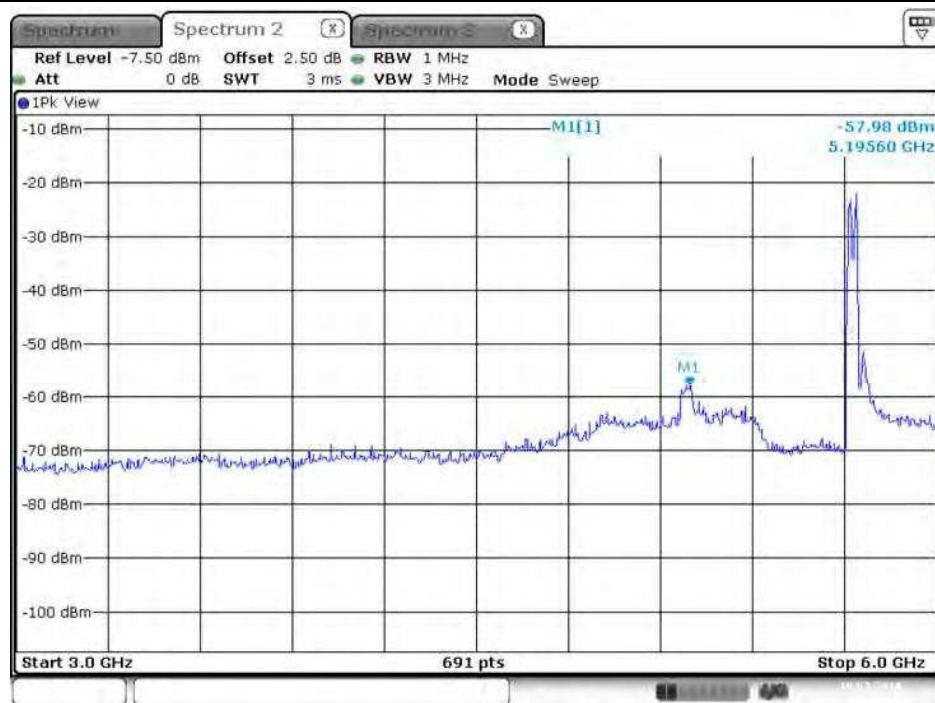
**Plot on Configuration QPSK, 40M / 5700 MHz / Peak / Port 1 / 3GHz~6GHz**

**Plot on Configuration QPSK, 40M / 5700MHz / Peak / Port 2 / 3GHz~6GHz**


**Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Average / Port 1 / 3GHz~6GHz**


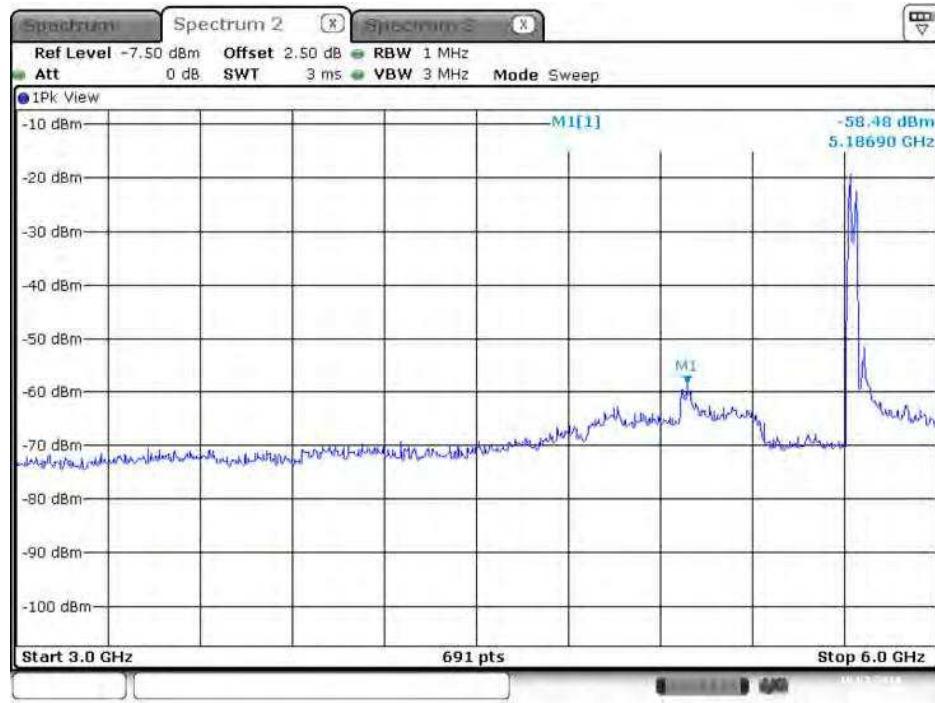
Date: 10.FEB.2018 12:36:39

**Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Average / Port 2 / 3GHz~6GHz**

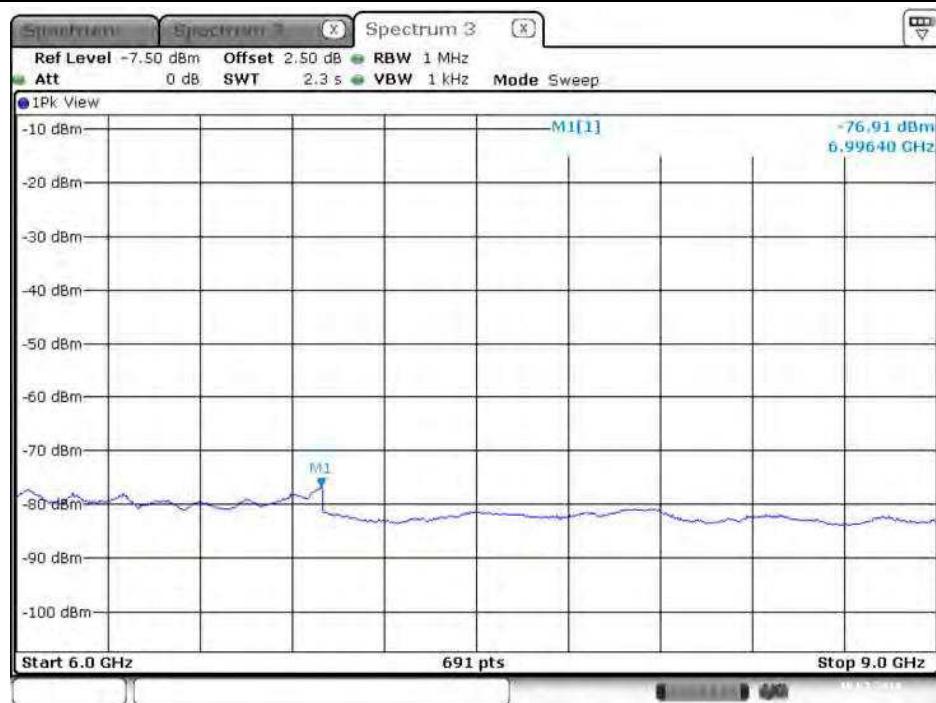

Date: 10.FEB.2018 12:39:05

**Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Peak / Port 1 / 3GHz~6GHz**


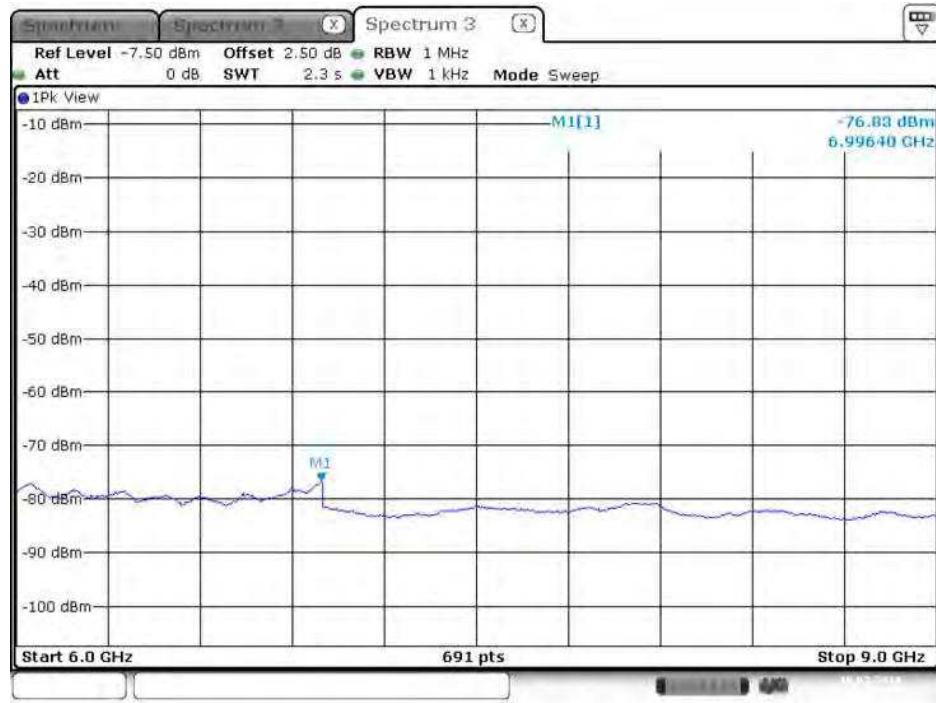
Date: 10.FEB.2018 12:36:54

**Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Peak / Port 2 / 3GHz~6GHz**


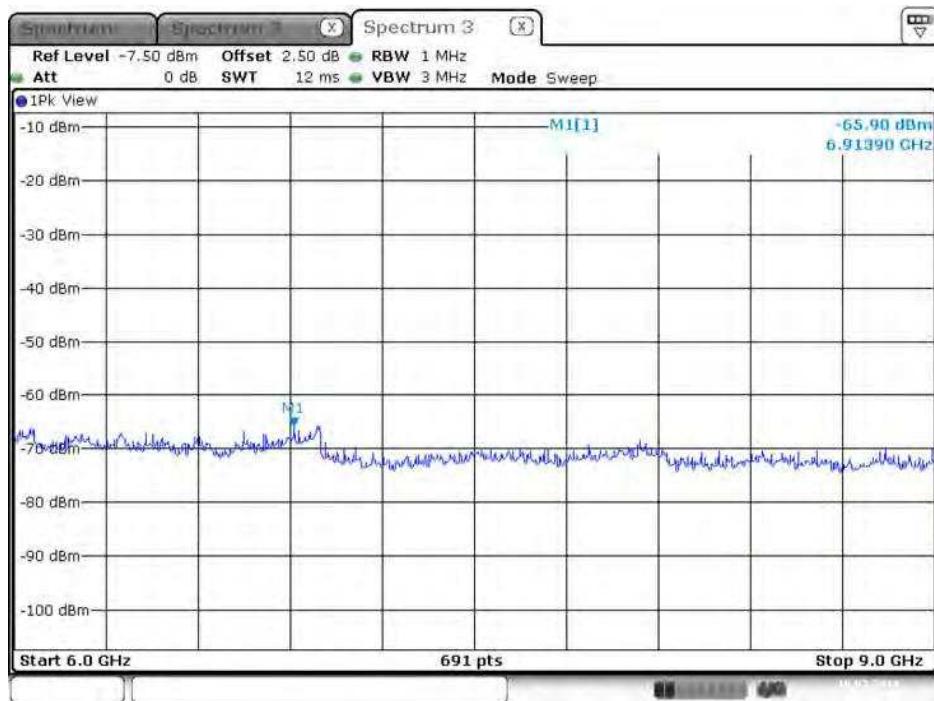
Date: 10.FEB.2018 12:39:27

**Plot on Configuration QPSK, 10M / 5480 MHz / Average / Port 1 / 6GHz~9GHz**


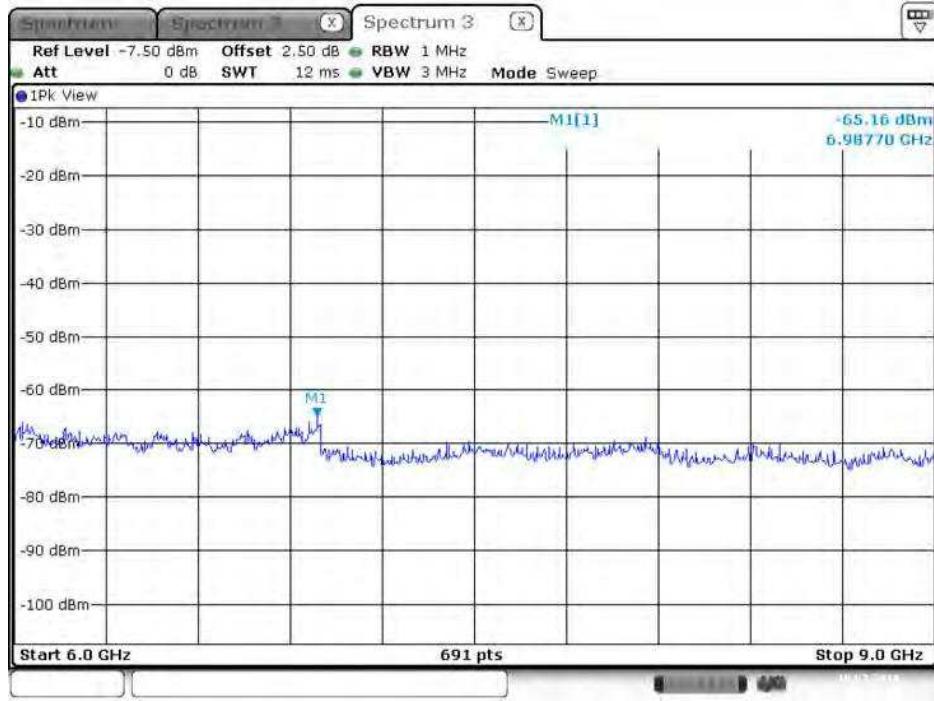
Date: 10.FEB.2018 11:27:48

**Plot on Configuration QPSK, 10M / 5480 MHz / Average / Port 2 / 1 6GHz~9GHz**


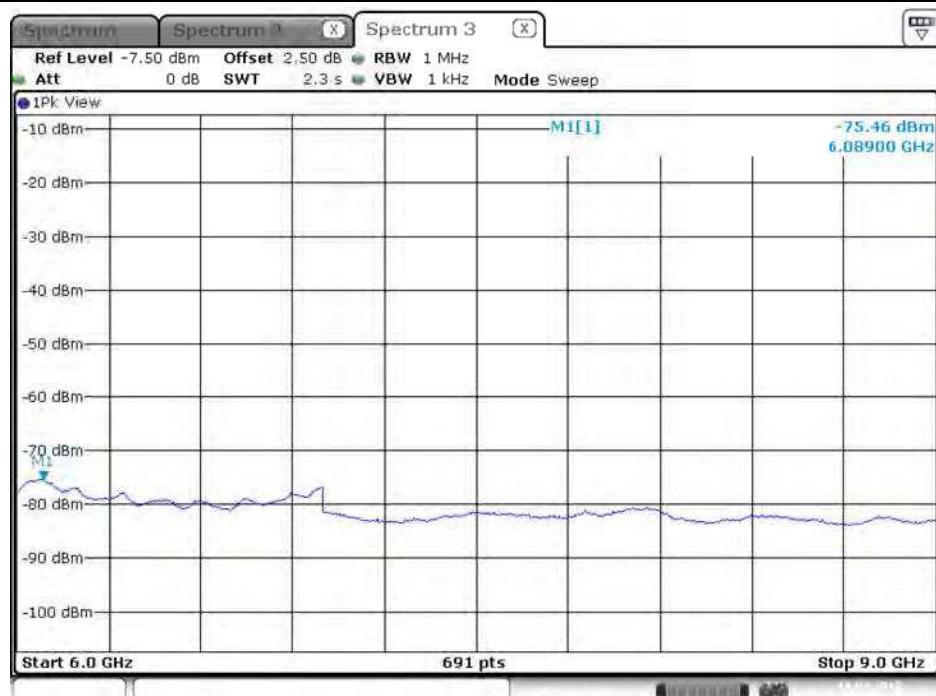
Date: 10.FEB.2018 11:23:57

**Plot on Configuration QPSK, 10M / 5480 MHz / Peak / Port 1 / 6GHz~9GHz**


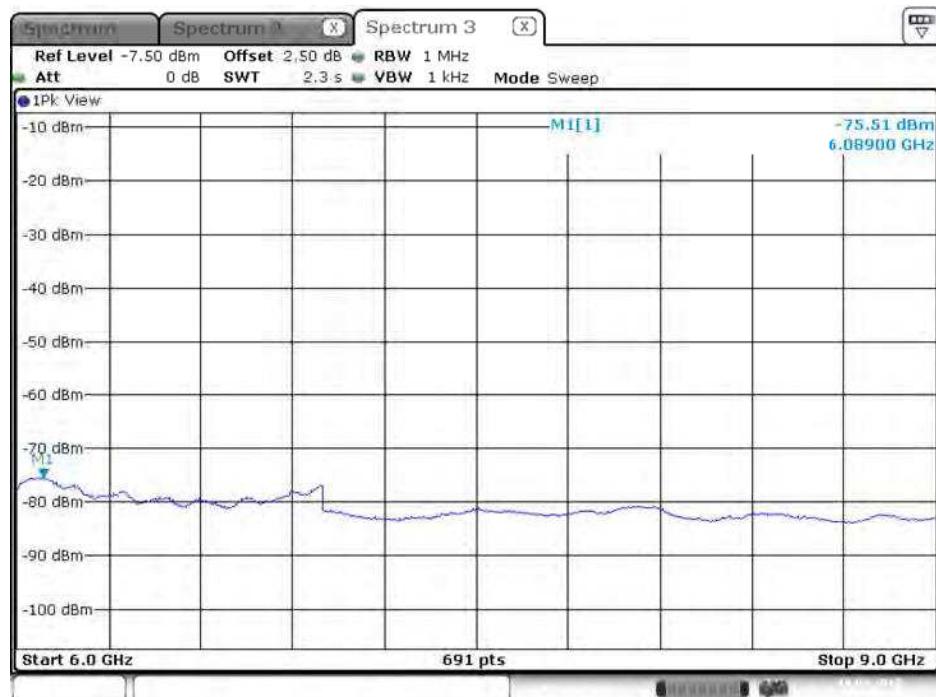
Date: 10.FEB.2018 11:28:04

**Plot on Configuration QPSK, 10M / 5480 MHz / Peak / Port 2 / 6GHz~9GHz**


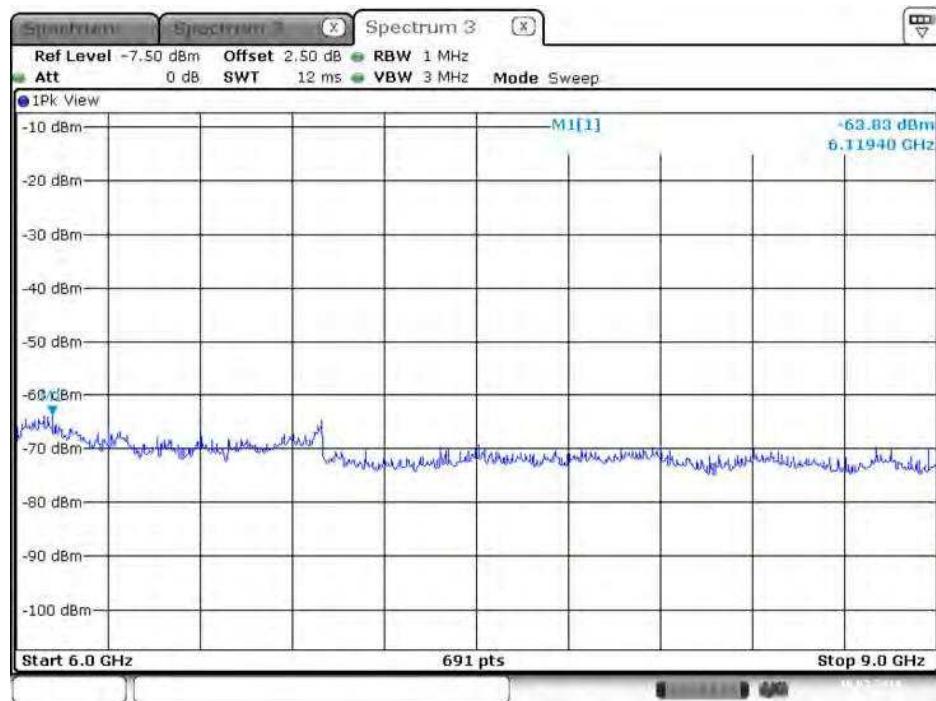
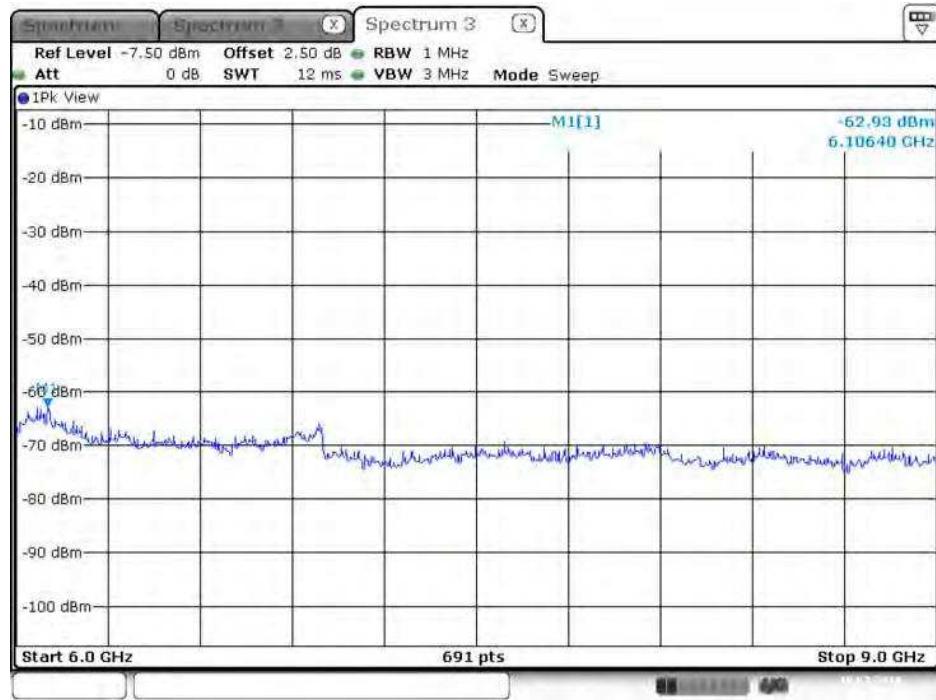
Date: 10.FEB.2018 11:24:10

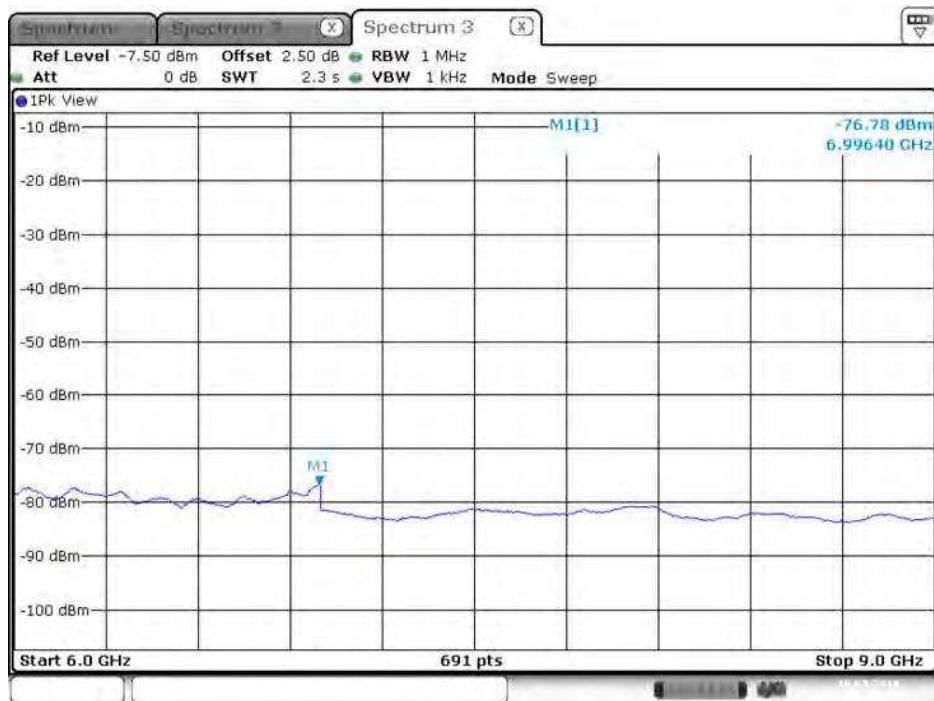
**Plot on Configuration QPSK, 10M / 5575 MHz / Average / Port 1 / 6GHz~9GHz**


Date: 10.FEB.2018 11:42:07

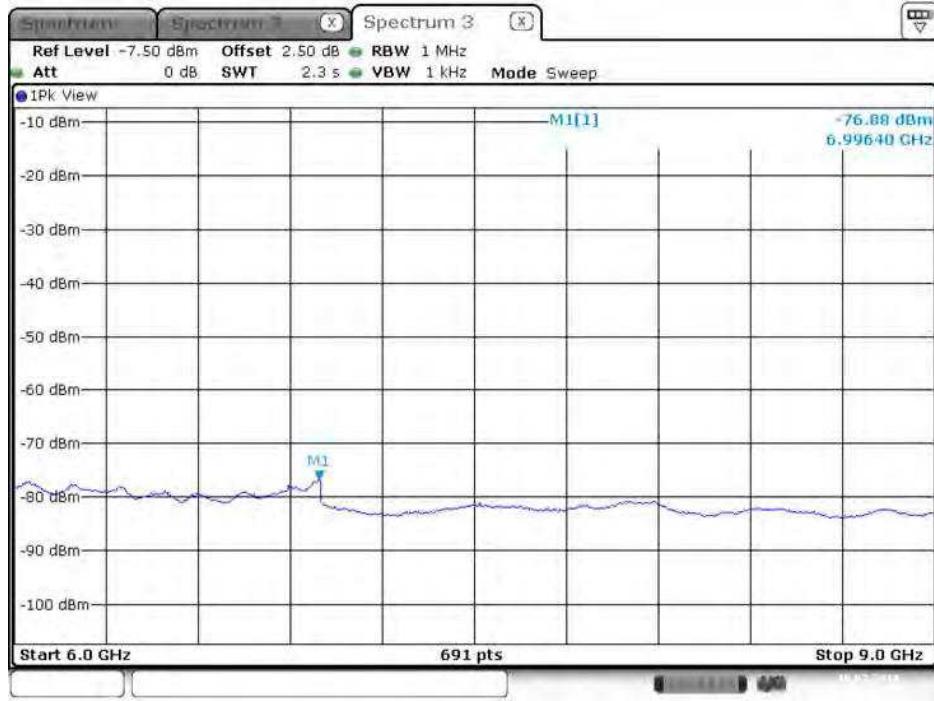
**Plot on Configuration QPSK, 10M / 5575 MHz / Average / Port 2 / 6GHz~9GHz**


Date: 10.FEB.2018 11:42:44

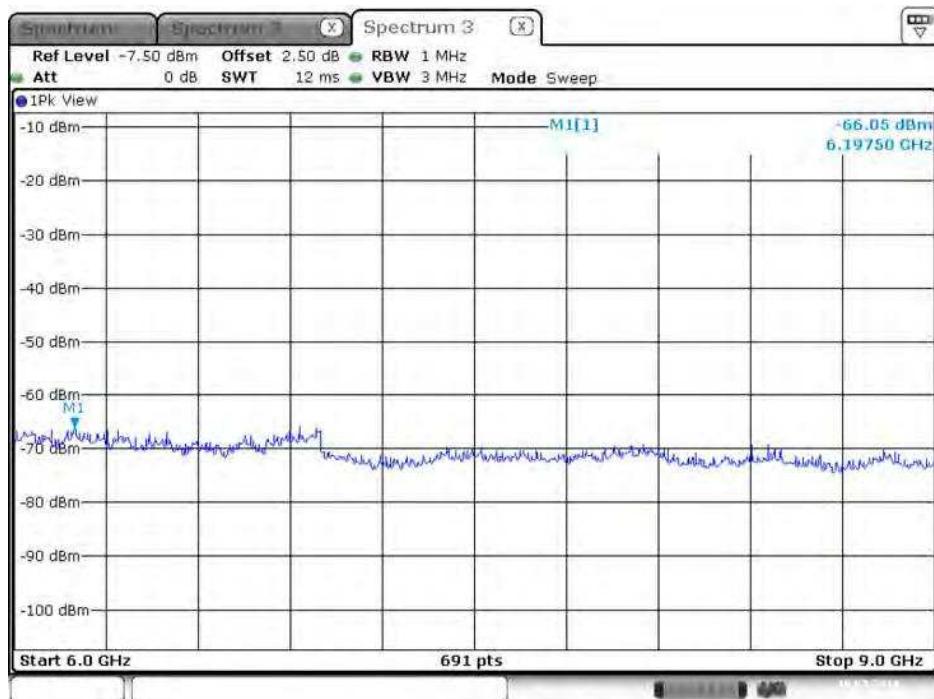
**Plot on Configuration QPSK, 10M / 5575 MHz / Peak / Port 1 / 6GHz~9GHz**

**Plot on Configuration QPSK, 10M / 5575 MHz / Peak / Port 2 / 6GHz~9GHz**


**Plot on Configuration QPSK, 10M / 5715 MHz / Average / Port 1 / 6GHz~9GHz**


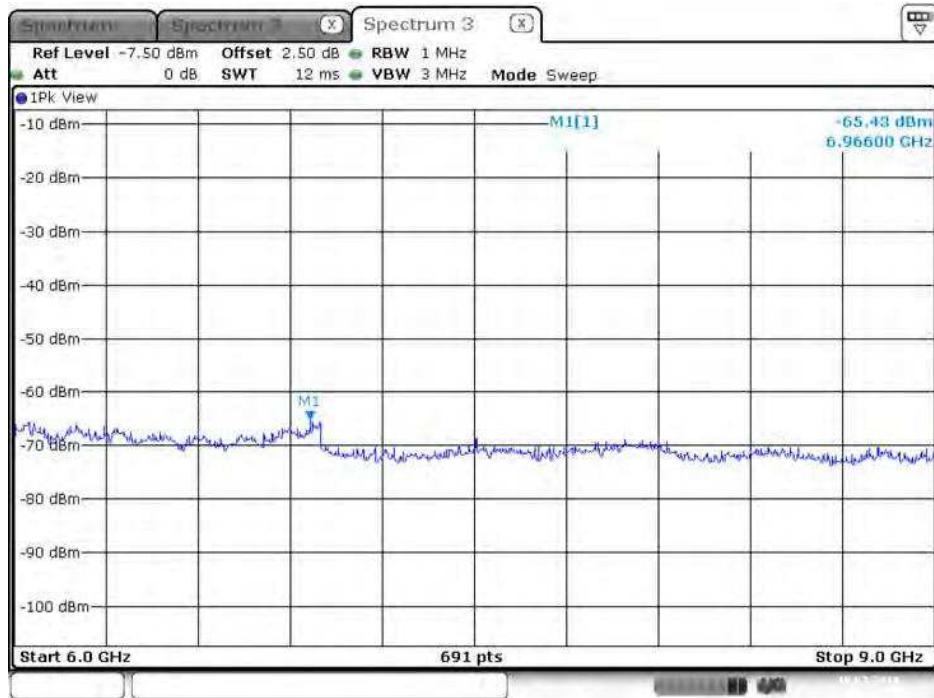
Date: 10.FEB.2018 11:51:34

**Plot on Configuration QPSK, 10M / 5715 MHz / Average / Port 2 / 6GHz~9GHz**


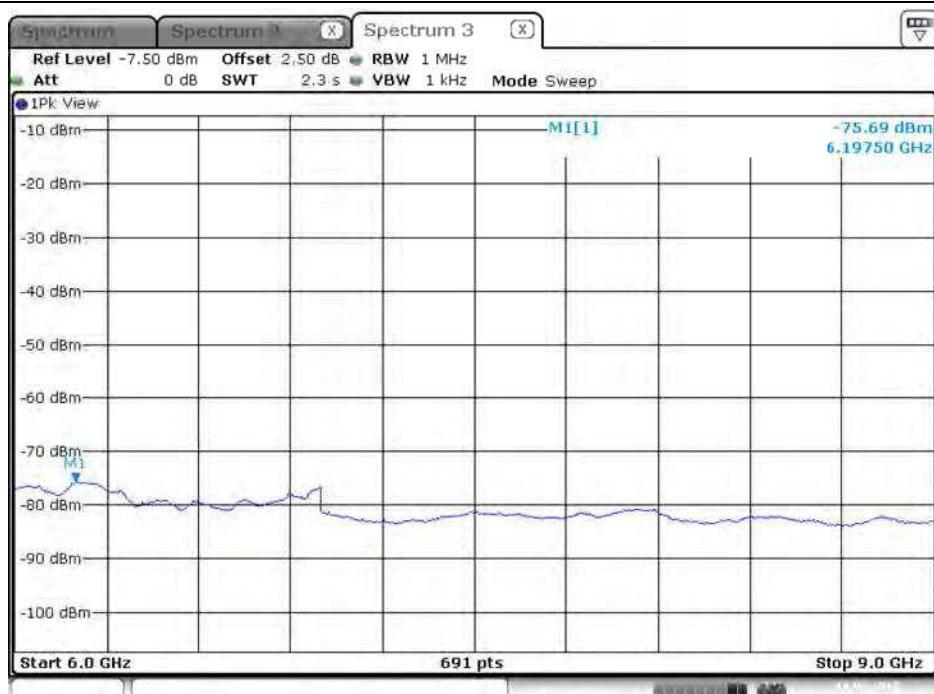
Date: 10.FEB.2018 11:49:21

**Plot on Configuration QPSK, 10M / 5715 MHz / Peak / Port 1 / 6GHz~9GHz**


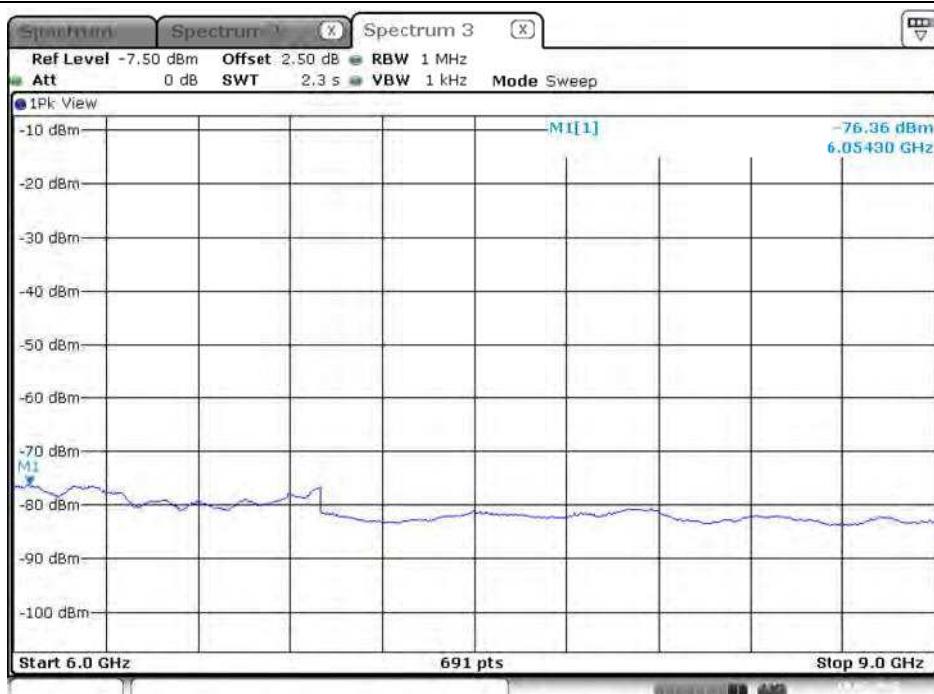
Date: 10.FEB.2018 11:51:45

**Plot on Configuration QPSK, 10M / 5715 MHz / Peak / Port 2 / 6GHz~9GHz**


Date: 10.FEB.2018 11:49:31

**Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Average / Port 1 / 6GHz~9GHz**


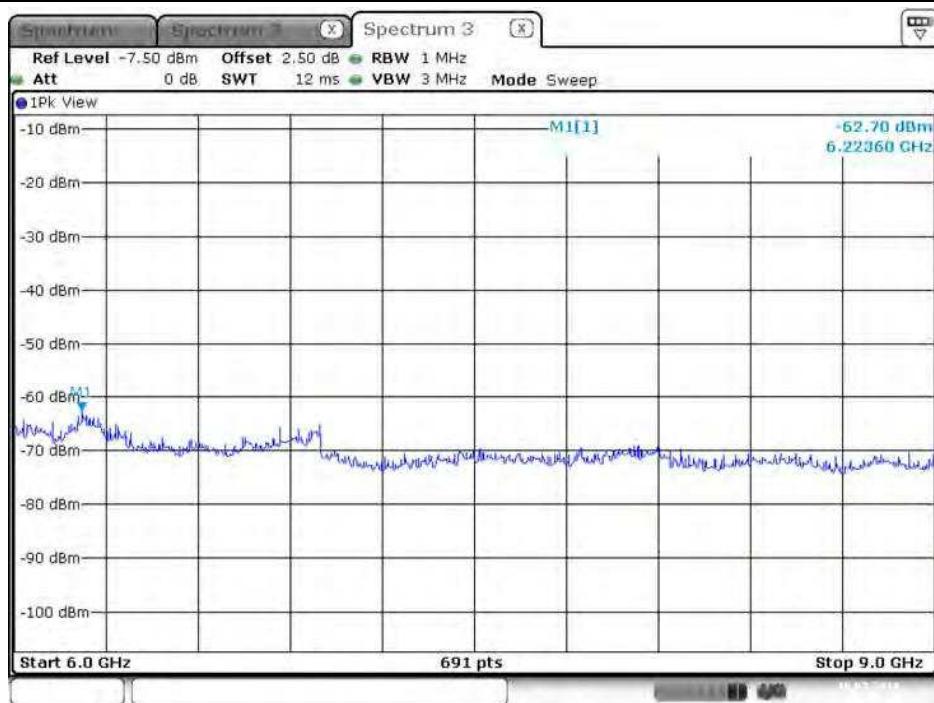
Date: 10.FEB.2018 11:56:34

**Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Average / Port 2 / 6GHz~9GHz**


Date: 10.FEB.2018 11:57:18

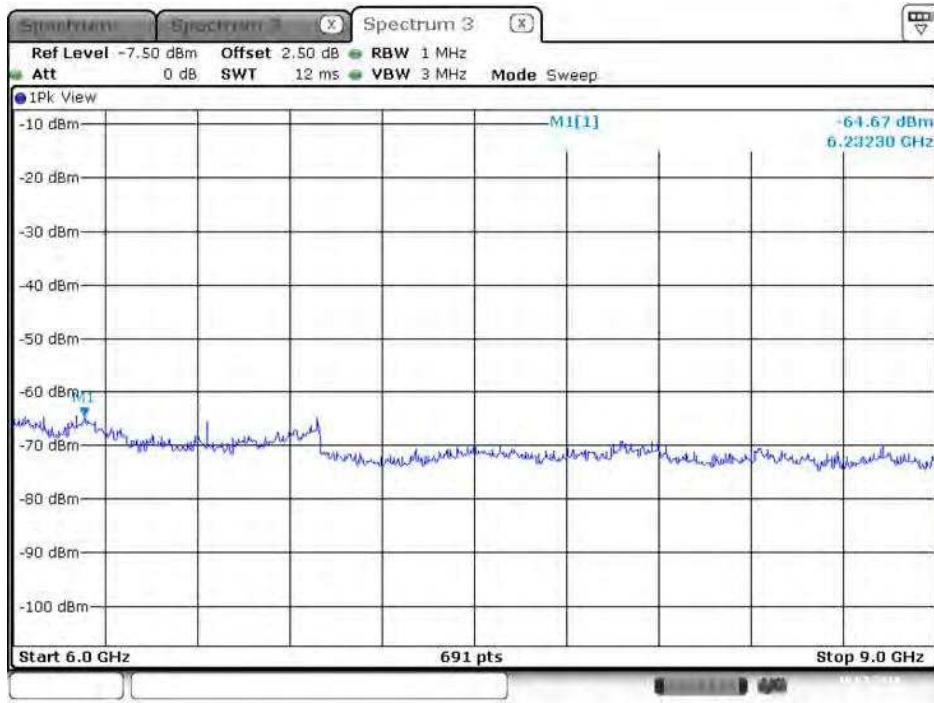


## Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Peak / Port 1 / 6GHz~9GHz

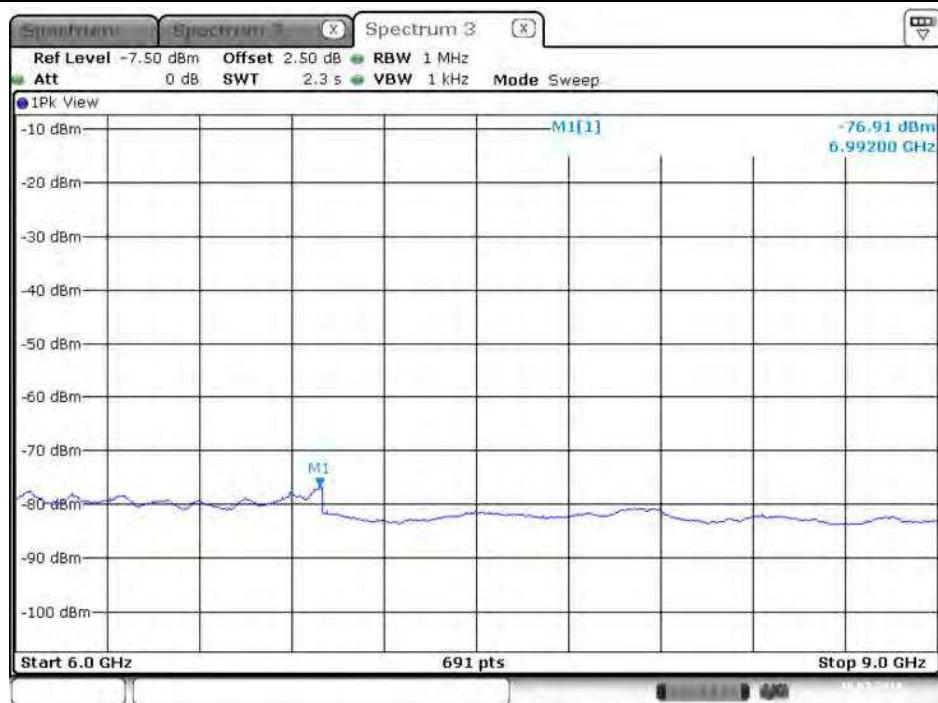


Date: 10.FEB.2018 11:56:42

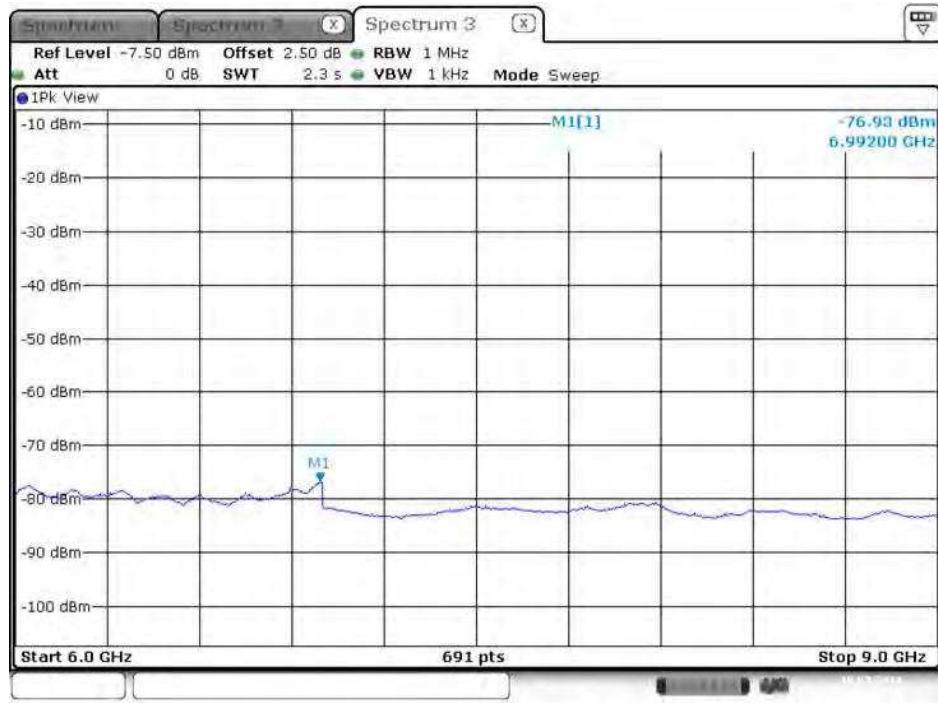
## Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Peak / Port 2 / 6GHz~9GHz



Date: 10.FEB.2018 11:57:25

**Plot on Configuration QPSK, 40M / 5495 MHz / Average / Port 1 / 6GHz~9GHz**


Date: 10.FEB.2018 12:09:11

**Plot on Configuration QPSK, 40M / 5495 MHz / Average / Port 2 / 6GHz~9GHz**


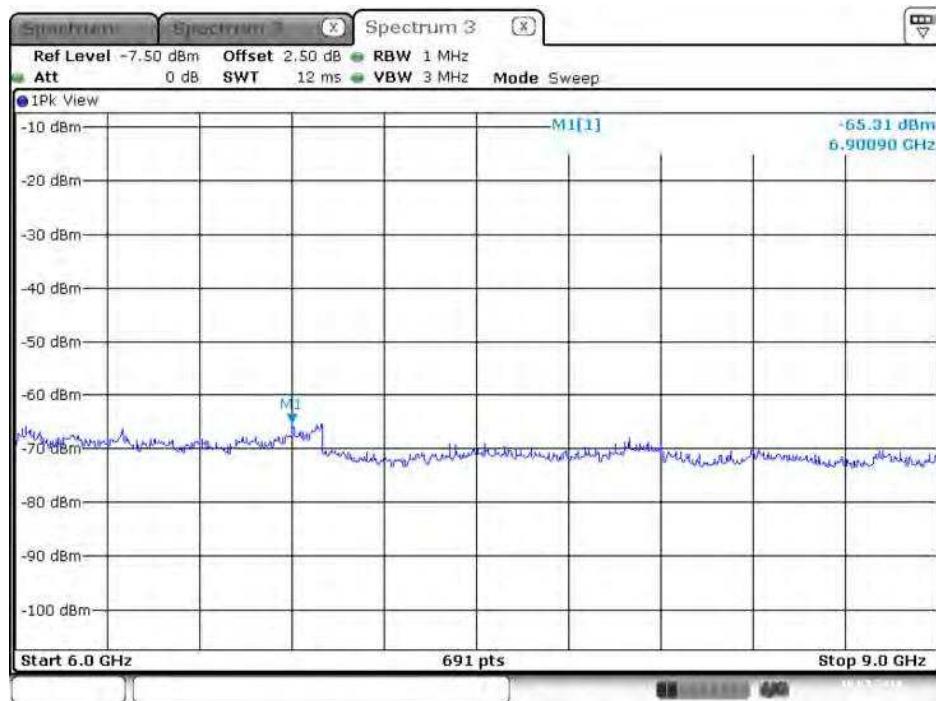
Date: 10.FEB.2018 12:06:48



## CSE TX above 1GHz Result

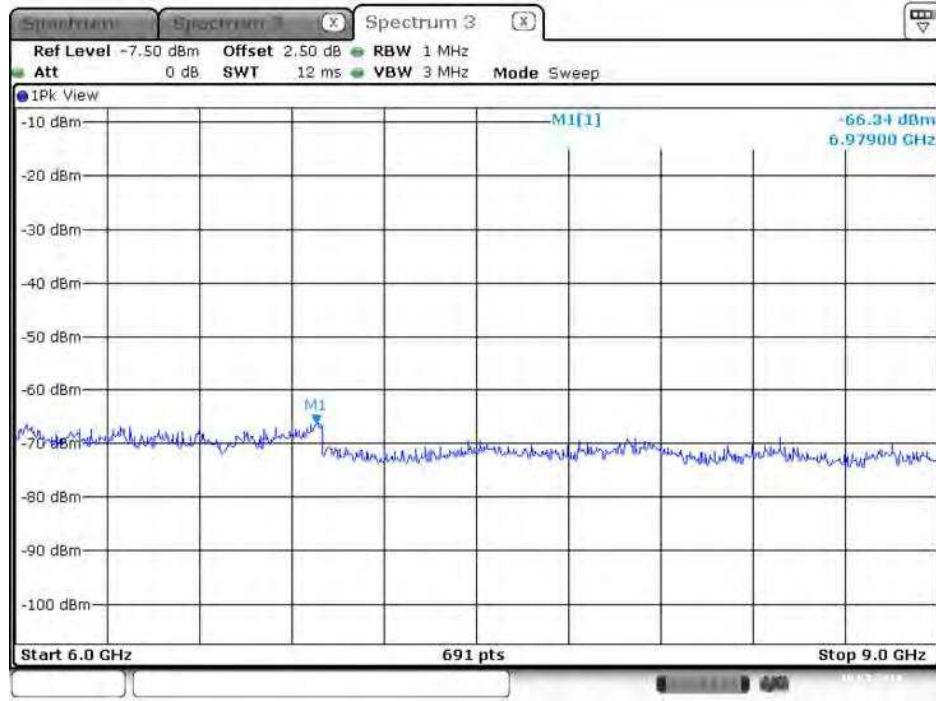
Appendix D.1

### Plot on Configuration QPSK, 40M / 5495 MHz / Peak / Port 1 / 6GHz~9GHz

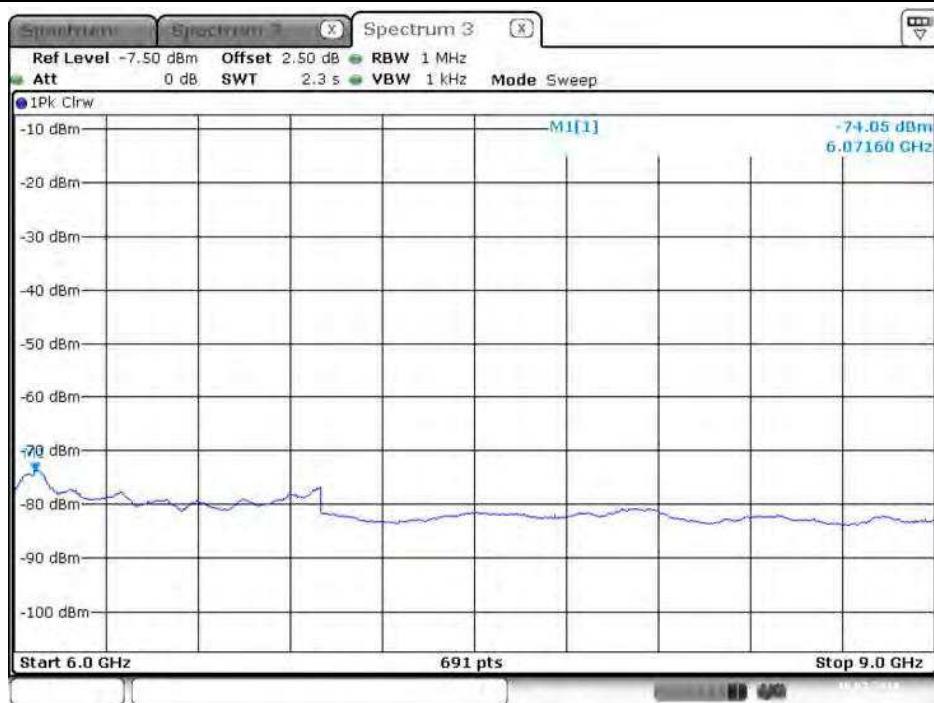


Date: 10.FEB.2018 12:09:24

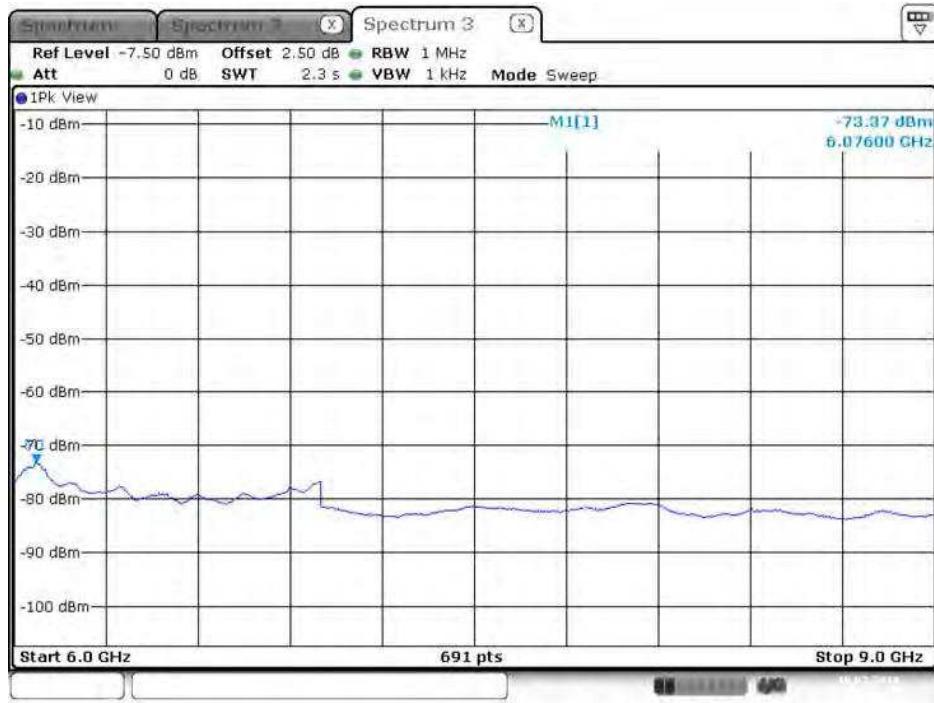
### Plot on Configuration QPSK, 40M / 5495 MHz / Peak / Port 2 / 6GHz~9GHz



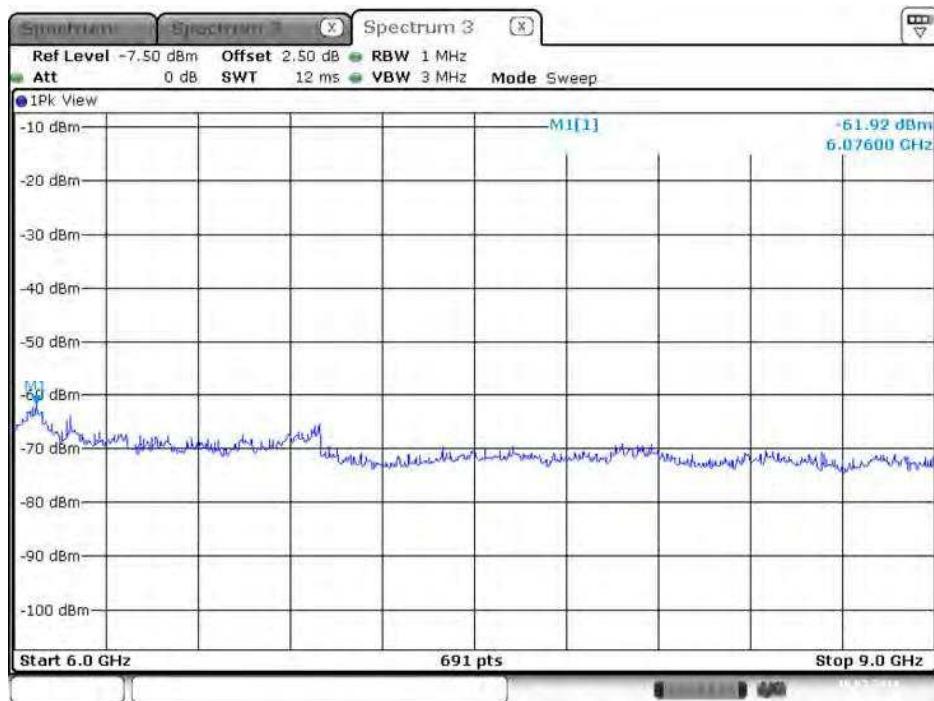
Date: 10.FEB.2018 12:06:56

**Plot on Configuration QPSK, 40M / 5550 MHz / Average / Port 1 / 6GHz~9GHz**


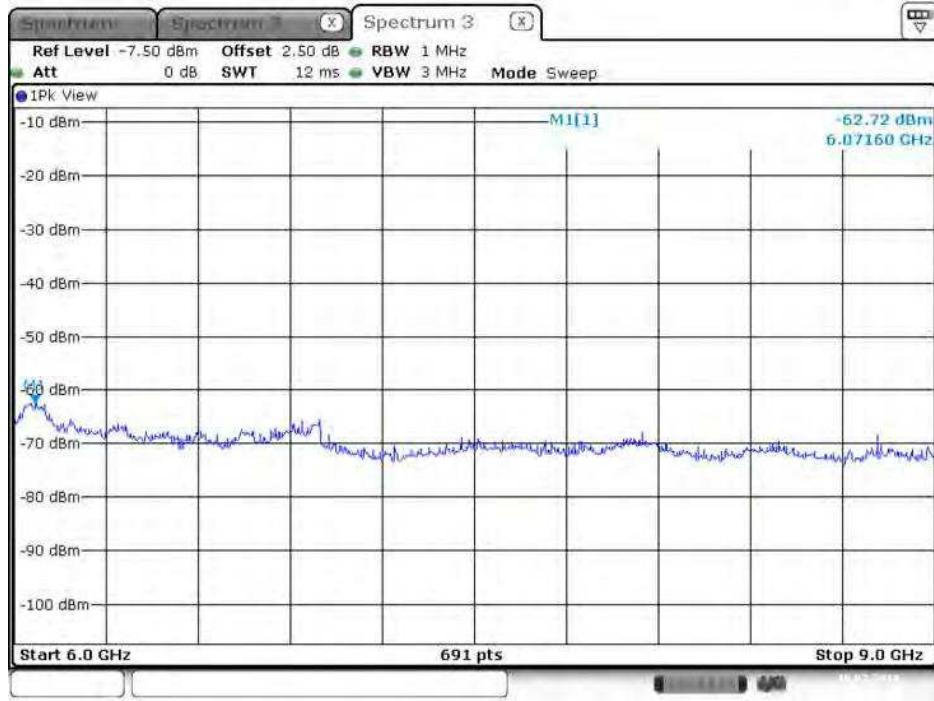
Date: 10.FEB.2018 12:13:57

**Plot on Configuration QPSK, 40M / 5550 MHz / Average / Port 2 / 6GHz~9GHz**


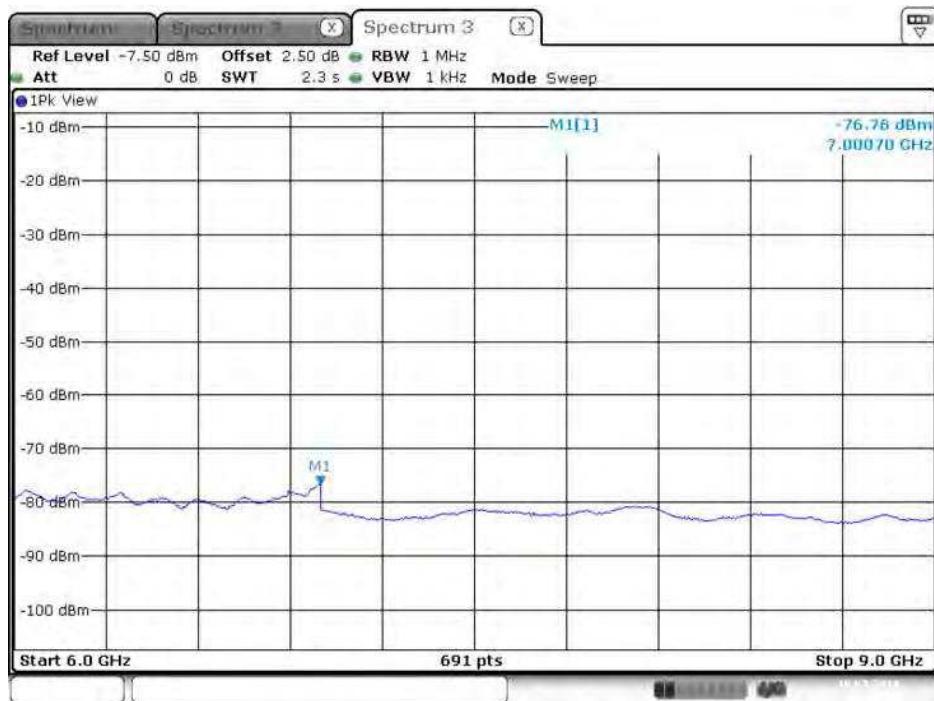
Date: 10.FEB.2018 12:16:11

**Plot on Configuration QPSK, 40M / 5550 MHz / Peak / Port 1 / 6GHz~9GHz**


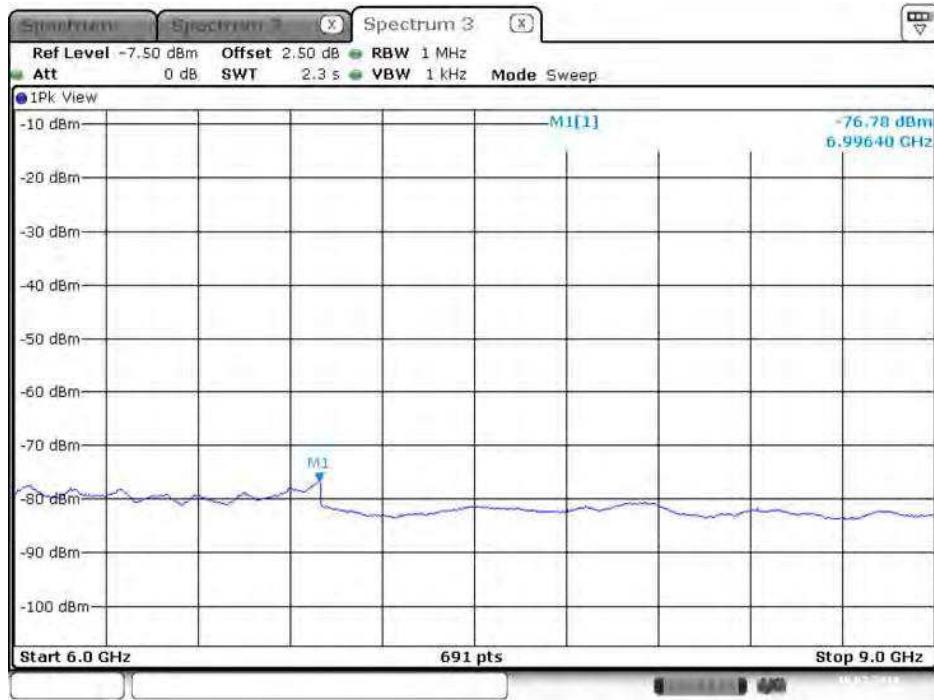
Date: 10.FEB.2018 12:14:09

**Plot on Configuration QPSK, 40M / 5550 MHz / Peak / Port 2 / 6GHz~9GHz**


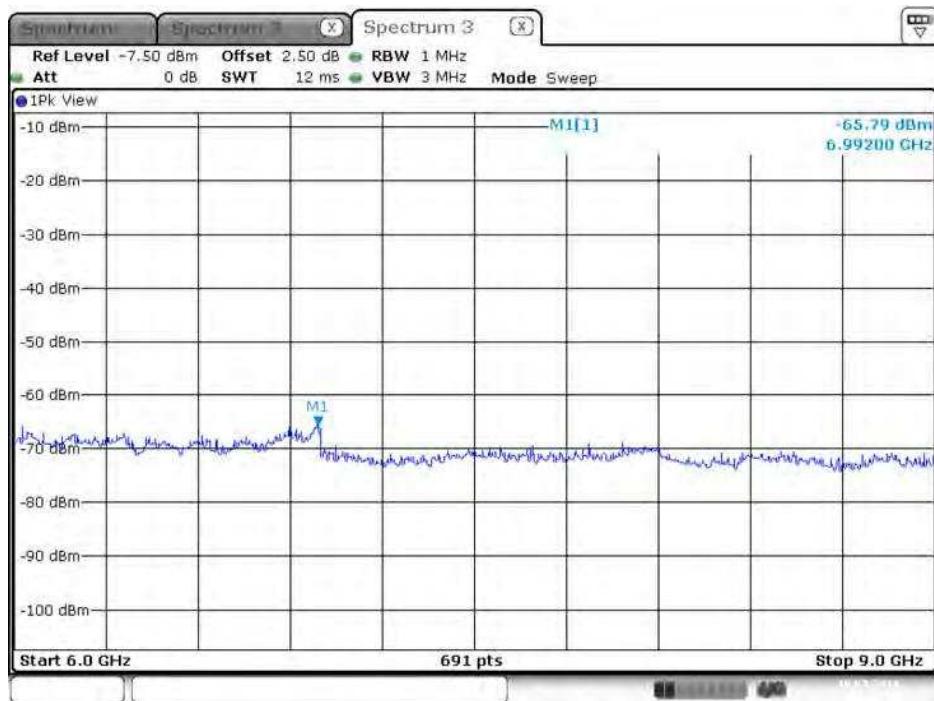
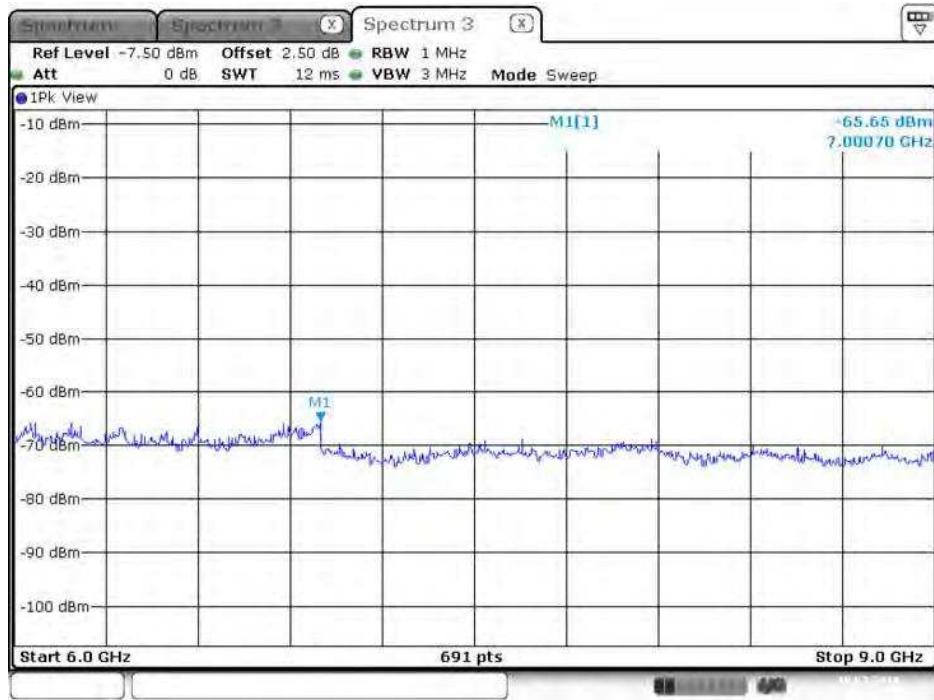
Date: 10.FEB.2018 12:16:26

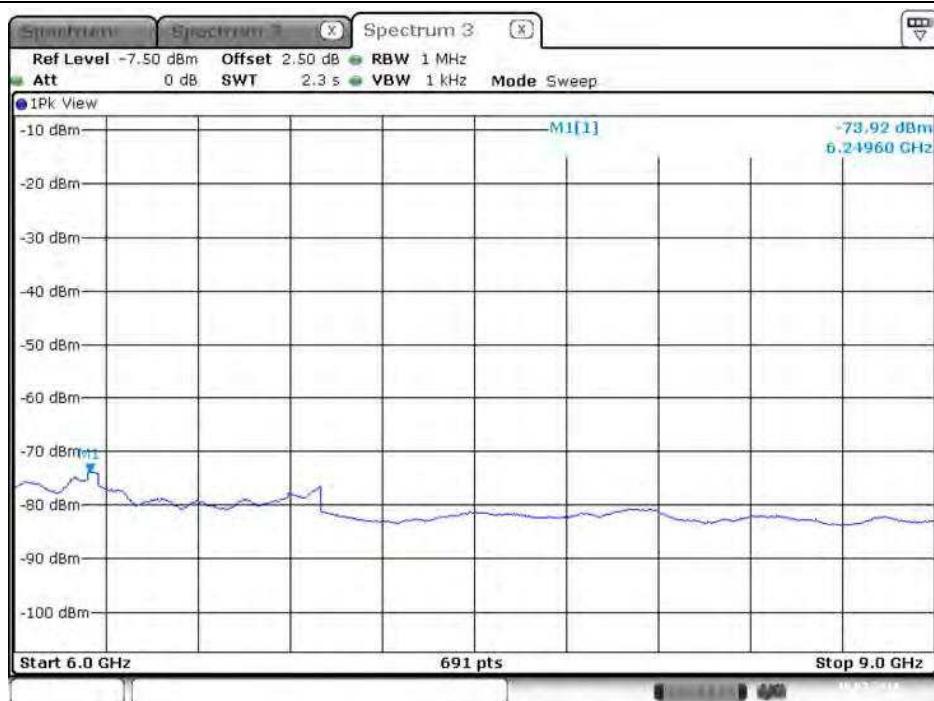
**Plot on Configuration QPSK, 40M / 5700 MHz / Average / Port 1 / 6GHz~9GHz**


Date: 10.FEB.2018 12:31:08

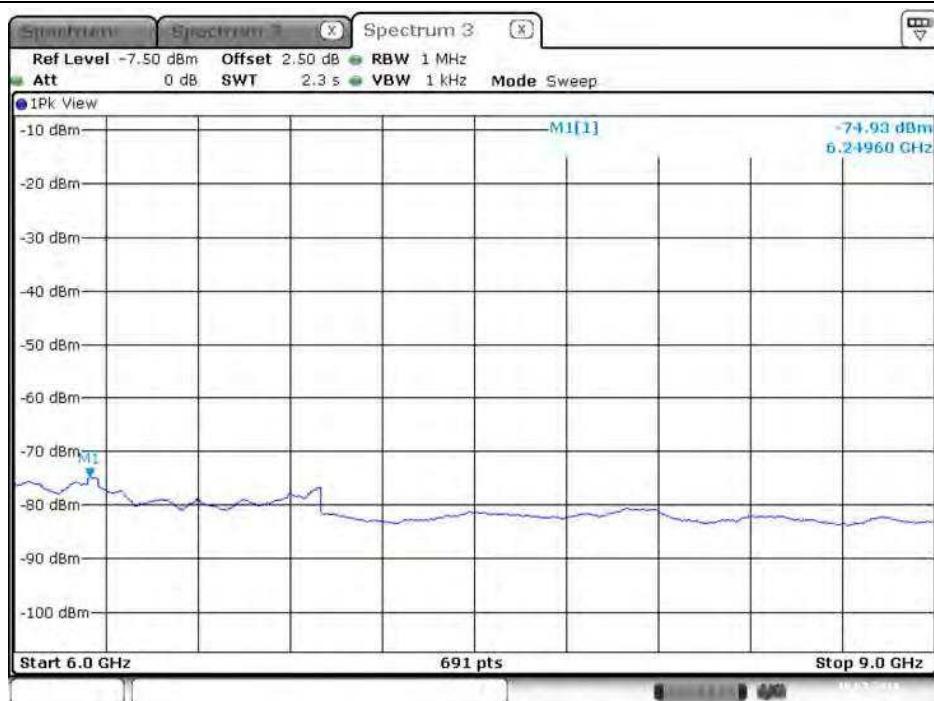
**Plot on Configuration QPSK, 40M / 5700 MHz / Average / Port 2 / 6GHz~9GHz**


Date: 10.FEB.2018 12:30:26

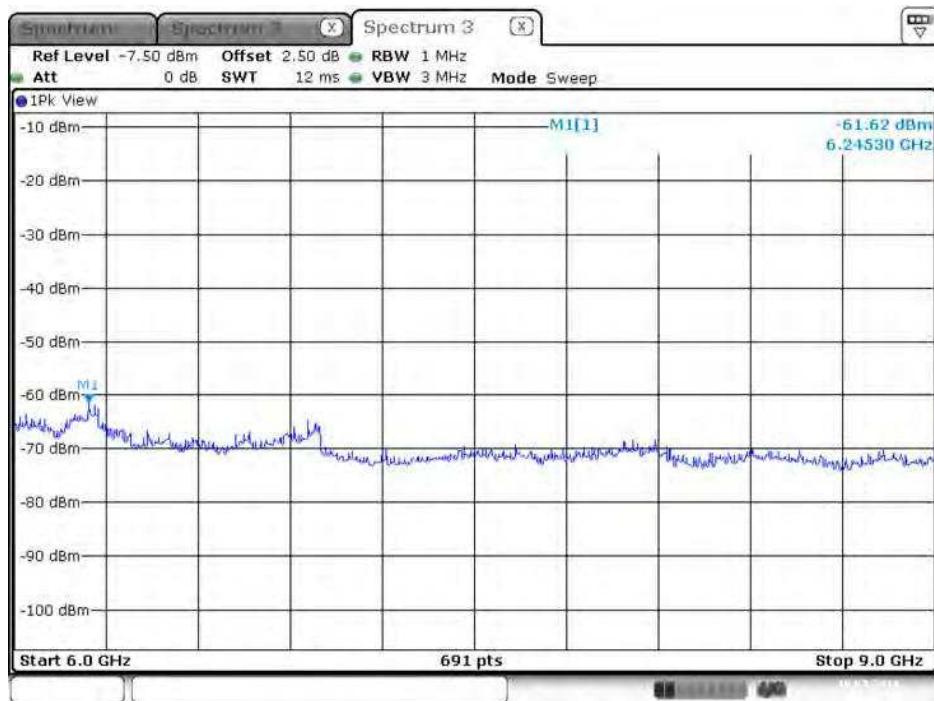
**Plot on Configuration QPSK, 40M / 5700 MHz / Peak / Port 1 / 6GHz~9GHz**

**Plot on Configuration QPSK, 40M / 5700 MHz / Peak / Port 2 / 6GHz~9GHz**


**Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Average / Port 1 / 6GHz~9GHz**


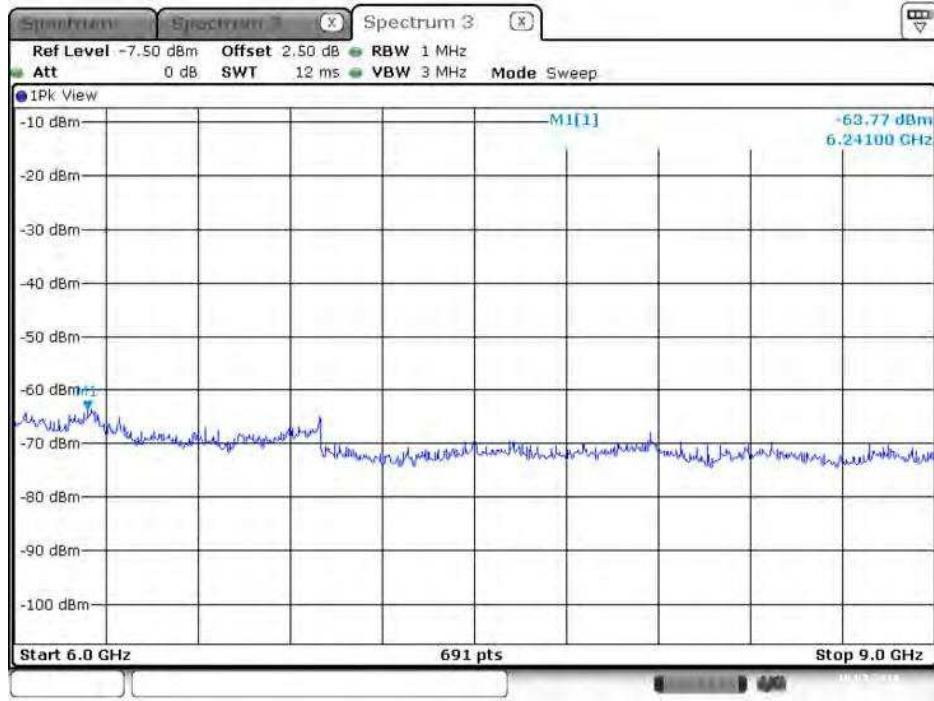
Date: 10.FEB.2018 12:37:21

**Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Average / Port 2 / 6GHz~9GHz**


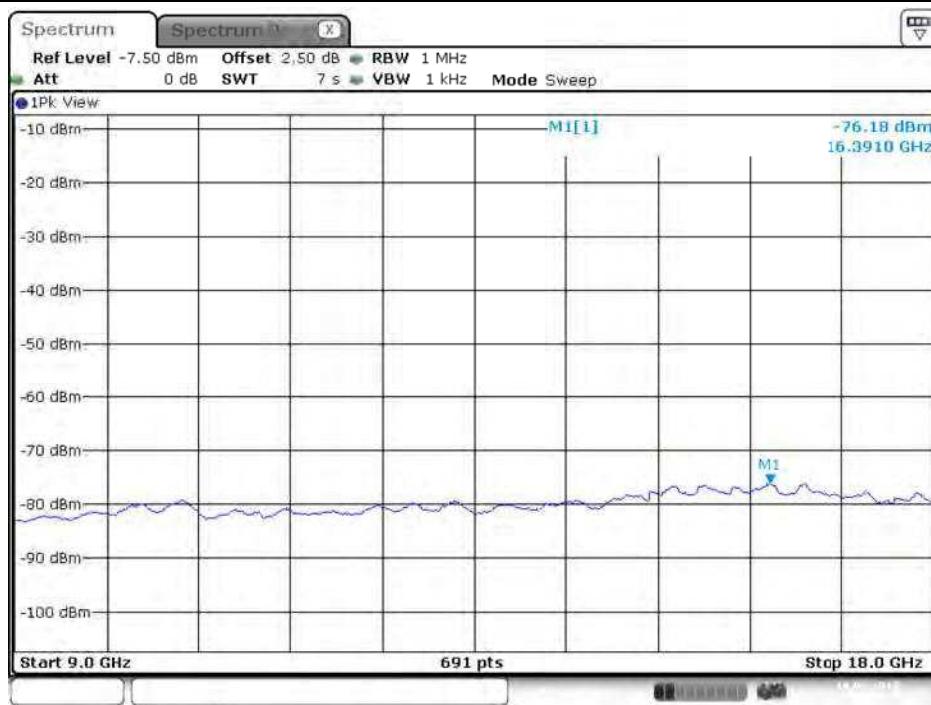
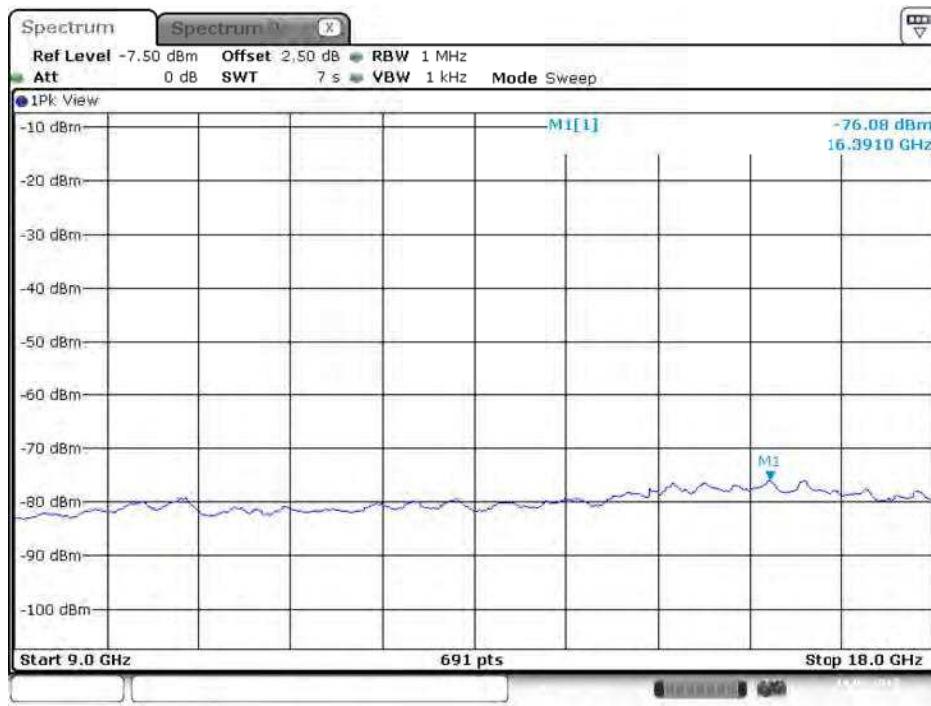
Date: 10.FEB.2018 12:38:18

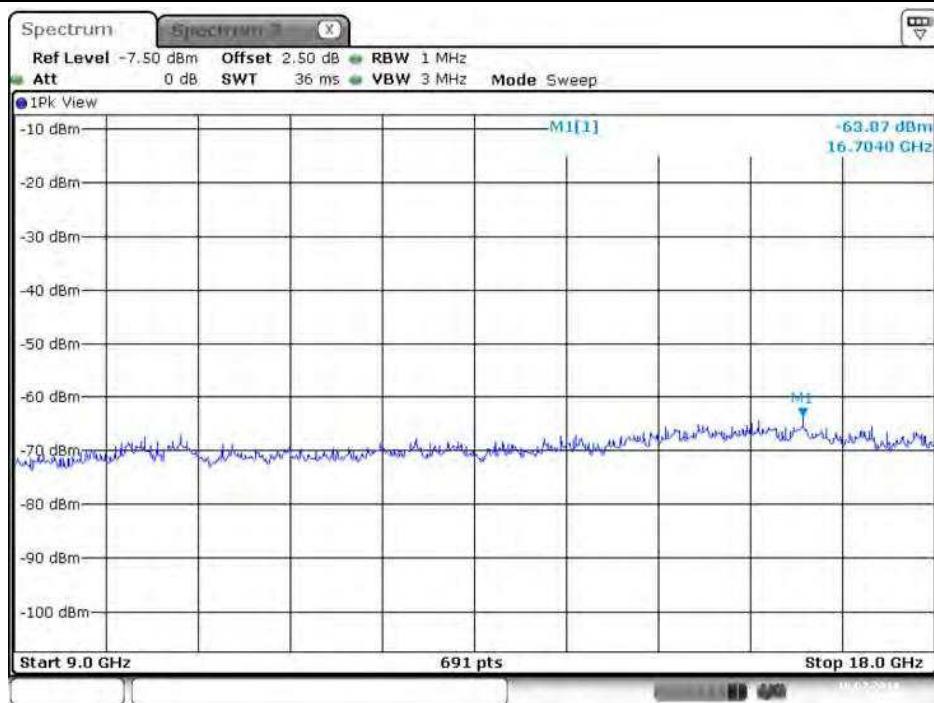
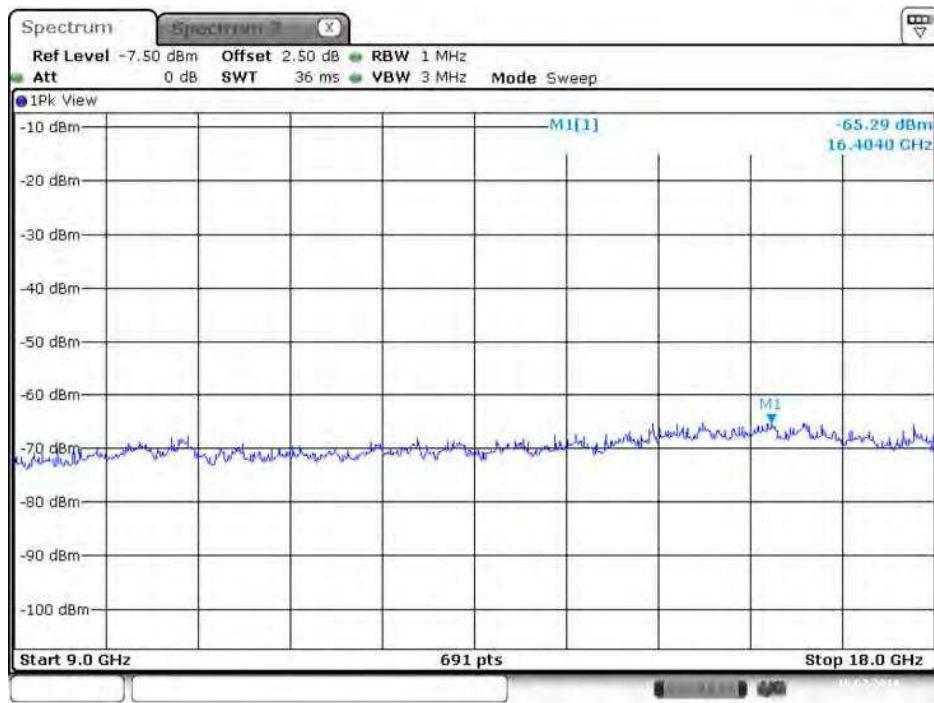
**Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Peak / Port 1 / 6GHz~9GHz**


Date: 10.FEB.2018 12:37:36

**Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Peak / Port 2 / 6GHz~9GHz**


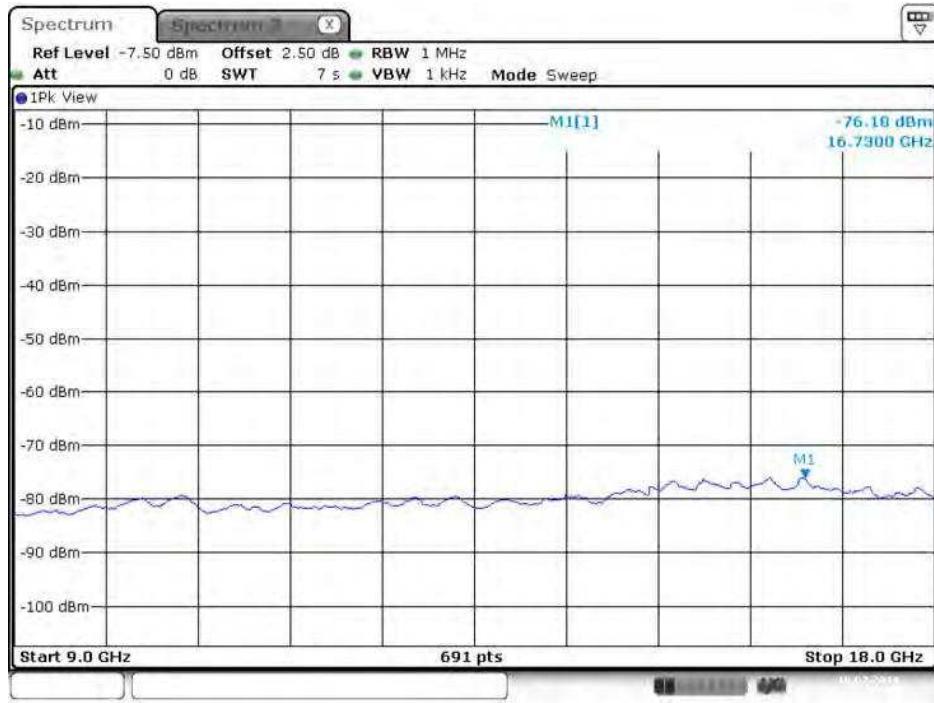
Date: 10.FEB.2018 12:38:45

**Plot on Configuration QPSK, 10M / 5480 MHz / Average / Port 1 / 9GHz~18GHz**

**Plot on Configuration QPSK, 10M / 5480 MHz / Average / Port 2 / 9GHz~18GHz**


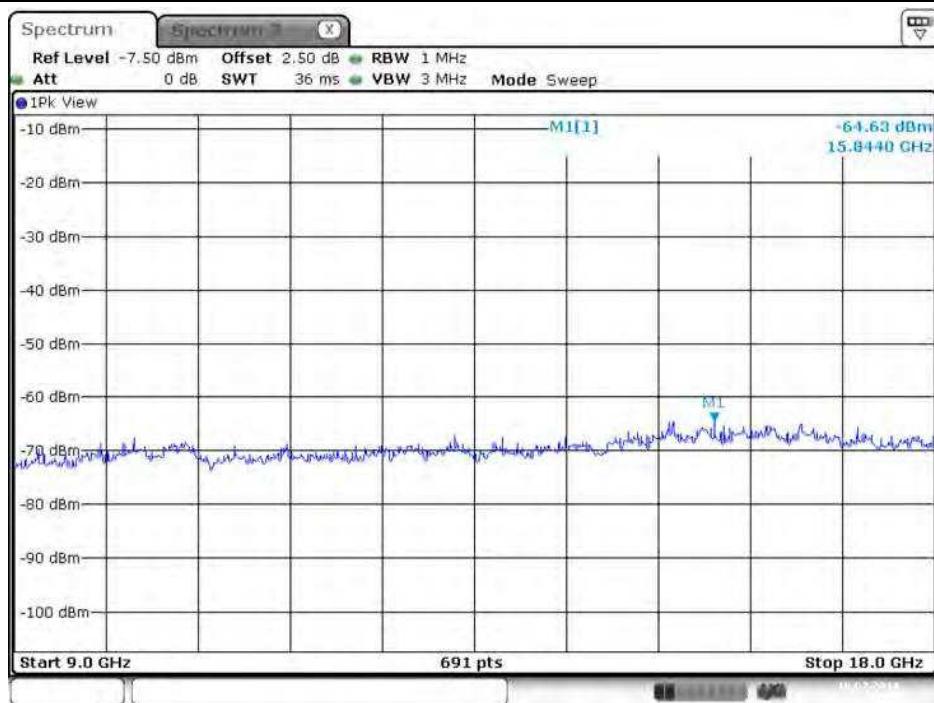
**Plot on Configuration QPSK, 10M / 5480 MHz / Peak / Port 1 / 9GHz~18GHz**

**Plot on Configuration QPSK, 10M / 5480 MHz / Peak / Port 2 / 9GHz~18GHz**


**Plot on Configuration QPSK, 10M / 5575 MHz / Average / Port 1 / 9GHz~18GHz**

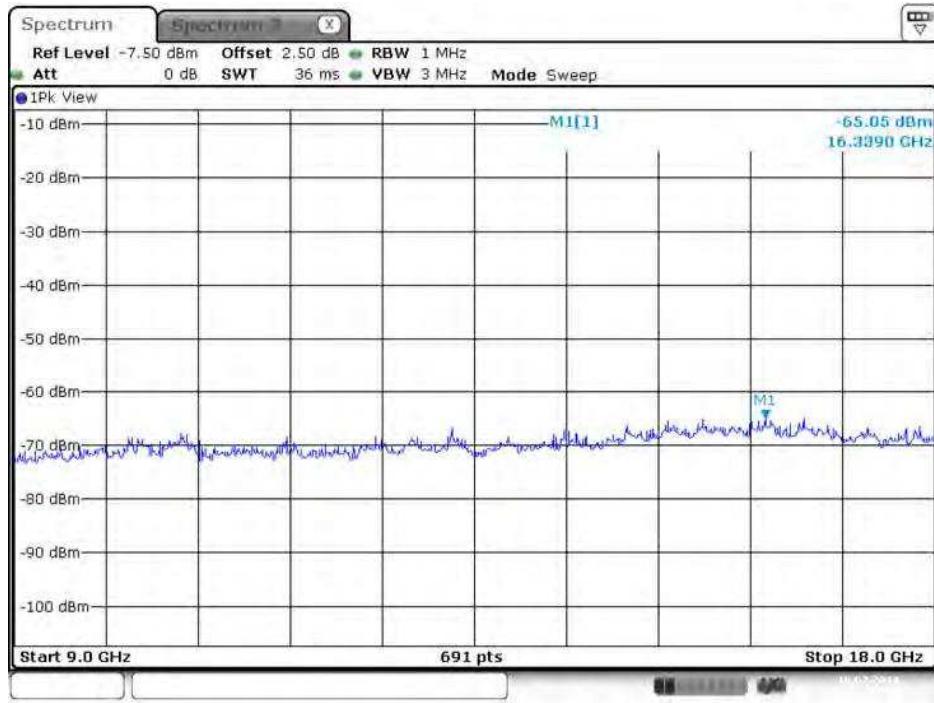

Date: 10.FEB.2018 10:55:47

**Plot on Configuration QPSK, 10M / 5575 MHz / Average / Port 2 / 9GHz~18GHz**


Date: 10.FEB.2018 10:55:05

**Plot on Configuration QPSK, 10M / 5575 MHz / Peak / Port 1 / 9GHz~18GHz**


Date: 10.FEB.2018 10:55:55

**Plot on Configuration QPSK, 10M / 5575 MHz / Peak / Port 2 / 9GHz~18GHz**


Date: 10.FEB.2018 10:55:10

**Plot on Configuration QPSK, 10M / 5715 MHz / Average / Port 1 / 9GHz~18GHz**

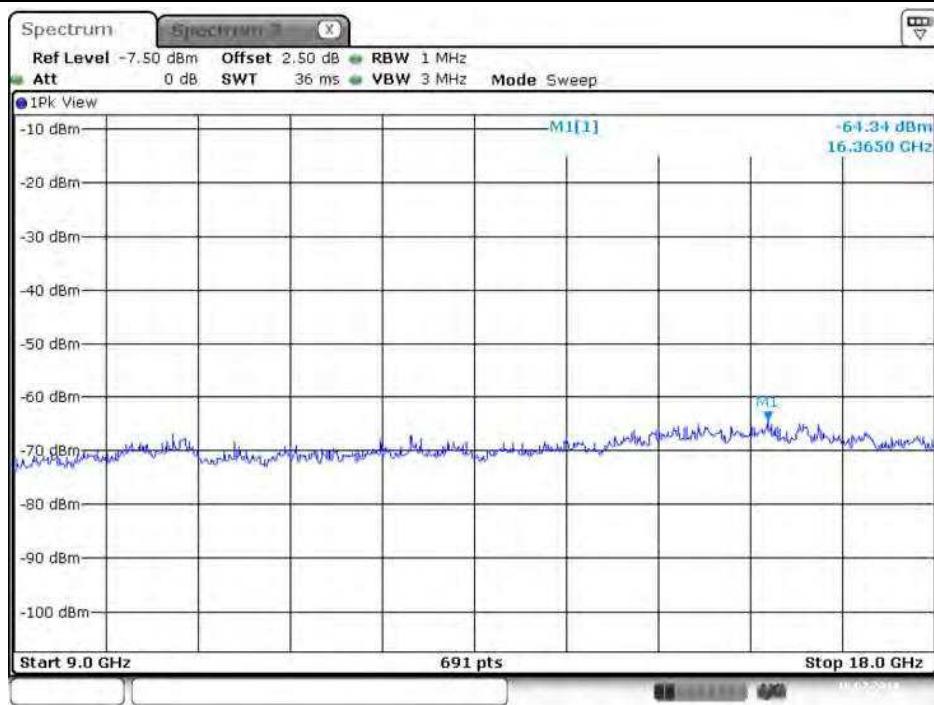

Date: 10.FEB.2018 10:56:46

**Plot on Configuration QPSK, 10M / 5715 MHz / Average / Port 2 / 9GHz~18GHz**


Date: 10.FEB.2018 10:57:34

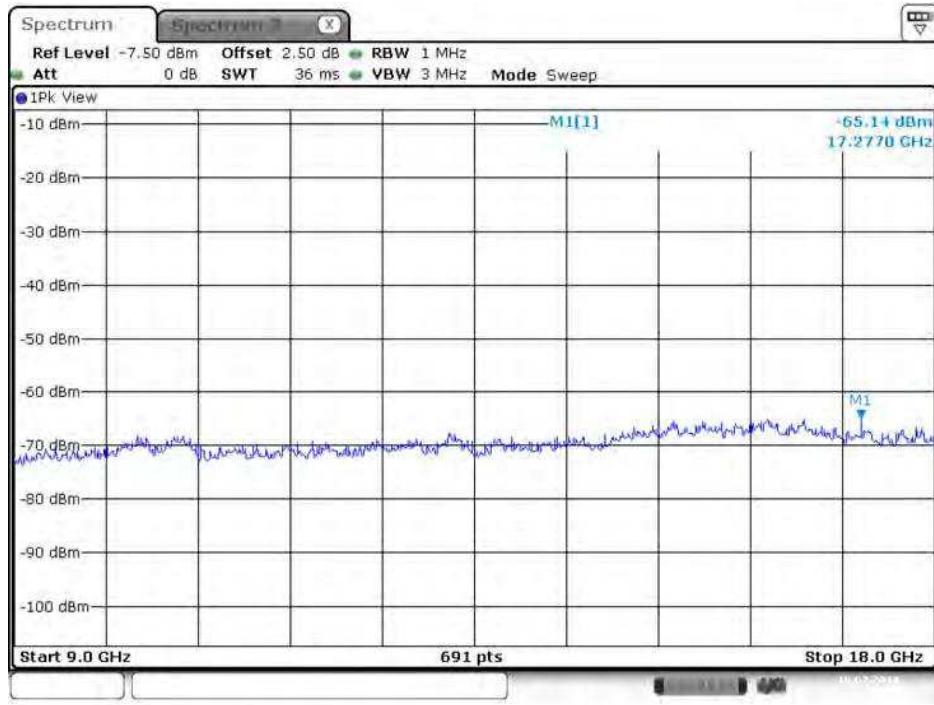


## Plot on Configuration QPSK, 10M / 5715 MHz / Peak / Port 1 / 9GHz~18GHz

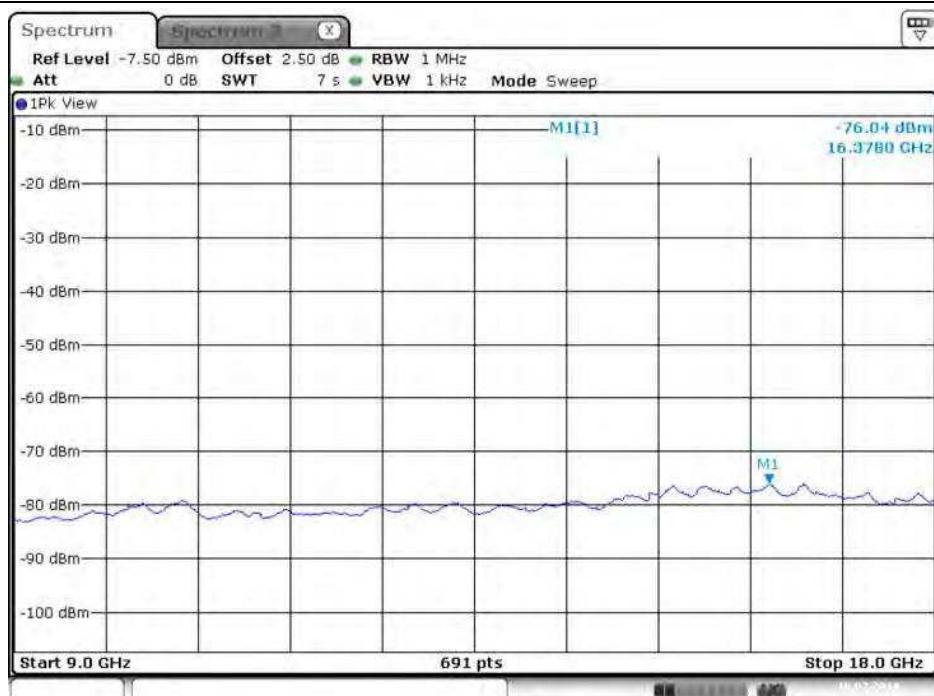
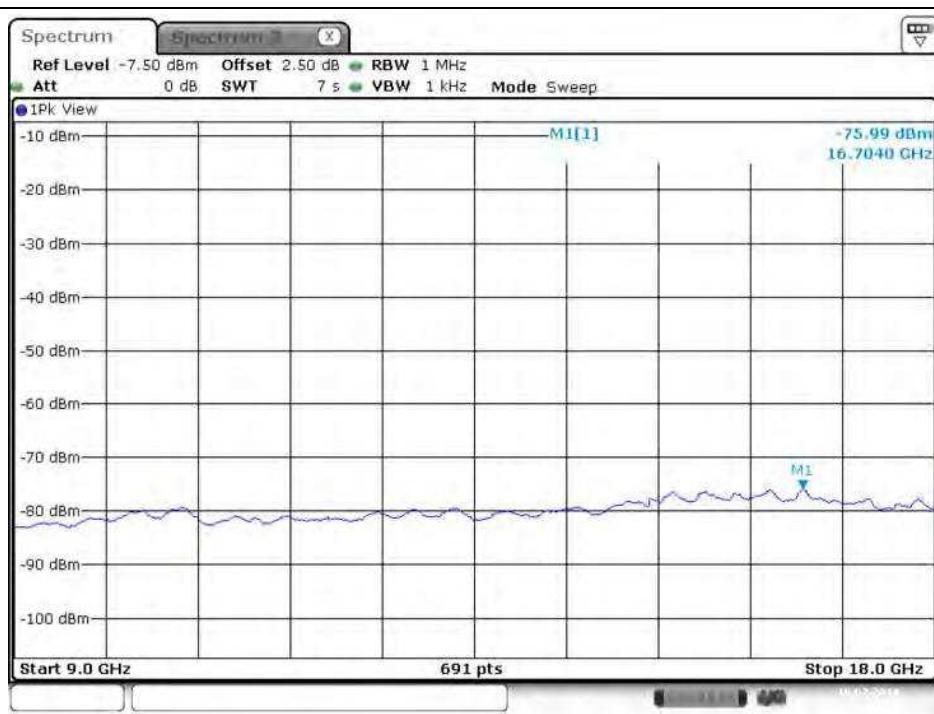


Date: 10.FEB.2018 10:56:55

## Plot on Configuration QPSK, 10M / 5715 MHz / Peak / Port 2 / 9GHz~18GHz

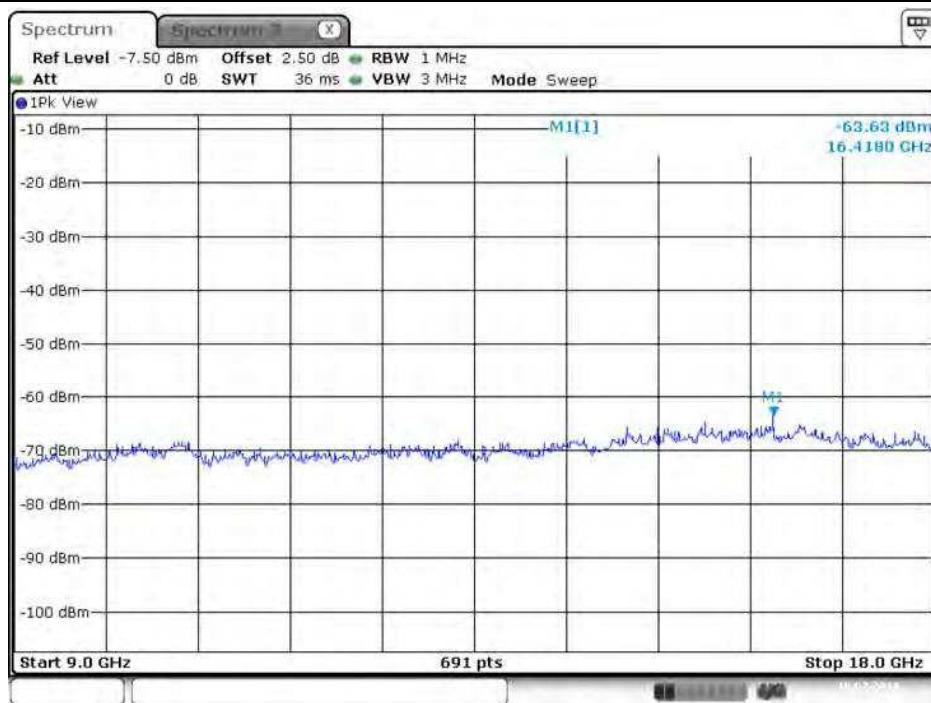


Date: 10.FEB.2018 10:57:41

**Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Average / Port 1 / 9GHz~18GHz**

**Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Average / Port 2 / 9GHz~18GHz**


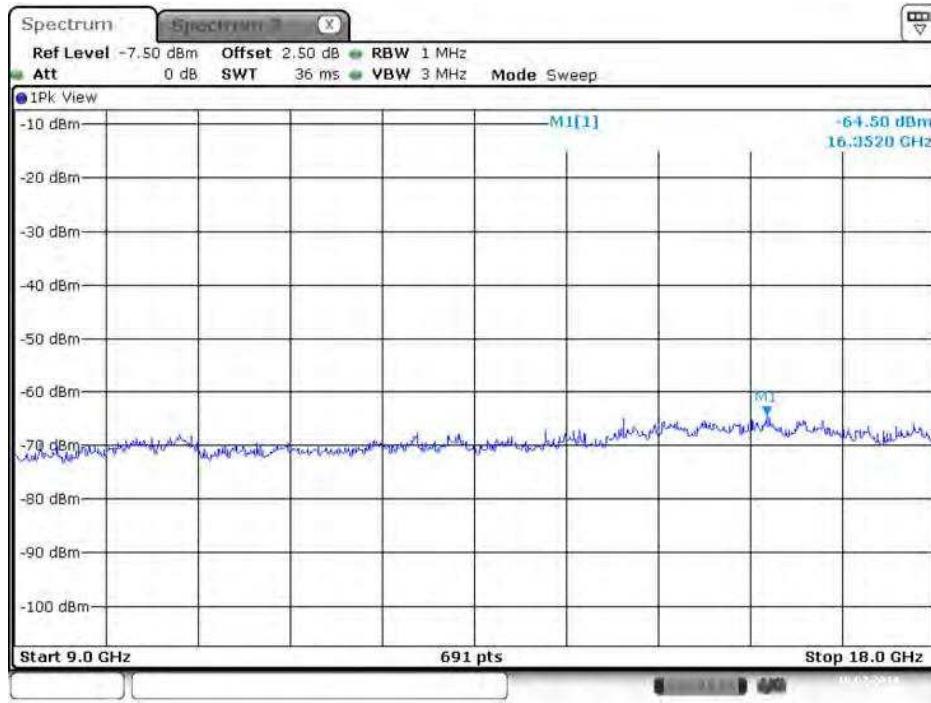


## Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Peak / Port 1 / 9GHz~18GHz



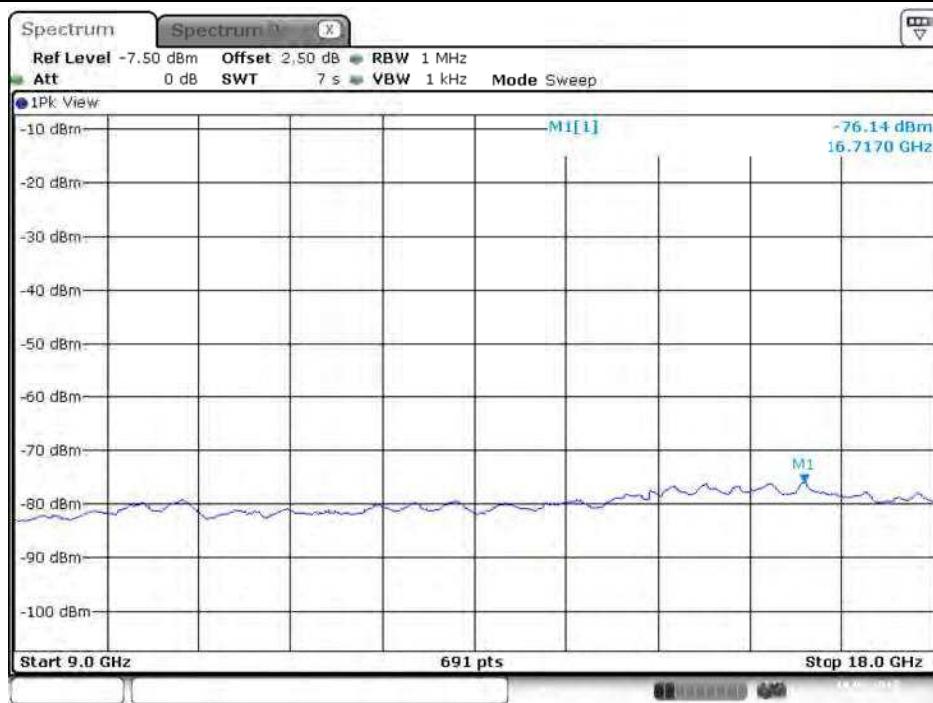
Date: 10.FEB.2018 10:59:29

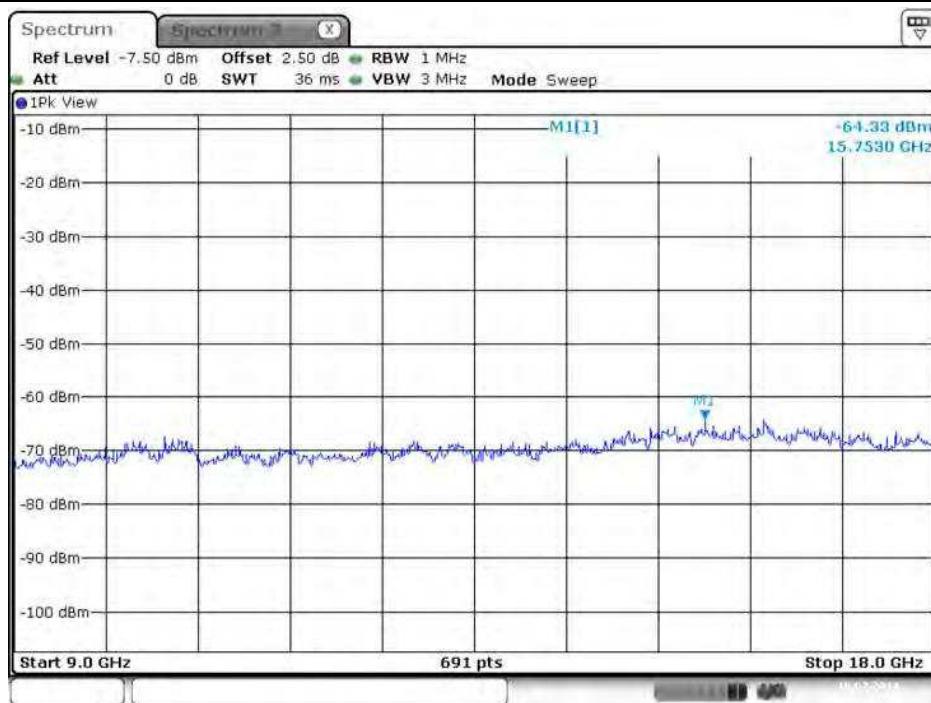
## Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Peak / Port 2 / 9GHz~18GHz



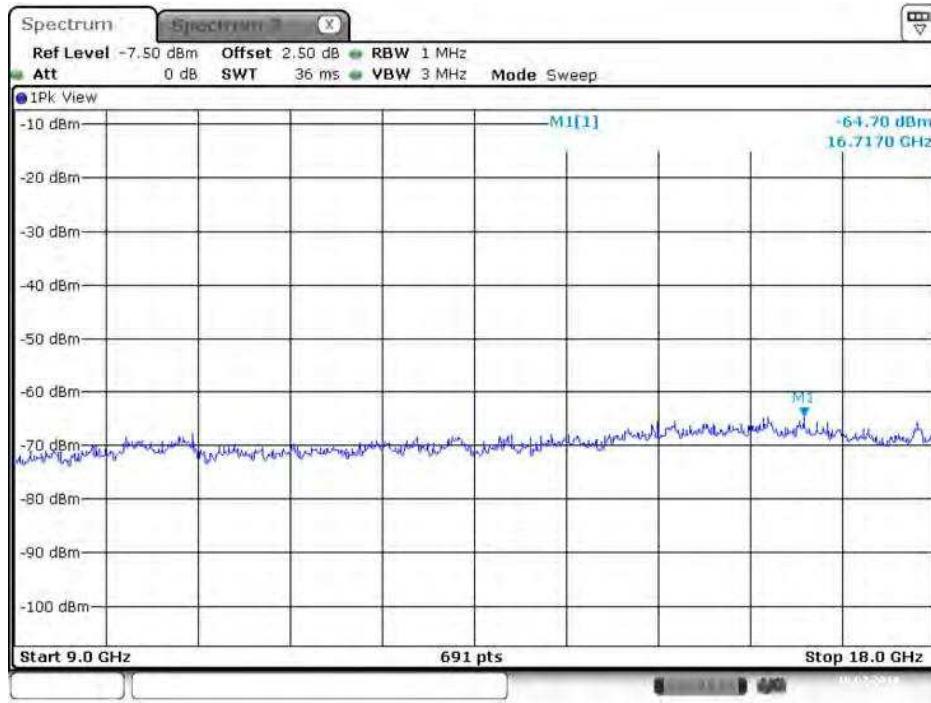
Date: 10.FEB.2018 10:58:41

**Plot on Configuration QPSK, 40M / 5495 MHz / Average / Port 1 / 9GHz~18GHz**

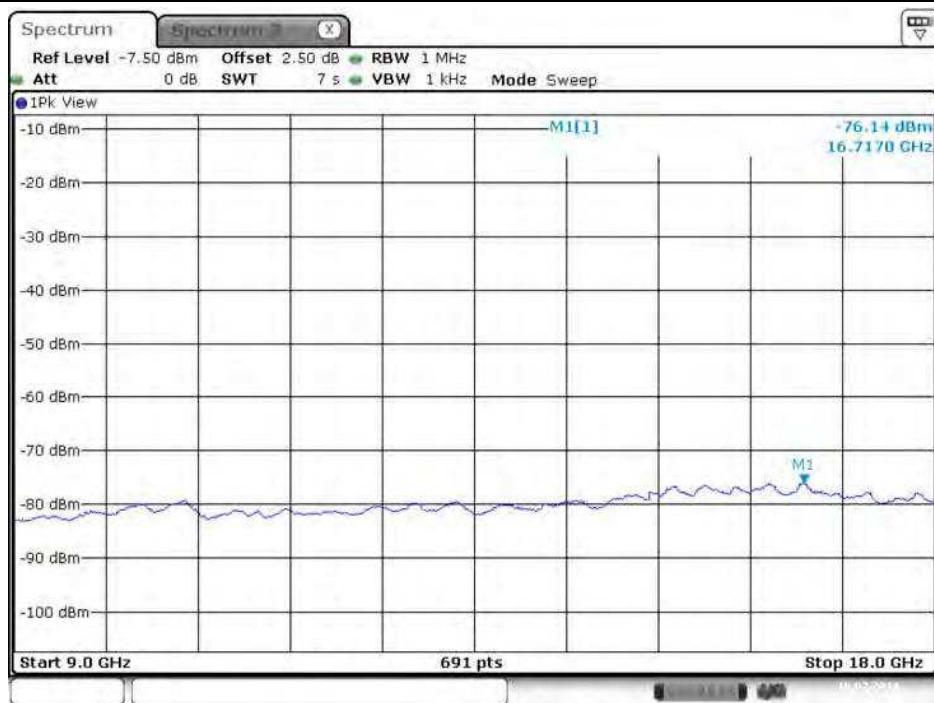
**Plot on Configuration QPSK, 40M / 5495 MHz / Average / Port 2 / 9GHz~18GHz**


**Plot on Configuration QPSK, 40M / 5495 MHz / Peak / Port 1 / 9GHz~18GHz**


Date: 10.FEB.2018 11:01:49

**Plot on Configuration QPSK, 40M / 5495 MHz / Peak / Port 2 / 9GHz~18GHz**


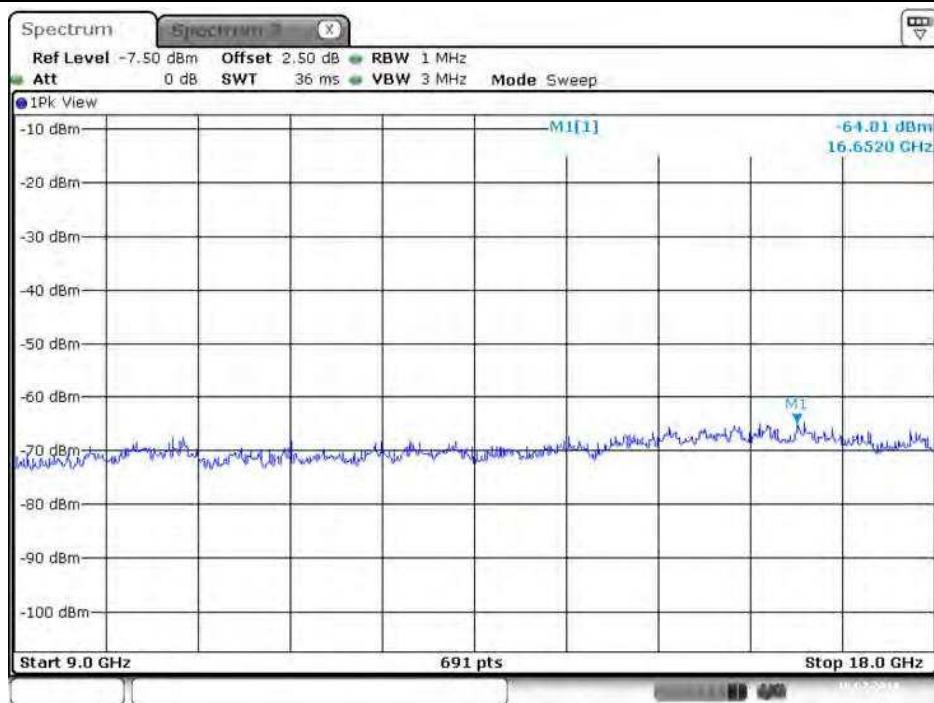
Date: 10.FEB.2018 11:02:37

**Plot on Configuration QPSK, 40M / 5550 MHz / Average / Port 1 / 9GHz~18GHz**


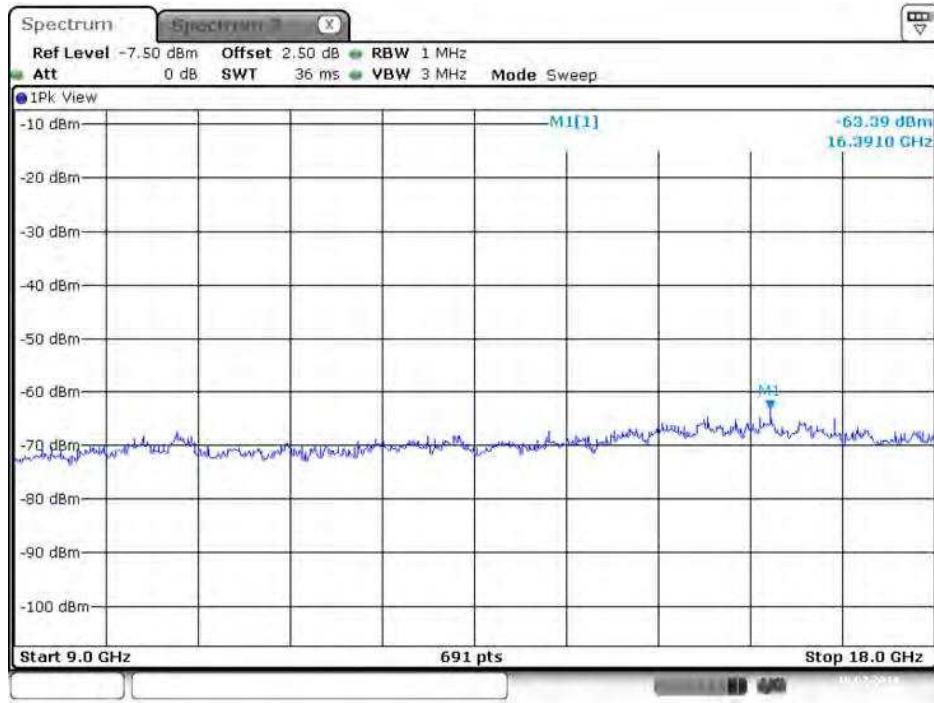
Date: 10.FEB.2018 11:04:30

**Plot on Configuration QPSK, 40M / 5550 MHz / Average / Port 2 / 9GHz~18GHz**


Date: 10.FEB.2018 11:03:32

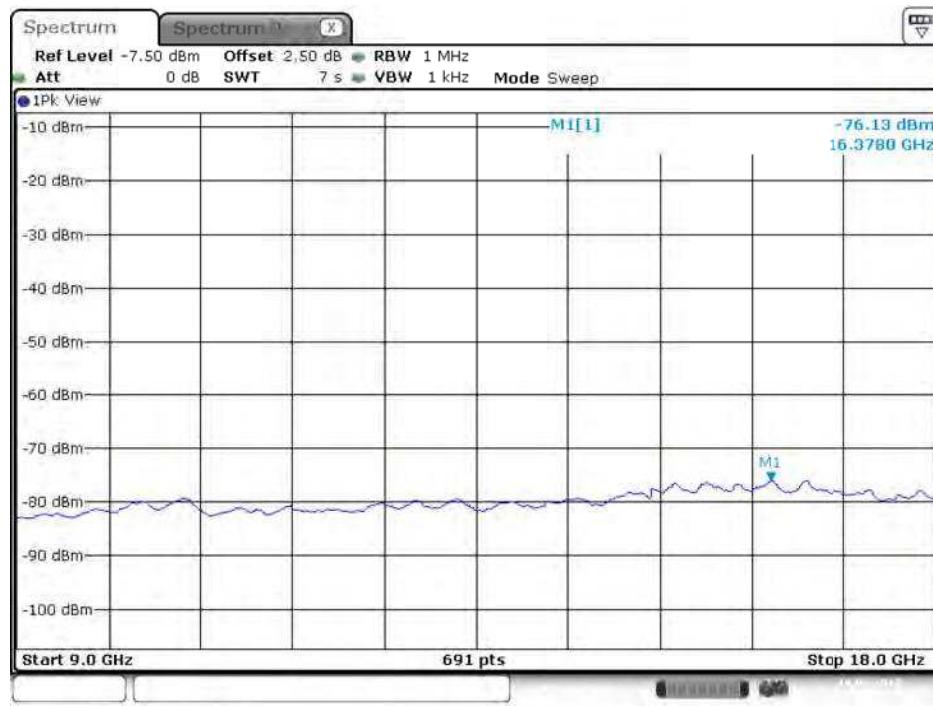
**Plot on Configuration QPSK, 40M / 5550 MHz / Peak / Port 1 / 9GHz~18GHz**


Date: 10.FEB.2018 11:04:38

**Plot on Configuration QPSK, 40M / 5550 MHz / Peak / Port 2 / 9GHz~18GHz**


Date: 10.FEB.2018 11:03:41

**Plot on Configuration QPSK, 40M / 5700 MHz / Average / Port 1 / 9GHz~18GHz**

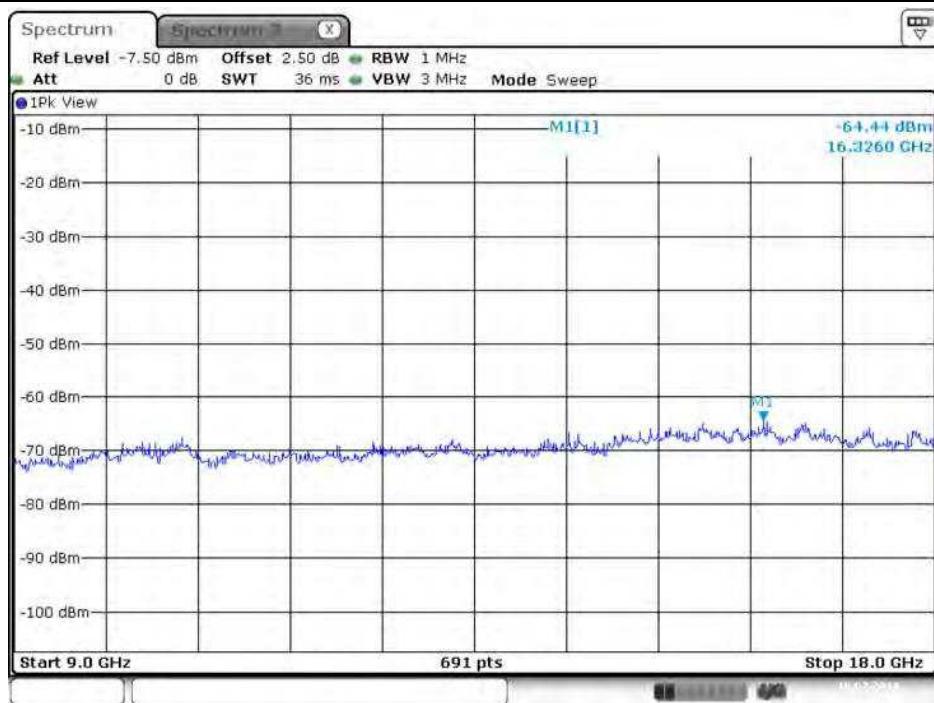
**Plot on Configuration QPSK, 40M / 5700 MHz / Average / Port 2 / 9GHz~18GHz**




## CSE TX above 1GHz Result

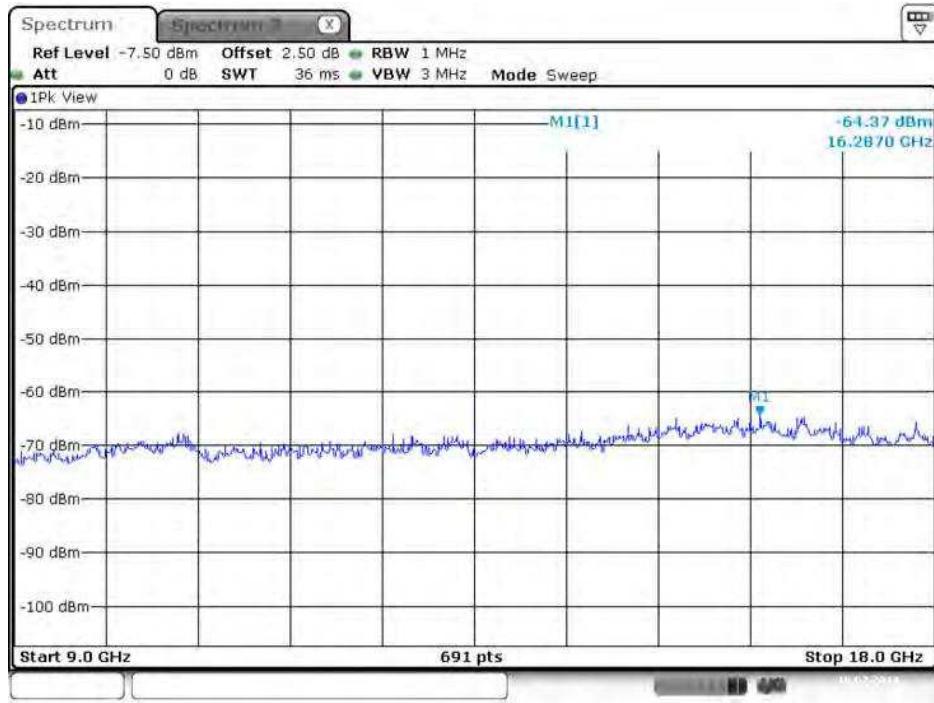
Appendix D.1

### Plot on Configuration QPSK, 40M / 5700 MHz / Peak / Port 1 / 9GHz~18GHz

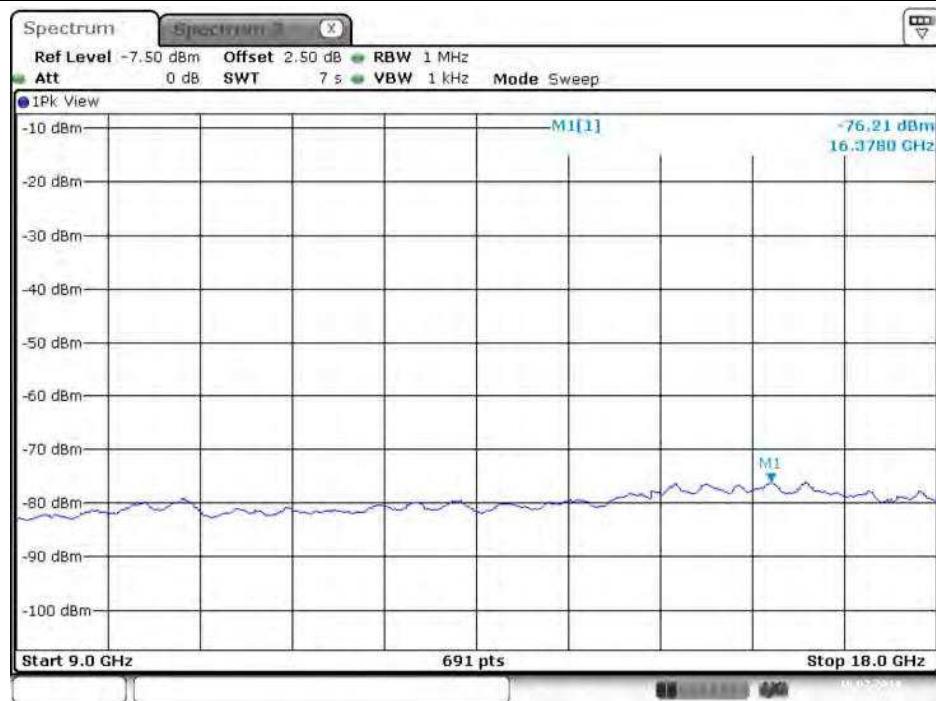


Date: 10.FEB.2018 11:05:30

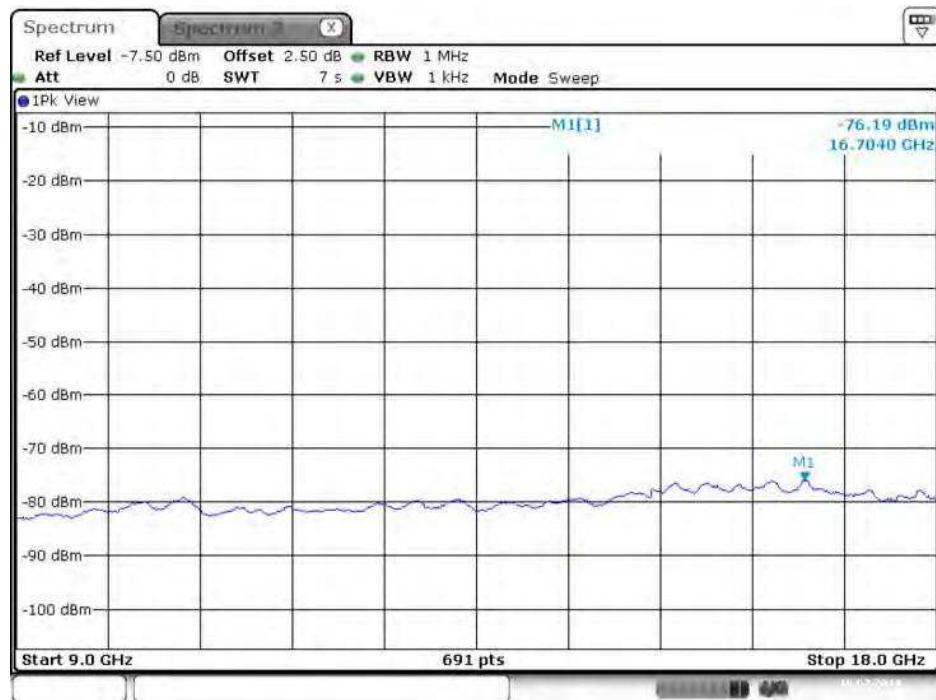
### Plot on Configuration QPSK, 40M / 5700 MHz / Peak / Port 2 / 9GHz~18GHz



Date: 10.FEB.2018 11:06:16

**Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Average / Port 1 / 9GHz~18GHz**


Date: 10.FEB.2018 11:08:02

**Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Average / Port 2 / 9GHz~18GHz**


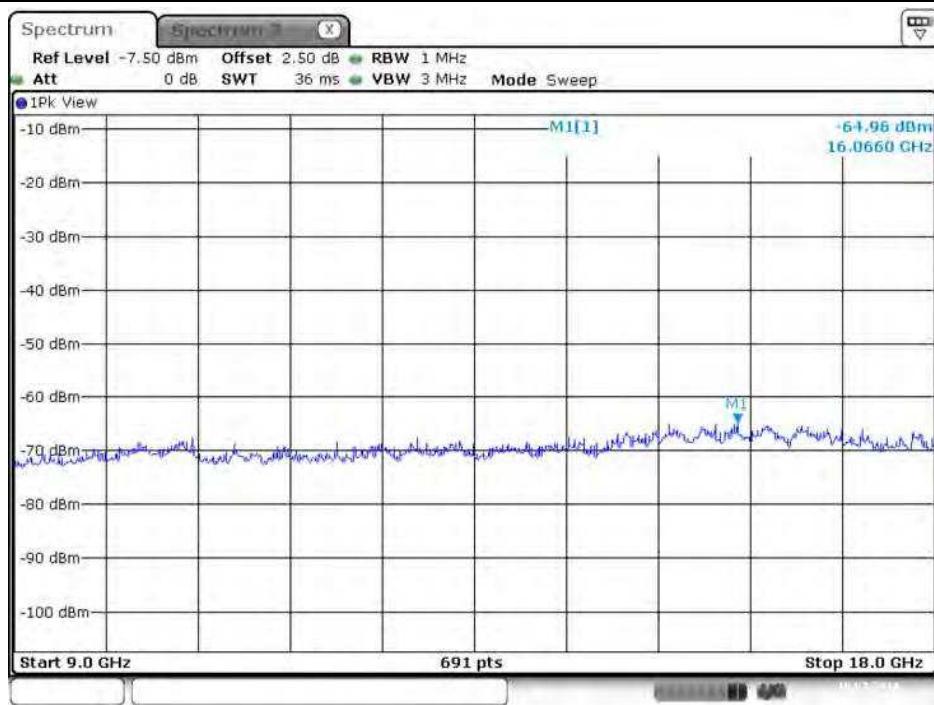
Date: 10.FEB.2018 11:07:05



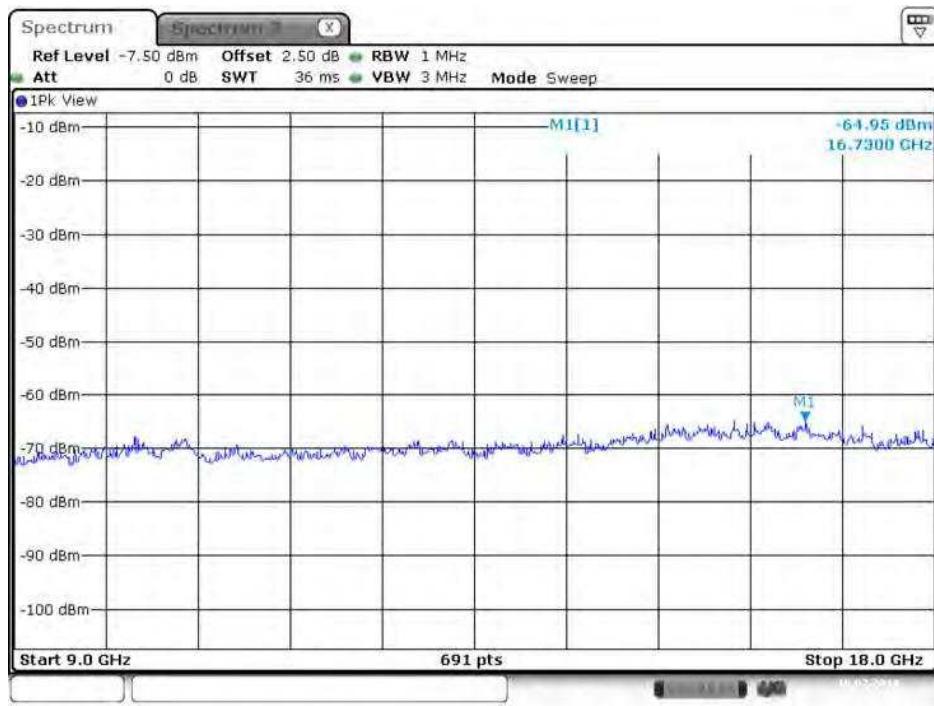
## CSE TX above 1GHz Result

Appendix D.1

### Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Peak / Port 1 / 9GHz~18GHz



### Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Peak / Port 2 / 9GHz~18GHz

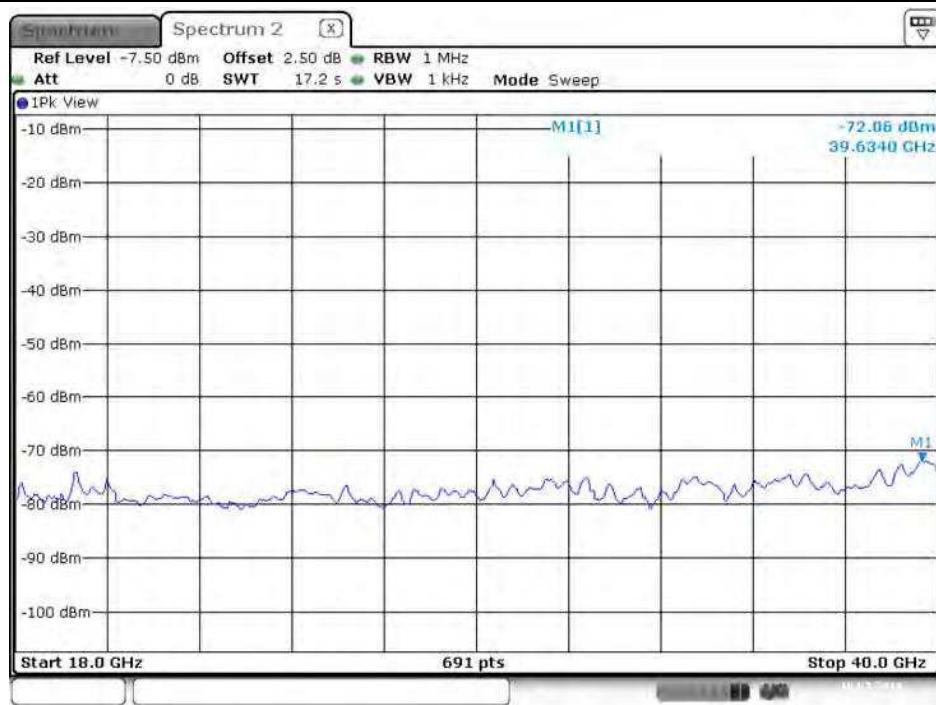




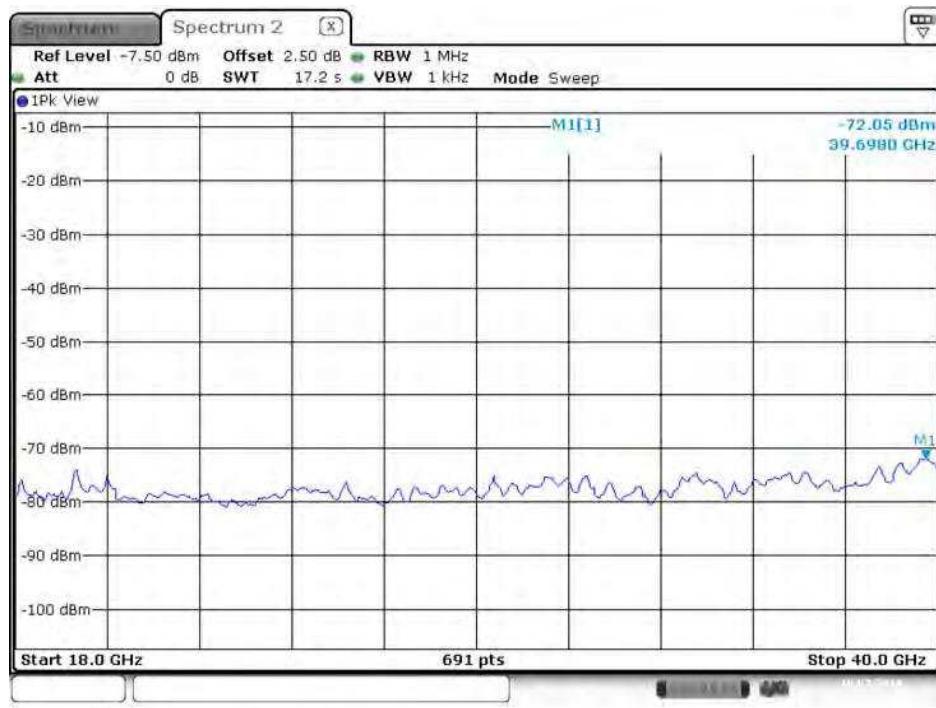
## CSE TX above 1GHz Result

Appendix D.1

### Plot on Configuration QPSK, 10M / 5480 MHz / Average / Port 1 / 18GHz~40GHz



### Plot on Configuration QPSK, 10M / 5480 MHz / Average / Port 2 / 18GHz~40GHz

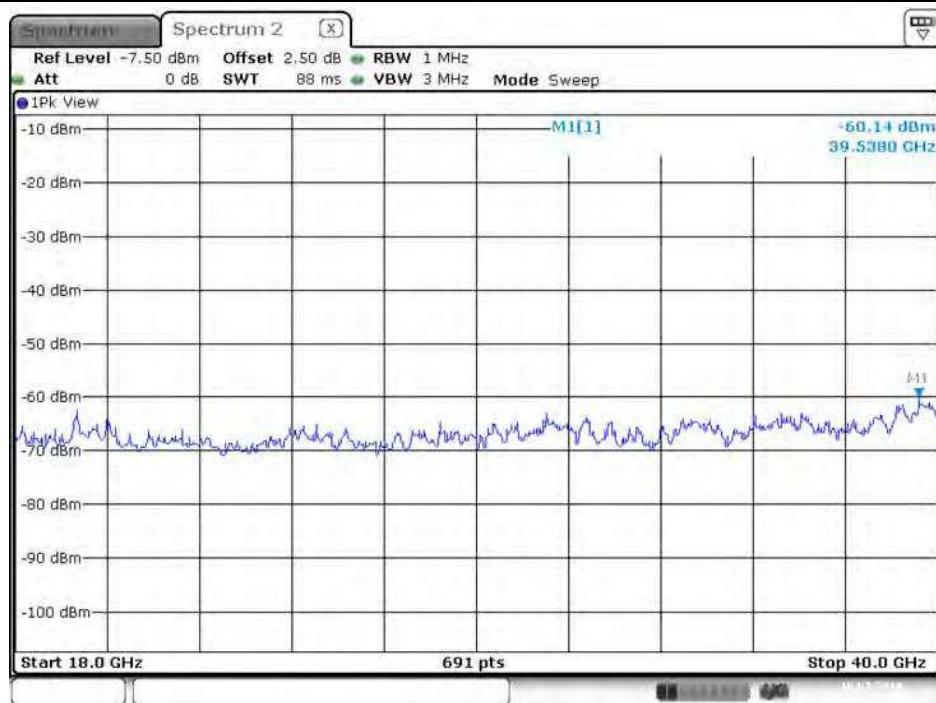




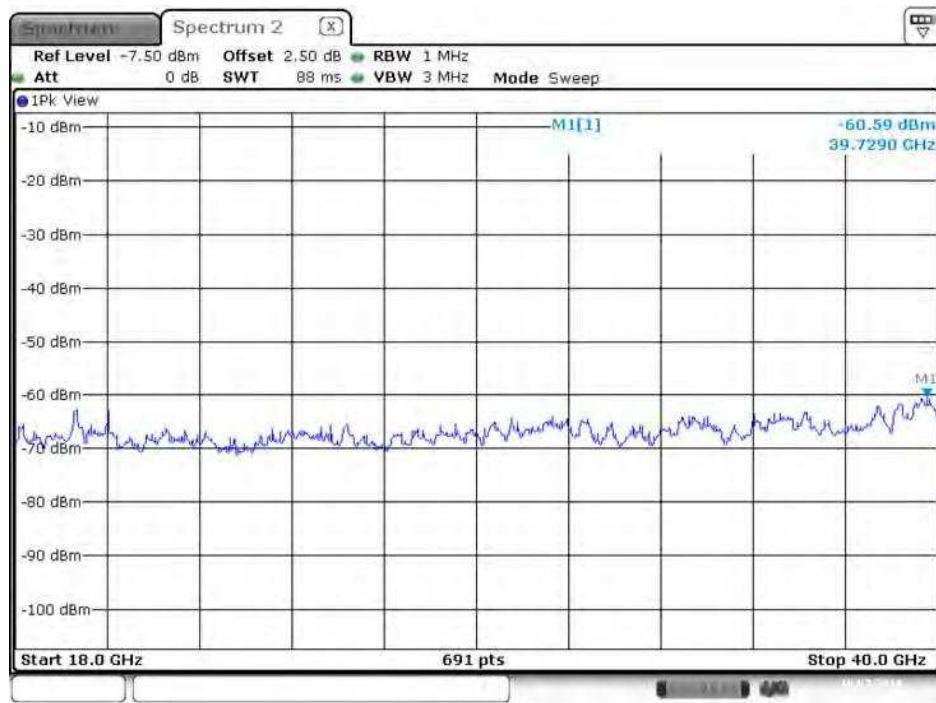
## CSE TX above 1GHz Result

Appendix D.1

### Plot on Configuration QPSK, 10M / 5480 MHz / Peak / Port 1 / 18GHz~40GHz



### Plot on Configuration QPSK, 10M / 5480 MHz / Peak / Port 2 / 18GHz~40GHz

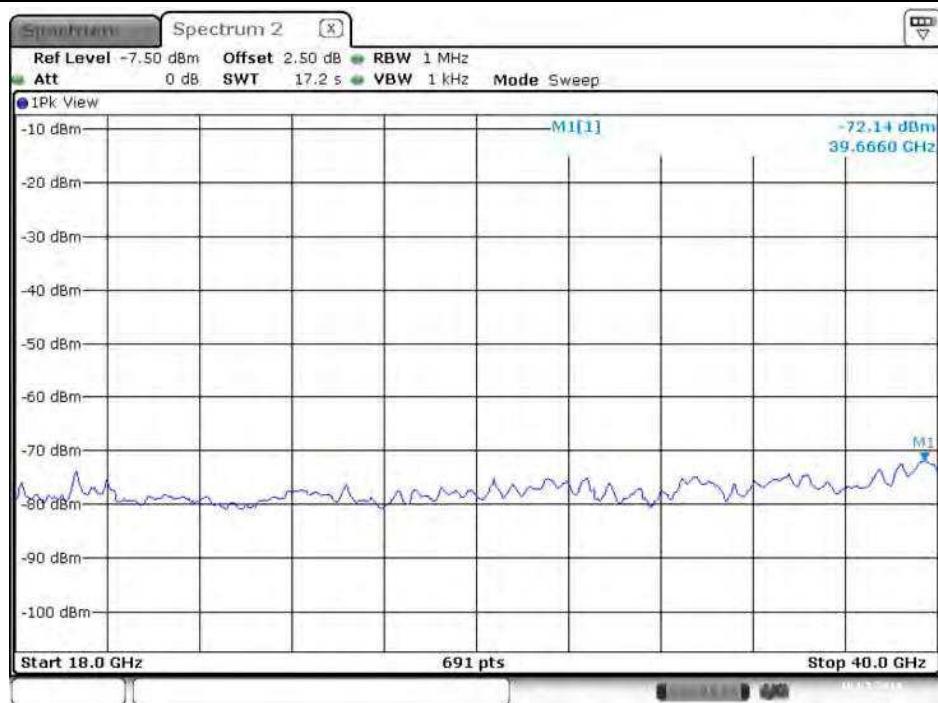




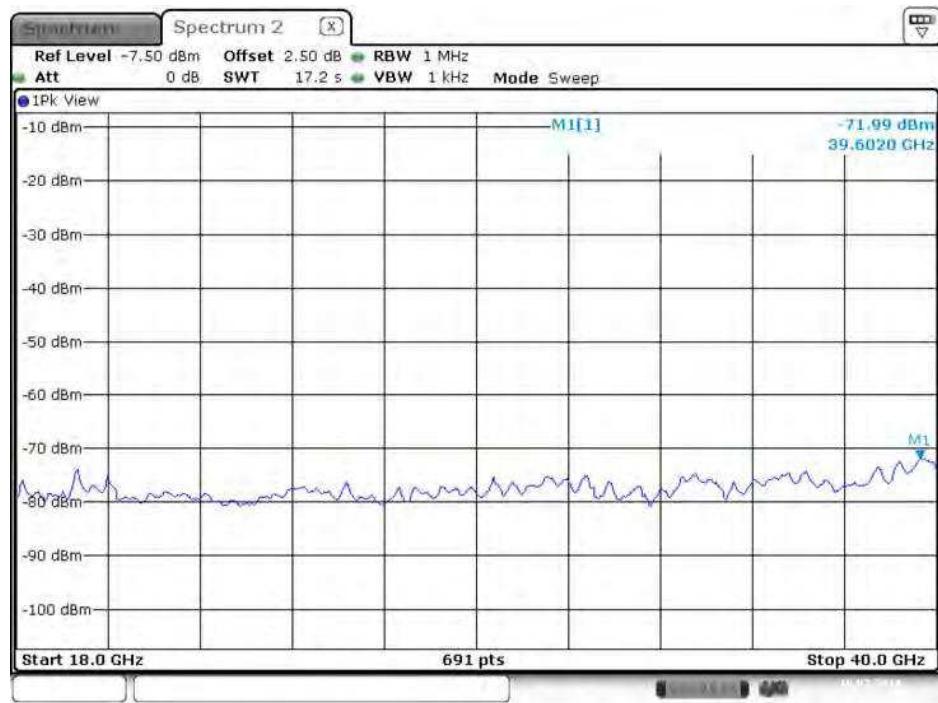
## CSE TX above 1GHz Result

Appendix D.1

### Plot on Configuration QPSK, 10M / 5575 MHz / Average / Port 1 / 18GHz~40GHz

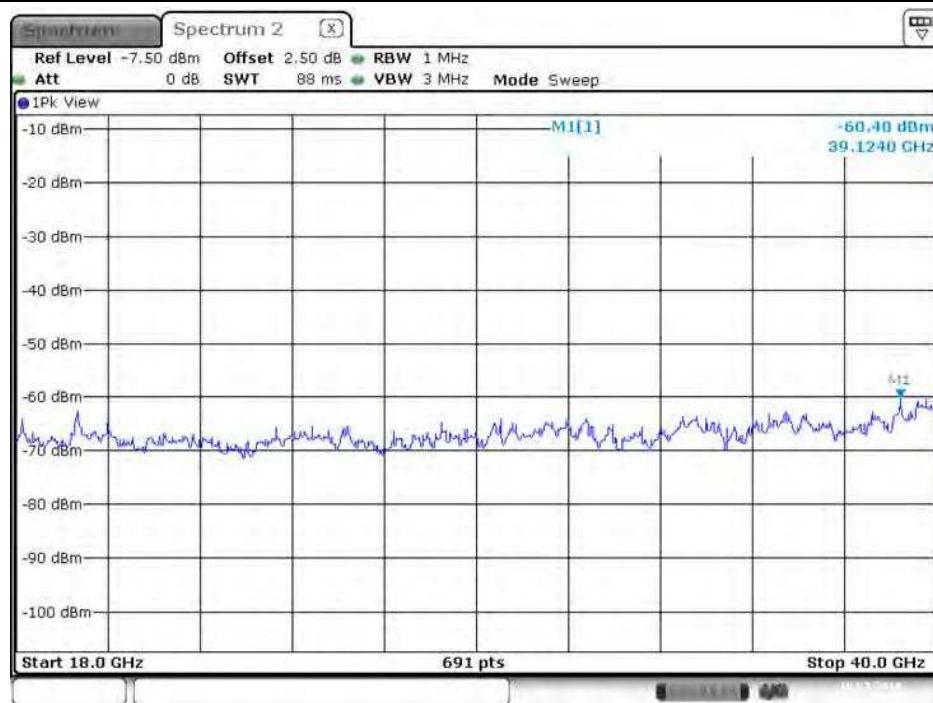


### Plot on Configuration QPSK, 10M / 5575 MHz / Average / Port 2 / 18GHz~40GHz

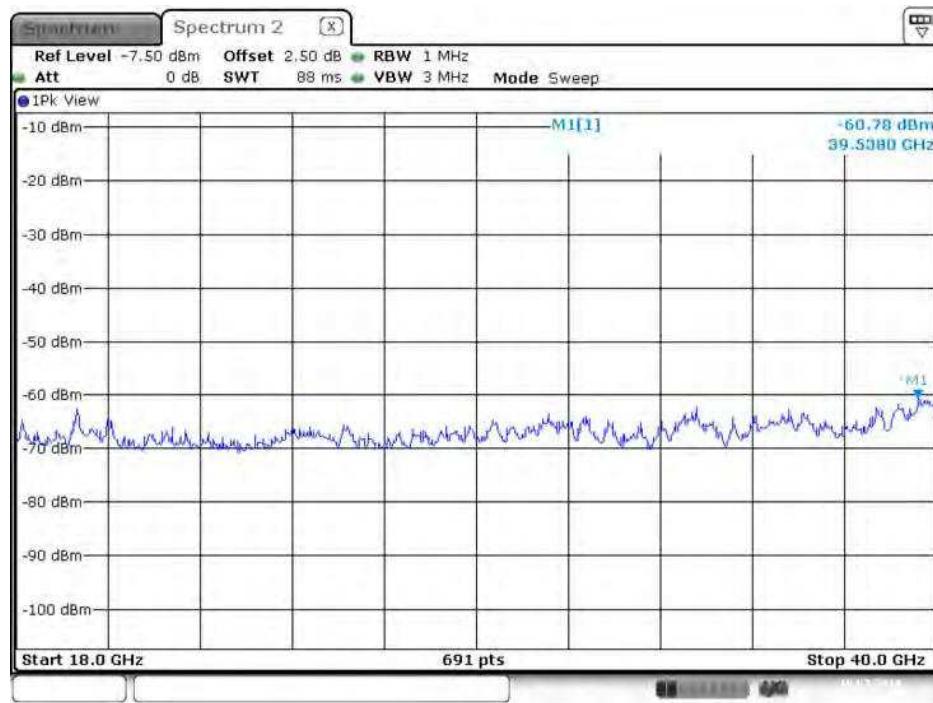




## Plot on Configuration QPSK, 10M / 5575 MHz / Peak / Port 1 / 18GHz~40GHz



## Plot on Configuration QPSK, 10M / 5575 MHz / Peak / Port 2 / 18GHz~40GHz

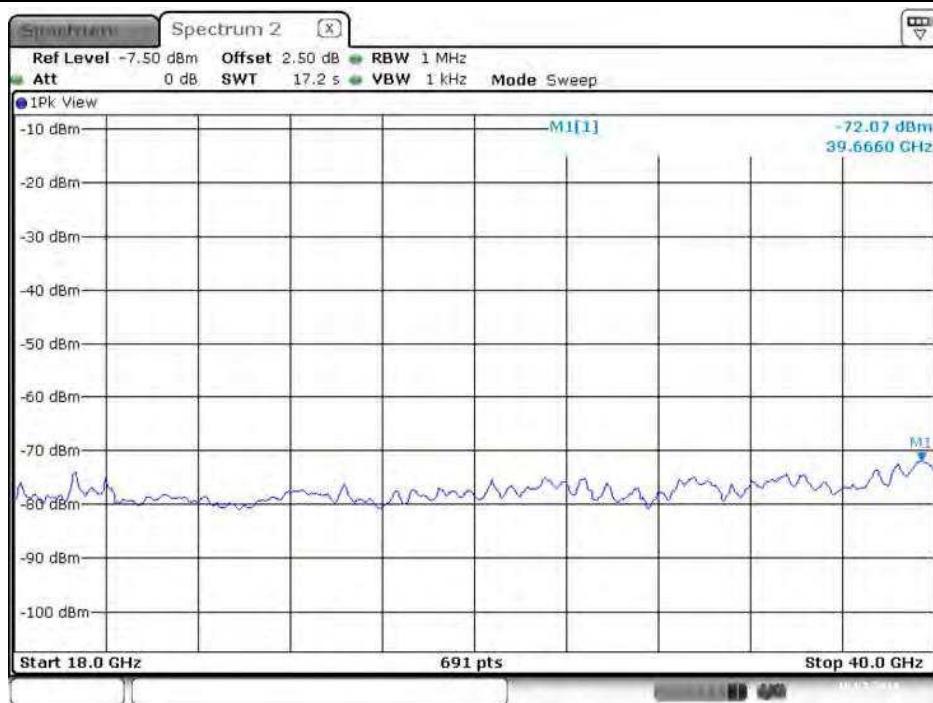




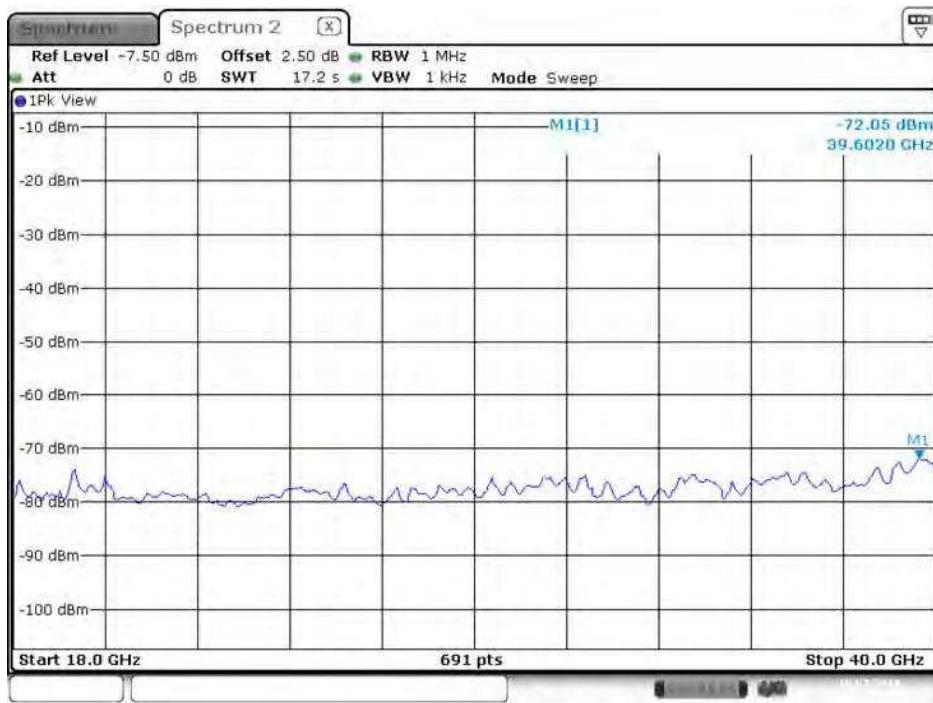
## CSE TX above 1GHz Result

Appendix D.1

### Plot on Configuration QPSK, 10M / 5715 MHz / Average / Port 1 / 18GHz~40GHz

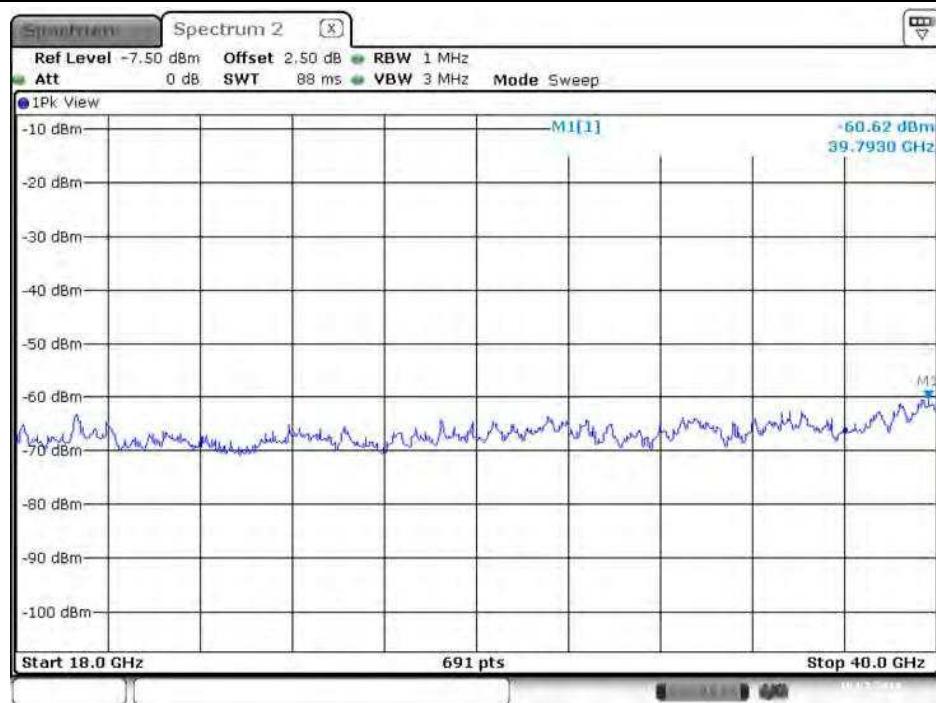


### Plot on Configuration QPSK, 10M / 5715 MHz / Average / Port 2 / 18GHz~40GHz

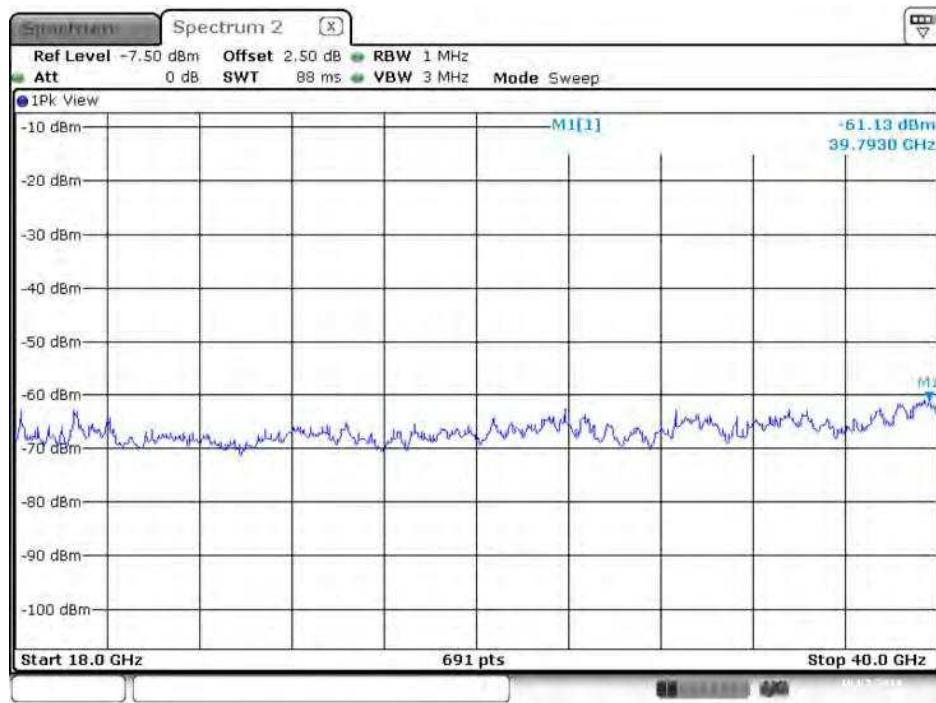


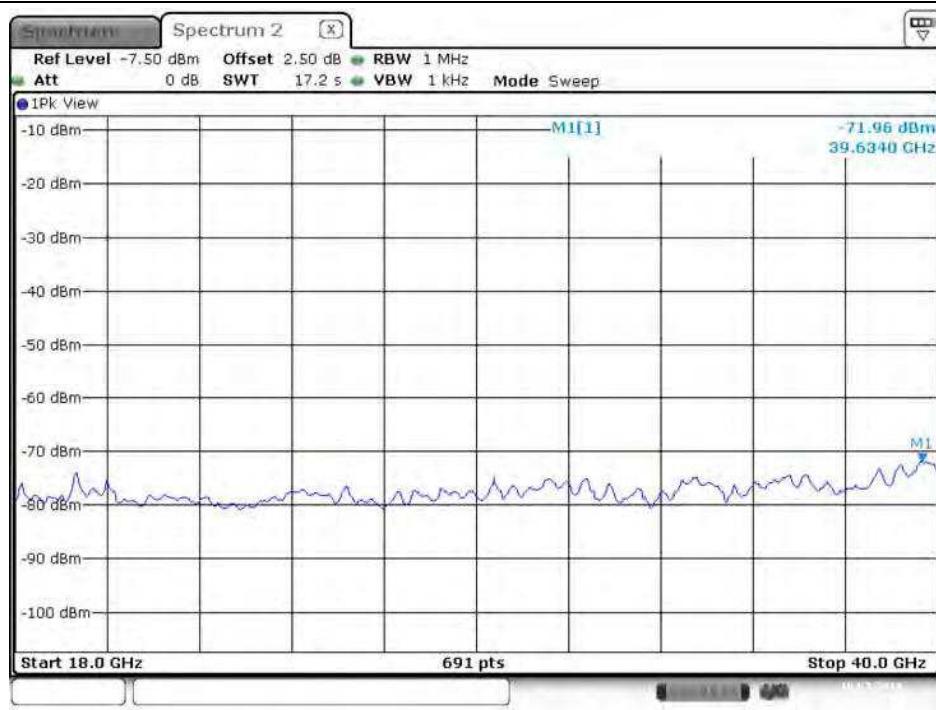


## Plot on Configuration QPSK, 10M / 5715 MHz / Peak / Port 1 / 18GHz~40GHz

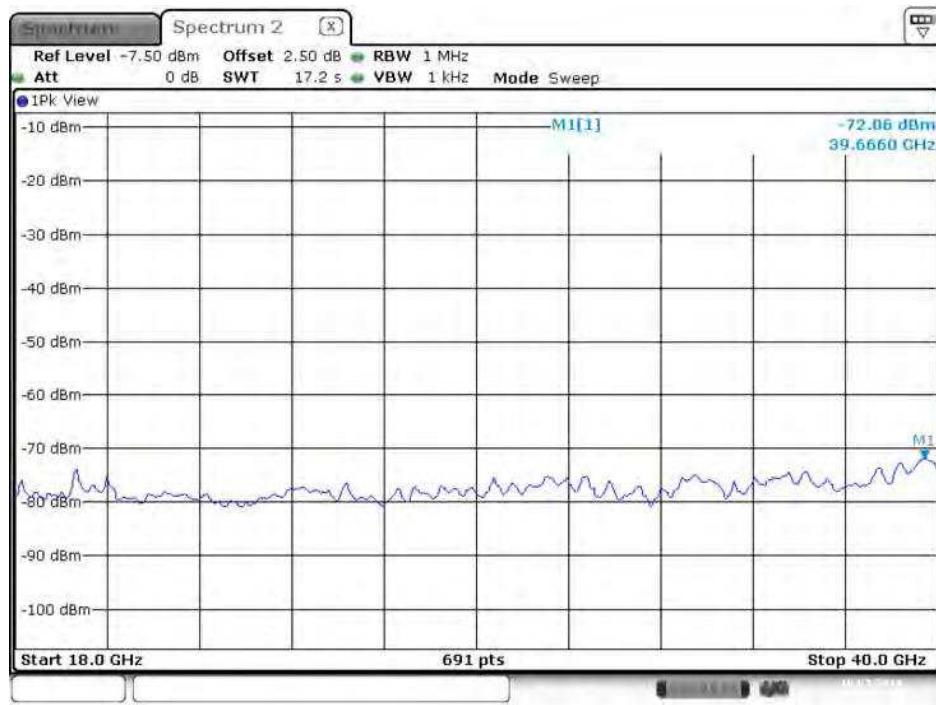


## Plot on Configuration QPSK, 10M / 5715 MHz / Peak / Port 2 / 18GHz~40GHz



**Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Average / Port 1 / 18GHz~40GHz**


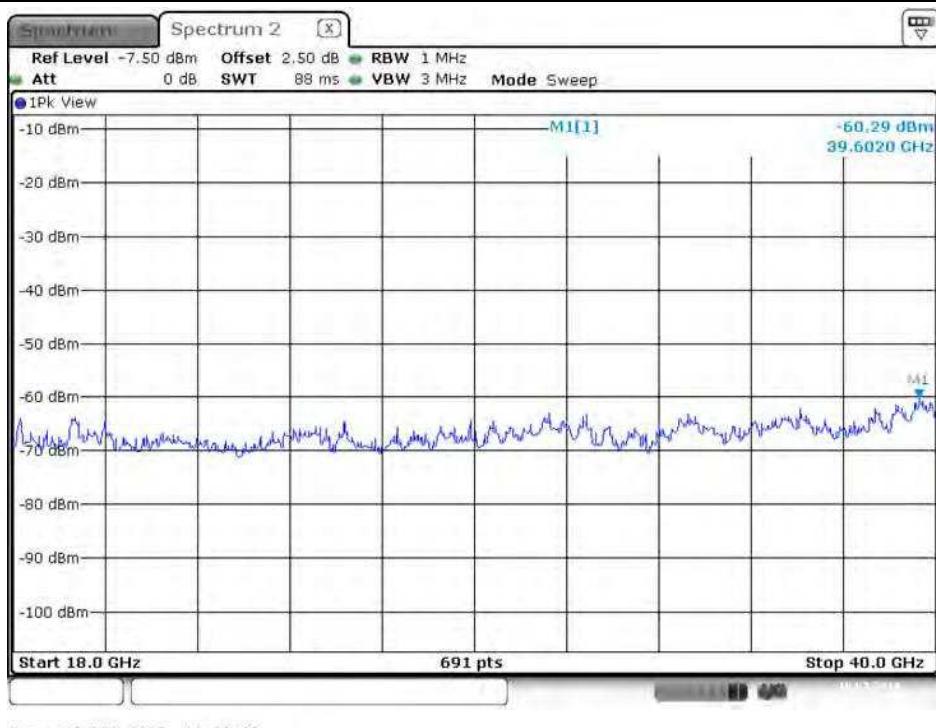
Date: 10.FEB.2018 10:30:16

**Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Average / Port 2 / 18GHz~40GHz**


Date: 10.FEB.2018 10:29:06



## Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Peak / Port 1 / 18GHz~40GHz



## Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Peak / Port 2 / 18GHz~40GHz