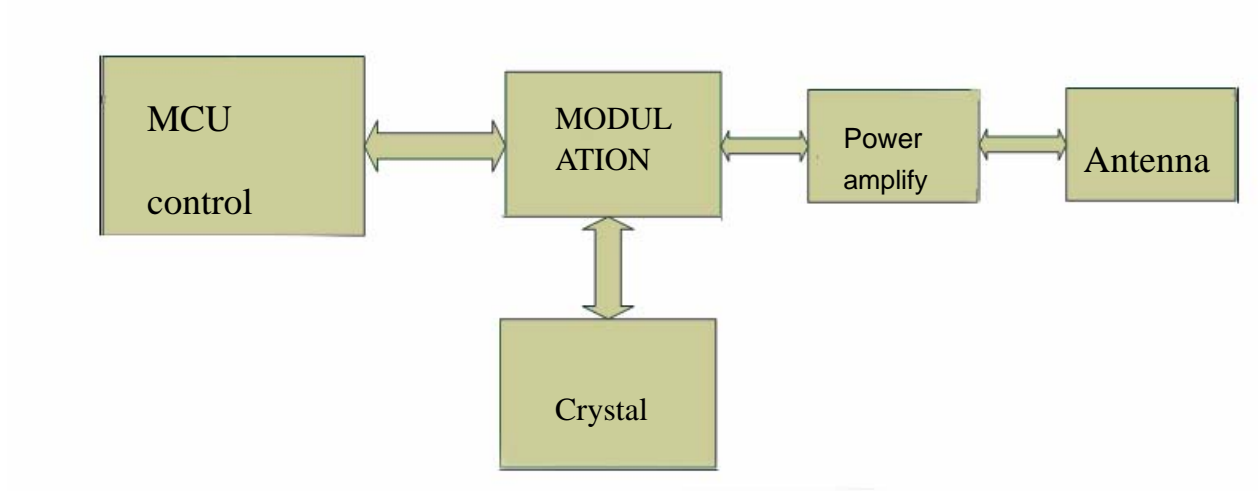# FCC ID Z96MP-PAC

Features

◆ Frequency between the 2412MHz ~ 2472MHz;

◆ Design of the output power up to +13 dbm or +14dbm;

Module Block Diagram

Modulation: T ÙSÌ£ÙSÁ

a) Amplitude modulation

LT2011 supports two different forms of amplitude modulationmode: off keying and amplitude shift keying. Off keyingmodulation, respectively, through simply set to "1" or "0" to turn onor turn off power make a big function, and amplitude shift keyingcan be programmed to adjust to a more sophisticated range ofsizes.

b) Frequency Shift Keying

FSK symbol coding as follows:

Format	Symbol	Coding
2FSK	'0'	– Deviation
	'1'	+ Deviation

c) Phase shift keying

Phase shift keying is in transit to maintain a certain Period to transmit data.

Limit rating

Parameter	Min	Max	Unit	Remarks
Input voltage	2. 8	3. 6	V	All power supply pin have the same voltage input
Max output power		14	dbm	
Max data transfer rate		250	Kbps	
Operating Temperature	−40	85	° C	
Storage Temperature	−50	150	° C	

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