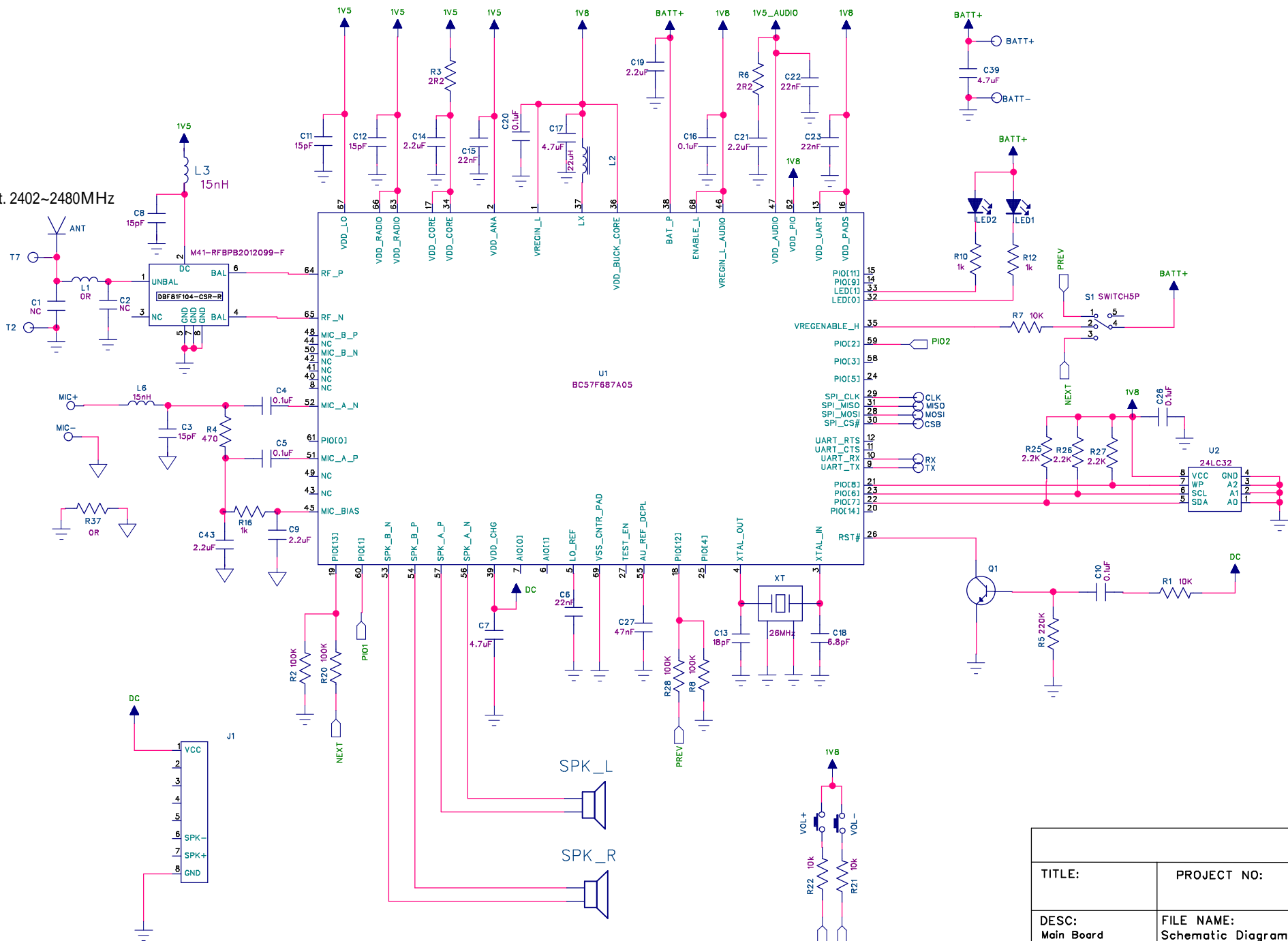


Ant. 2402~2480MHz



TITLE:	PROJECT NO:	SHEET 1 Of 1	VER: 0A
DESC: Main Board	FILE NAME: Schematic Diagram	CD NO:	
DRAWN:	CHECKED BY:	APPROVED BY:	
09/May/2010			