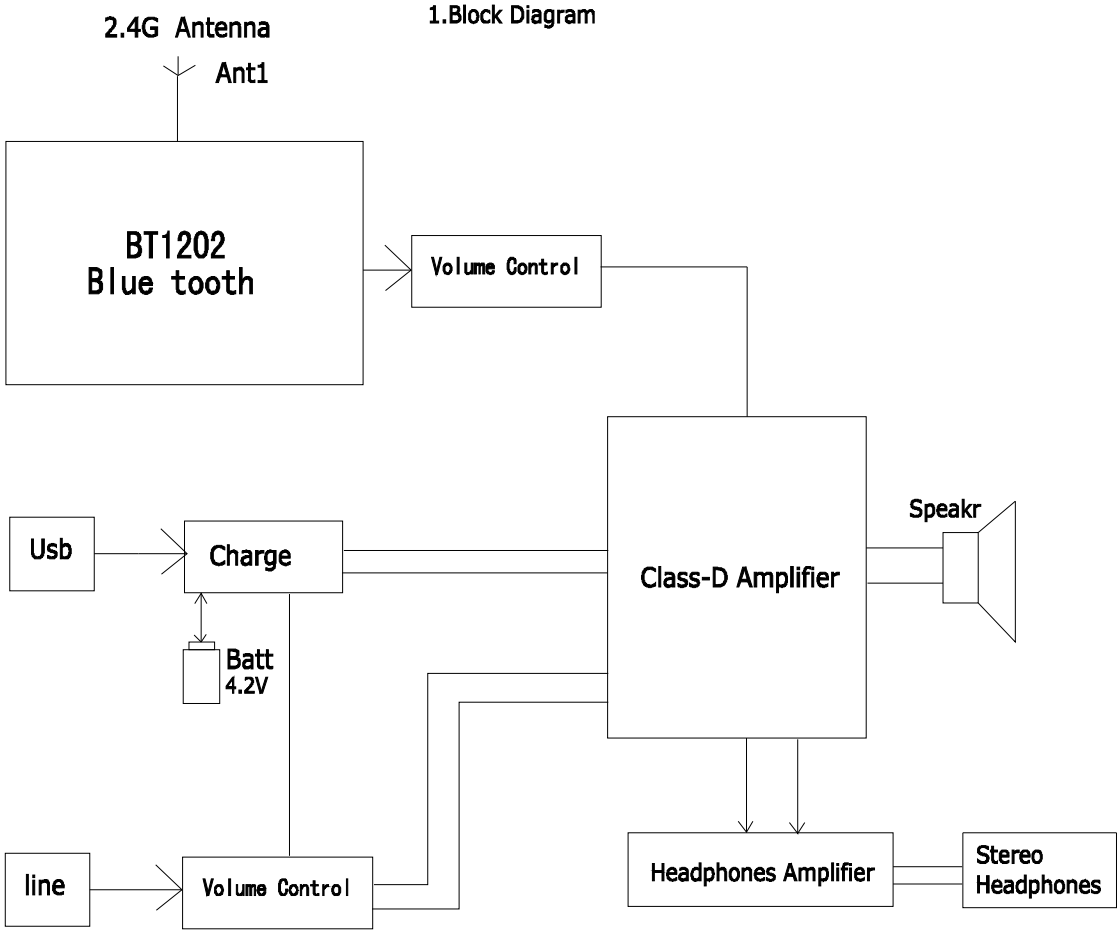


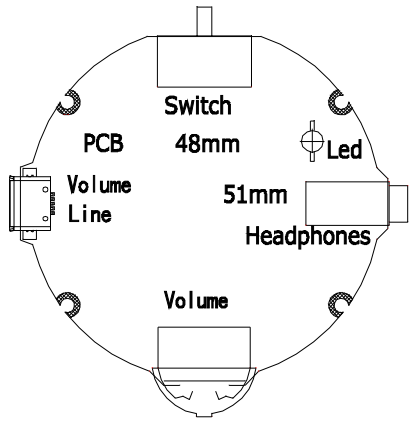
1.Block Diagram



2: PCB Layout Definition

- 1: Top Traces
- 2: Bottom Traces

3: PCB Interface Definition



Shenzhen Akun Electronic Co.,Ltd.

Model : C3BR

DRN BY: JIANPING ZHANG

Rev

0.0