Annex A, additional

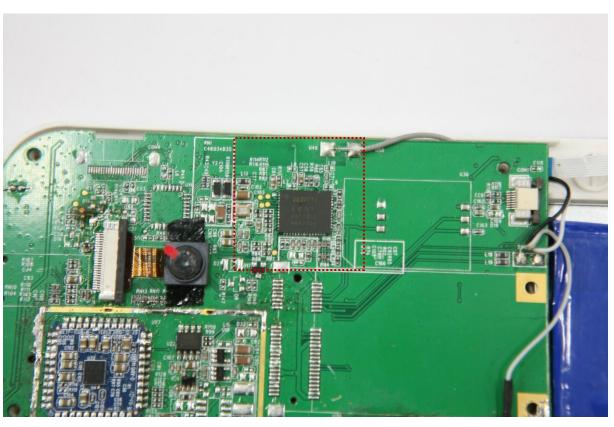


Fig.1 (Internal Photo: RF chip)