# Annex no. 4

## Label Sample

#### Label sample win master



### **Label location win master**



#### Label sample win slave



#### Label location win slave



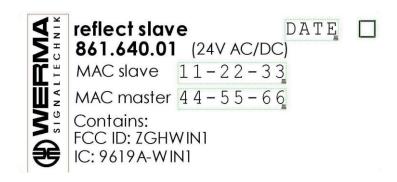
### Label sample reflect master



#### Label location reflect master



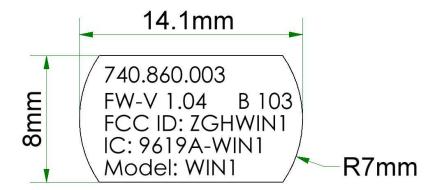
## Label sample reflect slave



#### Label location reflect slave



### Label sample module WIN1



## **Label location module WIN1**

