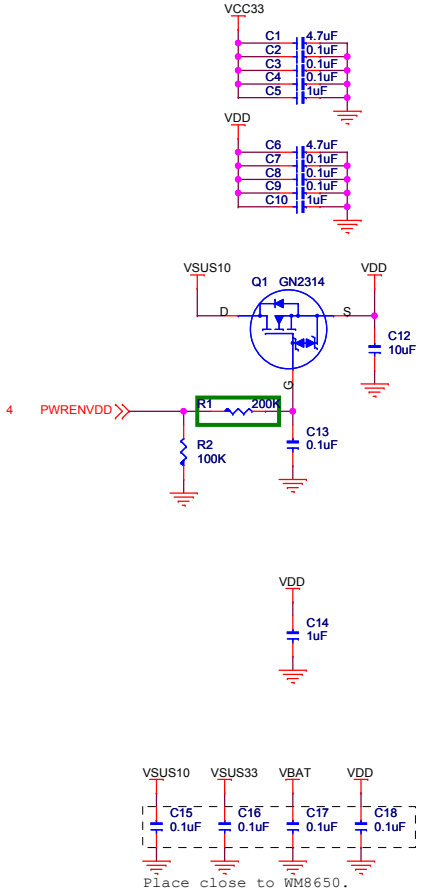
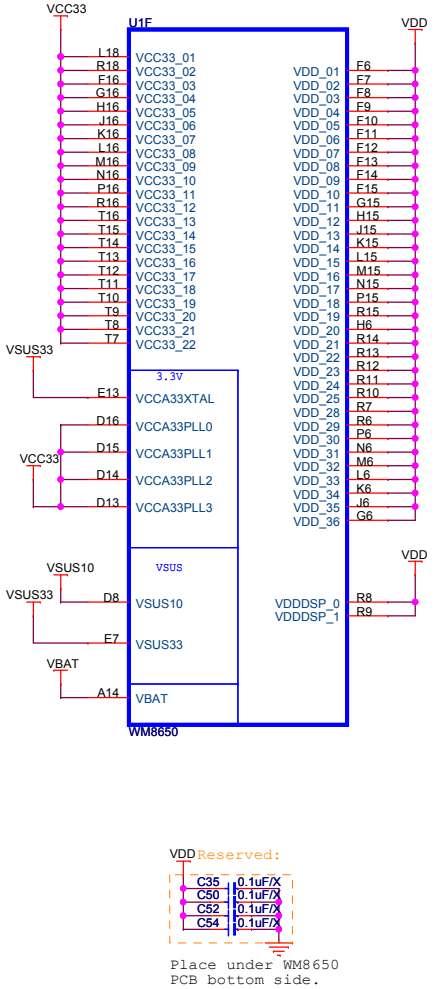
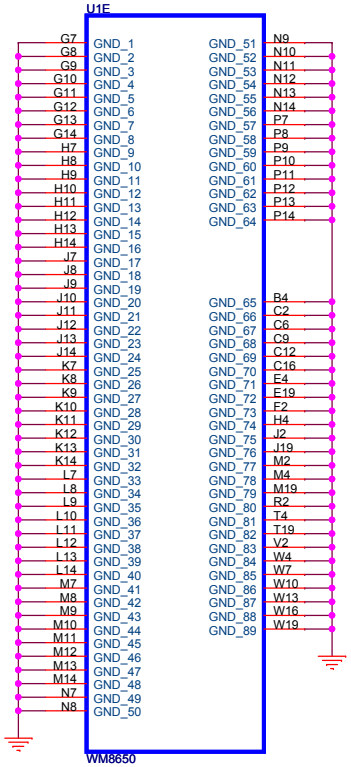
WM8650: Power / Ground

PART: E+F







WonderMedia Technologies, Inc.		
Title: WM8650: Power/Ground		
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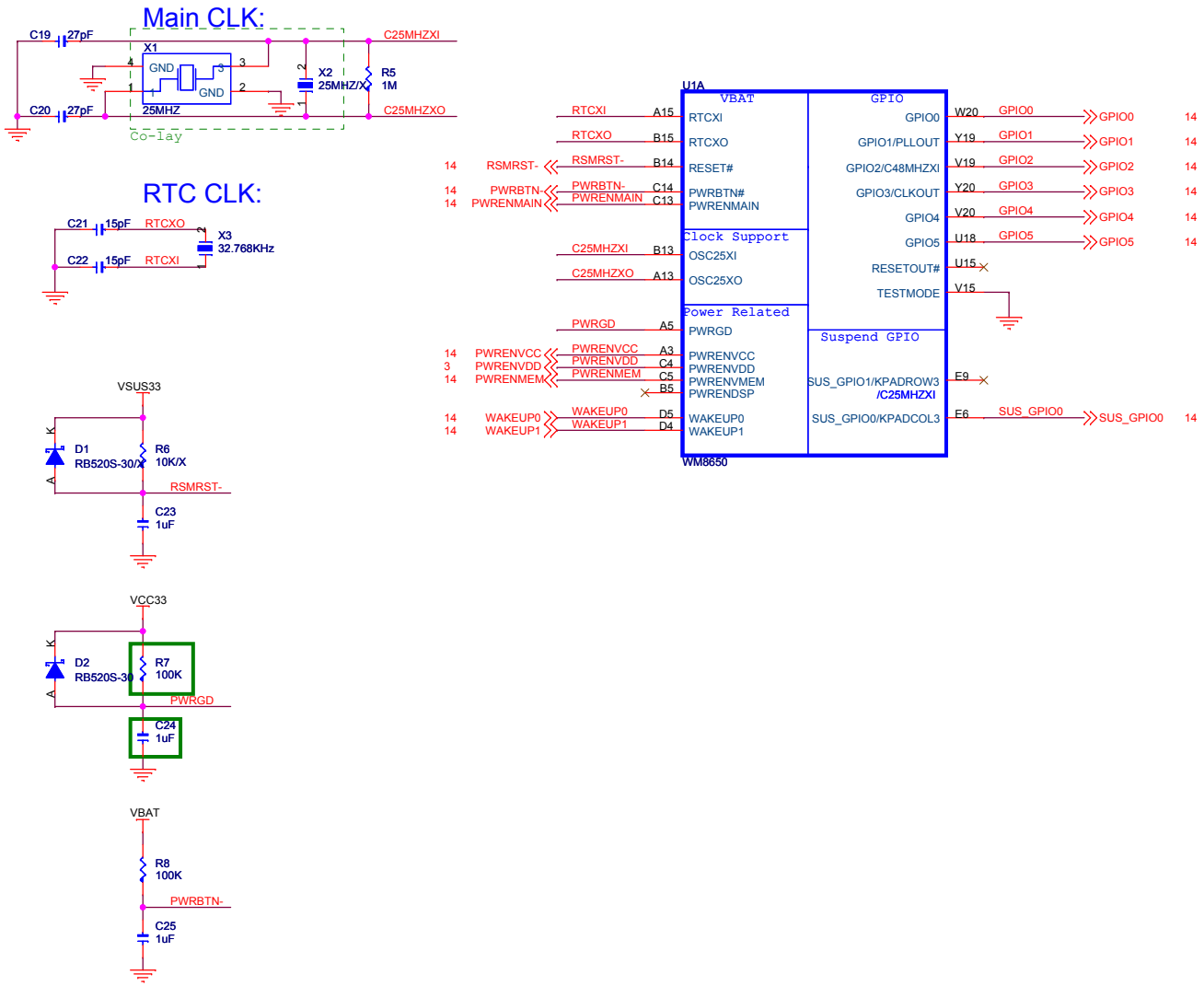
WM8650: CLOCK/RESET/PMC/GPIO

PART:A



WonderMedia Technologies, Inc.			
Title: WM8650: CLOCK/RESET/PMC/GPIO			
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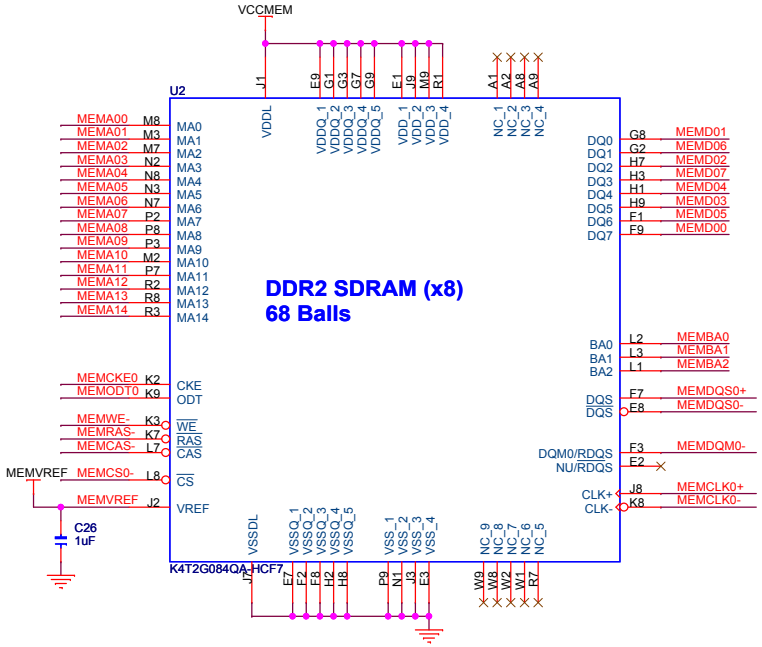
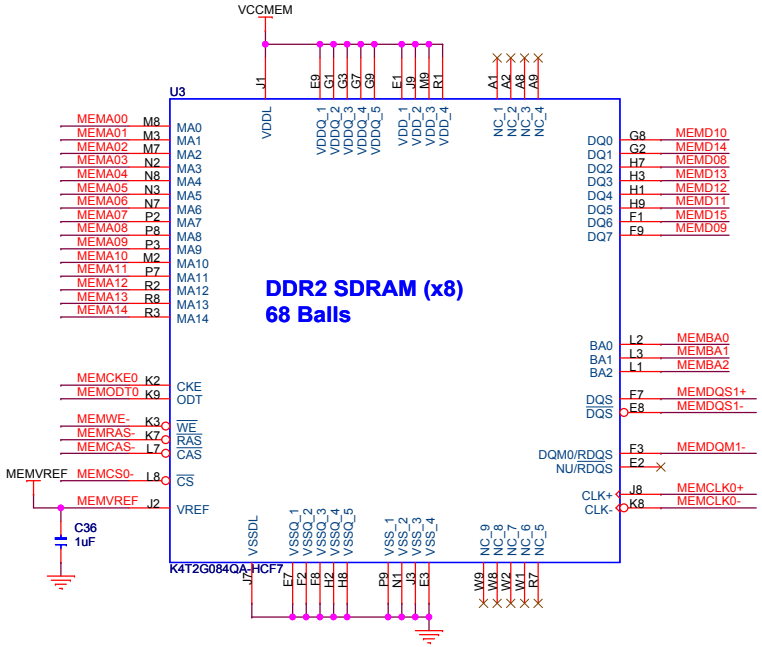
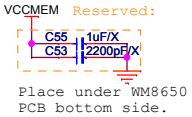
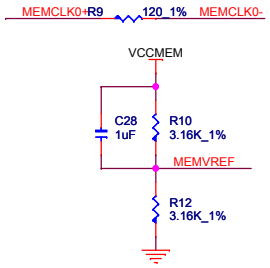
WM8650: DDR2 SDRAM

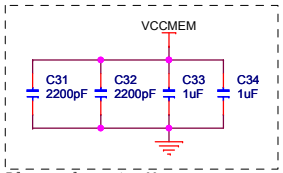
PART: B

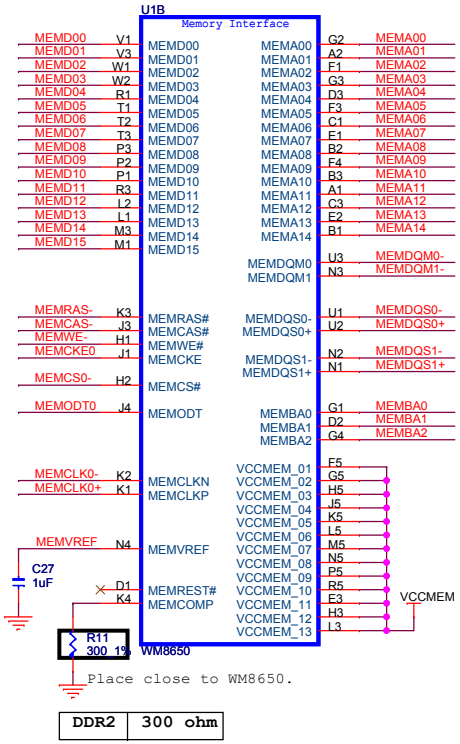




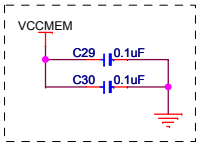








DDR2	300 ohm



WonderMedia Technologies, Inc.

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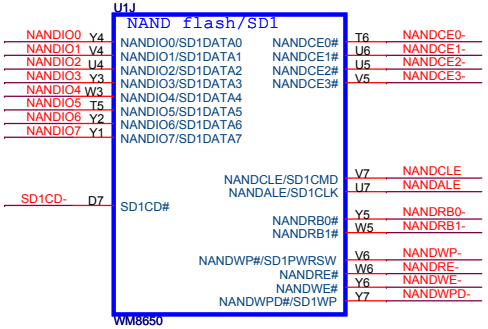
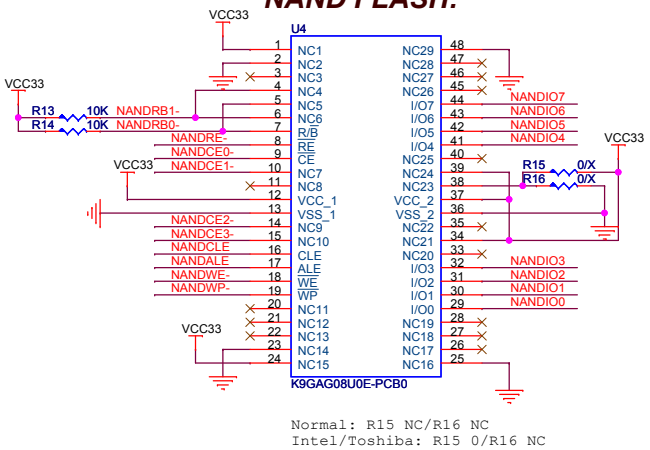
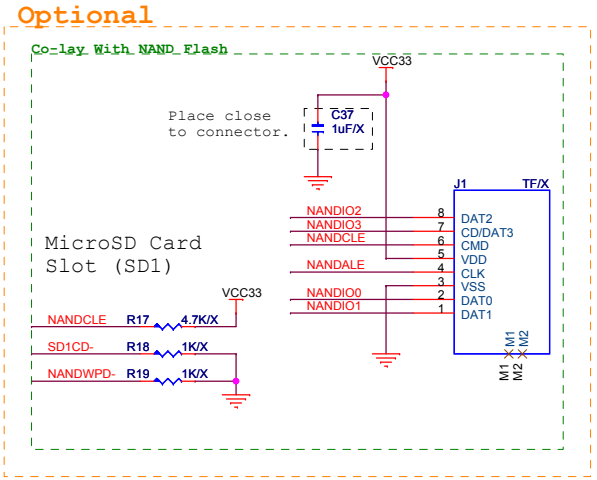
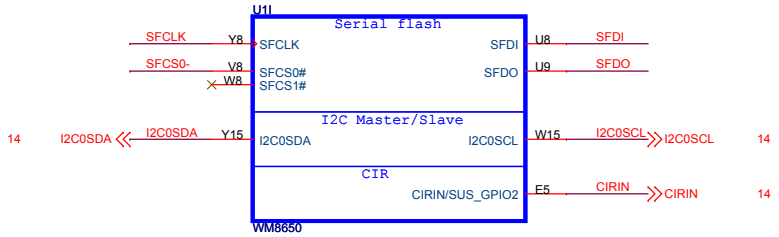
WM8650: NAND FLASH
PART: J

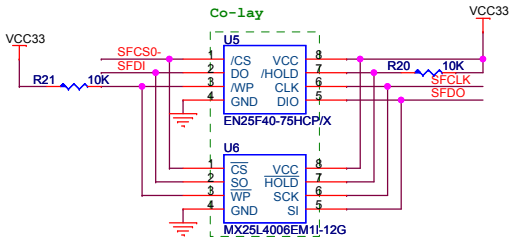


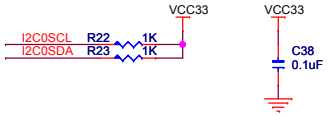




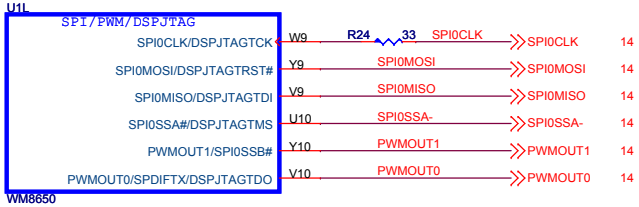
WM8650: SPI FLASH
PART: I







WM8650: SPI + PWM
PART: L



WonderMedia Technologies, Inc.

Title	WM8650: NAND FLASH	
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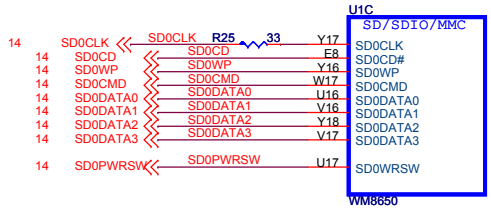
WM8650: SD/SDIO

PART: C



WonderMedia Technologies, Inc.

Title	WM8650: SD/SDIO	
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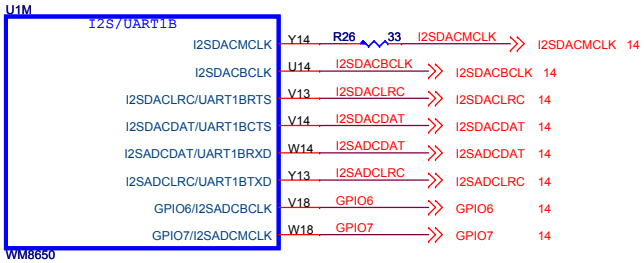
WM8650: I2S

PART: M



WonderMedia Technologies, Inc.		
Title: WM8650: I2S		
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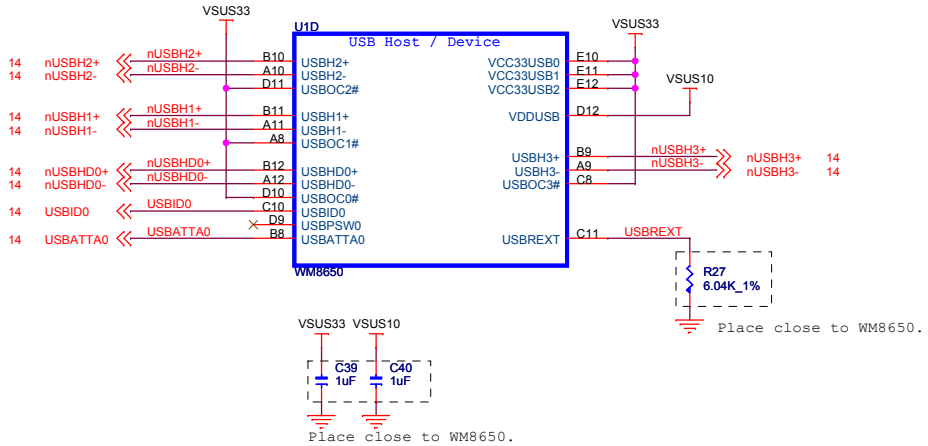
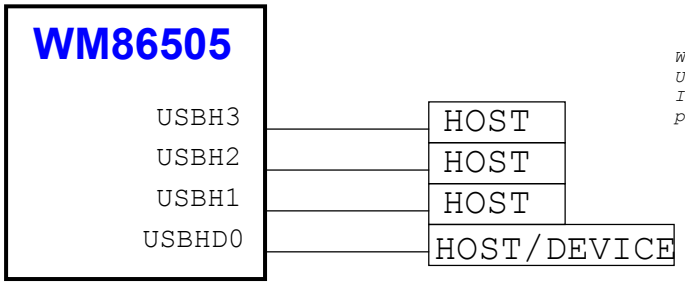
WM8505: USB (HOST+DEVICE)

PART: D



(USBH3) to
(USBH2) to
(USBH1) to
(USBHD0) to USB mini-AB connector.



Wakeup signal (USBATTA) is asserted from external USB-Host only.
If WM8650 at USB-Host mode, should ignore USBATTA pin

WonderMedia Technologies, Inc.		
Title: WM8650: USB (H/D)		
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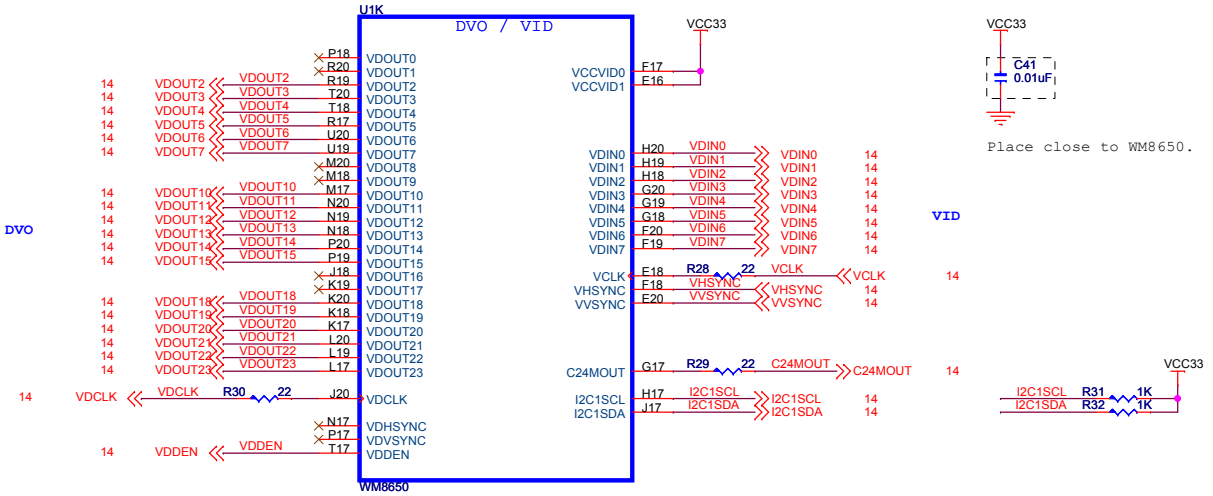
WM8650: DVO/VID

PART: K



WonderMedia Technologies, Inc.		
Title: WM8650: DVO/VID		
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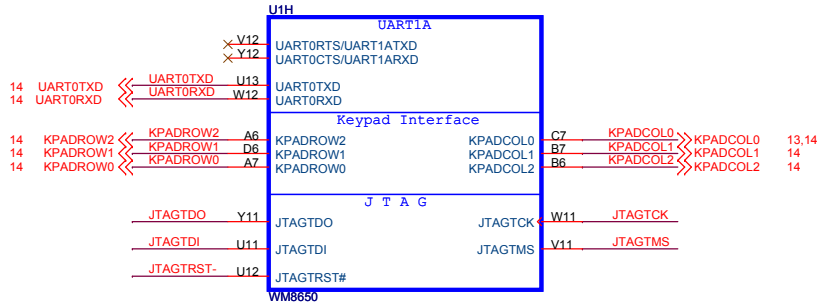
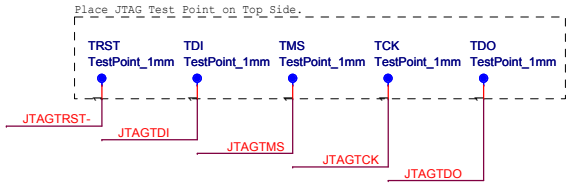
WM8650: UART/KEYPAD/JTAG

PART: H





WonderMedia Technologies, Inc.

Title	WM8650: UART/KEYPAD/JTAG	
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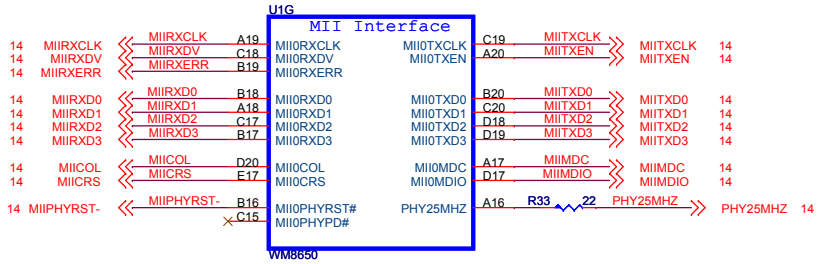
WM8650: MII
PART: G



WonderMedia Technologies, Inc.

Title: WM8650: MII		
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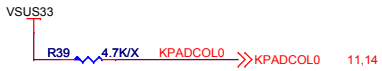
Strapping Option

Pin	Internal Pull up/down	Default	Description
KPADCOL0	pull down	0	Inert PWRBTN# 0:debounced for a few cycles 1:kept 2s to trigger



WARMING:

ALL others strappings not listed above recommend to use internal settings as default. Otherwise un-expected results may encounter.

WonderMedia Technologies, Inc.

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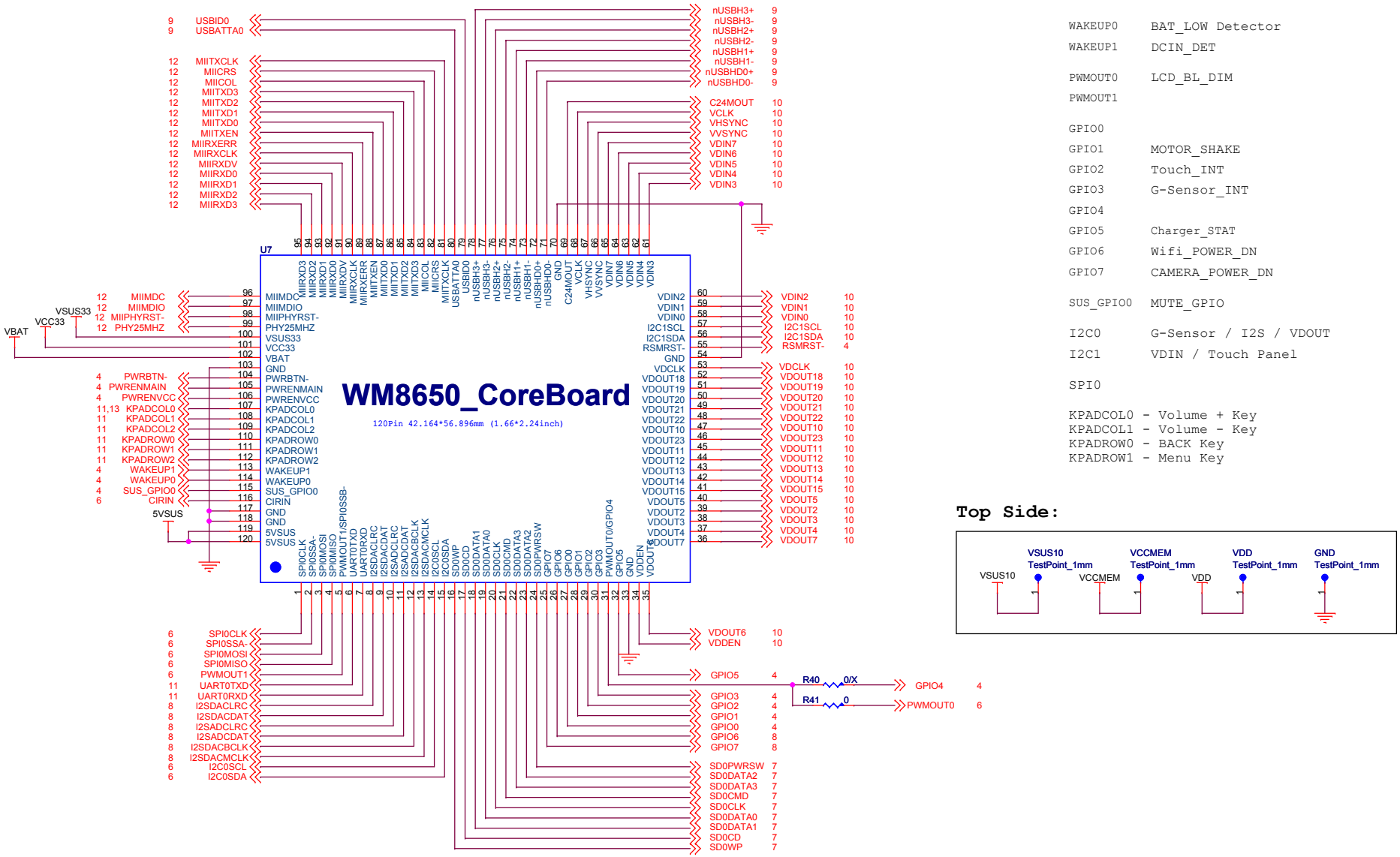
WM8650_CoreBoard
120Pin 42.164*56.896mm (1.66*2.24inch)
U7
WAKEUP0 BAT_LOW Detector
WAKEUP1 DCIN_DET
PWMOUT0 LCD_BL_DIM
PWMOUT1
GPIO0
GPIO1 MOTOR_SHAKE
GPIO2 Touch_INT
GPIO3 G-Sensor_INT
GPIO4
GPIO5 Charger_STAT
GPIO6 Wifi_POWER_DN
GPIO7 CAMERA_POWER_DN
SUS_GPIO0 MUTE_GPIO
I2C0 G-Sensor / I2S / VDOUT
I2C1 VDIN / Touch Panel
SPI0
KPADCOL0 - Volume + Key
KPADCOL1 - Volume - Key
KPADROW0 - BACK Key
KPADROW1 - Menu Key
Top Side:
VSUS10 TestPoint_1mm
VCCMEM TestPoint_1mm
VDD TestPoint_1mm
GND TestPoint_1mm
R40 0/X
R41 0
Vout=0.6*(1+R1/R2)
0.6*(1+95.3K/100K)=1.172V
U8
AAP2408VIR1
L1 SFE3015-3R3M-R-S-LF
R34 95.3K_1%
R36 100K_1%
C42 56pF
C46 4.7uF
C47 10uF
C48 10uF
C49 0.1uF
Vout=0.6*(1+R1/R2)
0.6*(1+200K/100K)=1.8V
U9
AAP2408VIR1
L2 SFE3015-3R3M-R-S-LF
R35 200K_1%
R37 100K_1%
R38 100K
C51 4.7uF
C43 22pF
C44 10uF
C45 0.1uF
WonderMedia Technologies, Inc.
Title
Core Board Package
Size Custom
Document Number WMS8121A
Rev 1.0
Date: Sunday, December 12, 2010
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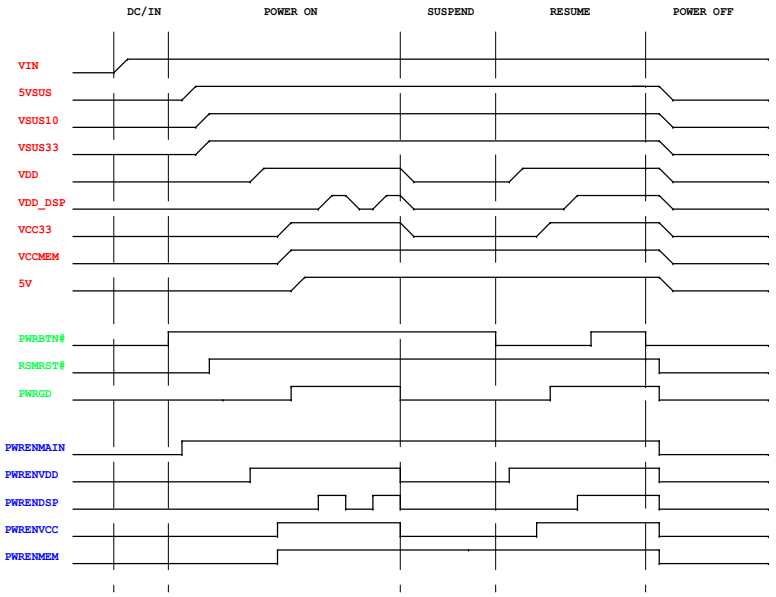
Version History

Version	Modify Content	Responsibility	Date
WMS8121A	Initial released.	Ben Huang	2010/12/12

Power Sequence:



WonderMedia Technologies, Inc.

Title: Version History		
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