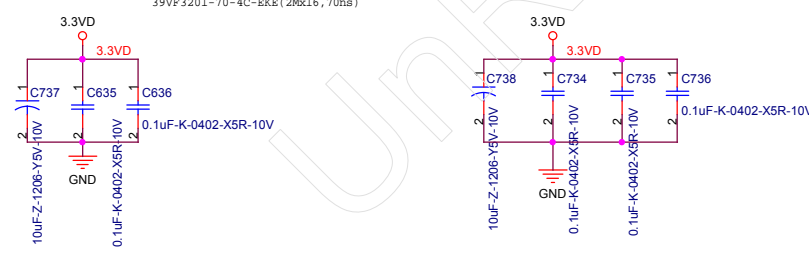
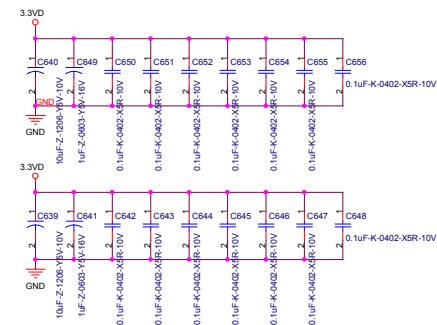
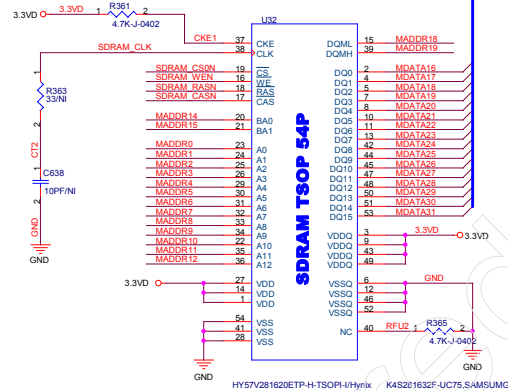


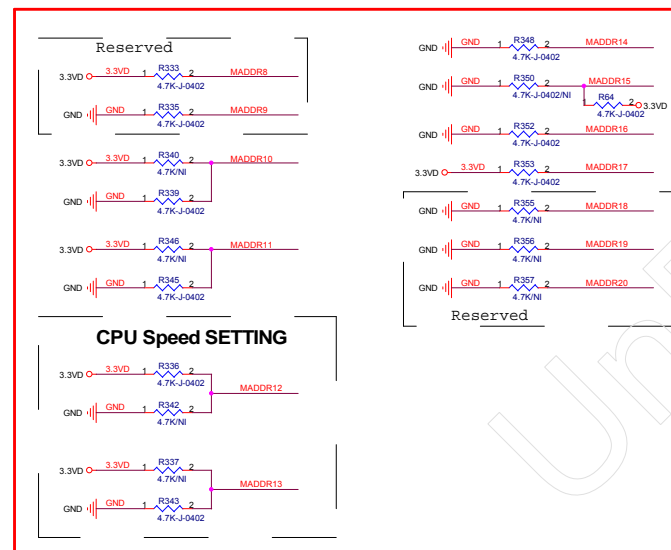
39VF1601-70-4C-EKE(1Mx16,70ns)
39VF3201-70-4C-EKE(2Mx16,70ns)

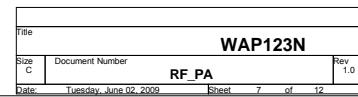


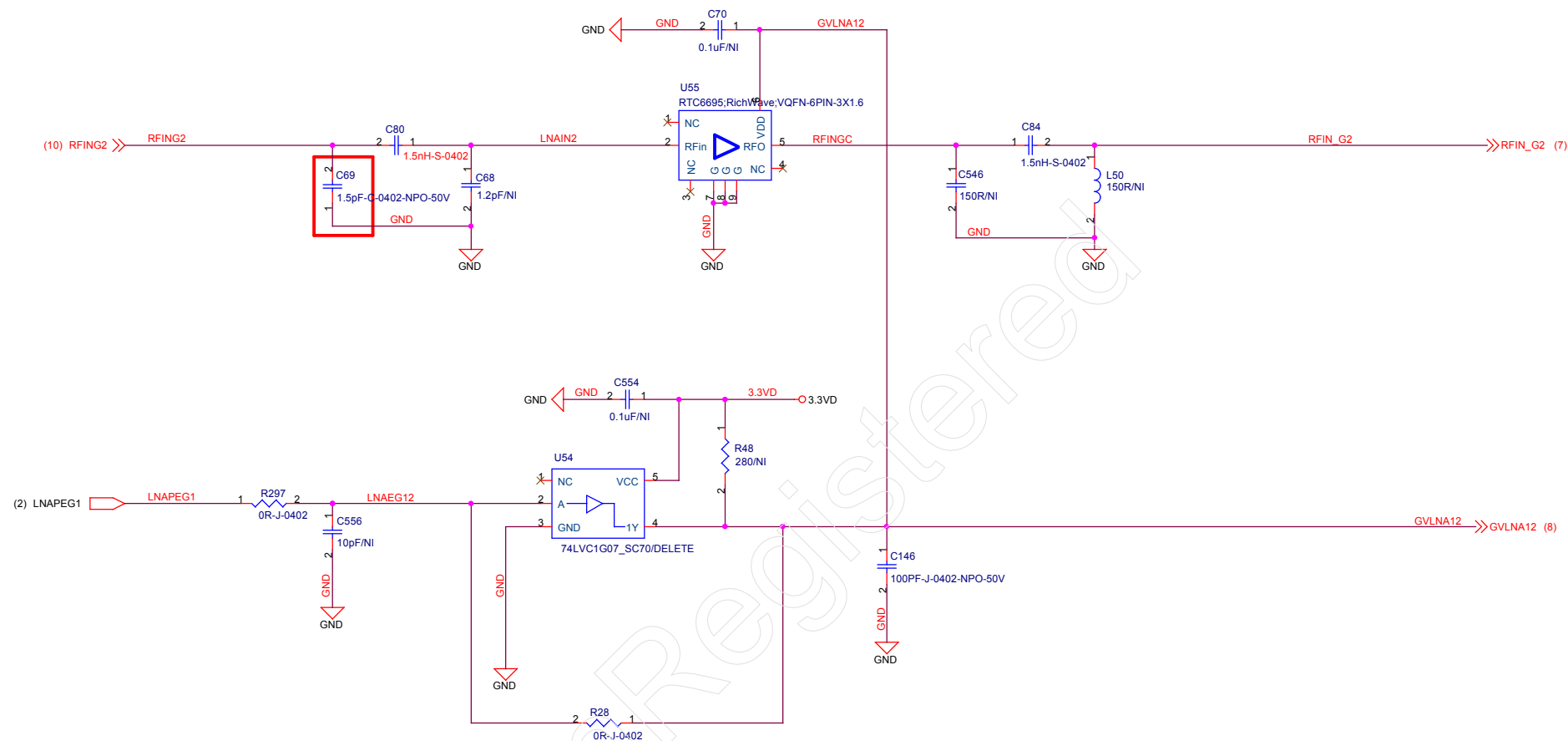
Title			
WAP123N			
Size B	Document Number	Flash	Rev 1.0
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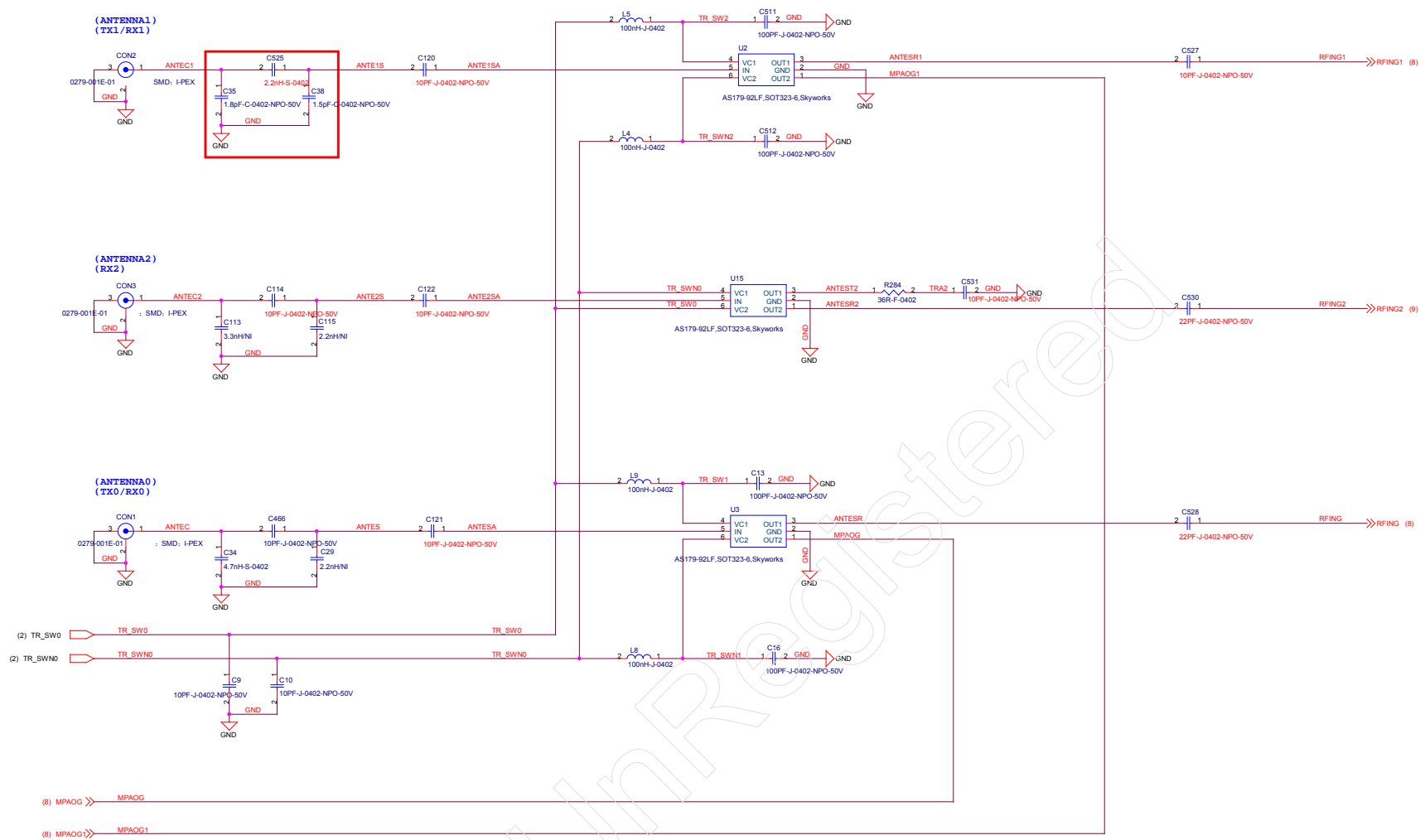
Pin Name	Description	Value=0	Value=1
MA8	Flash	8bit	16bit
MA9	Boot ROM	ext Flash	int ROM
MA10	Endian	Little Endian	Big Endian
MA11	PCI Clock	33MHz	66MHz
MA[13:12]	CPU clock	00 : 250MHz 01 : 266MHz 10 : 280MHz 11 : 300MHz	
MA14	Gigabit Enet I/O	3.3V	2.5V
MA15	Enet TX clock	Input	Output
MA16	PLL Select	Not Bypass	Bypass
MA17	PCI Mode	Device	Host
MA18	Enet RX clock	Input	Output
MA19	INIC, SDRAM	4MB	8MB
MA20	INIC, Config.	from MA19	EEPROM



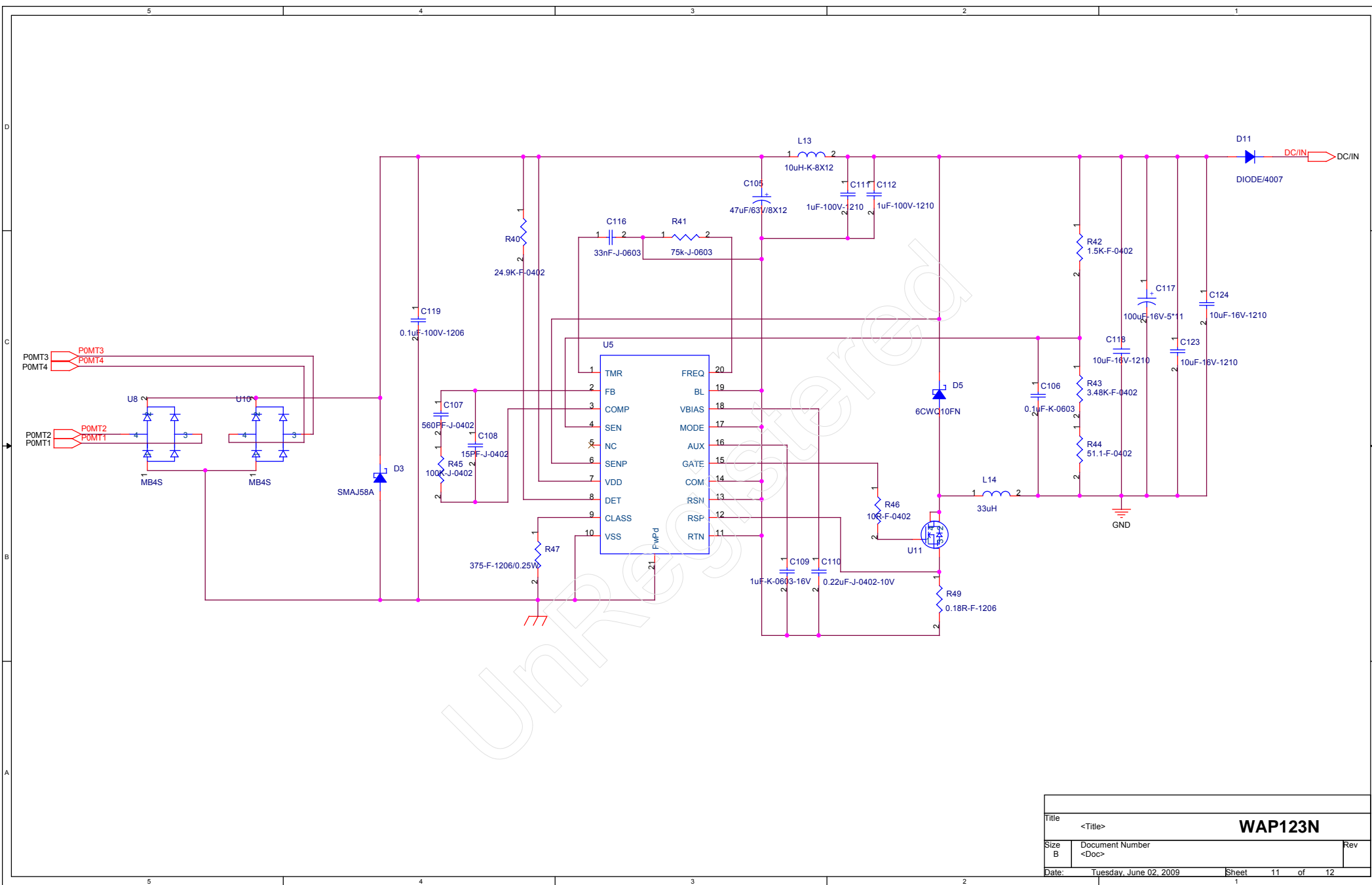




Title				
WAP123N				
Size B	Document Number			Rev 1.
RF_LNA				
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Title			WAP123N
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C	RF_FrontEnd	1.0	
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12V-->3.3V/3A

12V DC ADAPTER

Regulator	Install	Not Install	C11	R285	R286	L46
CAT7108	U70, R30, R285, R286	U10	33pF	270K	270K	4.7uH
RT8010 (ADG)	U10, C11, R285, R286	U29	22pF	270K	270K	2.2uH

Reset Circuit

POWER-ON RESET

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Size	Document Number	Rev	1.0
C	Power_Reset	Sheet	12 of 12
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