Dielectric Chip Antenna

partron

■ **P/N ; ODBTPTR3015**

Chip Antenna Dimension

Chip Antenna Soldering Pad Dimension

PCB Soldering Land Dimension

Parameter	L	W	T	A	B	C	D	E
Value[mm]	3.0 ± 0.1	1.5 ± 0.1	1.2 ± 0.1	1.0	0.55	0.4	1.1	0.65

Unless Specified tolerances are ± 0.05 mm

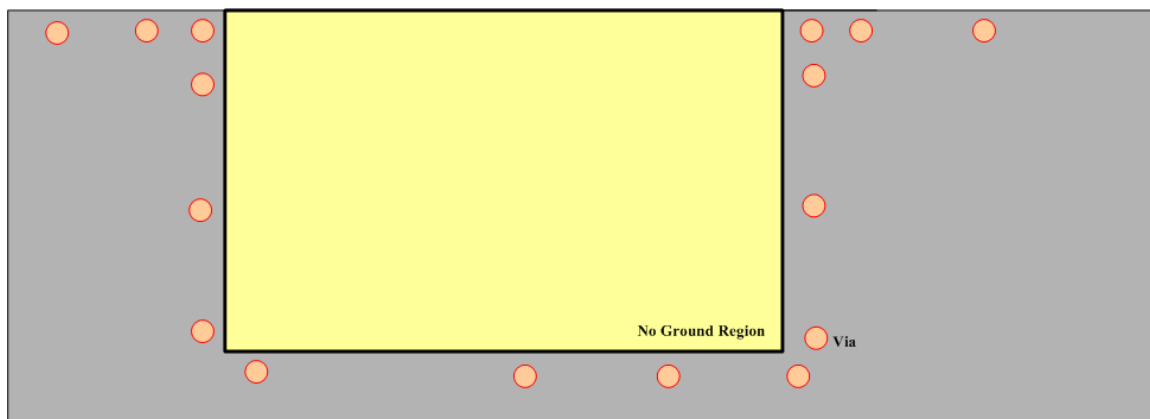# Dielectric Chip Antenna

■ PCB Drawing
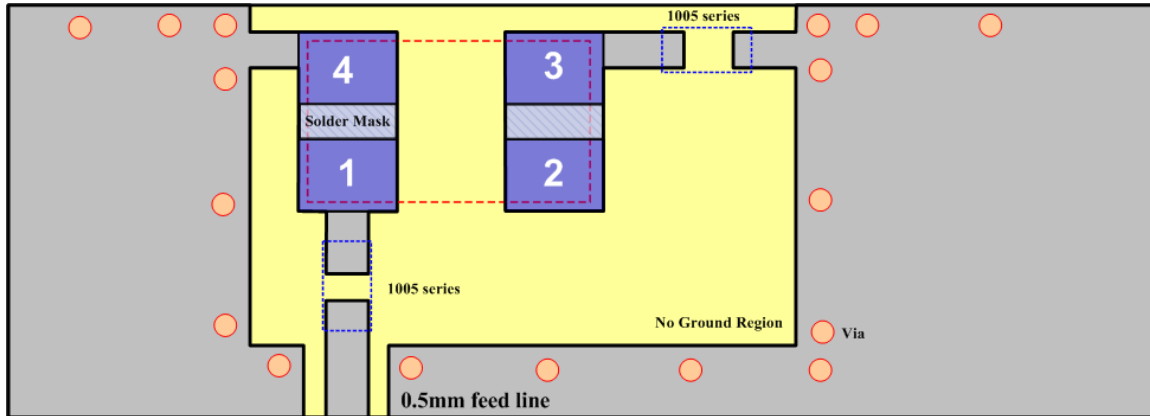
< Top View >

< Bottom View >
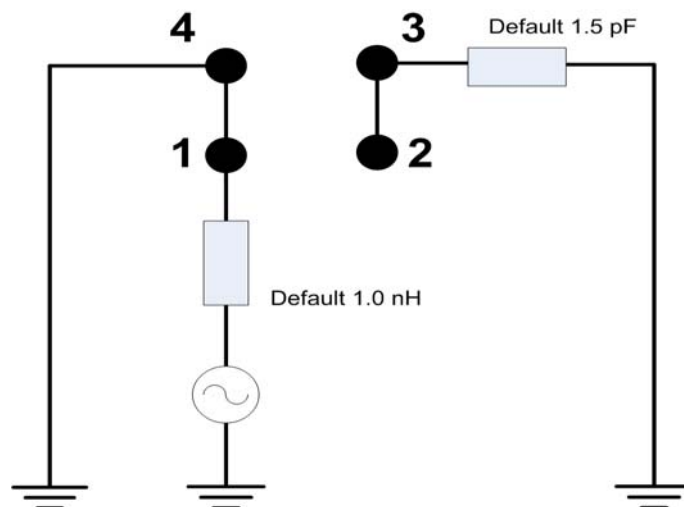
< Default Condition Equivalent Circuit >

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■ Test Result for Ver 0.0

SWR & Smith chart	3D	Peak [dBi]	Average [dBi]	Efficiency [%]
	2400MHz	1.794	-1.415	72.19
	2425MHz	2.807	-0.625	86.59
	2445MHz	2.502	-0.927	80.77
	2465MHz	1.947	-1.220	75.51
	2485MHz	1.435	-1.656	68.29
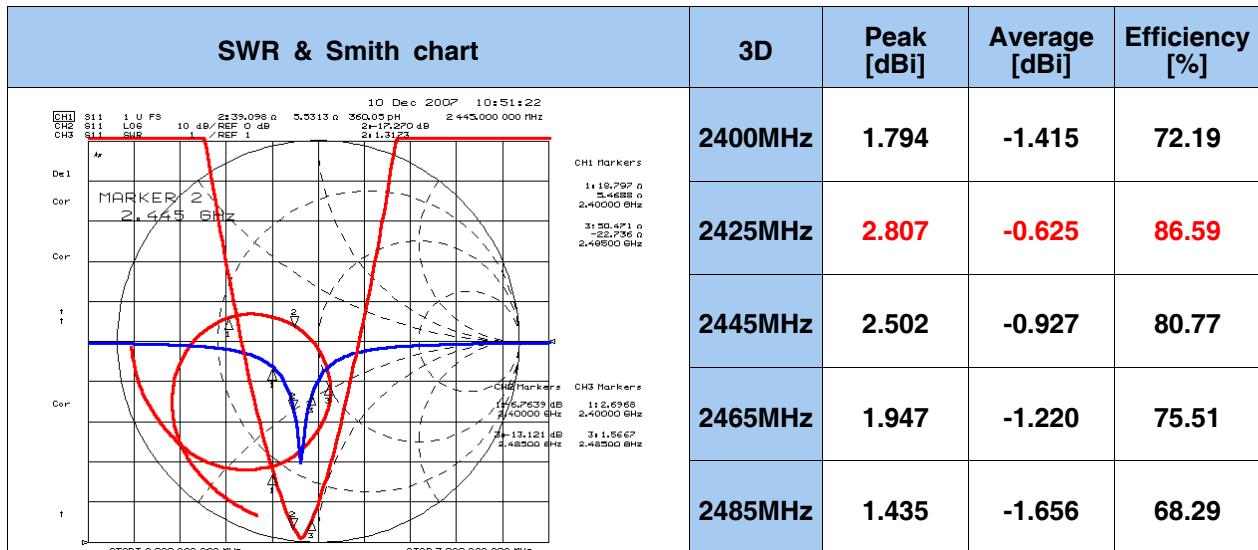
■ Radiation Pattern   Ver 0.0

3D Passive

Gain [dBi]

Test Name : ODBT
Operator : Operator Name
Date : 2007-12-10
Time : 오전 10:49:23
Description :
301512
Loading (1nH / 1,5pF)
Series 10pF
Shunts N/C
Calibration : 20071201

Frequency [MHz]
2400.000
2425.000
2445.000
2465.000
2485.000

Plot Data Type
VH Sum
Amplitude [dB]
Normalization
Global Peak
Default Range
Range Change
Max Amplitude
5
Chart 2D
Chart 3D Polar
PLOT

Freq.[MHz]	Polarization	Beam Peak			Beam Null			Gain[dBi]	Efficiency
		Value	Phi	Theta	Value	Phi	Theta		
2400		1,794	0	105	-21,406	90	135	-1,415	72,19%
2425		2,807	0	105	-20,541	90	135	-0,625	86,59%
2445		2,502	0	105	-19,881	90	135	-0,927	80,77%
2465		1,947	0	105	-20,855	270	135	-1,220	75,51%
2485		1,435	0	105	-21,742	270	135	-1,656	68,29%