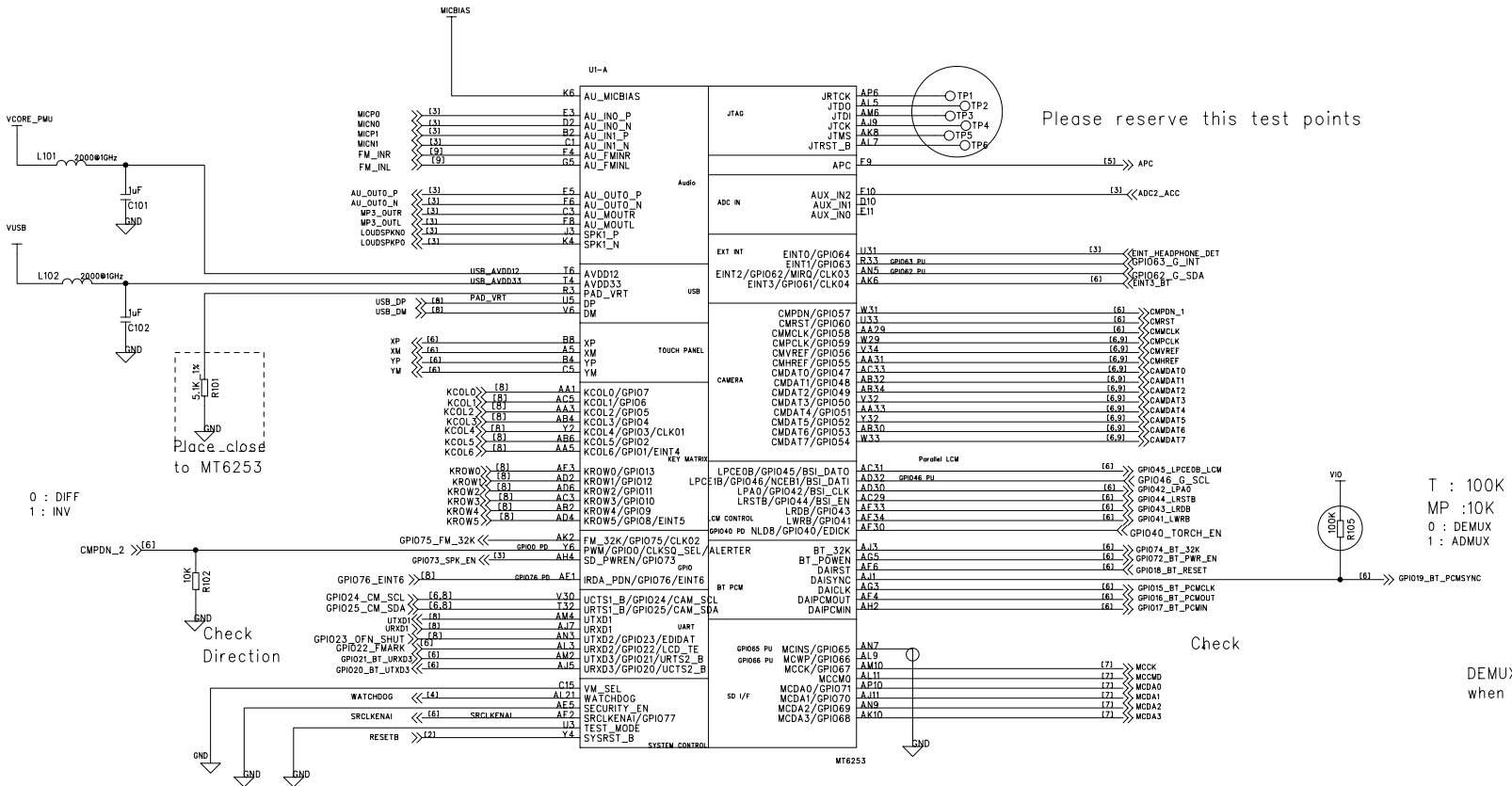


BASEBAND

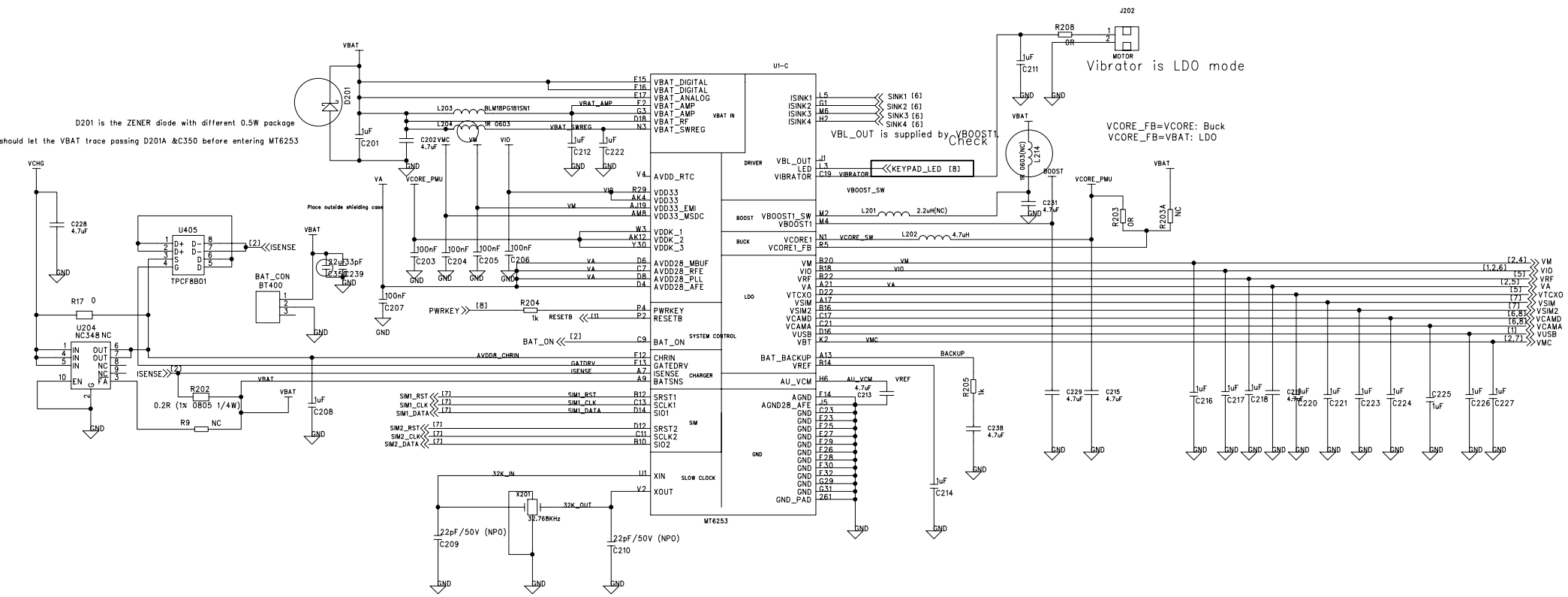


Q25 Application Note:

Q25_W5	Pin AE1/RDA_PDN
Q25_DATA	Pin AN3/UTXD2
Q25_CLK	Pin AF30/NLDB

File NO:	
MODEL: Q013	Drawing NO:
TITLE:	
SCH: V0.2	PCB: V0.2
DESIGN:	CHECK:
APPROVE:	

BASEBAND-POWER



D201 is the ZENER diode with different 0.5W package
should let the VBAT trace passing D201A & C350 before entering MT6253

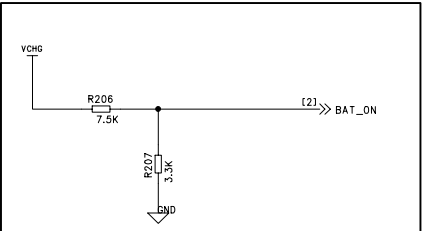
Vibrator is LDO mode

VBL_OUT is supplied by VBOOST1
Check

VCORE_FB=VCORE: Buck
VCORE_FB=VBAT: LDO

Place X1 close to BB

Check, Trace 4mil is enough

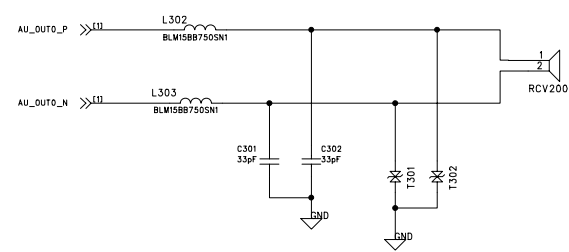


	Symbol	Vout (V)	Iout (mA)	Setting
Buck	VCORE	1.2	200	
	VREF	2.8	200	
RF LDO	VTCX0	2.8	20	
	VA	2.8	125	
Analog LDO	VCAM_A	1.5/1.8/2.5/2.8	150	
	VM	1.8/2.8	200	VM_SF1
Digital LDO	VIO	2.8	100	
	VSIM	1.8/3.0	100	
	VUSB	3.3	100	
	VBT	1.3/1.5/1.8/2.5/2.8/3.0/3.3	100	
	VCAM_D	1.3/1.5/1.8/2.5/2.8/3.0/3.3	100	
	VSIM2	1.3/1.5/1.8/2.5/2.8/3.0/3.3	100	
	VIBR	1.8/3.0	200	
Vibrator	VBACKUP	2.8	2	
Boost Converter	VBOOST1	3.4~5.5	100	

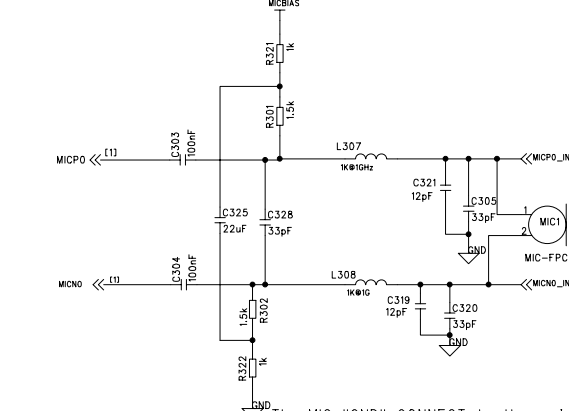
L20701 DELETE GOLD-BATTERY;
DELETE DUAL-DIODE DESIGN;
L20701 change L203 labe 0603 package;
L20701 ADD R208 OR FOR MOTOR;
delete D203 add L204;

File NO:	
MODEL: Q013	Drawing NO:
STANDARD:	
SCH: V0.2	PCB: V0.2
CHECK:	
APPROVE:	

HANDSET RECEIVER

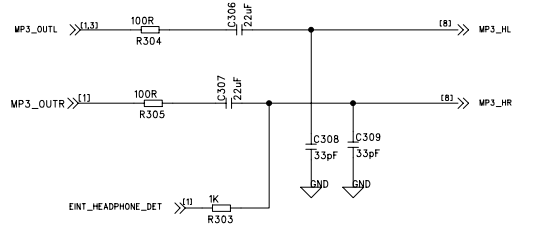


HANDSET MICROPHONE

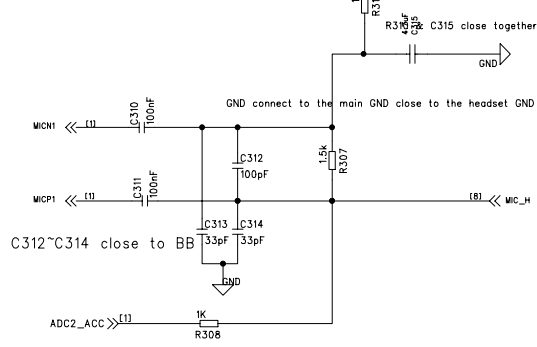


The MIC "GND" CONNECT to the main GND

Earphone Receiver

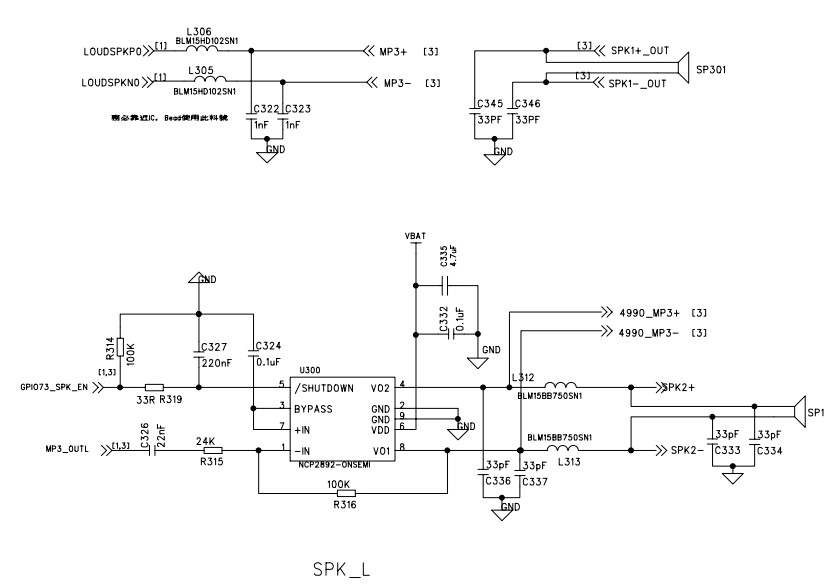


Earphone MICROPHONE

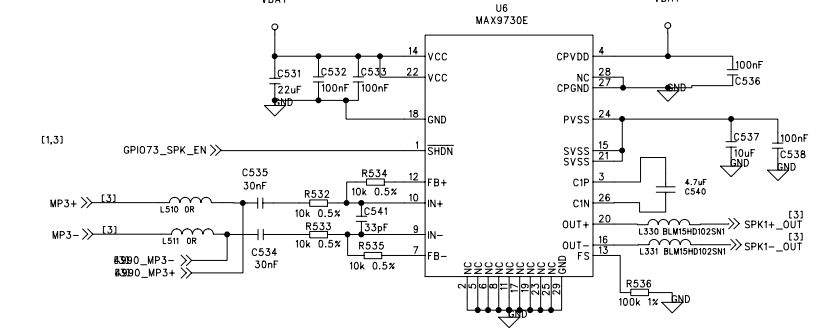


Detection of Send key on Earphone

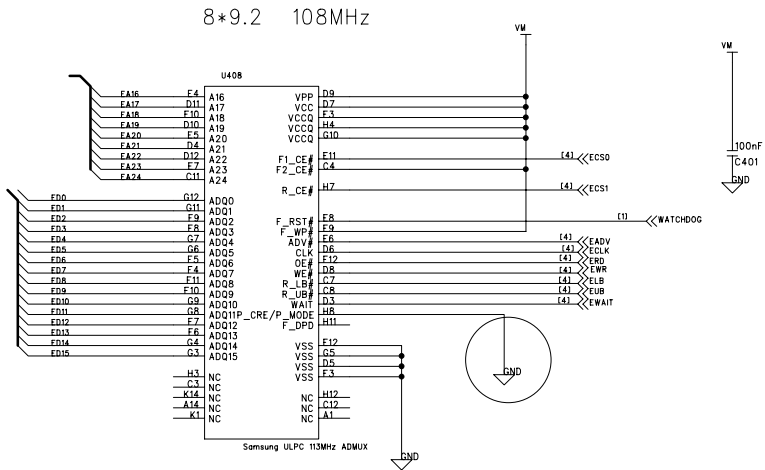
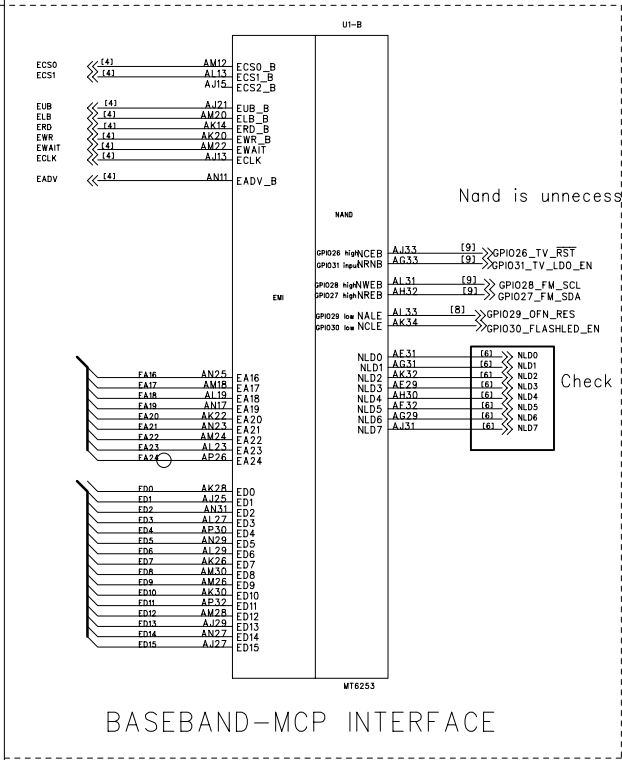
LoudSPK



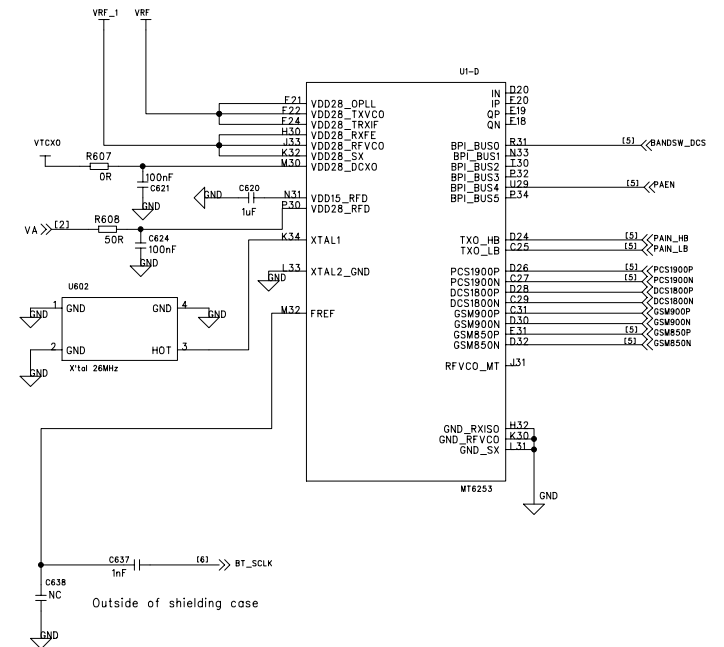
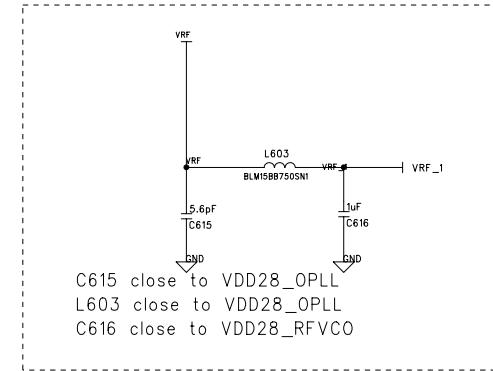
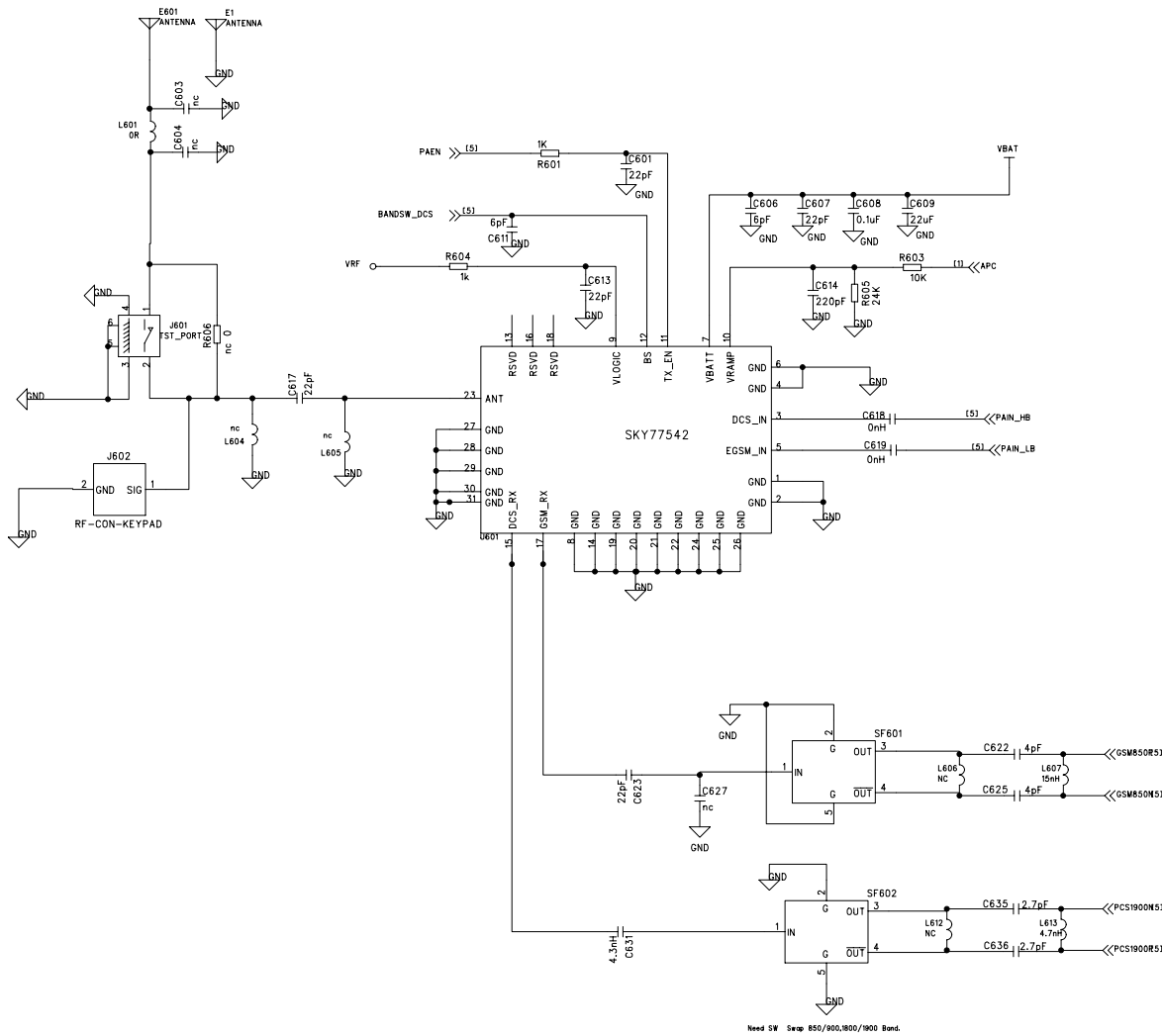
AUDIO PA



		File NO:
MODEL: Q013		Drawing NO:
TITLE:		STANDARD:
SCH: V0.2	PCB: V0.2	CHECK:
DESIGN:		APPROVE:

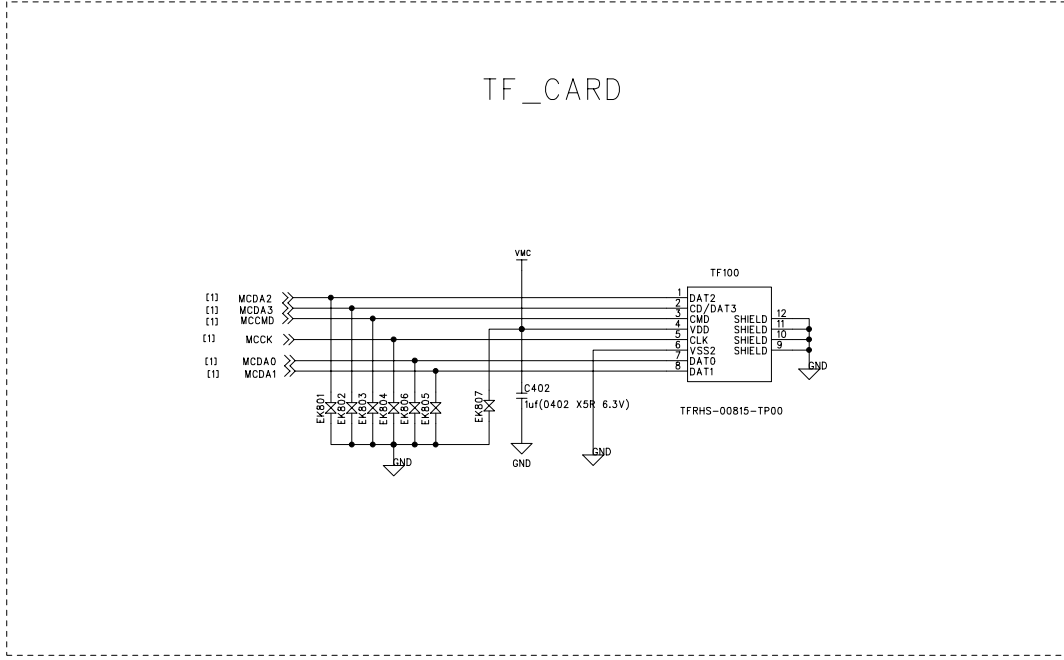
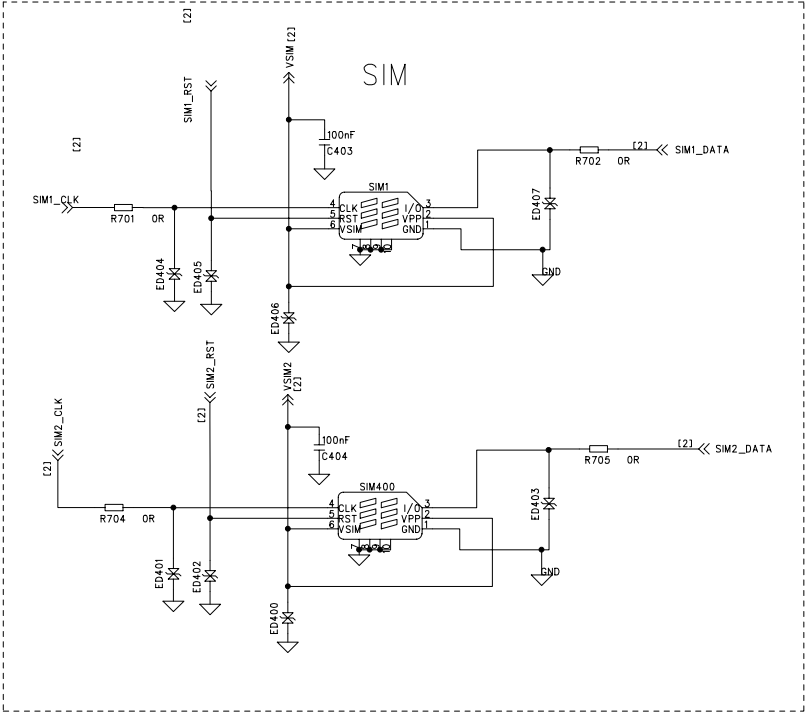


		File NO:
MODEL: Q013		Drawing NO:
TITLE:		STANDARD:
SCH: V0.2	PCB: V0.2	CHECK:
DESIGN:		APPROVE:

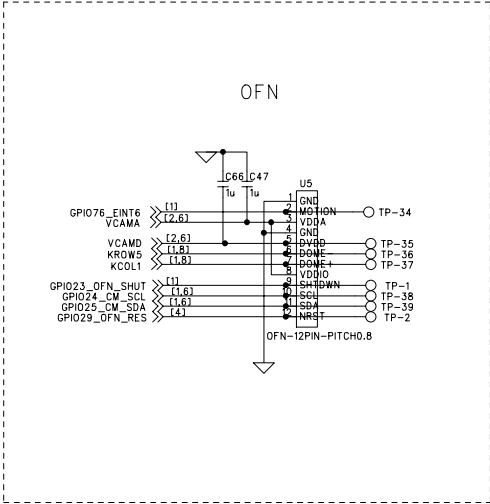
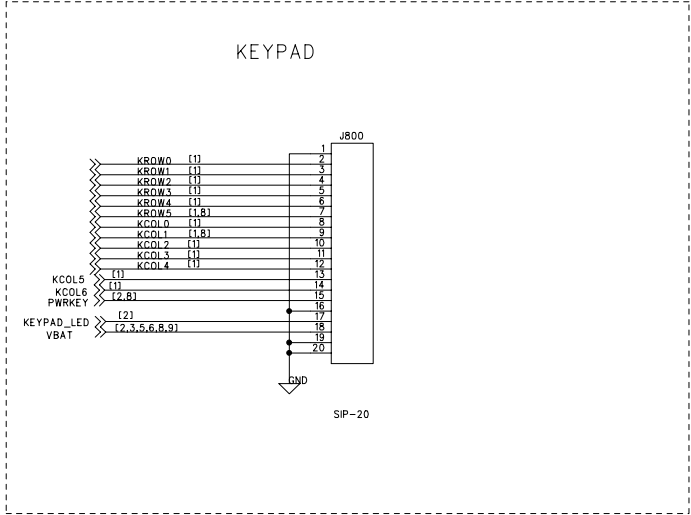
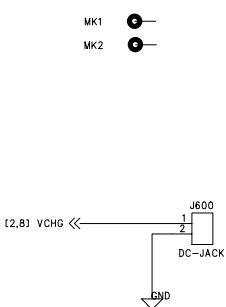
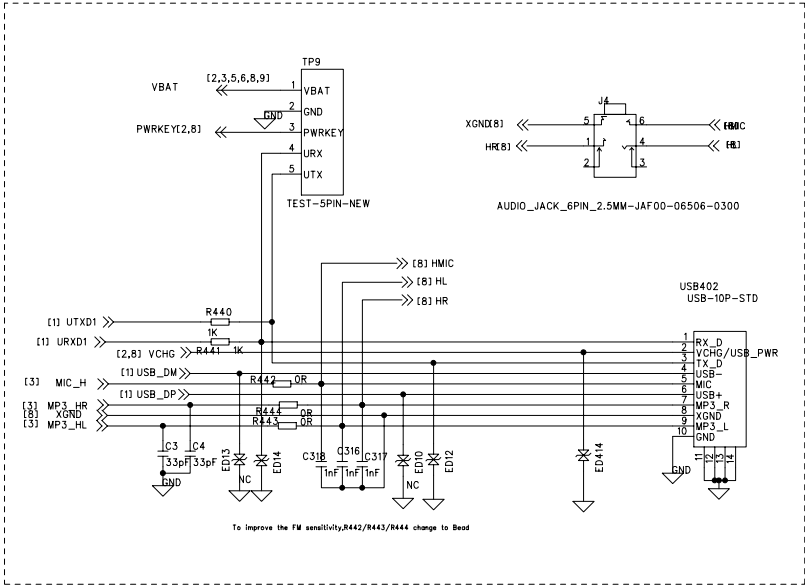


delete R606,change R609's net 0708;

		File NO:
MODEL: Q013		Drawing NO:
TITLE:		STANDARD:
SCH: V0.2	PCB: V0.2	CHECK:
DESIGN:		APPROVE:



		File NO:
MODEL: Q013		Drawing NO:
TITLE:		STANDARD:
SCH: V0.2	PCB: V0.2	CHECK:
DESIGN:		APPROVE:



		File NO:
MODEL: Q013		Drawing NO:
TITLE:		STANDARD:
SCH: V0.2	PCB: V0.2	CHECK:
DESIGN:		APPROVE:

[9] ASCK_AUDIO_L

R907 2.4K

15nF C940

R922 2.4K

1nF C920

C943 1uF

[1] FM_INL

[9] AMCK_AUDIO_R

R923 2.4K

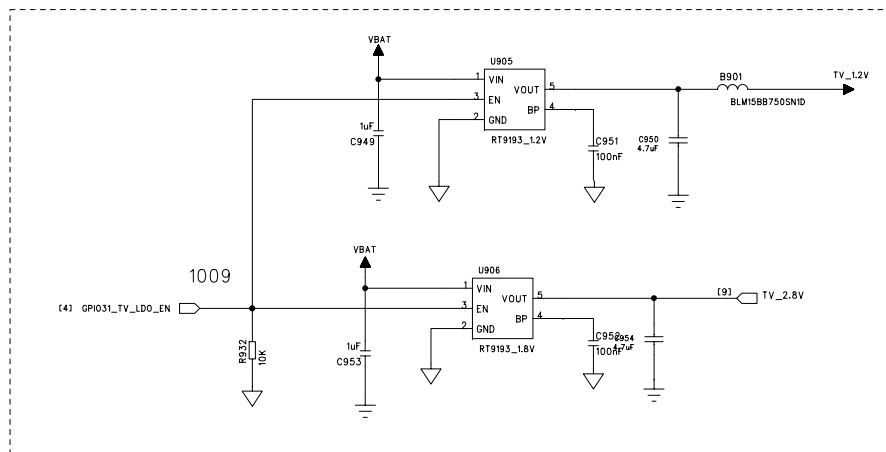
15nF C941

R924 2.4K

1nF C942

C944 1uF

[1] FM_INR



		File NO:
MODEL: Q013		Drawing NO:
TITLE:		STANDARD:
SCH: V0.2	PCB: V0.2	CHECK:
DESIGN:		APPROVE: