REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

RF_VDD
CPU_VDD
R1 0R
BAT1
C29 220u/6.3v
+
T11
T12

R1 0R USE C29 220uf ,
R1 NOT 0R USE C29,C30 100uF

CPU_VDD
R5 1M   R4 2M
T3
T4
REED-SW2 CAD
BC857B Q2
E B C
C1 22n
GND

CPU_VDD
R11 2M   R12 1M
BC857B Q4
E B C
T1
T2
REED-SW1 SPD
C11 22n
GND

RF_VDD
C27 10n   C16 1n
5% X7R 25V   10% X7R 50V

C28 X7R 10% 16V
33n
R7 22.1K 1%
RF_VDD

U2 NRF24L01+
21 SULG
20 TEST
19 DVDD
18 VDD
17 VSS
16 IREF
15 VDD
14 VSS
13 ANT2
12 ANT1
11 VDD_PA
1 CE
2 CSN
3 SCK
4 MOS
5 MSO
6 IRQ
7 VDD
8 VSS
9 XC2
10 XC1

C19 100n
RF_VDD

ANT1
C3 1.2pF
C4 open
L5 1.5nH
C20 1p
+/-0.25pF 50V JHT/C
L4 2.2nH JTI L-07C2N7S
L3 1.8nH JTI L-07C1N8S
C22 1.2pF +/-0.1pF 50v JHT/C
L1 3.9nH JTI L-07C4N7S
L2 18nH
C17 4p7
C18 2n2 5% X7R 50V
+/-0.25pF 50V JHT/C

CPU_VDD
R8 47k 5%
C10 100n

U3 AXB02
8 RF_SDO
7 RF_CE
6 CADENCE_IN
5 EVENT_IN
9 RST_IN
10 NC2
11 XOUT
12 XIN
4 RF_CSN
3 RF_SCK
2 RF_IRQ
1 LED_OUT
17
13 NC3
14 VSS
15 NC4
16 VCC
CPU_VDD

T6
T5
Y2 32768Hz
C24 15p   C25 10p

RF_VDD

R9 1M
Y1 16MHz +/-10PPM 12.5PF
C21 15p   C23 15p

C8 100n

Q1 BC857B
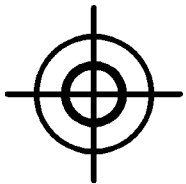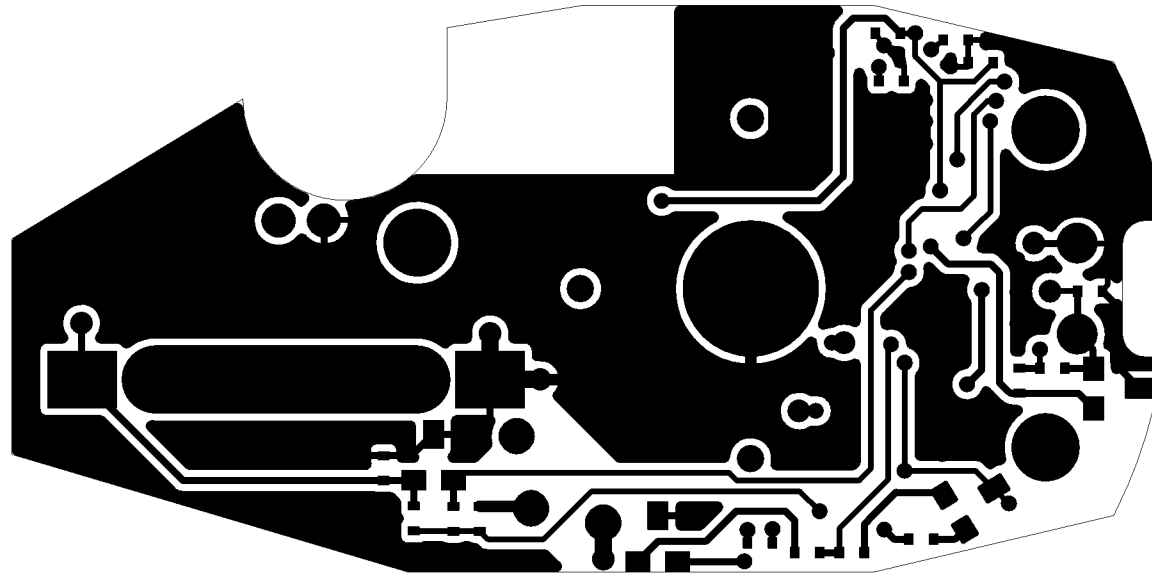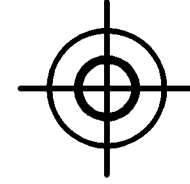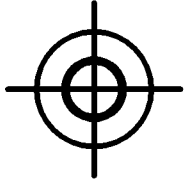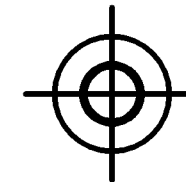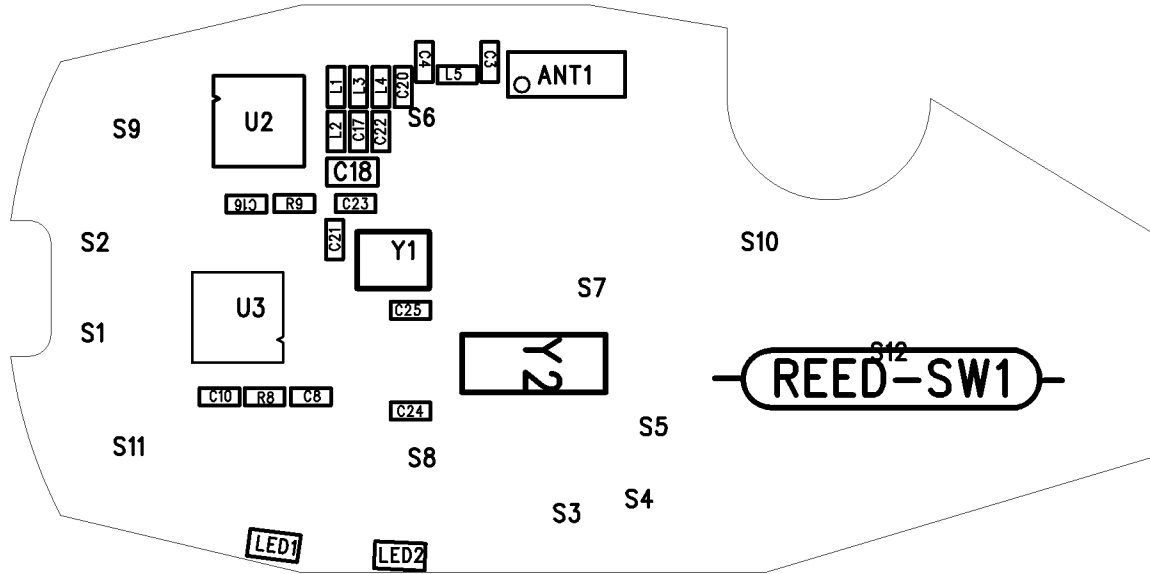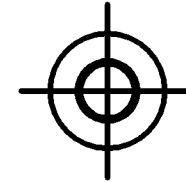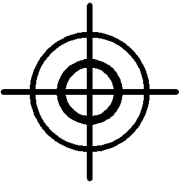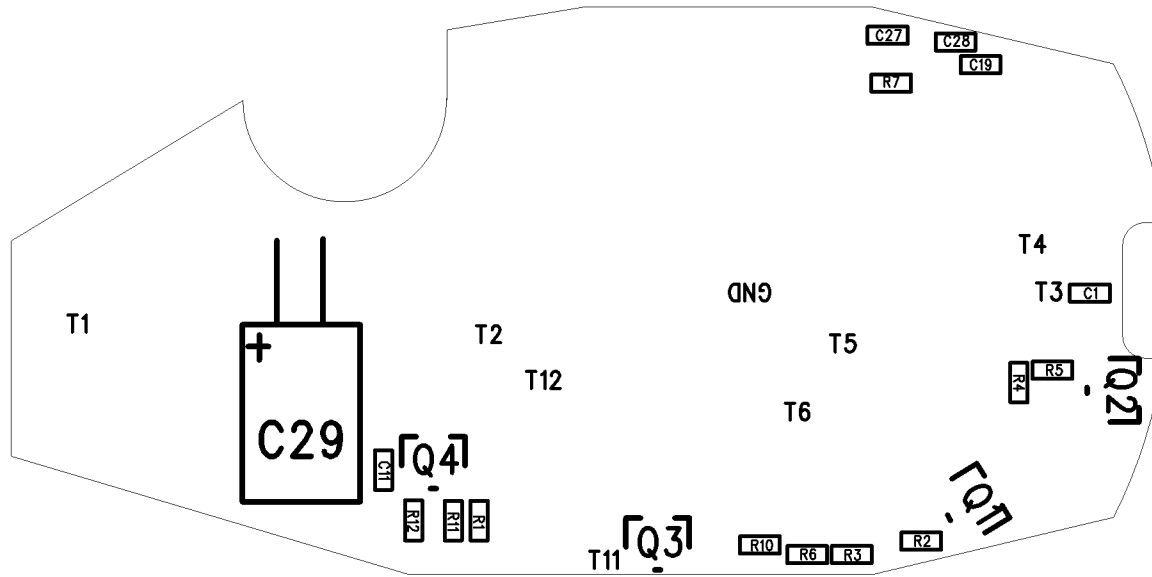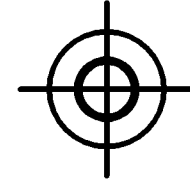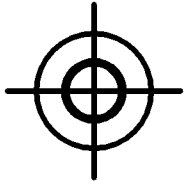C B E
R3 47K
R6 47K
R2 39R
D1 CAD LED K A
R10 39R
D2 SPD LED K A
Q3 BC857B
E B C

RideSense-01A3

#COMP SIDE
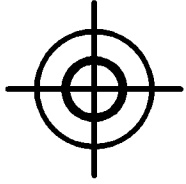
RideSense-01A3

#SOLD SIDE

RideSense-01A3

S9

U2

L1
L3
L4
C20
C2
L5
C5
ANT1

L2
C17
C22
S6

C18

C16
R9
C23

C21

S2

Y1

S10

U3

S7

C25

S1

Y2

S12

REED-SW1

C10
R8
C8

C24

S5

S11

S8

S3
S4

LED1
LED2

#COMP SILK

ЯideSense-01A3

C27
C28
C19
R7

T4
T3 C1

GND
T1
T2
T5
R5
T12
R4
Q2
T6
C29
+
Q4
C11
Q1
R12 R11 R1
Q3
R10 R6 R3 R2
T11

#SOLD SILK

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