


The diagram illustrates the system architecture of the Main Transmit Box, showing the interconnections between various components:

- Power Section:**
 - AC POWER** is connected to the **External SMPS BOARD #1**.
 - The SMPS board provides **VCC** and **GND** to the **DC IN** block.
 - The **DC IN** block feeds into the **+5V DC-DC** converter, which outputs **+5V**.
 - The **+5V** line also feeds into the **+3.3V DC-DC** converter, which outputs **+3.3V**.
 - The **+3.3V** line feeds into the **1.8V LDO**, which outputs **+1.8V**.
- Control and Data Section:**
 - The **MCU For SYSTEM** is the central control unit.
 - It is connected to **EEPROM** and **MFI341S2500** via **I2C**.
 - It receives **Remote / KEY/LED** input.
 - It outputs **VEDIO OUT** (labeled with a video icon) and **UART**.
 - It outputs **I2C** to the **Audio Processor AUDIO CODEC**.
 - It outputs **+3.3V** to the **Audio Processor**.
 - It outputs **STB control** to the **+5V DC-DC** converter.
 - It outputs **SPI** to the **Wireless Transmitter STS Lcos82** via a **wire connector**.
- Audio Section:**
 - The **Audio Processor AUDIO CODEC** (24Bit / 96KHz) receives **L/R** input from the **IPOD_30PIN_JACK**.
 - It outputs **L/R** to the **AUX IN** (labeled with an audio icon).
 - It outputs **Digital audio** to the **wire connector**.
- Timing and Reference:**
 - Y1** (11.0592M) is connected to the **MCU**.
 - U12** (74V04GW) is connected to the **MCU** and **Y2** (12.288MHZ).
 - Y2** is connected to the **LDO**.
- Output Section:**
 - The **LDO** outputs **+5.0V** to the **wire connector**.
 - The **wire connector** is connected to the **Wireless Transmitter STS Lcos82**, which has an antenna.

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