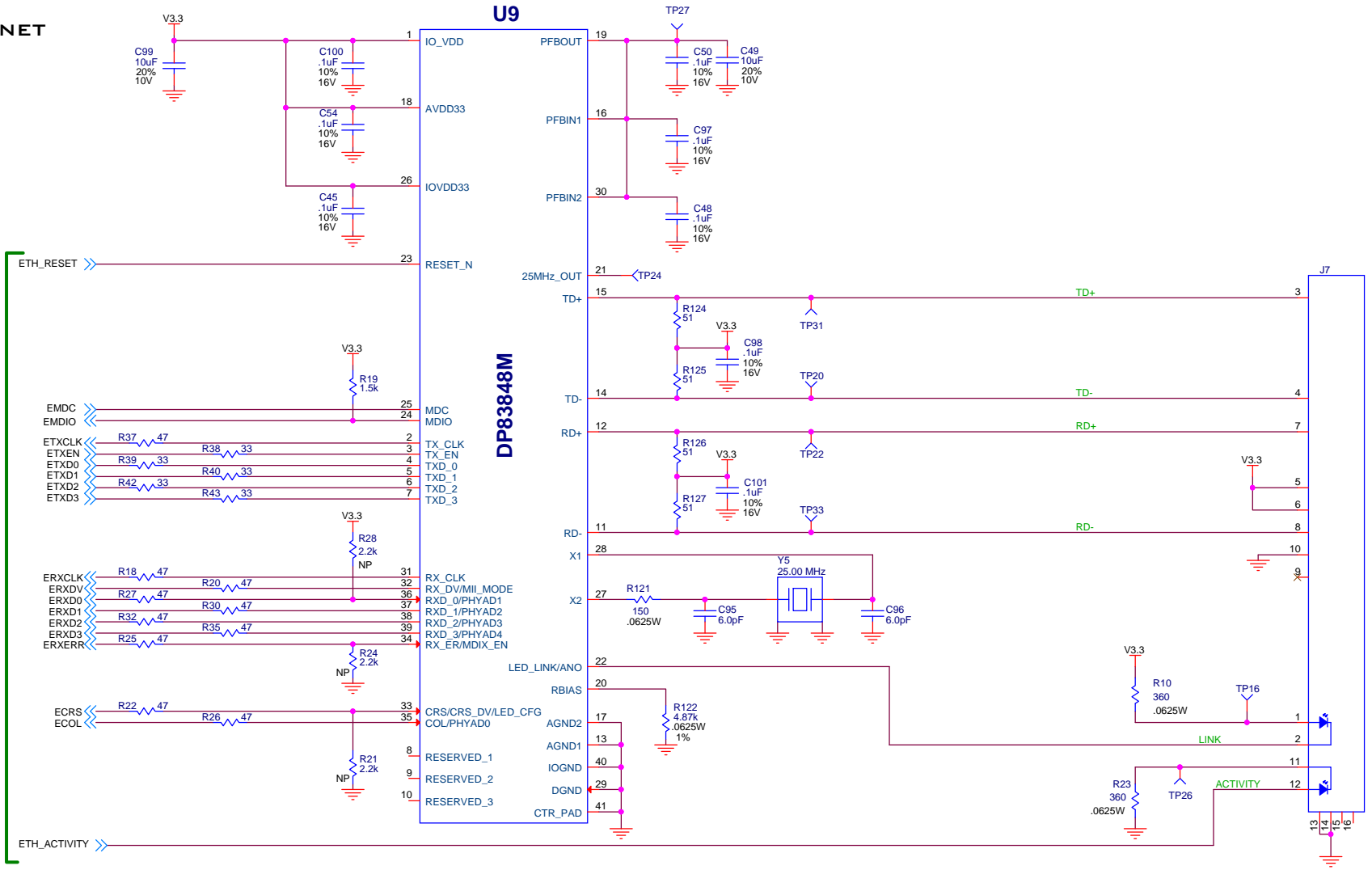


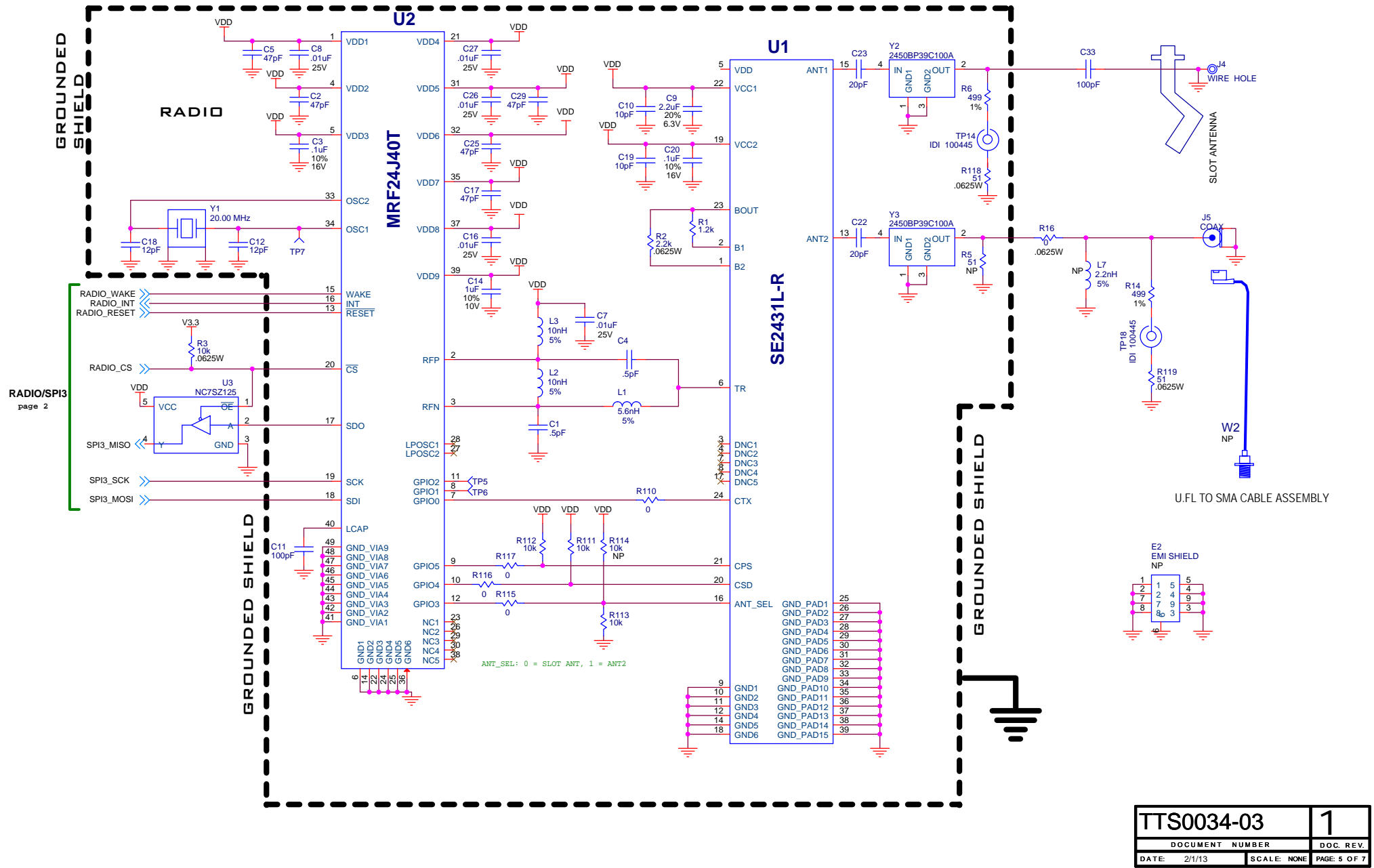
ETHERNET

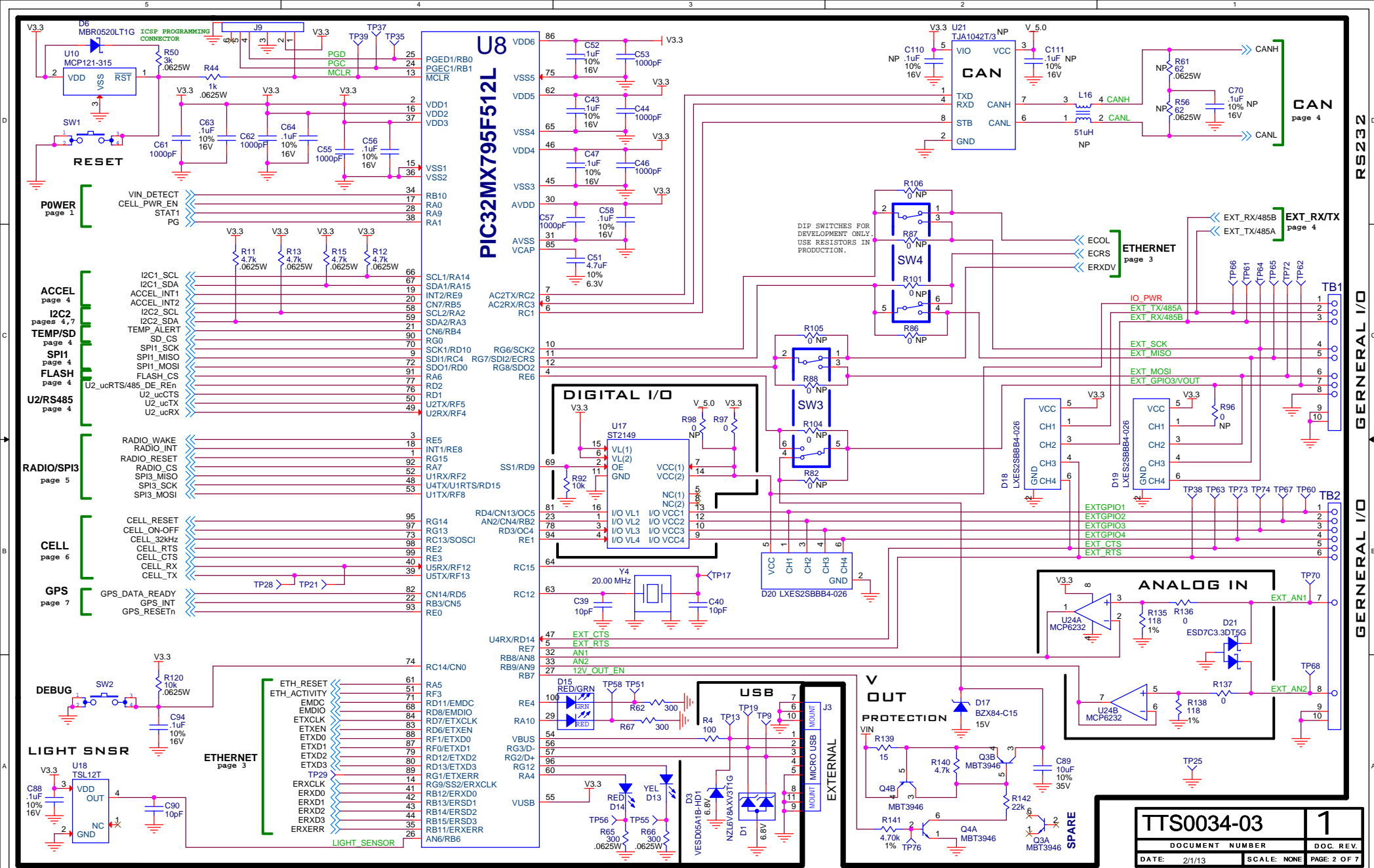
ETHERNET
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RADIO/SPI3
page 2





ACCEL

page 2

I2C1_SCL
I2C1_SDA
ACCEL_INT1
ACCEL_INT2

EEPROM

U6
24LC01BT
SCL VCC
SDA
WP
VSS

C35
.1uF
10%
16V

V3.3
R76
10k
.0625W

TEMP SENSOR

U14
MCP9800
SCLK VDD
SDA
ALERT GND

ACCELEROMETER

U7
ADXL345
CS
SDO/ALT ADD
SCL/SCLK
SDA/SDI/SDIO
RSVD2
RSVD1
NC
INT1
INT2
VDD I/O
GND3
GND2
GND1

C38
.1uF
10%
16V
C34
1uF
10%
10V
C37
.1uF
10%
16V

I2C2 TEMP/SD

I2C2_SCL
I2C2_SDA
TEMP_ALERT
SD_CS

V3.3
R29
10k
.0625W
NP

SPI1

SPI1_SCK
SPI1_MOSI
SPI1_MISO

FLASH

FLASH_CS

FLASH (8MBit)

U15
SST25VF080B
SCK VDD
SI WP#
SO HOLD#
CE# VSS

C83
.1uF
10%
16V

C113
.1uF
10%
16V

C112
.1uF
10%
16V

C117
.1uF
10%
16V

C116
.1uF
10%
16V

C114
.1uF
10%
16V

U23
MAX3232
VCC C1+
C1- C2+
C2-
DIN2 DOUT2
ROUT2 RIN2
DIN1 DOUT1
ROUT1 RIN1
GND

R134
0

R143
0

R69
0

R52
0

R75
0

R57
0

R63
0

R83
0

R84
0

R85
0

R102
0

R107
0

R132
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R133
10k
.0625W

R103
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R81
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R100
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.0625W

R103
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R85
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R107
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R132
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R133
10k
.0625W

R103
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R85
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R107
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R132
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R133
10k
.0625W

TO:
OFF
BOARD
ANTENNA

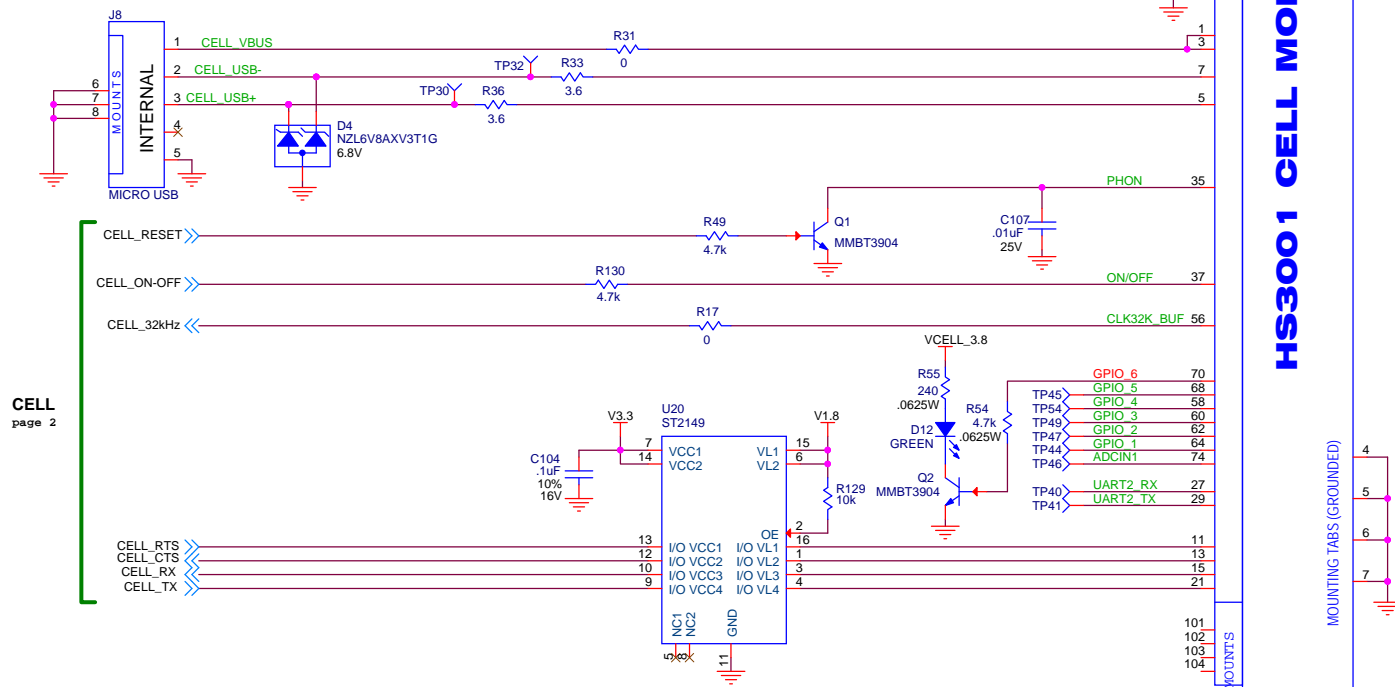
W1 U.FL TO SMA CABLE ASSEMBLY

CELL INTERFACE

ON
BOARD
CHIP
ANTENNA

E3
ANTENNA

HS3001 CELL MODULE

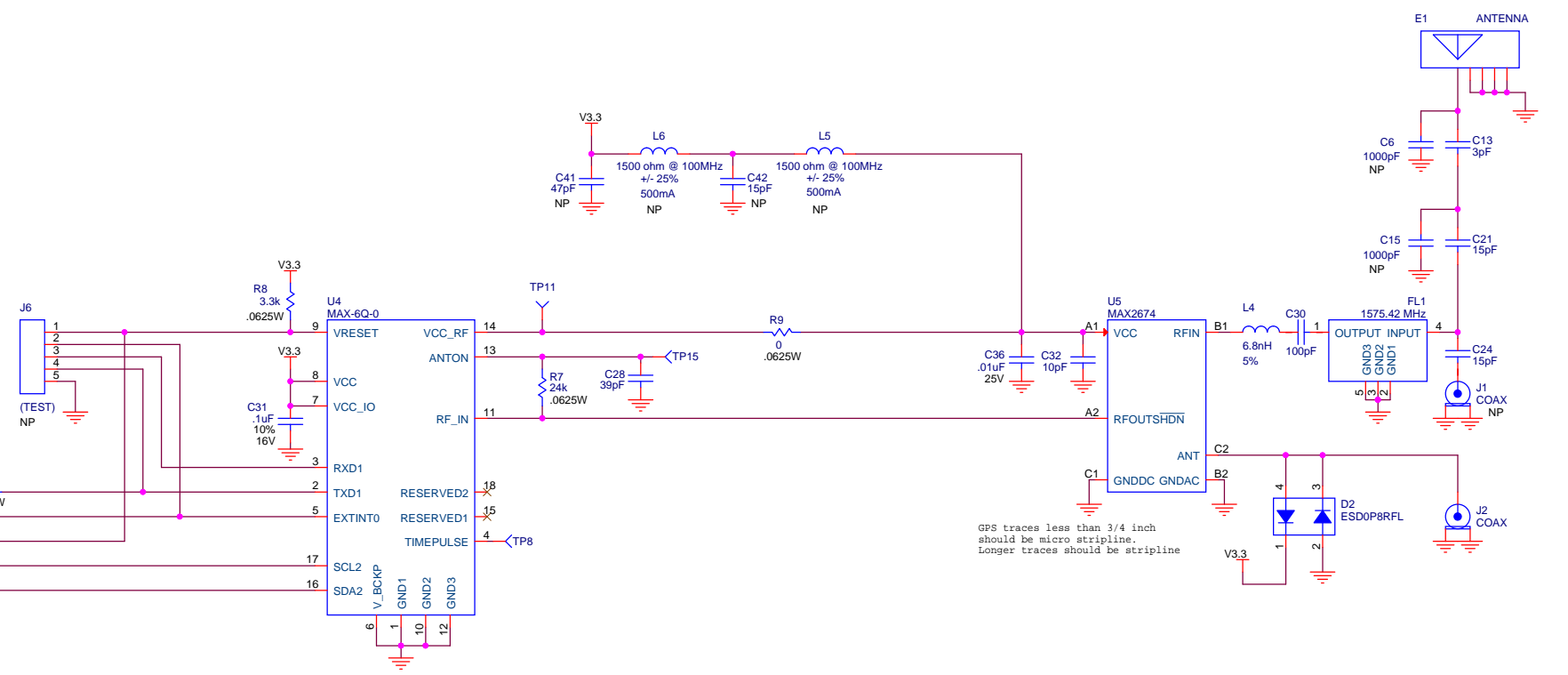


CELL
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GPS
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I2C2
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UNLESS OTHERWISE SPECIFIED:
ALL CAPACITORS ARE RATED AT 50 VOLTS.
CAPACITOR TOLERANCES ARE AS FOLLOWS:

.5pF - 5.0pF	+/- .25pF
5.1pF - 9.1pF	+/- .5pF
10pF - 820pF	+/- 5%
1000pF - 1000uF	+/- 10%

UNLESS OTHERWISE SPECIFIED
ALL RESISTORS ARE .0625 WATT COMPONENTS
AND RESISTANCE VALUES ARE +/- 5%

TO WALL
XFORMER

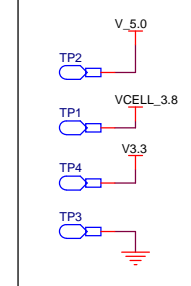
POWER
page 2

TO BATT
PACK

1 MHz SWITCHING

OUTPUT = 3.82V

TEST LOOPS



OUTPUT = 4.99V

OUTPUT = 3.34V

TITLE: SCHEMATIC DIAGRAM		
CUSTOMER: TT Proactive		
PROJECT: M2M Reference Design		
CODE:		
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DATE: 2/1/13	SCALE: NONE	PAGE: 1 OF 7

twistthink
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